XEROX

Daybreak Technical Reference Manual

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The information contained in this document is preliminary data and is subject to change without notice.

Preface

P.1 Purpose

The Daybreak Technical Reference Manual describes the hardware that is specific to the Daybreak version of Dove. The manual describes the theory of operation of the components and presents information important for programmer interface.

P.2 Audience

The following groups are expected to refer to this publication:

Engineers
Field Service personnel
Microprogrammers
System designers

P.3 Organization

After a brief overview, each of the three Daybreak boards (Mesa Processor, Display Control and Memory, and Memory Expansion) is discussed in a separate section. Components are described as to hardware, theory of operations, and programmer interface.

Hardware describes board layout, interfaces to the backplane, power requirements, and external and internal signals of functional units.

Theory of Operations describes system operating modes.

Programmer Interface describes register assignments and timing.

Appendix A describes the standard cell Daybreak Display Controller.

Appendix B, printed in a separate volume, contains schematics for the three Daybreak boards and includes the Display Control and Memory board with the Daybreak Display Controller.

P.4 References

The following documents contain supplementary information.

Dandelion Hardware Manual (preliminary draft), version 2.2, March 1982.

Daybreak Microcode Reference Manual, Version 1.0, April 1986.

Dove IOP Board Technical Reference Manual, November 1985.

Dove PCE Board Technical Reference Manual, Version 1.0, January 1986.

Mesa Processor Principles of Operation, version 4.0, May 1985.

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