UNIVAC 9000 CARD ASSEMBLER

Programmed Instruction Course

Book 1 - Introduction





EDUCATION CENTER

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UNIVAC 9000 CARD ASSEMBLER PROGRAMMED INSTRUCTION COURSE

Introduction to UNIVAC 9000 Series Computer Programming

Book 1

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INTRODUCTION

This text is the first of a series of programmed instruction manuals designed to teach 9000 Series Card Assembler programming. Successful completion of this text and the self-test evaluation are prerequisites for starting Book 2 of the course.

In this introductory text, the novice acquires the basic computer programming concepts he will need before he begins to learn card assembler language coding.

HOW TO STUDY THIS TEXT



This is a programmed text designed for self-study and self-evaluation. Each section is made up of numbered teaching units called frames. An EXPRESS STOP frame precedes each section. This is a self-evaluation frame that permits you to test your knowledge of the material to be covered in the section. If all of your responses to the EXPRESS STOP frame are correct, you may skip to the next EXPRESS STOP frame. If any response is incorrect you are expected to study the frames that follow.

Each EXPRESS STOP frame is followed by a PREVIEW frame. This frame introduces the material to be covered in the subsequent teaching frames. The PREVIEW frame requires no response.

The following types of teaching frames are used:

Simple response frame (A blank is filled in with a missing word, phrase, or symbol.)

Multiple choice frame (The correct response is selected from two or more alternate responses.)

Matching frame (Corresponding items from two lists are matched.)

Macro frame (A paragraph of text material, covering several related teaching points, is followed by a series of simple response frames that spotlight the teaching points.)

The correct response to each frame is printed in the right-hand margin. Mask the response column with a blank keypunch card as shown in the illustration. Then, check your response with the correct response by lowering the mask as you work down the page.

Note:

EXPRESS STOP frame 1 is a pretest of the material covered in frames 2 through 22. If you do not know this material, do not attempt to guess the correct responses. Skip frame 1 and proceed to frame 2.

1. EXPRESS STOP		
Match each of the fol its corresponding fun	lowing basic data processing activities to ction:	
A. Classifying	Information is arranged in a desired numeric or alphabetic	В
C. Calculating	Information is grouped for numeric or alphabetic	A
E. Recording	The results of calculations are written on a physical medium	E
	Addition, subtraction, multi- plication, or division is	с
	The results of processing are arranged in condensed form.	D
Match each of the foldevice:	lowing functions with a corresponding	
A. Input	Card reader	A
B. Output	Card punch	В
C. Input/Output	Magnetic tape unit	с
D. Processing	Magnetic disc unit	С
	Paper tape unit	с
	Central processor	D
	Printer	В
	Console typewriter	С
Name the three basic the symbols below:	data processing functions represented by	
	23	 Input Processing Output

•

1. EXPRESS STOP (Continued)	
A standard EAM punched card can record a maximum of:	
B0 characters.	80 characters
12 characters.	
Data stored on tape is normally arranged in:	
sequential order.	sequential order
random order.	
·	
Data stored on magnetic discs may be arranged in:	
sequential order only.	· ·
random order only.	
sequential or random order.	sequential or random order
Which of the following devices provides faster input speed?	
Card reader	
Magnetic tape unit	Magnetic tape unit
	IF YOU ANSWERED ALL
	QUESTIONS CORRECTLY, SKIP TO FRAME 23 ON PAGE 1-21.
	<u></u>

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	evices.
3. Manual data processing is generally performed in the follo sequence:	owing
1. Classifying (Information is grouped for numeric or alphabetic identification.)	
2. Sorting (Information is arranged in a desired numeric alphabetic sequence.)	c or
3. Calculating (Processing operations such as addition, subtraction, multiplication, or division are performe	d.).
4. Summarizing (The results of calculations are summarized or condensed.)	rized
 Recording (Data is written on a physical medium suc a ledger or a check.) 	ch as
List the five basic manual data processing activities in the sequence they are generally performed:	3
	Classifying
	Sorting
	Calculating
	Summarizing
	Recording
 Although the method of performing these activities is automatic rather than manual, electronic data processing (EDP) also involves classifying, sorting, calculating, summarizing, and data. 	recording

5.	Data processing includes three basic functions:	
	 Input (sales slips, time cards, stock cards, or similar source documents). 	
•	 Processing (manipulating data and performing calculations). 	
	 Output (printed reports, checks, etc.). 	
	When sales slips are recorded in a ledger, the sales data represents	input
z	When a clerk computes weekly earnings by multiplying the hourly rate of an employee by the hours worked, this operation represents	processing
	Printing paychecks represents thefunction of data processing.	output
6.	The three basic data processing functions can be represented by the following simplified diagram.	
	Whether data processing functions are performed manually or by a computer, the sequence is always the same. Which of the following is the correct sequence?	
	Processing, Input, Output	
	Input, Processing, Output	Input, Processing, Output
	Input, Output, Processing	
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Magnetic Tape Unit

The magnetic tape unit shown above is a high speed input/ output (I/O) device that provides auxiliary storage on magnetic tape. Information is recorded as magnetized spots on a ferrous oxide coated tape. The tape medium is similar to that used to record sound. Numeric and alphabetic characters are represented by magnetized spot patterns as shown in the simplified drawing of a tape segment below.

MAGNETIC TAPE

14.

(Magnified View)

ARTAN KAROPANAN ATTANAN Arti salahatikan		197 - 111 11111111111111111111111111111111	IKA PITATANAN Kasatakan Ingeria	0000001000 001000000000000000000000000
ATTAL KARAN TATAN MATANA Attal Karan Tatan	111111111111111 1111111111111111111111		ISATTI KITAN Kakupunin ((((((((((((((((((ESHINIU BADA IIIA
	63.03309 68.03999945909 69.039999459	400 0039 9899 - 89900 9 - 889900	1111 1111 11111111 1111111111111111111	
a na na sa	anna an ann ann ann ann ann ann ann ann	81 111111 131 11111	LABOREPSKI ATA Referencia	THEATER AND A STREET

 Appendix
 Appendix

Alphanumeric information is recorded on tape just as songs are recorded on tape, one after the other (serially). If the data from 2000 sales slips is recorded on a tape, the magnetized spot patterns representing sales slip No. 1000 can be read and processed:

- only after all sales slips from No. 1 thru No. 999 are searched sequentially.
- □ directly without searching sequentially thru the tape.

Each character in the tape segment above is represented by a pattern of:

magnetized spots.

punched holes.

only after all sales slips from No. 1 thru No. 999 are searched sequentially.

magnetized spots







18. Input or output operations punched tape are relatively tape or disc operations. Th of electromechanical holes Compared with the speed of input or output speed of punctions.		
🗆 slow.		slow
□ fast.		
19. Match the following:		
A. Off-line device	Card reader	В
B. Input device	Paper tape unit	D
C. Output device	Magnetic tape unit	D
D. Input/Output device	Magnetic disc unit	D
	Keypunch	А
	Printer	С
	Card punch	С
	Console typewriter	D
20. Match the following:		
A. Card reader	Converts punched-hole	A,C
B. Card punch	code to electrical pulses.	
C. Paper tape unit	spots to electrical pulses.	D,E
D. Magnetic tape unit	Converts electrical pulses	B,C
E. Magnetic disc unit	to punched holes.	
F. Printer	Converts source data to punched holes.	G
G. Keypunch	Converts electrical pulses	F,H
H. Console typewriter	to printed characters.	
_	Converts operator instruc- tions to electrical pulses.	н
	,	



REVIEW	
Grouping information in categories for numeric or alphabetic identification is called	classifying
Arranging data in ascending or descending numeric order for input to a computer is called	sorting
Multiplying hours worked by the hourly rate is an operation called	calculating
The totals produced at the end of a monthly payroll report are an example of the function of data processing.	summarizing
The printing of a payroll check and the attached stub listing gross earnings, deductions, and net earning is an operation called	recording
Source data read from punched cards is	input
When a computer operates on data, the operation is called	processing
When processing is completed, the results are printed or recorded as	output
Devices electrically connected directly to the Central Pro- cessor are referred to as devices.	peripheral
A keypunch is referred to as andevice.	off-line
Punched-hole code in cards can be sensed by a	card reader
Character-coded holes are punched into cards to represent computer output by a	card punch
The maximum number of characters that can be punched in a standard EAM card is	80

Nomo th				
symbols	ie medium represented t ::	by each of the followir	ng	
		· · · · · · · · · · · · · · · · · · ·	- 5	Punched card
	· · · · ·	·		Punched paper tape
			-	
$\left(\right)$		· · · · ·		
)		-	Magnetic tape
((· · ·	_	Magnetic disc
		· · · · · · · · · · · · · · · · · · ·		Printed document
A single	punched card usually re	anresents one.		
П	TIPIA			
	Tield.			
	record.		•• ••	record
	record. file.			record
	record. file.			record
	riela. record. file.	• ·		record
A group same sub	record. file. of punched cards repres ject is a:	senting data related to	the	record
A group same sub	record. file. of punched cards repres ject is a: field.	senting data related to	the	record
A group same sub	record. file. of punched cards repres ject is a: field.	senting data related to	the	record
A group same sub	record. file. of punched cards repres ject is a: field. record.	senting data related to	the	record
A group same sub	record. file. of punched cards repres ject is a: field. record. file.	senting data related to	the	file
A group same sub	record. file. of punched cards repres ject is a: field. record. file.	senting data related to	the	file
A group same sub	record. file. of punched cards repres ject is a: field. record. file.	senting data related to	the	file

23. EXPRESS STOP (Continued)	
A unit of data in a punched card is a:	a Antonio de la composición de 1997 Antonio de la composición de 1997
🗆 field.	field
🗅 file.	
enter en <u>en la constante de la</u> constante de la c	
Match each letter below to a corresponding designation:	
Field	A, B, C, D
Image:	E
File	F
E	
Blocked tape format is represented below by:	
tape segment A.	
tape segment B.	tape segment B
A Record 1 Record 2 Record 3 Record 4	
B Record 1 Record 2 Record 3 Record 4 Record 5 Record 6	- -
	IF YOU ANSWERED ALL
	SKIP TO FRAME 42 ON
	PAGE 1-35.
24. PREVIEW	
In this section you will learn the symbols that represent data	
recording media and the devices on which the media are used. The organization of data into fields, records, and files will be	
discussed. Machine code will be introduced as the binary rep- resentation of electrical pulses in patterns of 1's and 0's.	
External and internal labels for identifying tape files will also	1





1-24





31. 31462742 тиве 764061250 PART NO. DESCRIPTION ON HAND DATE 17 48 49 50 51 52 53 54 N 20 21 22 23 24 25 28 27 28 29 30 The punched card shown above is an inventory stock card. Each unit of data (Part No., Description, On Hand, Date) is called a field. The four fields represent the complete data for this stock item. The complete data punched into this card is called a record. In the punched card above, the characters that represent the PART NO. form a ______ and are contained field within card columns _____ 1-15 _____ The characters that represent the DESCRIPTION of the stock item form a ______ within card columns field . 16-45 ON HAND and DATE are the two remaining fields in the record The characters 7 6 4 in the field named ON HAND are punched into columns _____ 53, 54, 55 The characters in the field named DATE are punched into columns _____ 56-61

1-27



34. Match each letter below to a corresponding designation:	
Field	A,B,C,D
$\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 1 \\ 1 & 1 &$	E
A B C D F F F F F	F ·
Ĕ	
35. Records and files may be maintained on magnetic tape as well	
as on cards. A single reel of magnetic tape is generally referred to as a volume and may contain many small files or	
one large file. A file may be continued from one volume to another.	
While card files may be visually identified, tape files cannot	
be examined visually. To assure adequate volume and file identification of tape files, both external and internal labels	
are required. An external written label is provided on each	
by the computer. Each internal volume or file label is an 80 character record.	
The first label recorded on a volume is a volume (VOL) label	
record. The data in each file is preceded by a header (HDR) label record and is followed by an end-of-file (EOF) trailer label record	
VOL HDR INVENTORY FILE C	
In the above simplified drawing of a single file stored in one	
volume.	
The first record in the volume is a label record.	VOL
The second record in the volume is a label record.	HDR
The first record in the file is a label record.	HDR
The last record in the file is an label record.	EOF

36.	When a file is stored in two volumes, an end of volume (EOV) trailer label designates the end of the first volume and indicates to the computer that the file is continued on volume 2.	
	Volume 1 of 2.	1
	VOL1 HDR1 INVENTORY FILE A EOV1	
	Volume 2 of 2.	
	VOL2 HDR1 INVENTORY FILE A EOF1 (CONTINUED)	
	Volume 1 above is terminated by label record:	
	🗆 EOF1.	
	EOV1.	EOV1
	Volume 2 is terminated by label record:	
	EOF1.	EOF1
	□ EOV1.	
	If a file requires two volumes to store all the data, how many label records will be required?	
,	🗆 Two	
	□ Four	
	□ Six	Six
37		
VOL1	HDR1 EOF1 HDR2 EOF2	
	How many files are stored in the above volume?	Тwo
	How many label records are there?	Five
	How many header and trailer label records will be written on a volume that stores three files?	
	□ Five	
	Seven	Seven


 9. Punched-card files are not labeled internally since they can be identified visually. 	
 A punched-card file is treated as an unlabeled file contained on a single volume. 	
 The end of a punched-card file is indicated by an end-of-file (EOF) card punched with special characters designated for this purpose. 	
A punched-card file is treated as:	
□ an unlabeled file.	an unlabeled file
□ a labeled file.	
Labels are used to identify a:	
punched-card file.	
🗆 magnetic tape file.	magnetic tape file
The end of a punched-card file is indicated by:	
an end-of-file (EOF) card.	EOF card
🗇 a trailer label.	
40.	
A B C D E	
Which of the above symbols represents an output device	
only?	C

REVIEW		
A unit of data on a punched card is called a	field	
The complete data on a punched card is called a	record	
A group of punched cards containing data related to the subject is called a	he same file	
A single reel of magnetic tape is called a	volume	
The first record in a volume is called a label.	volume	
Each file in a volume is preceded by a label.	header	
Each file in a volume is followed by an label.	EOF	
The end of an intermediate volume in a multivolume fil indicated by an label.	file is EOV	
The end of a volume that stores one or more complete indicated by an label.	e files is EOF	
The standard length of a label record is(how many) characters.	80	
Are punched-card files labeled?	No	
The end of a punched-card file is indicated by an card.	EOF	

<u> </u>	I _ I		
			· · · · · · · · · · · · · · · · · · ·
•····•			
	<u></u>		
_ <u></u>			
	*	· · · · · · · · · · · · · · · · · · ·	·

Machine Code; Symbolic Code; Mnemonic Code; Storage; Bit; Byte; Character; Address

		F
42.	EXPRESS STOP	
	Binary digits are called	bits
	Binary code consists of (how many) symbols.	two
	The presence of a pulse in computer input represents a:	
	□ 0 bit.	
	□ 1 bit.	1 bit
v	High-speed memory is made up of:	
	plated wires.	piated wires
	🗆 data.	
	□ instructions.	
	Instructions are interpreted and executed by:	,
	□ the arithmetic unit of a CPU.	
	□ the control unit of a CPU.	the control unit of a CPU
	□ the storage unit of a CPU.	
	A program is a logical sequence of:	
	🗆 data.	
	□ instructions.	instructions
	The 9200/9300 programmer writes instructions in:	
	machine code.	
	symbolic code.	symbolic code.
l		

42.	EXPRESS STOP (Continued)	
	A character is represented in memory by:	
	🗆 one bit.	
	□ four bits.	
	eight bits.	eight bits
•	A character stored in memory:	44 C
	requires no addressable location.	
	will have an addressable location.	will have an addressable location -
	The numbers 1000, 1001, and 1002 above represent:	
·	content of memory.	
	addressable locations in memory.	addressable locations in memory
24	Name the two parts of an instruction:	
,		Operation code
		Operand
	Mnemonic code is used to represent:	
	□ an operand.	
	□ an operation code.	an operation code.
		IF YOU ANSWERED ALL OF THE ABOVE QUESTIONS CORRECTLY, SKIP TO FRAME 62 ON PAGE 1-47.

43.	PREVIEW	
	Information is stored in computers in a binary form called machine code. This will be discussed in relation to bits, bytes, and characters. We will also discuss addressing, the parts of an instruction, and symbolic language.	
-		
44.	Just as the language of telegraphy consists of patterns of dots and dashes, the language of computers consists of patterns of ones (1's) and zeros (0's). In computer (machine) code each numeric, alphabetic, or special character is represented by a unique coded pattern of 1's and 0's. Computer code is a binary language based on the binary numbering system, which con- sists of the two digits 1 and 0. Each binary digit is called a bit. (The word bit is a contraction of the two words <u>binary</u> digi <u>t</u>).	
	The decimal numbering system is based on ten digits. The binary numbering system is based on:	
	one digit.	
	two digits.	two digits
	□ three digits.	
	A binary digit is called a	bit





49. Both data and instructions are stored in memory in binary code. An alphabetic character is represented by eight bits. For example, the letter A is represented in memory as 11000001. (This is Extended Binary-Coded-Decimal Interchange Code and will be discussed later.)	
Each alphabetic character in memory is stored in:	
one bit	
four bits	
eight bits	eight bits
	ч. Т
50. When stored in memory, each character has an addressable location as shown in the diagram below.	
In the above diagram:	
The address of the character A is	0975
How many bits are required to store the character A?	
	eight
The number 0982 represents the:	
content of a memory location.	
address of a memory location.	address of a memory location
The address of the character \$ in storage is	0979

	······································
51 An eight hit character stored in memory is called a hyte	
51. An eight-bit character stored in memory is called a byte.	
BYTE	
The character A, shown above, is stored in memory as a	
	byte
Each byte of stored data occupies (how many)	eight
bits in memory.	
· · · · · · · · · · · · · · · · · · ·	
50	
52.	
4 3 2	
2100 2101 2102	
The numbers 2100, 2101, and 2102 above represent:	
Li content of memory.	
addressable locations in memory.	addressable locations in memory
The numbers 432 above represent:	
content of memory.	content of memory
addressable locations in memory.	
	1

+

53	 Operation of a computer is automatically directed by a program. 	
	• A program is a specified sequence of instructions written by a programmer to operate on data to solve a problem.	
	• Each instruction defines an operation to be performed and defines the location of the data or specifies a device to be used.	
	Computer operation is normally directed:	
	automatically by a program.	automatically by a program
	manually by an operator.	
	A program is a specified sequence of:	
	🗆 data.	
	instructions.	instructions
;	An instruction (check one or more):	
.'	specifies an operation.	specifies an operation
	defines the storage location of the instruction.	
· ·	specifies the data to be used.	
. •	defines the storage location of the data.	defines the storage location of
	can specify a device to be used.	can specify a device to be used
54	. Instructions are written by the 9200/9300 programmer in:	
	□ machine code.	
	□ symbolic code.	symbolic code
	Instructions are stored in memory in:	
	machine code.	machine code
	symbolic code.	

55.				-
		ASSEMBLE		Dure (
Form A	LABEL & OPERATION &	OPERAND	3 COMME	NTS
			·····	
Form B		COBOL Ogramming form		PR0
	1 NUMBER 6 7 8 11 12	20	<u>30 40</u>	
Form C	PROGRAM TATEMENT TATEMENT TATEMENT FOR TRAIN STATEMEN		FORM	PROG
				<u>50</u>
	UNIVAC			IT {
Form D		FORM PARTS		
Instructions may be co languages available to t symbolic languages inc	ded in any one of sever he programmer. Comm lude the following:	al symbolic only used		
Assembly Langua	no			
	n Rusiness Oriented Lar	nulane)		
FORTRAN (Form	nula Translation) langua	igeuge,		
RPG (Report Pro	gram Generator) langua	ge		
Name the symbolic lan forms illustrated above	guage used with each of :	f the coding		
Form A			Assembly langu	age
Form B			COBOL	
Form C			FORTRAN	
Form D			RPG	

56.	Instructions direct computers to read, write, edit, move data, compare, branch, add, subtract, multiply, divide, etc. Such operations are written in mnemonic code. (Mnemonic is pronounced "nē-mon'-ik" and means easy to remember.) In the coding example below, the mnemonic MVC means \underline{MoVe} Character.	
	LABEL & OPERATION & OPERAND	
	The mnemonic operation code for an Assembly language Move Character instruction is	MVC
57.	 The two parts of an instruction include the operation code and one or more operands. The operation code specifies the operation to be performed. 	
	 An operand defines the storage address (location) of data in memory. 	
	LABEL 5 OPERATION 5 OPERAND 1 10 16	
	In the above Add Packed Decimal instruction the mnemonic AP is the code.	operation
	The symbolic names (tags) BAL,TOTAL represent the addresses of data in the above instruction and are called	operands
	The operation code AP is written in code.	mnemonic

58. The operation code specifies:	
the address of the instruction.	
the operation to be performed.	the operation to be performed
An operand defines:	
the address of an instruction.	
the address of data or specifies a device.	the address of data or specifies a device
59. Match the following: A. Program A set of coded instructions. B. Instruction Specifies operation to be performed and defines C. Operation code address of data or specifies a device. D. Operand Defines storage address of data or specifies a device.	A ' B D C
60. The symbolic representation of an operation code is written incode.	mnemonic

61.	REVIEW	
	A binary digit is called a	bit
	Binary code consists of (how many) symbols.	two
	The presence of a pulse represents a bit.	1
	The absence of a pulse represents a bit.	0
	An alphabetic character is represented by (how many) bits in memory.	eight
	An eight-bit character configuration is called a	byte
	Instructions and data are internally stored in the high-speedunit of the CPU.	memory
	The solution of a problem by a computer is automatically directed by a	program
	A program is a specified sequence of	instructions
	Instructions are written by a programmer in code.	symbolic
	The operation to be performed by an instruction is specified by the	operation code
	The address of data is defined by an	operand
	The operation code of an instruction is written incode.	mnemonic

System Flowchart; Block Diagram; Process Flowchart





1-48





65. The system flowchart illustrates the problem, defines the input and output, and specifies the media. How the problem is to be solved is the responsibility of the programmer. The basic processing requirements are generally planned first by the programmer at the manual level as shown in the block diagram of a simplified payroll problem below. READ EMPLOYEE RECORD COMPUTE GROSS WAGE COMPUTE FED. & SOC. SEC. TAXES SUBTRACT TAXES FROM GROSS WAGE WRITE CHECK The illustration above is a: □ system flowchart. block diagram □ block diagram.

66. When the block diagram has been checked for completeness and accuracy, the programmer is ready to develop the process flowchart. The process flowchart illustrates the step-by-step coded operational instructions the computer will be directed to perform. The standard symbols used to construct the process flowchart represent processing steps and are shown in Panel 1 on page 1-135.





Terminal

Input/Output

Decision

Process

Connector















· · · · · · · · · · · · · · · · · · ·	·····
Match each symbol in the above flowchart with the corre- sponding symbol name.	
Input	С
Decision point	D
Process	F
Output	G
Terminal	A,E
Connector	в,н
Match each symbol in the above flowchart with a correspond- ing processing step:	
START	A
STOP	E
EOF CARD?	D
ADD A TO B	F
READ A CARD	с
PRINT RESULT	G

79.	REVIEW	
	Illustrating the problem, defining the input and output, and specifying the media to be used are the responsibilities	
	of the analyst.	systems
	The processing steps required to solve a problem can be planned at the manual level by means of a diagram.	block
	The detailed process flowchart is prepared by the	programmer
	The symbols used in a system flowchart represent	media
	The symbols used in a process flowchart represent	
		processing steps
	Connectors are used to represent a	loop or a continuation
	A test point to determine the direction of a branch in the flow path is illustrated in a flowchart by a	decision block
	The symbol > represents	greater than
	The symbol < represents	less than
	The symbol \geq represents	greater than or equal to
	The symbol \leq represents	less than or equal to
	The symbol : represents	compare
	The symbol \neq represents	not equal
	The symbol () represents	a connector

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	 		#***,t	
		 ······································		
	 	 •		
	•			
· · · · · · · · · · · · · · · · · · ·	 	 		







81. PREVIEW

A process flowchart must contain all of the information that a programmer will need to write a usable program. Usually each step to be coded is represented by one or more symbols.

Some of the symbols represent data manipulation activities. Others represent operations that are required by the machine. Housekeeping activities such as setting counters or clearing output areas are typical examples of operations that are necessary. These operations are not usually obvious from the statement of a problem or from the system flowchart. Yet the programmer must know when these activities are required and the exact point at which they must be included in the flowchart.

We will examine flowcharting techniques and machine considerations in the following frames.


83. The flowchart in the preceding frame implies that the pro- grammer must write code that will reserve two storage areas: One input area will receive the sales data from a card, and one area will accumulate the sum in the field named TOTAL.	
MEMORY	
TOTAL	
Is the programmer's task of reserving the storage areas illus- trated in the flowchart?	
🗆 No	Νο
Before the first card is read into the input area, information is contained in this location from the previous program as shown in the simplified illustration below. When the first card is read into memory, the previous contents of memory are overlayed and destroyed. Similarly the data from the first card is over- layed and destroyed when the second card is read into memory.	
INPUT AREA N A M K C A S I T R E B O R	
TOTAL 4 2 4 6 2 6 6	
When new data is read into memory, it appears in the above location designated:	
🗆 Input Area.	Input Area
TOTAL.	
When processing is performed, the resulting sum will be accumulated in the location designated:	
🗆 Input Area.	
🗆 TOTAL.	TOTAL





87. The allocation of storage areas is usually not illustrated in the flowchart. The input/output and work areas that are required are implied. However, housekeeping activities should always be explicitly indicated in the flowchart.



The housekeeping operation ZERO TOTAL should be included in the above flowchart. It will be performed only once. Therefore the housekeeping block should be inserted after the:

□ START block.

.

□ READ block.

□ ADD block.

How many times will the above housekeeping function be performed?

□ Only once.

□ After each card.

Only once

START block

88.	A blank or space is <u>not</u> usually considered to be a numeric value. An area that is to be used in calculations should always be cleared with:	
	an alphabetic character.	
	□ zeros.	zeros
	D blanks.	
89.	Housekeeping functions, such as clearing a print-line or setting a counter to zero, frequently appear as the first block in the flowchart. In some programs, housekeeping blocks appear at several key points.	
	In the following flowchart, which blocks indicate housekeep- ing operations?	Blocks 2 and 6
	1 START 2 ZERO TOTAL 3 READ 4 EOF CARD? 4 CLEAR PRINT-LINE MOVE TOTAL TO PRINT-LINE B PRINT-LINE 5 ADD SALES TO TOTAL 9 STOP	

usually placed at:	the beginning of the program
$\square the end of the program.$	
The "CLEAR PRINT-LINE" operation in the previous flow- chart is performed only once. Therefore, we can combine this block with the:	
READ A CARD block.	
ZERO TOTAL block.	ZERO TOTAL block
ADD SALES TO TOTAL block.	
91. (1)	
2 READ A CARD CLEAR PRINT-LINE MOVE TOTAL TO PRINT	
VES EOF CARD? NO ADD SALES VES STOP WRITE PRINT LINE 2	
Some housekeeping operations must be repeated.	
How many housekeeping operations are indicated in the above flowchart?	Тwo
The CLEAR PRINT operation is performed:	
□ once for each card.	once for each card
only once.	
Could we combine the housekeeping operations into one block?	
□ Yes	
□ No	No

92.	When a printed report is required as a result, the flowchart will usually include housekeeping operations that must be repeated.	
	TURN TO PANEL 2 ON PAGE 1-136.	
	PART 1 of PANEL 2 shows an area of memory that has been reserved by the programmer. This area will always be referred to as PRT-LINE. All data to be printed will be moved into this area before it is written out to the printer.	
	How many character positions of memory have been reserved for PRT-LINE?	132
	PART 2 illustrates the headings that must be printed on the first page of the report.	
	How many times must PRT-LINE be filled with blank spaces before the headings are completely printed?	
	□ One	
	🗆 Тwo	
	□ Three	Three
93.	Areas of memory that are reserved for use in calculations are always cleared by filling with zeros. What must the pro- grammer use to clear the printer output area?	
	Blanks	Blanks
	Alphabetics	





95.	As illustrated in the flowchart on the facing page, when the first card is read into memory, the account number punched into this card is compared with the contents of location TEST. What value will TEST contain at this time?	
	\Box The account number from a previous card.	
	□ Zeros.	Zeros
	Data left from a previous program.	
	What value will TEST contain when the first card has been written to tape?	
	Zeros.	
	□ The account number of the first card.	The account number of the first card.
	The account number of the next (second) card to be read.	
	Will the second card be read before or after the first card is written to tape?	
	Before	
	□ After	After
	How many times will the housekeeping activity of moving zeros to TEST be performed?	Once
	What value will TEST contain after the 10th card has been written to tape?	
	The account number of the 9th card.	
	The account number of the 11th card.	
	The account number of the 10th card.	The account number of the 10th card











102.	EXPRESS STOP		
	Match the following:		
	A. Source program Wri	itten by programmer.	Α
	B. Assembly program Sup	oplied by manufacturer.	В
	C. Object program Ou	tput of assembly run.	С
	In i	machine langu age .	B,C
	In s	symbolic langu age .	Α
	Dir	ects assembly process.	В
	An Assembly run will usually result in t	wo outputs. Name	
	them.		
	1		Object program
	2		Program listing
	A	activities in the order in	
	which they are performed:	activities in the order in	
	Assembly run 1		Process flowchart
	Process flowchart 2.		Write source code
	Test run 3		Assembly run
	Debug assembly run 4		Debug Assembly run
	Production run 5		Test run
	Write source code 6.		Production run
			IF YOU ANSWERED ALL QUESTIONS CORRECTLY, SKIP TO FRAME 119 ON PAGE 1-91.
1			

·		
103.	PREVIEW	1
	The first steps in program preparation are frequently the responsibility of the systems analyst. If we assume that the analyst has supplied the programmer with a system flowchart and a definition of the problem in which all input and output formats are specified, we may then trace the programmer through the steps required to solve the problem when a symbolic language is used.	
	In the following frames you will learn to identify the steps required to produce a usable program.	
		· · · · · · · · · · · · · · · · · · ·
104.	Before the programmer starts to code the program, he must illustrate the logic of his program by drawing a:	
	system flowchart	
	process flowchart	process flowchart
105.	TURN TO PANEL 3 (Page 1-137).	
	A programmer must be capable of performing all of the steps required to solve a data processing problem. Steps 1 and 2 of the flowchart in Panel 3 show activities that are usually per- formed by a systems analyst. The remaining steps are usually assigned to a programmer.	
	Notice that steps 5 and 7 are performed by the computer. What is the input to the Assembly run, step 5?	
	Cards punched from the programmer's coding form.	Cards punched from the pro- grammer's coding form
	Test data.	

106.	If the programmer makes a clerical error when coding the program, it will be detected during the Assembly run. What are the next two steps to be performed when an error is indicated? (Hint: look at Panel 3)	
	1	Correct errors
	2	Assembly run
	······································	
107.	Is it always necessary to reassemble the program when an error is detected?	
	□ Yes	Yes
	□ No	
	· · · · · · · · · · · · · · · · · · ·	
108.	Suppose an error is detected during the test run, step 7, what are the next three steps to be performed?	
	1	Correct errors
	2	Assessible mus
	3	Assembly run
	3	Any errors?
109.	The computer manufacturer supplies an <u>Assembly program</u> that will translate the symbolic language <u>Source program</u> into a form that can be executed by the computer.	
	The program that is produced by this Assembly run is called an Object program. To produce the Object program, two inputs are required. What are they called?	
	1	Source program
	2	Assembly program



111. As you can see in the illustration below, the Assembly run has two inputs and two outputs. Name the two that are frequently resident on magnetic tape.	
SOURCE PROGRAM ASSEMBLY RUN	ASSEMBLY PROGRAM
ASSEMBLY LISTING 2.	OBJECT PROGRAM
Which is the program written on the coding form by the programmer?	Source program
If errors are detected during the Assembly run shown above, they will be printed on the	Assembly Listing
112. It is your job as a programmer to locate any error and correct it. When all errors are corrected, the program must then be:	
reassembled.	reassembled
□ scrapped.	
113. The listing produced by the Assembly run will identify errors made by the programmer. If the listing indicates the program is error-free the programmer may:	
stop; his task is complete.	
prepare to take the next step.	prepare to take the next step

114.	When your listing is error-free you are ready to test your program with data. The computer is then ready to read your object card deck (or tape) and follow the instructions you have written for it. What is this next computer run called?	
	□ Assembly run	
	Production run	
	Test run	Test Run

115.	Data that the programmer makes up to test his program looks like real data but offers many advantages over the use of real data, for example:	
	• It is available when you need it.	
	• The programmer can make sure that all of the paths in the program are tested.	
	 The amount of data needed to test the program can be limited by the programmer. 	
	Checking the test output is as important as writing the pro- gram. When a program has an error and does not work properly, it is said to have "bugs." The process of finding the error and correcting it is known as "debugging."	
	Is it necessary to repeat the Assembly run after debugging the program?	
	□ Yes	Yes
	□ No	



18. 	During a production run the c program in machine language Arrange the following activitie are performed:	omputer executes the Object to obtain the final result. as in the order in which they	
	Assembly run	1	Flowchart
	Flowchart	2	Write source code
	Test run	3	Assembly run
	Write source code	4	Test run
	Production run	5	Production run
·	Real data is used during the:		
	Test run.		
	□ Assembly run.		
	Production run.		Production run
	What program is in control of a production run?	the central processor unit during	Object program

Binary Code; EBCDIC; Packed and Unpacked Formats

119.	119. EXPRESS STOP			
	The smallest unit of memory storage that may be addressed in the 9200/9300 computer is called a			byte
	Data f stored	ields in:	that are alphabetic or alphanumeric will always be	
		🗆 p	acked format.	
		🗆 u	npacked format.	unpacked format
	Exclue one al	— ding p phabe —	parity, how many binary bits are required to store etic character?	Eight
	,Check	the f	following statements as true (T) or false (F):	
	Т	F		
			Each magnetized plane of a plated wire can	True
			A decimal data field read in from a card character format enters the processor in unpacked format.	True
			A packed field consists of two numerics in all but the most-significant byte of the field.	False
			There are two formats for the storage of decimal data.	True
			In unpacked format each byte contains two decimal digits.	False
			Data is usually represented in Extended Binary- Coded Decimal Interchange Code.	True
				IF YOU ANSWERED ALL QUESTIONS CORRECTLY, SKIP TO FRAME 142 ON PAGE 1-103.

120. PREVIEW

Data may concern inventory, accounts receivable, accounts payable, payroll, and the like. In many cases, processing involves not only performing calculations on some part of each record to arrive at balance, amount, or earnings, but also involves adding, changing, or deleting records as new transactions occur.

The data is made available to the memory of the system for processing by an input device. The information is sensed or read and is converted to a code used within the computer. The following group of frames will discuss the methods of representing data within the computer.

121. REVIEW

Computers function in binary states. This means that each magnetized plane of a plated bit wire can indicate only two conditions that we represent by the symbols 0 and 1. Patterns of these symbols are called binary code.

Information is represented in memory by:

- punched holes.
- printed characters.
- magnetized plated bit wires.

The system of symbolizing information with 1's and 0's is called ______ code.

magnetized plated bit wires

binary

The positional value of bit symbols is based on the progression of powers of 2. The units position has a decimal value of 1; the next position, the value of 2; the next 4, and so on. A 1 bit represents the presence of a positional value as shown in the examples below:

	BIN	ARYF	POSIT	IONAI		UE.		
128	64	32	16	8	4	2	1	
0	0	0	0	0	0	0	0	=
0	0	0	0	0	0	0	1	=
0	0	0	1	0	0	0	0	= 1
0	0	0	1	0	1	0	0	= 2
0	0	1	0	0	0	1	1	= 3
0	1	0	0	0	0	0	0	= 6

Convert each of the following binary numbers to its equivalent decimal value:

0	0	0	1	0	1	1	1	=
1	0	0	0	1	0	1	0	=
1	1	1	1	1	1	1	1	=

23 138

255

123.	In the 9200/9300, each plated bit wire can store eight data bits.	
	How many bits in a byte?	Eight
	A byte can store at least one	character
124.	The 9200/9300 uses some numeric data in binary form by grouping 4 bytes (32 bits) into a unit called a word.	
	How many bits in a byte?	Eight
	How many bytes in a word?	Four
	Since a word is four bytes long, how many planes of memory are required to store a word?	32 planes
	How many bits in a halfword?	16 bits
	Is it necessary for the planes to be in consecutive memory positions?	
	□ Yes	Yes
	□ No	
	How many consecutive bits of memory are required to store a doubleword?	64 bits
		·····
125.	Match the following:	
	A. Byte Single magnetized plane.	В
8 	B. Bit Contains four bytes.	с
	C. Word Contains eight bits.	А
	Zero (0) or one (1).	В .
	Contains 32 bits.	С
	Storage of at least one character.	A

			· · · · · · · · · · · · · · · · · · ·	
126.	The Exte (EBCDIC 9200/930 structed code are assigned character In EBCD sented as The four following	ended Binary Coc C) is the principal DO System. Altho of zeros and one not applicable. I to represent spec rs. CC, each decimal a binary value ir leftmost bit pos g examples:	ded Decimal Interchange Code internal coding scheme used in ugh each byte in EBCDIC is con s, the positional values of binary instead, unique bit patterns have ific decimal, alphabetic, or speci value from 0 through 9 is repre- to the rightmost four bit positions itions are all ones as shown in th	the - been al S. e
		Decimal	EBCDIC	
		1	11110001	
		2	11110010	
		3	11110011	
	What is t ing decin	he EBCDIC bit r nal values?	epresentation for each of the fol	low-
		4		11110100
		0		11110000
		8	·	11111000
127.	The four decimal v	leftmost bits in value: do not have a u have a unique p	the EBCDIC representation of a inique pattern. pattern.	do not have a unique pattern
	Are the f sentation	our leftmost bits of decimal value	meaningful to the EBCDIC represented represented in the EBCDIC represented in the territory of	re-
		Yes		
		No		Νο
			x.	

128.	The principal internal coo System is:	ling scheme used in the 9200/9300	
	Binary.		
	□ Octal.		
	EBCDIC.		EBCDIC
	The numeric, alphabetic, checks or reports are calle on page 1-138 and exam under PRINTER GRAPH		
129.	Assume the following cha cards. Show in EBCDIC of memory. (Refer to Panel	aracters are read in from punched ode how they would appear in 4.)	
	Character	EBCDIC Code	- · · ·
	* (asterisk)	· · · · · · · · · · · · · · · · · · ·	01011100
	R		11011001
	1	<u>на при на селото на</u> Посто на селото на сел	11001001
	С		11000011
	E	:	11000101
· .	\$ (dollar sign)		01011011
	4		11110100
	6		11110110

130.	Check the f	ollowing statements as true (T) or false (F):	
	ΤF		
		At present, many EBCDIC patterns have no assigned printable/recognizable symbols in the 9200/9300 System.	True
		The pure binary system can only be used to repre- sent numeric data.	True
		In the binary number system, the position of a bit determines the value of the bit.	True
		The highest decimal value that can be represented in 4 bit positions is 15.	True
-			
131.	FOOTNOT	E	
	With only a	few exceptions, we will not be concerned with the	
	binary num	bering system. The discussion up to this point was	
	the internal	coding of data and instructions. From this point,	
	we will con	sider all data to be stored in EBCDIC.	
132.	When we re appear in m	ad the value 367 into memory from a card, it will emory as:	
		3 6 7	
	111	10011 11110110	11110111
	How many	bytes of memory are required to store this value?	
			3
	When decimis called unp	nal data is represented as shown above, the format backed decimal. (Packed and unpacked format will	
	be discussed	l later.)	

133.	Suppose we are writing a stock-control program that reads in punched cards containing the number of items added to or removed from stock. One field in this transaction card is called QTY (quantity).	
	We read a card with the value 829 in the QTY field. How would this appear in memory?	
		11111000 11110010 11111001
	The four leftmost bits of each byte contain:	
	□ all zeros.	
	□ all ones.	all ones
	a combination of ones and zeros.	
	When the four leftmost bits of the least significant byte are all ones, the field is considered to be positive. In the illustration above, the value 829 is:	
	positive (+).	positive (+)
	🗋 negative (-).	
	The sign of the field is indicated by the four leftmost bits of the:	
	most-significant byte.	
	🔲 least-significant byte.	least-significant byte
		Y
		-a

ind	of the least-s icate the	agnificant b	byte in the nu 	umeric fiel	d always	sign
The zor dec	e leftmost fou e bits. The ill imal field.	r bits of the ustration b	e other bytes elow shows a	in the fie three-byt	ld are called e unpacked	
Zone	Numeric	Zone	Numeric	Sign	Numeric	
Usi	ng our positiv	ve value, 82	9, we could i	llustrate t	he data as:	
Z	8	Z	2	+	9	
L						
The 2 a	e four leftmos re called	st bits of th	e bytes conta bits.	aining the	digits 8 and	zone
The 2 a The the	e four leftmos re called e four leftmos	at bits of the	e bytes conta bits. e byte contai _ of the field.	aining the ining the 9	digits 8 and) indicate	zone sign
The 2 a The the	e four leftmos re called e four leftmos these leftmos	at bits of the at bits of the st bits have	e bytes conta bits. e byte contai _ of the field. a decimal val	aining the ining the 9 lue?	digits 8 and) indicate	zone sign
The 2 a The the Do	e four leftmos re called e four leftmos these leftmos U Yes	at bits of the	e bytes conta bits. e byte contai _ of the field. a decimal val	aining the s ining the 9 lue?	digits 8 and 9 indicate	zone sign
The 2 a The the Do	e four leftmos re called e four leftmos these leftmos U Yes U No	at bits of the	e bytes conta bits. e byte contai _ of the field. a decimal val	aining the s ining the 9 lue?	digits 8 and 9 indicate	zone sign No
The 2 a The the Do The are	e four leftmos re called e four leftmos these leftmos Q Yes Q No e bits that rep the:	at bits of the at bits of the st bits have	e bytes conta bits. e byte contai _ of the field. a decimal value	aining the ining the 9 lue?	digits 8 and 9 indicate backed byte	zone sign No
The 2 a The the Do	e four leftmos re called e four leftmos these leftmos Q Yes Q No e bits that rep the: Q zone b	at bits of the at bits of the st bits have resent the c	e bytes conta bits. e byte contai _ of the field. a decimal value	aining the s ining the 9 lue?	digits 8 and) indicate backed byte	zone sign No
The 2 a The the Do	e four leftmos re called	it bits of the at bits of the st bits have resent the o bits.	e bytes conta bits. e byte contai _ of the field. a decimal value	aining the a ining the 9 lue?	digits 8 and 9 indicate backed byte	zone sign No numeric bits

.

135.	Since zone bits have no decimal value, we can replace them with numeric information by packing two decimal digits into an eight-bit byte. All numeric data used in calculations must be converted to packed format before arithmetic operations are performed. A single instruction can convert a field from unpacked to packed format. The instruction preserves the sign of the unpacked field in the packing operation by reversing the sign and numeric portion of the least-significant byte in the field.	
	Before packing After packing 0 0 8 2 9 +	
	After packing, the sign of the field is located in the:	richtmost position of the field
	rightmost position of the field.	rightmost position of the field.
	leftmost position of the last byte.	
	An unpacked numeric field that is three bytes in length can	
	be packed into by tes.	two
	An unpacked numeric field that has a length of nine bytes can be packed into a field having a length of:	
	eight bytes	
	five bytes	five bytes
	□ four bytes	
136.	Data read from cards enters the computer memory in:	
	packed format.	
	unpacked format.	unpacked format
	How many bits are used to represent the sign of the field?	
	□ Eight	
	Four	Four

137.	All decimal arithmetic performed in the 9200/9300 System requires the numeric data to be in:	
	packed format.	packed format
	unpacked format.	
	The packed format provides a significant saving in storage because numeric data is stored in approximately half the area that would be required if the unpacked format were used.	
		· · · · ·
138.	Show how a five-column field in a record read from punched cards would appear in memory. Use Z for zone and use the positive value 40637.	
	Now show this value in packed format.	
		000040637+
120	MATCH the following	
139.	MATCH the following:	
	A. Packed format	
	B. Unpacked format	
	Data read from cards directly into memory.	В
	Used for arithmetic operations.	Α
	Applicable to decimal data only.	Α
	Sign represented in the rightmost four bits of field.	. A
	Applicable to alphanumeric data.	В

140.		
ZNZNZNSN		
The above illustration symbolizes data in:		
packed format.		
unpacked format.	unpacked format	
The symbol Z represents the portion of the byte.	zone	
The symbol N represents the portion of the byte.	numeric	
The symbol S represents the portion of the byte.	sign	
To store the above data in memory in packed format requires:		
□ three bytes	three bytes	
□ four bytes		
□ five bytes		
141. Packed data must be converted to the unpacked format before it can be punched into cards or printed as output. Again, a single instruction can perform this operation. Usually two storage areas are involved. The data stored in the smaller (packed) area is unpacked into a larger storage area. Unpack the contents of the field named AREA 1 into AREA		
2 below:		
AREA 2	z n z n z n z n s n	
Pack the contents of HOT into COLD below:		
HOT Z 4 Z 5 Z 7 + 6		
	0 0 0 4 5 7 6 +	
	\$ STOP	
----------------------	--	--------------------
CHECK	the following statements as true (T) or false (F):	
TF		
] All input/output devices are controlled by instruc- tions.	True
] Instructions are stored in random sequence.	False
	An instruction can be executed only once because it is destroyed.	False
	Instructions are usually executed in the order in which they are stored.	True
] Data is read into memory before the program is entered.	False
	Instructions can be executed over and over again.	True
	 All computer operations are directed by instructions. 	True
run?	Machine Inneurope	Machine Janguage
	Machine language	muomine nunguuge
	Symbolic language	indonno langaago
Input dat compute	Symbolic language symbolic language a is punched one record per card. What will enter the r when the first <u>read</u> instruction is executed? The entire file	
Input dat compute	Symbolic language symbolic language ta is punched one record per card. What will enter the r when the first <u>read</u> instruction is executed? The entire file Data from one card	Data from one card

	EXPRESS STOP (Continued)	
	Computer systems that use variable-length instructions provide more space for the storage of data.	
		True
	□ False	
		IF YOU ANSWERED ALL QUESTIONS CORRECTLY, SKIP TO FRAME 158 ON PAGE 1-113.
	· · · · · · · · · · · · · · · · · · ·	
143.	PREVIEW	
	The programmer writes a series of instructions that specify the operations to be performed by the computer. If data is involved, the instructions must direct the computer to the data. If a device is to be controlled, the instruction must specify the device and the operation to be performed.	
	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed.	
144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program?	
144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program?	In memory
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144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program? In memory In punched cards On magnetic tape	In memory
144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program?	In memory
144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program? In memory In punched cards On magnetic tape After the program is written and assembled, the entire program is usually read into memory before any data is brought in. In what form are instructions stored during the production run?	In memory
144.	In the following frames you will learn more about the infor- mation contained in instructions and how they are stored and executed. Where are instructions stored during execution of the program? In memory In punched cards On magnetic tape After the program is written and assembled, the entire program is usually read into memory before any data is brought in. In what form are instructions stored during the production run? Symbolic language	In memory

145.	In general, no particular storage areas are reserved for instruc- tions. They are usually grouped together and placed in ascending sequential locations in the order in which they are entered.	
	If a program is read into memory and stored in locations 1000 through 2000, the first instruction to be executed will be the one that starts at location	1000
	If the first instruction is six bytes long, the next instruction will start at location:	
	□ 1006	1006
	□ 1007	
	Are special areas of memory reserved for exclusive use by instructions?	
	Yes	
	🗆 No	Νο
146.	The space required in memory to store the many instructions in a program is an important factor in the efficiency of a computer system. If the space required for each instruction is large, less space is left in memory for data.	
	Some computer systems use fixed-length instructions while others, like 9200/9300, use variable-length instructions.	
	If the computer design called for each instruction to occupy the same fixed number of memory locations, this fixed-length would have to equal the:	
	Iongest instruction in the set.	longest instruction in the set
	shortest instruction in the set.	
	Which system will provide more space for the storage of data?	
	Fixed-length instructions	
	Variable-length instructions	Variable-length instructions
	The number of character locations (bytes) used to store a single instruction in the 9200/9300 is two, four, or six, depending on the type of instruction.	
	The maximum length of a 9200/9300 instruction is:	
	🗇 two bytes.	
	one word.	
	six bytes.	six bytes
	□ four bytes.	
	The minimum length for 9200/9300 instructions isbytes.	two

The type of information ion is described in the OPERATION Add Read Branch Move	OPERAND quantity stored in memory location 3000 to quantity stored in location 3500. one record into locations 5000 through 5079. to instruction in location 4800. 10 bytes starting at location 1200 to location 1600.	
OPERATION Add Read Branch Move	OPERAND quantity stored in memory location 3000 to quantity stored in location 3500. one record into locations 5000 through 5079. to instruction in location 4800. 10 bytes starting at location 1200 to location 1600.	
Add Read Branch Move	quantity stored in memory location 3000 to quantity stored in location 3500. one record into locations 5000 through 5079. to instruction in location 4800. 10 bytes starting at location 1200 to location 1600.	
Read Branch Move	one record into locations 5000 through 5079. to instruction in location 4800. 10 bytes starting at location 1200 to location 1600.	
Branch Move	to instruction in location 4800. 10 bytes starting at location 1200 to location 1600.	
Move	10 bytes starting at location 1200 to location 1600.	
Which part of the inst	•	
which part of the filst	ruction indicates the action to be taken?	
Operation p	art	Operation part
Operand par	t	
Vhat is the length of t System?	he smallest instruction in the 9200/9300	
Two bytes		Two bytes
Four bytes		
One word		
Six bytes		
	 Operation p Operand par /hat is the length of t ystem? Two bytes Four bytes One word Six bytes 	 Operation part Operand part /hat is the length of the smallest instruction in the 9200/9300 ystem? Two bytes Four bytes One word Six bytes

48.		
	length (six bytes)	
	Operation Operand First Operand Second Operand Operand Address Address	
	OPERAND	
	The above format is a typical six-byte 9200/9300 instruction as it appears in memory.	
	How many bytes of memory are required:	
	to store the address of the first operand?	Two bytes
	to store the address of the second operand?	Two bytes
	Which byte of the instruction:	
	determines the kind of operation to be performed?	The first (leftmost) byte
	determines how much data will be processed?	The second byte
	What is the total length of the operand?	Five bytes
<u> </u>		
49.	The symbolic code used by the programmer is translated during the Assembly run. During the production run, the control unit will recognize one machine code pattern of 0 and 1 bits to mean ADD and another pattern will indicate MOVE.	
	What part of the instruction provides this information?	Operation part

150. The programmer us operation he wishes the <u>first</u> letter in th interpreted as Add. What will be the in	ses a mnemonic code to represent the s the computer to perform. When "A" is e mnemonic code of an instruction, it is The letter "B" is interpreted as Branch. terpretation of the letter "C"?	
Subtract		
🗆 Compare	Compare	
□ Move		
The mnemonic cod	le is used in the:	
operand	part of the instruction.	
🗆 operation	n part of the instruction.	operation part of the instruction
· · · · · · · · · · · · · · · · · · ·		
	:	
151. MATCH the follow operation they performed	ing mnemonic operation codes to the form:	
AP	Move character	MVC
SP	Edit	ED
MVC	Add Packed Decimal	AP
MVI	Divide Packed Decimal	DP
CLC	Branch on Condition	вс
BAL	Subtract Packed Decimal	SP
ZAP	Compare Packed Decimal	СР
ED	Branch and Load	BAL
BC	Move Immediate	MVI
DP	Compare Logical	CLC
СР	Zero and Add Packed Decimal	ZAP

1	LABEL	8	OPERATION 1	6	OPERAND 16
Γ,		Γ			
		Γ	AP		Ω TY2, Ω TY1,
Ī,		Γ	1 1 1 1		
Ľ	.	Γ			
	and and an internet states of	1-			

This is a typical Add Packed Decimal instruction used in programming the 9200/9300. It tells the computer to add the packed decimal data stored in the location designated by the symbol QTY1 to the packed decimal data stored in the location designated by QTY2. (The result is stored in the QTY2 location.)

What information is contained in this instruction? (Check the correct statement(s).)

- □ The number of times the instruction will be executed.
- □ The location of the data to be operated upon.
- □ The operation to be performed.
- □ The format of the data.

What does the operand part of the instruction specify?

The location of the data to be operated upon.

The operation to be performed.

The format of the data.

The addresses of the data to be operated on.

153.	The use of symbols to represent the individual data items relieves the programmer of the task of keeping track of the actual memory locations of the data. As illustrated in the instructions below, the programmer may move data from one location to another without becoming concerned about the address of the memory location he is moving to. The computer relates the symbol to the address and maintains all the records it will need.	
	In the first instruction shown above, the data at location TOTL is moved to location	BAL
	Copy the operation code and operands of the last instruction.	
	OPERATION CODE:	MVC
	OPERAND:	TOTL,WDRW
154.	Suppose two fields are to be added. FLD1 is three bytes long and FLD2 is two bytes long. Both fields are in packed decimal format.	
	Of the two instructions shown below, which one contains all of the information required?	
	AP FLD1, FLD2	AP FLD1, FLD2
	□ AP 3,2	
	The operand part of this instruction must contain such infor- mation as the memory locations of the data to be added. How is this information included in the instruction?	
	Symbols represent the addresses.	Symbols represent the addresses.
	By including the actual memory addresses.	

155.	In addition to instructions that specify internal processing operations, the program usually includes instructions that bring (read) in data. They indicate which input device to bring it in from, and where to store it.	
	Such read instructions should be brought into memory:	
	after data is brought in.	
	before data is brought in.	before data is brought in
	Will all the data of a file be brought into memory at one time?	
	□ Yes	
	□ No	No
156.	In most data processing applications, the computer operates on the data fields of one record at a time.	
	If the input data is punched one record per card, what will enter the computer when the first READ instruction is encountered?	
	□ The entire file.	
	Data from one card.	Data from one card
	How much information is usually in memory at one time?	
	Part of the program and all of the data.	-
	The entire program and some data.	The entire program and some data
	Part of the data and part of the program.	
L		

157.	REVIE	EW		· · · · · ·
	Check	the f	ollowing statements as true (T) or false (F):	
	т	F		
			Instructions can be executed over and over again.	True
			A special area of memory is reserved for use by instructions.	False
			Instructions are stored in the same sequence as they are written.	True
			Data is brought into memory before the instructions.	False
			The maximum length of a 9200/9300 Assembly language instruction is six bytes.	True
			An instruction can be executed only once.	False
			Instructions are normally executed in consecutive order.	True
			Instructions contain the symbolic addresses of data in the operand.	True
			The card reader is controlled by instructions.	True
			An instruction contains information that indicates the number of times it will be executed.	False
			The entire program is usually resident in memory before data is brought in.	True

158.	EXPRI	ESS S	зтор	
	Check	the f	ollowing statements as true (T) or false (F):	
	т	F		
			The Assembly system converts symbolic language to machine code.	True
			The START and END statements generate machine code in the object program.	False
			Data storage areas are defined by Assembler- directing instructions.	True
			The Object program contains the machine code representation of Assembler-directing instructions.	False
			Data is edited for printing by an Assembler- directing instruction.	False
÷.			The Assembly system is controlled by Assembler- directing instructions.	True
			Data storage areas are reserved by the START and END statements.	False
			Data is moved from one area to another by an Assembler-directing instruction.	False
		D	The START and END statements perform no function during a production run.	True
			The Assembler assigns a storage address to each symbol written in the label field of the coding form.	True
			The START instruction will indicate when data is to be brought into memory.	False
			Define Storage (DS) instructions are used by the Assembly program.	True

158. EXPRESS STOP (Continued)

In the coding form below, complete the Define Storage (DS) statements to allocate memory for a file in which each record has the following format:

Employee Number	5 characters
Name	20 characters
Address	20 characters
Job Title	30 characters
Monthly Salary	5 characters

	ℽ⊰⊱⊔ℕℾ	
LABEL	B OPERATION B	OPERAND 16
EMPN		
NAME		
ADDR		
JOBT		
MOSA		

Assume that the tag WRK1 in the coding form below represents a work area. Complete the Define Storage statement to allocate working storage for each record in the above file.

1.000		00504440	
I LABEL	B UPERATION B	0PERAND 16	
WRK 1			
		<u> </u>	

	∕≁⊔Ւ	
LABEL 1	S OPERATION	NB OPERAND T6
سيست	1	
WRK1	DS	CI.80

SPERRY - UNIVAC

B OPERATION B

CL20

PROGRAM LABEL

EMPN L

1.1

NAME | ADDR

ASSE

OPERAND

CL30 CL5

سليت بايت ي

IF YOU ANSWERED ALL QUESTIONS CORRECTLY SKIP TO FRAME 171 ON PAGE 1-125.

159.	PREVIEW	
	The programmer must be familiar with the various functional categories of instructions before he can efficiently use reference manuals to select instructions for certain jobs.	
	In the next group of frames we will examine the processing operations performed by a few of the instructions frequently found under the categories of:	
	Assembler-directing instructions	
	Logical instructions	
	Branching instructions	
160.	Assembler-directing instructions provide auxiliary functions that assist the programmer to control the assignment of storage addresses, to define data and storage fields, and to control the Assembly System itself. These functions are performed during the Assembly run and, with few exceptions, do not result in the generation of any machine language code. The START and END statements are typical examples.	
	Does the object program contain a machine-code representa- tion of all the instructions written by the programmer?	
	□ Yes	
	□ No	No
	Check the functions below that are performed by Assembler- directing instructions:	
	Define data storage areas.	Define data storage areas
	Move data from one memory location to another.	
	Edit data for printing.	
	Control the assignment of storage addresses.	Control the assignment of storage addresses
	Control the Assembly system.	Control the Assembly system

161. The START and END statements are examples of Assemblerdirecting instructions. They define the beginning and end of the program, and are normally the first and last statements written in the program.

	ΠN	
LABEL	t OPERATION to	OPERAND 16
EXMP	START	
	AP	XXX,XXX
	MV/G	XXXXXX
<u> </u>	END	
		<u></u>

The START and END statements are necessary because they:

- □ reserve data storage areas.
- inform the Assembler of the beginning and end of the program being assembled.
- improve documentation.

Will the START and END statements appear in the Object program?

□ Yes

□ No

inform the Assembler of the beginning and end of the program being assembled

No

162. The Assembler system converts the symbolic language statements into machine code, determines memory requirements for data and instructions, performs error checking, and has other responsibilities during several examinations (passes) of the source statements. Several passes are generally required to produce an Object program. When the computer is examining one source statement after another in order to produce an Object program, it knows when the last statement is encountered because this is a function of the: □ START statement END statement **END Statement** What function will the START and END statements perform in the Object program during a production run? □ They indicate when data is to be brought in or written out. □ They indicate the amount of memory required to store the data. □ They perform no function and are therefore not They perform no function and present in the Object program. are therefore not present in the Object program

163. Following the START statement, the next logical step in coding a program would be to designate the memory area for record processing. An Assembler-controlling code is provided for this purpose. The 9200/9300 uses a Define Storage code identified by the letters DS in the Operation field of the coding sheet. A digit in the Operand field following CL (Character Length) specifies the number of bytes to be allocated.

If we are allocating memory to a data item, we will most likely want to reference this item during our program. The programmer can assign a tag to the item by writing a symbolic name in the Name field of the coding form as shown in the example below:

LABEL 1	B OPERATIO	N 16	OPERAND
	START		
QTY1	DS	CL9	
QTY2	DS	CL4	
STOT 1	DS	CL14	4
WRK 1	DS	ՀՀհ	
EMPN	, ps	CL5	

How many bytes of memory are reserved for:

EMPN	
STOT	

QTY2

What Assembler-directing mnemonic tells the system to reserve the required bytes? _____

The symbolic names listed above are called ______.

5 bytes

14 bytes

4 bytes

DS

é

tags

164. Write the necessary Define Storage (DS) statements to direct the Assembler to allocate memory for a file in which each record has the following format.

Employee Number	5 characters
-----------------	--------------

Name 20 characters

Address 20 characters

Job Title 30 characters

Monthly Salary 5 characters

We have used a symbolic tag that will describe the data to be stored in the field. Each tag must be unique and must not exceed four characters.

	~ -{ }-UNI	
LABEL 1	B OPERATION B	OPERAND
EMPN III		
NAME		
ADDR		
JOBT		
MOSA		

The Assembler will assign a memory address to each tag in the Label field. What Advantages are gained by using tags instead of actual memory addresses?

- □ A name can be chosen that will describe the data to be stored in the field.
- □ Words are more easily recalled than numbers.
- □ The internal language of the computer is alphabetic.

SPERRY PROGRAM	′≁⊱⊔∧	IVAC ABBEM
LABEL 1	B OPERATION	B OPERAND 16
EMPN	DS	CL5
NAME	DS.	CL20
ADDR	DS (_	CL20
JOBT	DA.	GL3Ø
MOSA	DS.	CL5

A name can be chosen that will describe the data to be stored in the field.

Words are more easily recalled than numbers.

	and the second
165. In the previous frame you allocated memory for the record of a single employee. What were the total number of bytes required?	80
Would it be necessary to write the DS statements for each employee in the file if there were 3000 employees?	
□ Yes	
□ No	No
The logic of the program will usually cause the data from a previously processed record to be overlayed by the data from a subsequent record since the same area is used for each record.	
Suppose that the logic of the program specifies that after each record is read in from a card it shall be moved to a work area (WRK1) before processing is performed. Write the Define Standard statement to allocate memory for this WRK1 field	
Storage statement to anocate memory for this writer here.	
PROGRAM	
SPERRY UNIVAC ASSEMBL PROGRAM	
SPERRY LINIVAC ASSEMBL PROGRAM	
SPERRY LINIVAC ASSEMBL PROGRAM	STERRY LINIVAC ABBEM PROGRAM 10 16 0PERATION 16 0PERAMD LABEL 10 16 0PERAMD 1 MRK1 L DS CL88 1
SPERRY LINIVAC ASSEMBL PROGRAM	
SPERRY LINIVAC Assembly PROGRAM	STPERRY LINIVAC ABBEM PROGRAM 10 14 0PERAND ILABEL 10 16 0PERAND WRK1 DS CLIQ8 14 WRK1 DS CLIQ8 14
SPERRY LINIVAC ASSEMBL PROGRAM	STERRY LINIVAC ABBEM PROGRAM 0
SPERRY LUNIVAC ASSEMBL PROGRAM	
	SPERRY UNIVAC ABBEM PROGRAM LABEL * OPERATION * OPERAND WRK1 DS. CL88

Livir	N N	🗲 JOBT				
∢ N	AME 🖛 ADDR		MOSA			
 	80 Bytes					
W	/RK1					
└─ ────			ł			
The memo	ory areas are identifie	ed by their tag	gs. Each tag r	efer- vidual		
field.	address of the most s	significant by				
How many	y individual fields are	e there in the	input area?			
	One					
	Five				Five	
П	Six					
Each of th	ne fields may be indiv	vidually addre	essed by name	e. The		
Each of th following into WRK	ne fields may be indiv instruction will move 1	vidually addre e the content	essed by name s of location	e. The EMPN		
Each of th following into WRK	ne fields may be indiv instruction will move 1	vidually addre	essed by name s of location	e. The EMPN		
Each of th following into WRK	ne fields may be indivin instruction will move 1 MVC WRK	vidually addre e the content 1, EMPN	essed by name s of location	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivin instruction will move 1 MVC WRK happen if this instruc	vidually addre e the content 1, EMPN ction is follov	essed by name s of location ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivin instruction will move 1 MVC WRK happen if this instruct MVC WRK	vidually addre e the content 1, EMPN ction is follov 1. NAME	essed by name s of location ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivi instruction will move 1 MVC WRK happen if this instruc MVC WRK	vidually addre e the content 1, EMPN ction is follov 1, NAME 1, ADDR	essed by name s of location ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivi instruction will move 1 MVC WRK happen if this instruc MVC WRK MVC WRK	vidually addre e the content 1, EMPN ction is follov 1, NAME 1, ADDR 1, JOBT	essed by name s of location ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivi instruction will move 1 MVC WRK happen if this instruc MVC WRK MVC WRK	vidually addre e the content 1, EMPN ction is follov 1, NAME 1, ADDR 1, JOBT 1, MOSA	essed by name s of location ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivi instruction will move 1 MVC WRK happen if this instruct MVC WRK MVC WRK MVC WRK	vidually addre e the content 1, EMPN ction is follow 1, NAME 1, ADDR 1, JOBT 1, MOSA	ved by:	e. The EMPN		
Each of th following into WRK What will	ne fields may be indivi instruction will move (1 MVC WRK happen if this instruct MVC WRK MVC WRK MVC WRK MVC WRK The total record wil WRK1 field.	vidually addre e the content 1, EMPN ction is follow 1, NAME 1, ADDR 1, JOBT 1, MOSA I be correctly	essed by name s of location ved by: positioned ir	e. The EMPN		
Each of th following into WRK What will	he fields may be indivi instruction will move 1 MVC WRK happen if this instruc MVC WRK MVC WRK MVC WRK MVC WRK The total record will WRK1 field.	vidually addre e the content 1, EMPN ction is follow 1, NAME 1, ADDR 1, ADDR 1, MOSA I be correctly	essed by name s of location ved by: positioned ir	e. The EMPN	The first par	t of the WBK
Each of th following into WRK What will	he fields may be indivi instruction will move 1 MVC WRK happen if this instruct MVC WRK MVC WRK MVC WRK MVC WRK The total record will WRK1 field. The first part of the when each subseque	vidually addre e the content 1, EMPN ction is follov 1, NAME 1, ADDR 1, ADDR 1, JOBT 1, MOSA I be correctly WRK1 field ent instruction	essed by name s of location ved by: positioned ir d will be over h is executed.	e. The EMPN n the layed	The first par field will	t of the WRK1 be overlayed w

five individual of data could b How many Mo relocate one er	ourse, define WRH e DS statements of d WRK2, WRK3, y addressable fiel be moved. ve Character instr nployee record?	K1 as a five-byte directly after it P and so on. Th ds into which the ructions would be	field and Perhaps they is will create a five items a required to	Five
168. Assuming that records, we wo executing five a work area. TI vided to elimin us to address tl can thus be wr	we are to process uld be wasting a instructions to mo ne Assembler-con ate this problem. ne entire unit as a itten as follows:	thousands of em considerable amo ove the data from trolling code, <u>OR</u> The ORG statem record. Our DS s	ployee unt of time an input to <u>G</u> , is pro- ient permits statements	
EMPN	DS	CL5		· · ·
NAME	DS	CL20		
ADDR	DS	CL20		
JOBT	DS	CL30		• • • • •
MOSA	DS	CL5		
	ORG	EMPN		
REC	DS	CL80		
WRK1	DS	CL5		
WRK2	DS	CL20		
WRK3	DS	CL20		
WRK4	DS	CL30		
WRK5	DS	CL5		
	ORG	WRK1		· · ·
WORK	DS	CL80		
Now the progra subsection that	mmer can addres has been assigned e names assigned	is the entire record d a name. The illu to the memory lo	d or any stration ocations.	
REC	- ADDR	JOBT	MOSA	
REC EMPN NAME	ADDR	JOBT	- MOSA	
Figure Shows the	ADDR	JOBT 30 bytes	← MOSA 5 bytes	
EIGW SNOWS TH REC EMPN NAME 5 20 bytes bytes WORK WORK WRK1 - WRK2	ADDR 20 bytes	JOBT 30 bytes	MOSA 5 bytes	
Below shows th REC EMPN NAME 5 20 bytes bytes WORK WRK1 - WRK2	ADDR 20 bytes	JOBT 30 bytes	 MOSA 5 bytes WRK 5 	
Derow shows th ← REC ← EMPN ← NAME 5 20 bytes bytes ← WORK ← WRK1 ← WRK1 ← WRK2 5 20 bytes bytes	ADDR 20 bytes WRK3 20 bytes	 →JOBT 30 bytes →WRK 4 30 bytes 	 MOSA 5 bytes WRK 5 5 bytes 	
REC EMPN NAME 5 20 bytes bytes WORK WRK1 - WRK2 5 20 bytes WORK WRK1 - WRK2 5 20 bytes	ADDR 20 bytes WRK3 20 bytes es of data will be executed?	JOBT 30 bytes WRK 4 30 bytes moved when the	 MOSA 5 bytes WRK 5 5 bytes 	
erow snows th ← REC ← EMPN ← NAME 5 20 bytes bytes ← WORK ← WRK1 ← WRK2 5 20 bytes bytes How many bytes How many bytes MVC W	ADDR 20 bytes WRK3 20 bytes es of data will be executed? RK1, EMPN	-JOBT 30 bytes -WRK 4 30 bytes moved when the	 MOSA 5 bytes WRK 5 5 bytes 	5

169.	FOOTNOT	E		
	The programmer will often use instructions that will guide the Assembly program. He may, for example, wish to have the program assembled in some specific area of memory, or he may wish to enter certain constants as part of the program. A large number of such tasks may be left to the Assembly program. Assembler-directing instructions are written on the same coding sheets as the symbolic instructions. NO RESPONSE REQUIRED			
	<u></u>			
1 70 .	REVIEW			
	This frame learned abo Check the f	will give you a chance to review what you have out instructions that direct the Assembly program. ollowing statements as true (T) or false (F):		
	ΤF			
		The START and END statements are used by the Assembly program.	True	
	· 🗆 🖸	The START and END statements will generate machine code in the Object program.	False	
		Data storage areas are defined by Assembler- directing instructions.	True	
		A symbolic name (tag) can be assigned to a data storage area.	True	
		Define Storage (DS) instructions are used by the Assembly program.	True	
		The START instruction will indicate when data is to be brought into memory.	False	
		The Assembler will assign a memory address to each tag written in the label field of the coding form.	True	

· · · · · · · · · · · · · · · · · · ·		•
	 	-
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	 · · ·	
	 ·	
	 	-
		_
	 	<u>-</u>
·	 	
~		

171.	EXPRESS STOP	
	Which of the following are Assembler-directing instructions?	
	DS	DS
		START
\overline{P}	□ BC	
	🗆 ED	
	Match the following:	. · · · ·
ets.	1. START Tests the condition code indica- tor for a specific condition.	2
	2. BC Compares the binary value of two fields	3
	3. CLC Marks the beginning of a	1
	4. DS program.	
	Allocates memory.	4
	When editing data, it is often necessary to suppress zeros. This means that:	
	all zeros are suppressed.	
	□ leading zeros are suppressed.	leading zeros are suppressed
	Which kind of data is edited?	
	Alphanumeric	
		Numeric
	Alphabetic	

171.	EXPRESS STOP (Continued)	
	If the programmer wishes to edit a data field by inserting the \$ and decimal point symbols, these symbols must be included in the applicable position of the:	
	🗆 data.	
	instruction.	
	🗆 mask.	mask
	What two functions are performed by the Compare Logical (CLC) instruction?	
	Performs a binary compare of two fields and sets an internal indicator.	Performs a binary compare of two fields and sets an internal indicator
	Sets an indicator and algebraically compares two fields of data.	
	Compares two fields and performs a branch.	
172.	PREVIEW	
	Most computer systems contain a group of instructions called logical instructions. Although it is beyond the scope of this chapter to teach the details of logical instructions, we will discuss them briefly.	
	The logical instruction set of the 9200/9300 Assembly language includes data movement, comparing, editing, bit testing, translating, and shifting instructions. We will discuss several applications of a few of these instructions in the next group of frames. We will also introduce several instructions that are included in the 9200/9300 branching instruction set.	
	:	
173.	An instruction that prepares data for printer output by insert- ing symbols or eliminating leading zeros is called:	
	□ an edit instruction.	an edit instruction
	a translate instruction.	
	an addition instruction.	

ł

,

174. Editing of data is an important function of any program that generates a printed output. A payroll program that produces checks would include editing instructions.	
Assume that you are programming such a problem and that you have determined that no check will exceed \$999.99. Afte performing the necessary calculations, the program should print a check for each employee. Using the sample data shown below, write the edited value, including the decimal point and the dollar sign as it should appear on the check for each of the three employees.	ar h
Note:	
The decimal point is not stored in memory. The symbol \wedge is used to represent the assumed decimal point position of the data in memory.	
Data in Memory Check Amount	
0 7 3 4 3 + Employee 1	\$73.43
7 6 7 1 4 + Employee 2	\$767.14
0 0 7 0 1 + Employee 3	\$7.01
	Dollar sign (\$) and decimal
What characters were erased (suppressed) by editing?	Leading zeros
Is a blank space permitted between the symbol \$ and the number 7?	Νο
•	

175.	The edit (I printing, Iu format, Th character I appear afte any comm been inclu	ED) instruction is used to prepare numeric data for in the 9200/9300 System this data must be in packed be programmer simply designs a mask that is a by character representation of the data as it is to er printing. When the ED instruction is executed, a, asterisk, decimal point, or other symbol that has ded in the mask is inserted in its proper position.	
	To be edit	ed, data must be:	
	alphabetic.		
	□ numeric.		numeric
		alphanumeric.	
	The progra the data a the	ammer must represent each character position of nd insert the edit symbols in the relative position in	mask
	Will it be r mask for e	necessary for the programmer to design and insert a ach data item he wishes to edit?	
		Yes	Yes
	🗆 No		
176.	Check eac An edit in	n of the following statements as true (T) or false (F). struction:	
	ΤF		
		is used to change instructions.	False
		is considered to be a logical instruction.	True
		is applicable to numeric data only.	True
		will translate internal code to binary.	False
		suppresses leading zeros.	True
		is used when output is on punched cards.	False
		is used to insert symbols in data to be printed.	True
		uses a programmer-designed mask.	True

177. The ability of a computer to change the order in which its instructions are executed has made the computer an extremely flexible data processing tool.

Programs can be written to provide for changes in the order of execution depending on the results of comparisons made between two fields of data. The instruction set of a given computer system will usually offer several different methods of performing this function.

Changing the sequence is usually dependent upon the combined action of two instructions. The first (Compare) will examine the two data fields and set an internal indicator that reflects the outcome of this comparison. The second instruction (Branch on Condition) will test this indicator and will branch or not branch to another point in the program depending on the condition of the indicator.

All Compare instructions in the 9200/9300 will perform two functions. They will compare two fields of data and:

add the two data items.

□ set an internal condition code indicator.

□ change the sequence of instructions.

set an internal condition code indicator.

The Compare Logical (CLC) instruction tests the binary value of two operands. Assume that the two numeric operands are in memory as shown in the following character (unpacked) format and that they have been read in from cards.	
 ➡ ITEM Z 5 Z 6 Z 7 Z 2 	
- AREA Z 5 Z 7 Z 7 Z 2	
The Compare Logical instruction would be written as shown below:	
LABEL & OPERATION & OPERAND 1 LABEL & OPERATION & OPERAND 10 16 1	
What are the two functions performed by this instruction?	
1	Compares the binary values of the fields.
2	Sets the condition code indicator
Which field has the greatest binary value?	AREA
· · · · · · · · · · · · · · · · · · ·	

179. The CLC instruction is useful when comparing two account numbers. If we are posting transactions (T) to a master file (M), we must be certain that each transaction is posted to the correct master record. The following illustration shows the three possible conditions. M>T ACCT. NO. М<Т M:T ROUTINE A **ROUTINE B** M=T **ROUTINE C** Which routine in the program should be followed if the condition code indicates the values to be equal. □ ROUTINE A. □ ROUTINE B. □ ROUTINE C. ROUTINE C If the master record account number is less than the transaction account number, which routine will be followed? _____ ROUTINE B Will the CLC instruction cause the program to branch to the correct routine? □ Yes 🗆 No No

180. Since three different conditions are possible, we need instructions that will test the condition code indicator and branch to the correct routine. The Branch-on-Condition instructions can test this indicator and the branch will be based on the particular setting being tested. In the following code we have assumed that the length of each account number is four bytes.

	· · · · · · · · · · · · · · · · · · ·	
B OPERATION	B OPERAND	
CLC	MAS, TRAN	
B C,2, ,	RTNA	_i_i
BC ₄		
MV,C	TO, FROM	_/ ,
		Ц
	b OPERATION 10 C_LC_ B_C_2 B_C_4 M_V_C I	b OPERATION b OPERAND 10 16 OPERAND CLC, MAS, TRAN,

The Branch on Condition 2(>) Instruction to Routine B will occur if the condition code is set to indicate:

MAS < TRAN

equal

MAS < TRAN

MAS > TRAN

MAS = TRAN

Neither the Branch on Condition 2 (>) Instruction nor the Branch on Condition 4 (<) Instruction will be executed if there is an _____ Condition.

ż

181. REVIEW		
⁴ Which of the following a	e Assembler-directing instructions?	т. Т
🗆 DS		DS
		START
□ BC		
MATCH the instruction or description on the right.	n the left with the functional	
Instruction	Function	
1. START	Tests the condition code indicator for a specific condition.	* 2
2. BC	Compares the binary value of two	3
3. CLC	fields.	
4. DS	_ Marks the beginning of a program.	1
	_ Allocates memory.	4
When editing data, it is of means that:	en necessary to suppress zeros. This	
all zeros are sup	pressed.	
leading zeros are	e suppressed.	leading zeros are suppressed
What kind of data can be e	edited?	
Alphanumeric		
🗖 Numeric		Numeric
Alphabetic		

Γ	182.	REVIEW	
;		If the programmer wishes to edit a data field by inserting the \$ and decimal point symbols, they must be included in the applicable position of the:	
		🗆 data.	
		□ instruction.	
		🗆 mask.	mask
		What two functions are performed by the Compare Logical (CLC) instruction?	
		Performs a binary compare of two fields and sets an internal indicator.	Performs a binary compare of two fields and sets an internal indicator
		Compares two fields and performs a branch.	
L	·· , ,		L







PANEL 4

CHARACTER CODE

EBCDIC	Printer Graphics
1100 0001	A
1100 0010	В
1100 0011	С
1100 0100	D
1100 0101	E
1100 0110	F
1100 0111	G
1100 1000	Н
1100 1001	I
1101 0001	L
1101 0010	K
1101 0011	L L
1101 0100	M
1101 0101	N
1101 0110	0
1101 0111	P
1101 1000	Q
1101 1001	R
1110 0010	S
1110 0011	T
1110 0100	U
1110 0101	V
1110 0110	W
1110 0111	X
1110 1000	Y
1110 1001	Z
1111 0000	0
1111 0001	1
1111 0010	2
1111 0011	3
1111 0100	4
1111 0101	5
1111 0110	6
1111 0111	7
1111 1000	8
1111 1001	9

EBCDIC	Printer Graphics
EBCDIC 1111 1111 0100 0000 0100 1010 0100 1011 0100 1100 0100 1101 0100 1110 0100 1111 0101 0000 0101 1011 0101 1100 0101 1111 0101 1110 0101 1111 0101 1110 0101 1111 0100 0001 0110 1010	Printer Graphics
0110 1010 0110 1011 0110 1100 0110 1101 0110 1110 0110 1111 0111 1010 0111 1011 0111 1100 0111 1101 0111 1101 0111 1110	 ∧ (logical AND) , (comma) % (percent) _ (underline) > (Greater than) ? (Question mark) : (Colon) # (Number) @ (at rate of) * (Apostrophe) = (Equal) * (Quotes)

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UNIVAC 9000 CARD ASSEMBLER PROGRAMMED INSTRUCTION COURSE BOOK 1 – INTRODUCTION

SELF-TEST

- 1. Match each of the following basic data processing activities to its corresponding function by writing the appropriate letter in each blank:
 - A. Classify 1._____Arrange data in a desired numeric or alphabetic sequence.
 - B. Sort 2.____Arrange data in numeric or alphabetic categories.
 - C. Calculate 3.____Represent data in or on a physical medium.
 - D. Summarize 4.____Add, subtract, divide, or multiply.
 - E. Record 5. Arrange the results of processing in condensed form.

2. Match each of the following functions with a corresponding device:

- A. Input 1.____Card reader
- B. Output 2.___Card punch
- C. Input/Output 3.____Magnetic tape unit
- D. Processing 4.____Magnetic disc unit
- E. Off-line 5.____Paper tape unit

6.____Central Processor

- 7.____Printer
- 8.____Console typewriter
- 9.____Keypunch

3. Match the following:



4. Match each of the letters in the punched card illustration below with the corresponding designation:



1	File		
2	Field		
3	Record		

5. Match the following:

A. Instruction 1. _____Defines storage address of data.

B. Operation code 2. ____A mnemonic.

C. Operand 3.____Specifies operation to be performed and defines address of data.

- 6. Match the following:
 - A. Symbolic language 1.____Assembly coding
 - B. Machine language 2.____Object program
 3.____Source program

7. Match each of the following symbols to its corresponding function:



8. Match the following:

Α.	s > T	1S is less than T.
В.	S < T	2S is greater than T.
C.	s <u><</u> т	3S and T are unequal.
D.	s <u>></u> т	4S is compared to T.
E.	S : T	5S is greater than or equal to T
F.	ѕ≠т	6S is less than or equal to T.

9. Match each operational step listed below with the appropriate flowchart symbol:



10. Assume that COUNT has been set to zero in the program represented by the flowchart section below.

ADD 10 TO TOTAL ADD 1 TO COUNT # COUNT:9 = PRINT TOTAL STOP The ADD 10 TO TOTAL operation will then be performed:

- A. 1 time
- B. 4 times
- C. 9 times
- D. 10 times

11. Match the letter in each of the symbols below to a corresponding designation:





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12. As indicated in this flowchart segment, what value will be printed if COUNT is set to zero and BALANCE contains the value 240?



13. Number the following 6 functions to indicate the order in which they are performed:

_____ Assembly run

_____Process flowchart

_____Test run

_____ Debug Assembly run

_____Production run

_____Write Source program

- 14. An EOF card indicates the end of:
 - A. Field
 - B. Record
 - C. File
 - D. Program
- 15. A program is a series of:
 - A. Questions
 - B. Instructions
 - C. Data
 - D. Operands

16. The basic function of high-speed memory is to store:

- A. Data and instructions
- B. Bytes
- C. Bits
- D. Registers
- 17. An object program is in:
 - A. Decimal code
 - B. Symbolic code
 - C. Mnemonic code
 - D. Machine code

18.

LABEL 1	B OPERATIO	0N 15 16	OPERAND
FOUT	TIVC	TO,T	ROM

The above statement represents:

- A. A program
- B. An instruction
- C. An operation code
- D. An operand
- 19. A source program is written in:
 - A. Decimal code
 - B. Binary code
 - C. Symbolic code
 - D. Machine code

20. A byte contains:

A. 8 bits

B. 4 bits

- C. 2 bits D. 1 bit

21. A process flowchart is prepared by a:

- A. Systems analyst
- B. Programmer
- C. Operator
- D. Designer

22. A systems flowchart is generally prepared by a:

- A. Manager
- B. Operator
- C. Programmer
- D. Systems Analyst
- 23. The symbols in a systems flowchart represent:
 - A. Operands
 - **B.** Instructions
 - C. Media
 - D. Coding

24. The symbols in a process flowchart represent:

- A. Operational steps
- Media В.
- C. Devices
- D. Storage

25. An Assembler program is produced by the:

- A. Computer manufacturer
- B. Systems Analyst
- C. Programmer
- D. Computer user

26. A storage area can be cleared for use in arithmetic calculation by filling it with:

- A. Numbers
- B. Spaces
- C. Zeroes
- D. Letters

27. A storage area can be cleared for use in a printing operation by filling it with:

- A. Numbers
- B. Spaces
- C. Zeroes
- D. Letters

28. The end of a punched card file is indicated by an:

- A. EOV label record
- B. EOV card
- C. EOF label record
- D. EOF card

29. Data for arithmetic calculations must be in which format?

- A. Unpacked
- B. Alphanumeric
- C. Packed decimal
- D. Hexadecimal

30. Which of the following can be used in a flowchart to represent a repetitive process?

- A. Device symbol
- B. Media symbol
- C. Processing symbol
- D. Connector symbol

31. Which of the following is an Assembler-directing instruction?

- A. Edit
- B. Start
- C. Move
- D. Branch

UNIVAC 9000 CARD ASSEMBLER

PROGRAMMED INSTRUCTION COURSE

BOOK 1 - INTRODUCTION

SELF-TEST ANSWERS

1	1:B (1 point each)	7	1:B (1 point each)	14	C (1 point)
	2:A		2:0	16	D (1 noint)
	3:E		3:E	10	B (I point)
	5:D ▼ are the second		4:A 5:C	16	A (1 point)
2	1:A (1 point each)	8	1:B (1 point each)	17	D (1 point)
2: 3: 4:	2:B		2:A		
	3:0	•	3:F	18	B (1 point)
	4:0		4:E		· · ·
	5:0		5:D	19	C (1 point)
	6:D		6:C V	~~	
	7:B			20	A (1 point)
	8:0	9	1:1 (1-1/3 points each)		
	9:E V		2:0	21	B (1 point)
^	1.D (1 point pook)		3:H		
3			4:B	22	D (1 point)
	2.6		5:A		0 (0))
	3.L 4.C			23	C (1 point)
	4.0 E.D		7:G	0.4	A (A
	5:B 6:A			24	A (1 point)
	7.0		9.0	05	A /A
	0.E	10	C = (E - points)	25	A (1 point)
	0.L 0.D	10	C (5 points)	26	C (1 point)
	10·B ¥	11	1.C (1 point each)	20	C (i point)
	10.0	1.1		77	P (1 point)
л	1.C (1 point each)		3.D	21	D (1 point)
-	2. A			28	D (1 point)
	2.∩ 3.B ¥		4.0 1	20	
	5.6 4	12	C (5 points)	29	C (1 point)
5	1:C (1 point each)		0111 XO8 .0 4		
	2:B	13	1:3 (2 points each)	30	D (1 point)
	3:A 🕴		2:4 MJT30#189		
		1	3:5	31	B (1 point)
6	1:A (1 point each)		4:4		
	2:B		5:6	a franciska se se	· · · · · · · · · · · · · · · · · · ·
	3:A 🛛		6:2 ♥		