

LOGIC DIAGRAMS

for

Type 1941/ 1	Core Store
Type 1941/ 2-3	Core Stores
Type 1942/ 1	Core Store
Type 1942/ 2-3	Core Stores

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Type 1941/ 1	5006496
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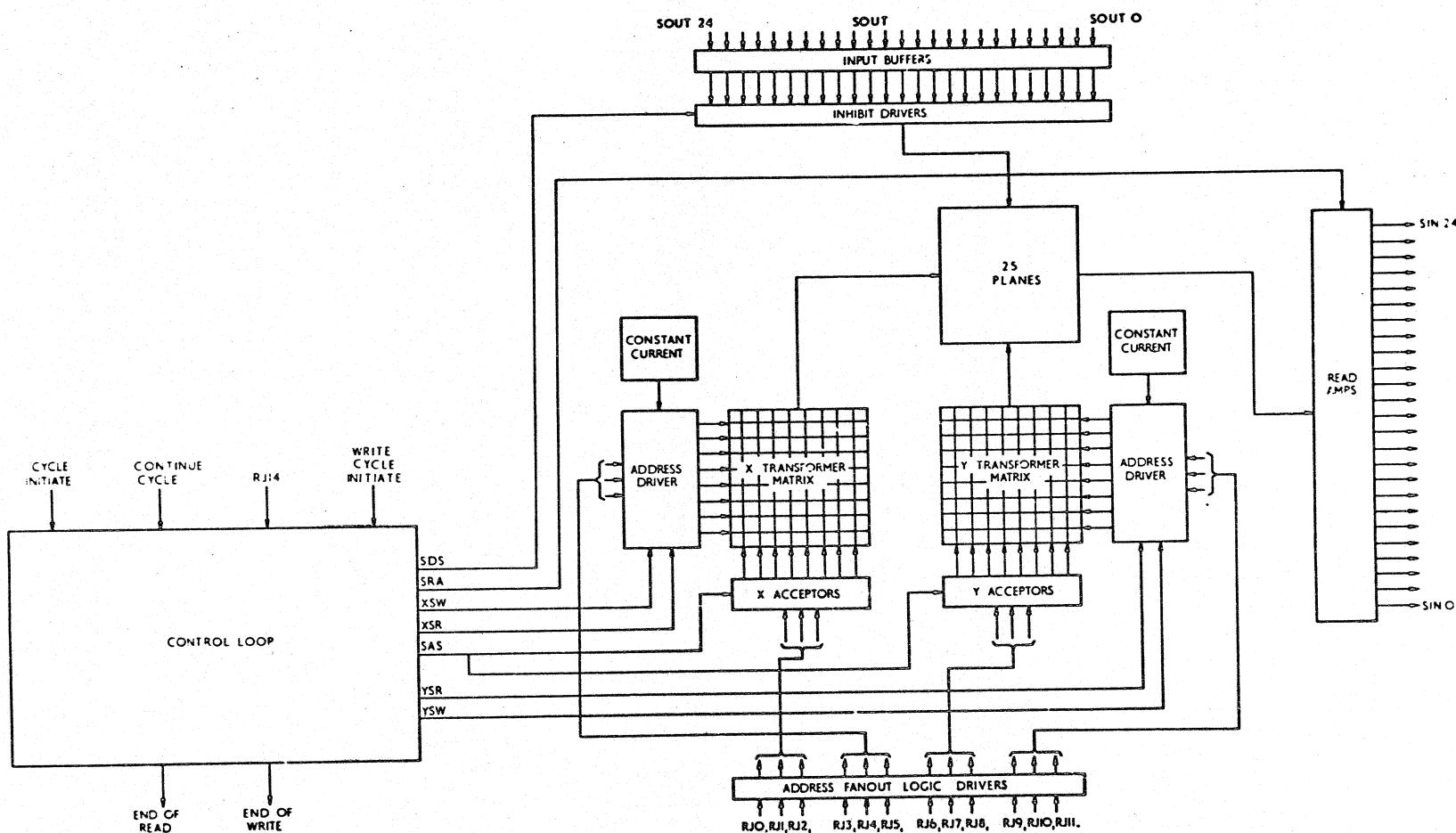
ISS.

CHANGES

3

7652

7-65

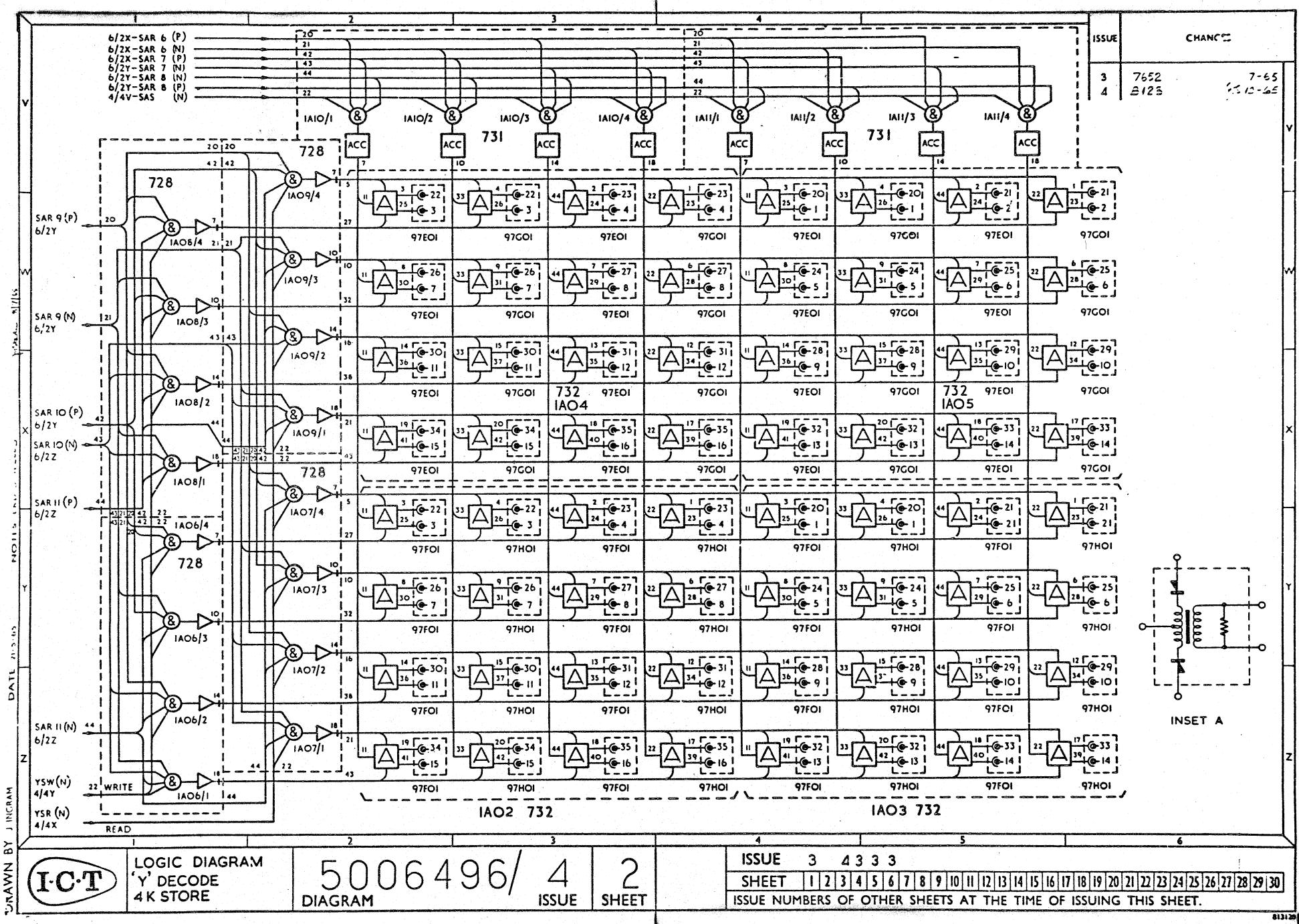


I.C.T

LOGIC DIAGRAM
4K STORE BLOCK
SCHEMATIC5006496 /
DIAGRAM3
ISSUE1
SHEETISSUE
3 3 3 3 3

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.





LOGIC DIAGRAM
'X' DECODE
4K STORE

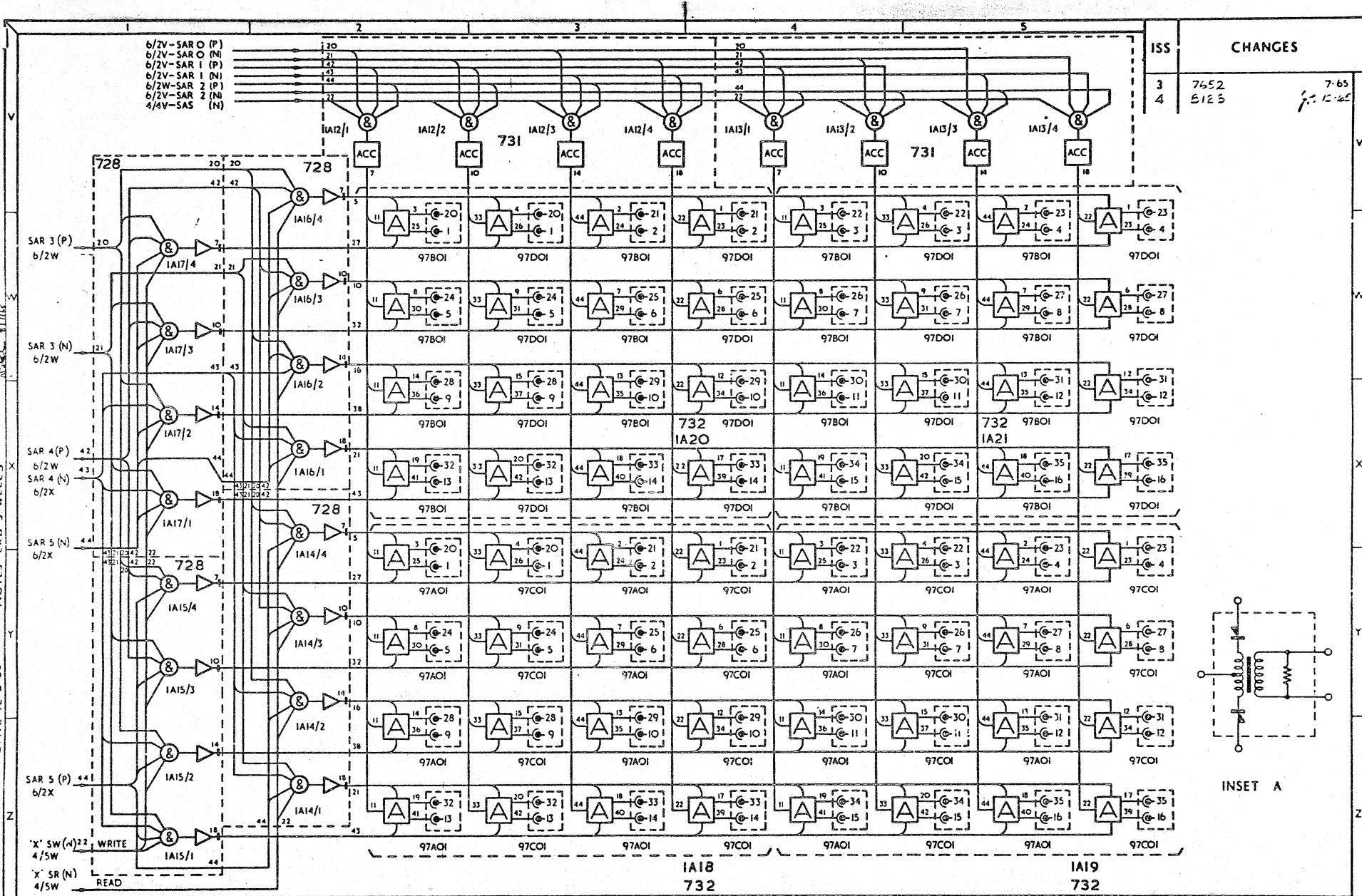
5006496/4
DIAGRAM
ISSUE 3

SHEET 3
ISSUE 3

ISSUE 3 4 3 3 3

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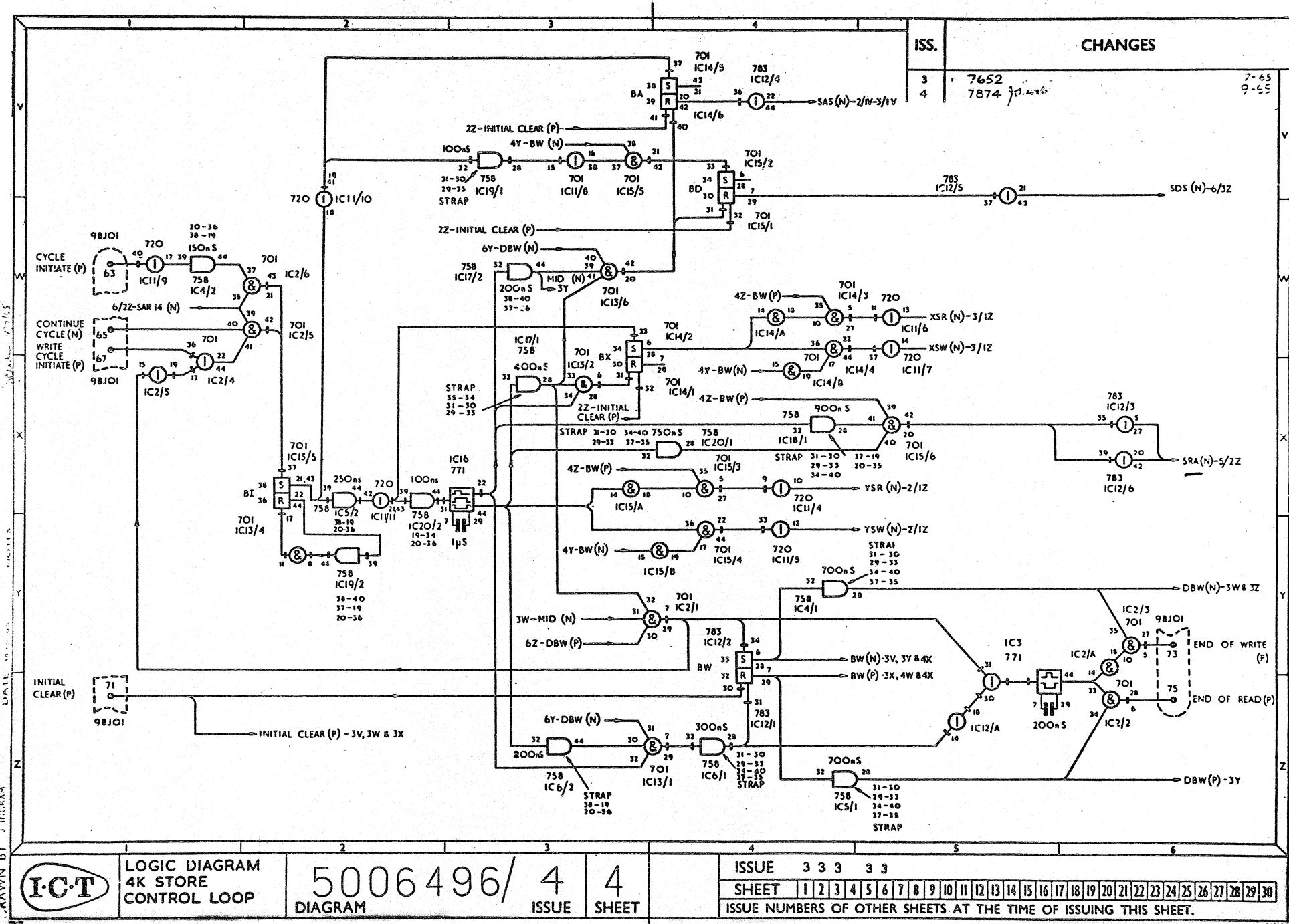
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CHANGES

3 7652
4 5125

7-65
12-66



The logo consists of the letters "I.C.T." enclosed within a stylized, rounded rectangular frame.

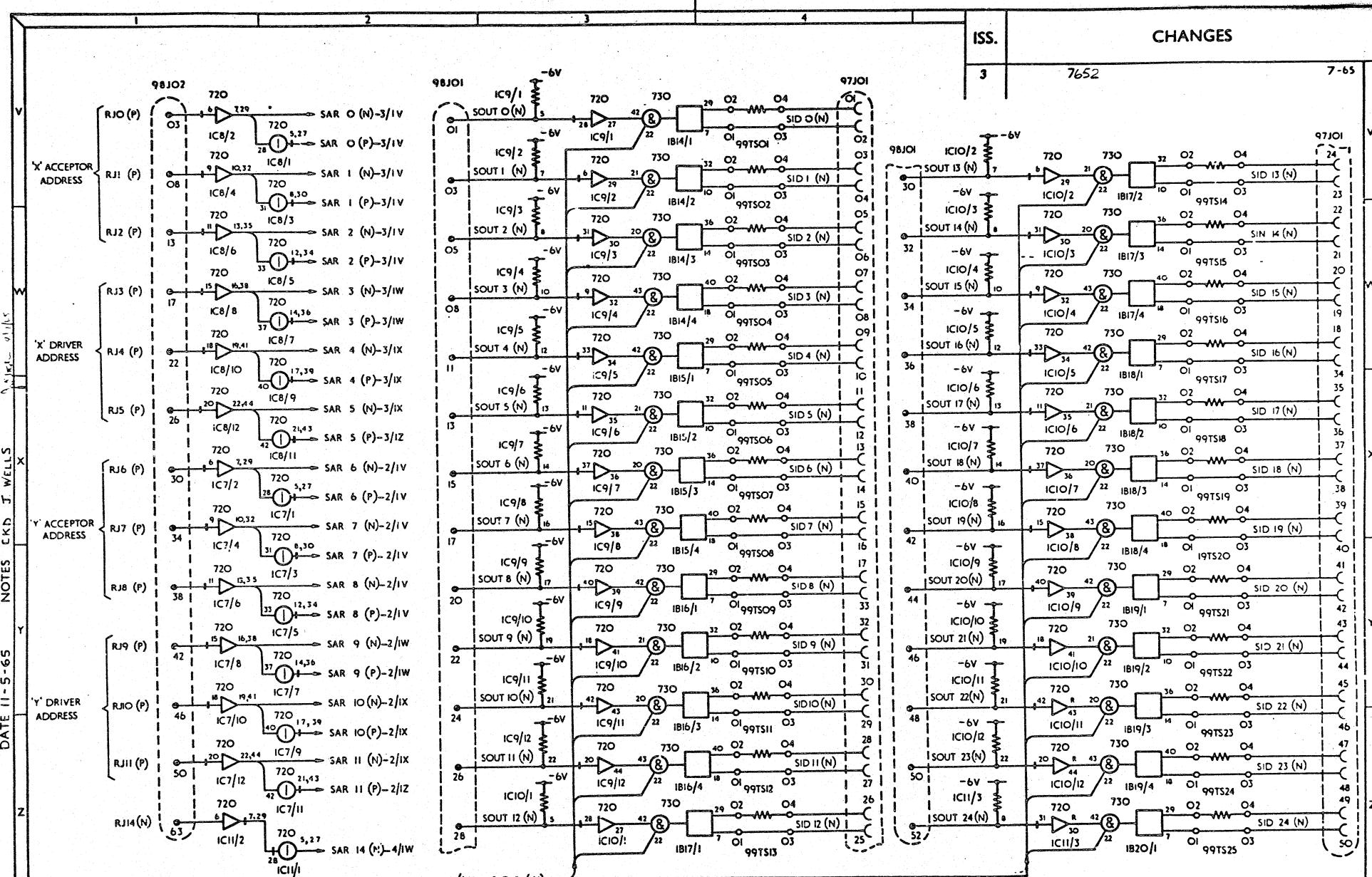
LOGIC DIAGRAM 4K STORE CONTROL LOOP

5006496
DIAGRAM

4 4
ISSUE SHEET

ISSUE 3 3 3 3
SHEET 1 2 3 4 5
ISSUE NUMBERS OF OTH

DRAWN BY I.C.T.	LOGIC DIAGRAM 4K STORE	5006496 / 3	ISSUE 3 3 3 3 3	SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30	CHANGES					
					1	2	3	4	5	6
V	W	X	Y	Z	V	W	X	Y	Z	
NOTES CKD J WELLS					97KO1	98JO3	97KO1	98JO3		
DATE										
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I.C.T.

LOGIC DIAGRAM 4 K STORE

5006496
DIAGRAM

3 6
ISSUE SHEET

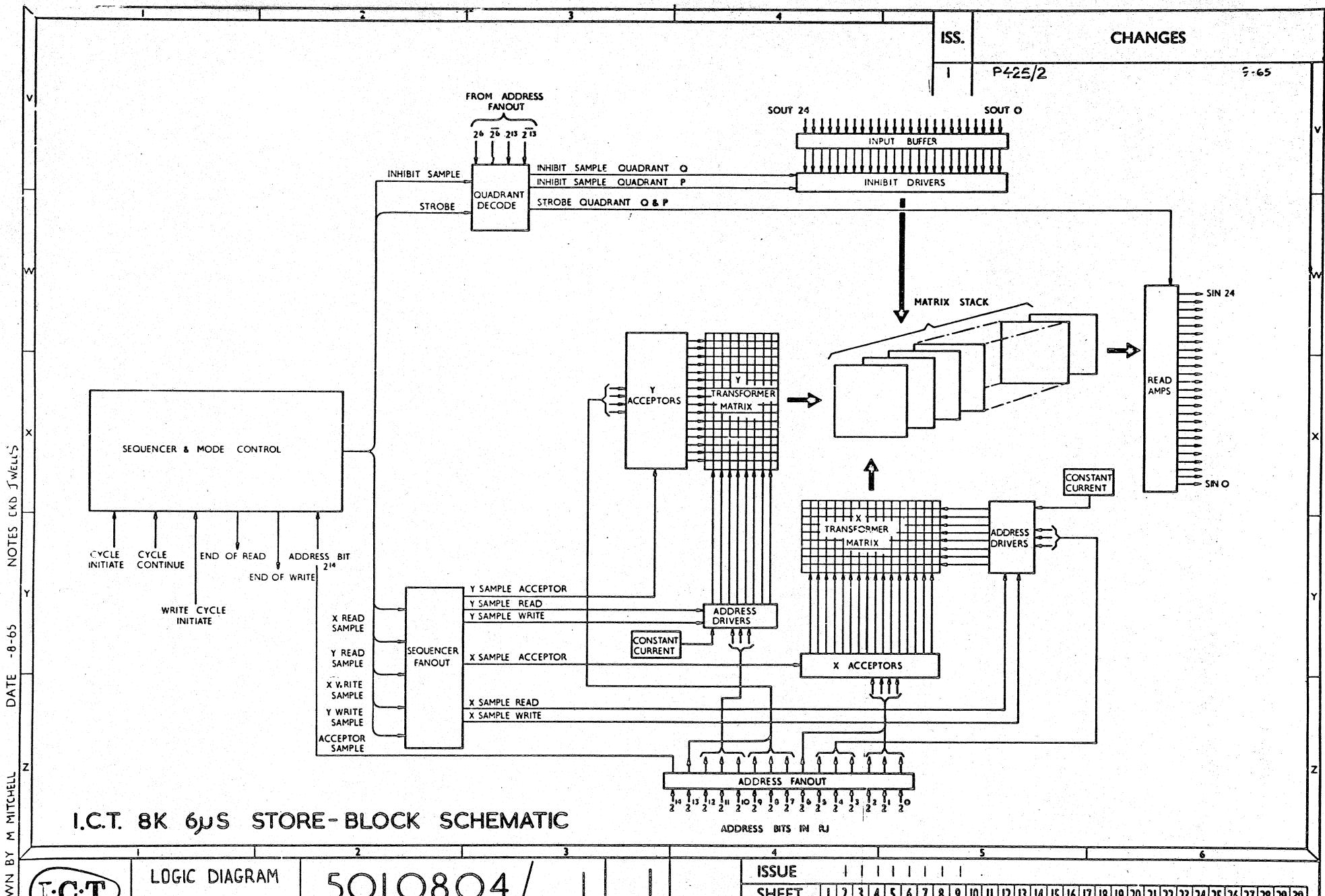
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

CHANGES

7-65



I.C.T. 8K 6μS STORE-BLOCK SCHEMATIC

DRAWN BY M MITCH



LOGIC DIAGRAM

5010804
DIAGRAM

1

SHEET

ISSUE

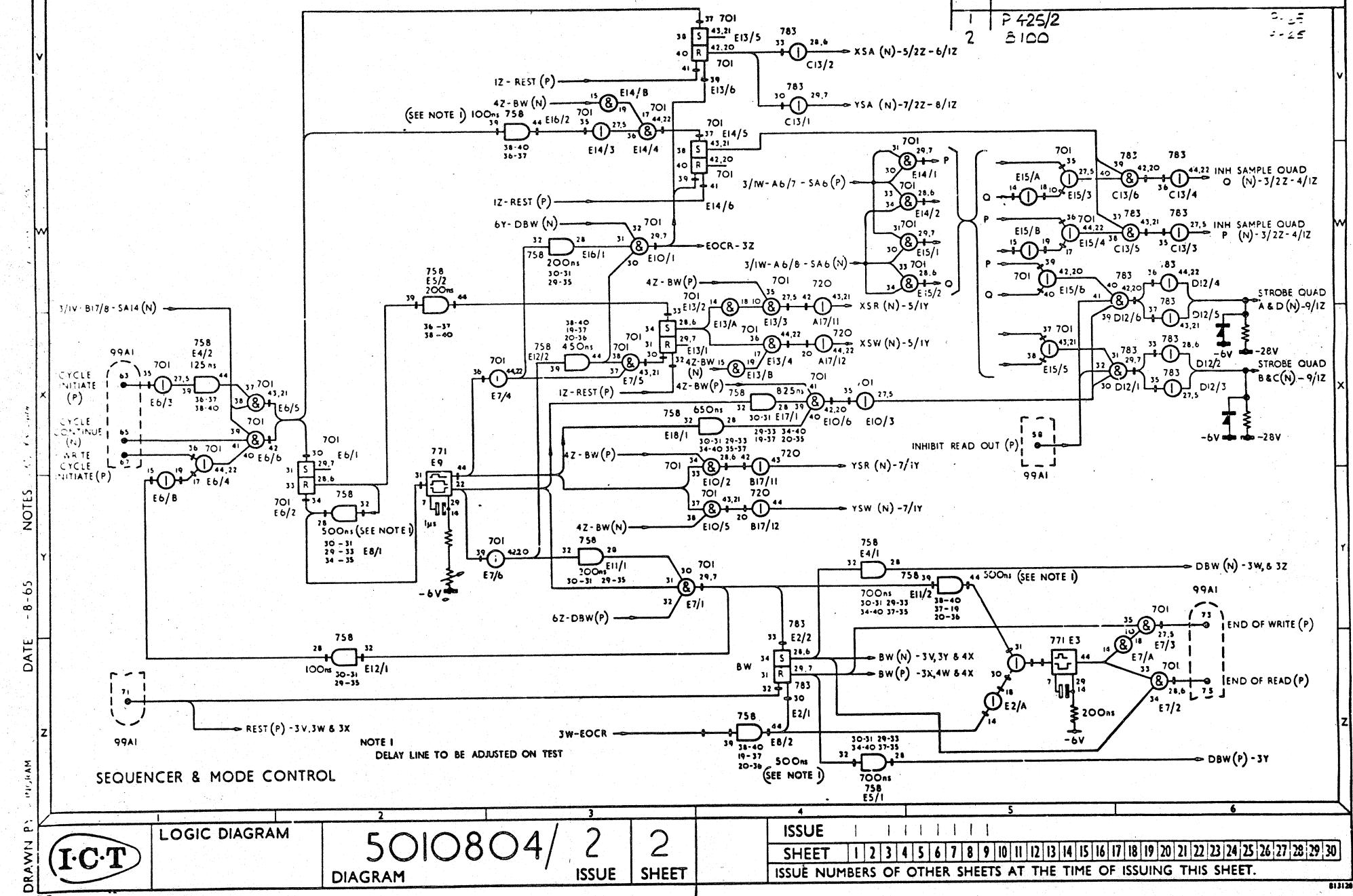
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

813120 E

ISS

CHANGES

256



SEQUENCER & MODE CONTROL

NOTE 1
DELAY LINE TO BE ADJUSTED ON TEST



LOGIC DIAGRAM

50100041

5010804/	2		ISSUE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
DIAGRAM	ISSUE	SHEET	ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.																														

I.C.T

LOGIC DIAGRAM
DIAGRAM

5010804

ISSUE 3 SHEET

ISSUE | | | | | | | |

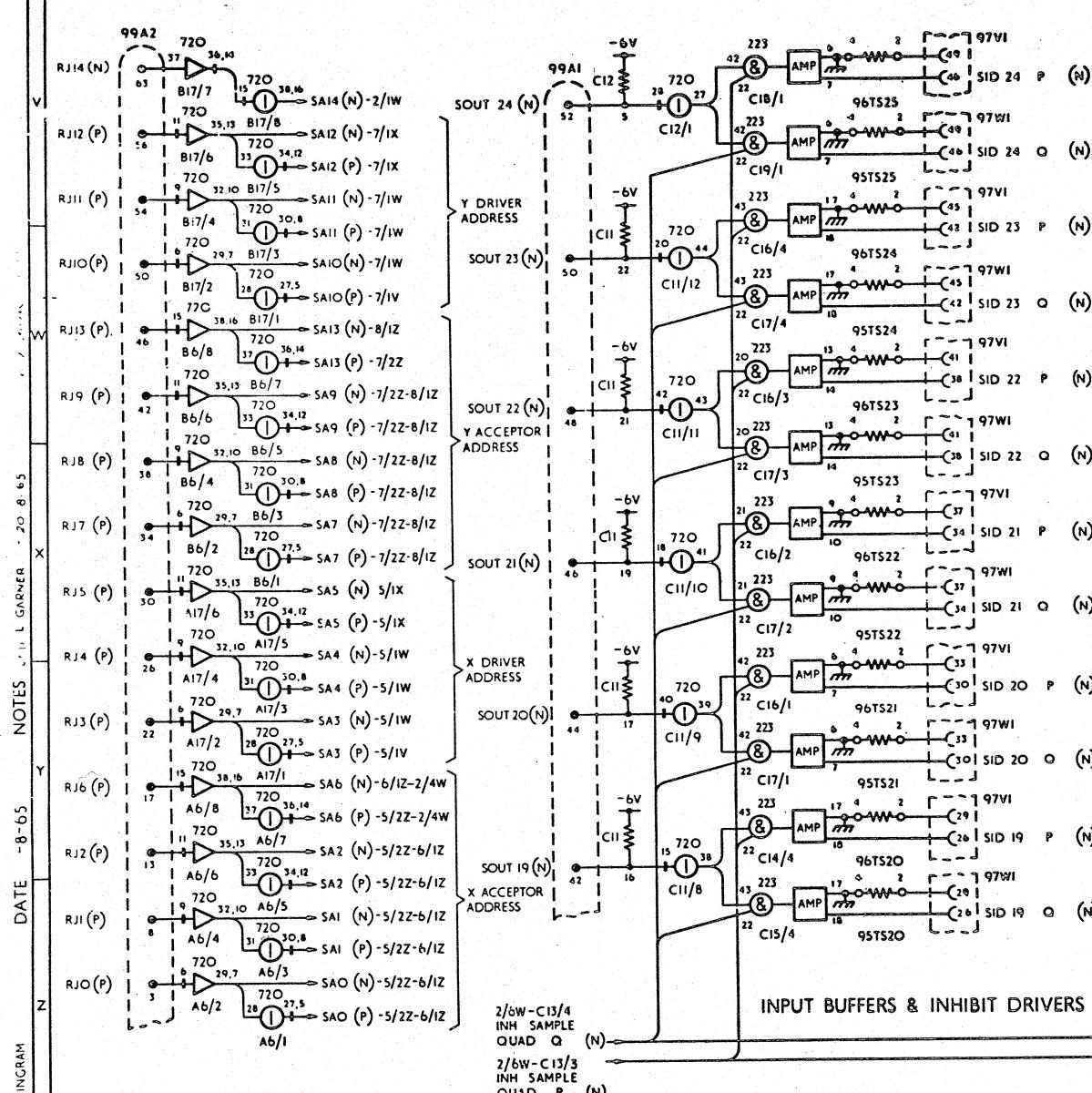
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NOTES L. GARNER

DATE 6-65

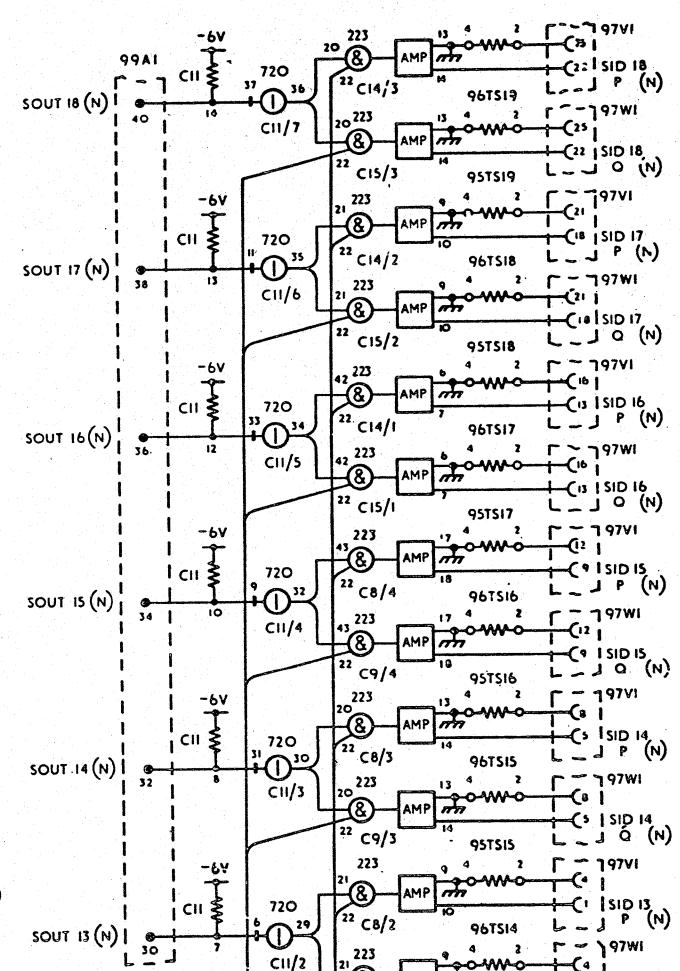
DRAWN BY J. INGRAM



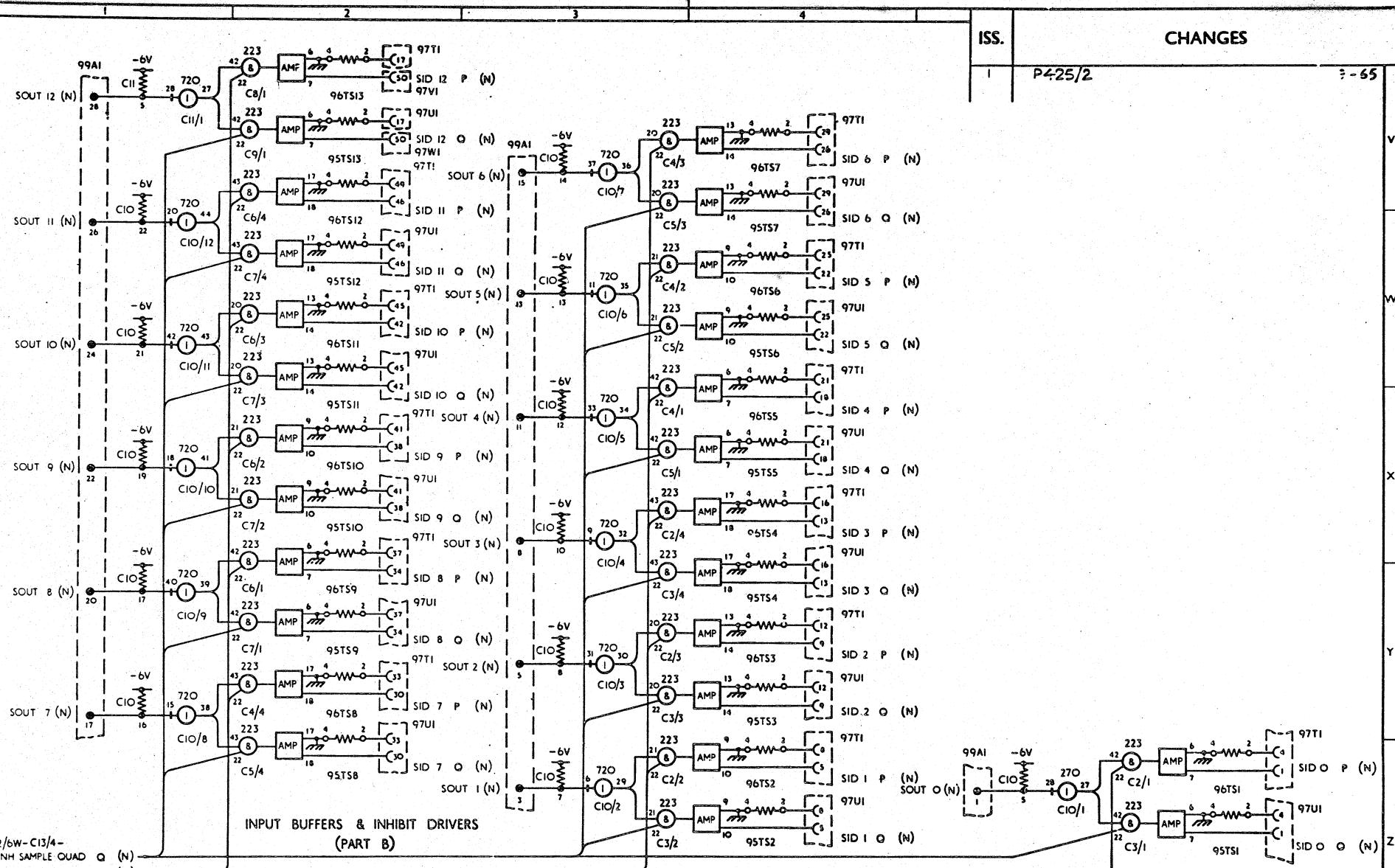
ISS. CHANGES

1 P 425/2

9-65



013128 E



Z 2/6W-C13/
INH SAMPLE
INH SAMPLE
2/6W-C13/

I.C.T

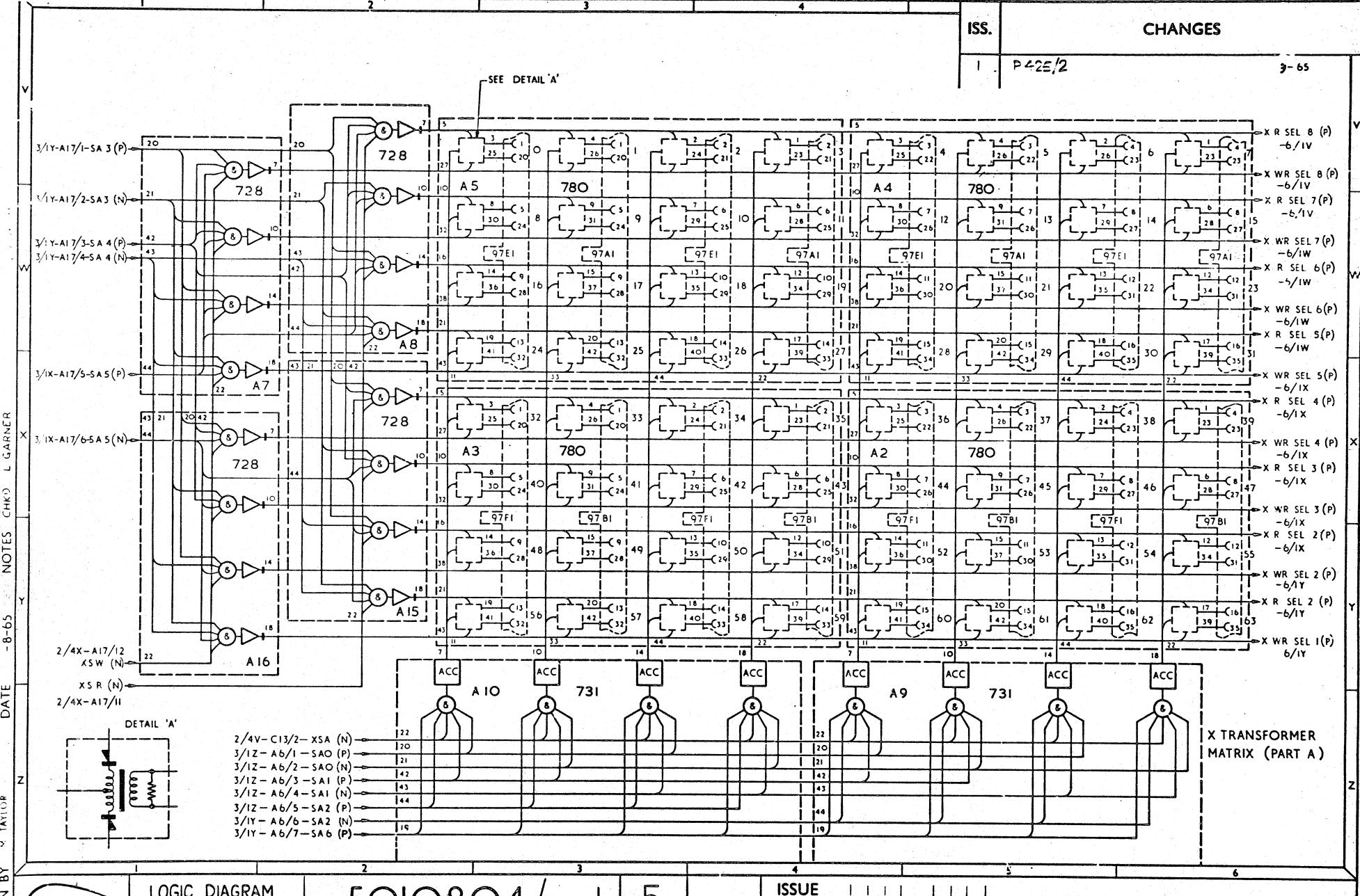
LOGIC DIAGRAM

5010804
MAGRAM

1 4
ISSUE SHEET

ISSUE

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CHANGES

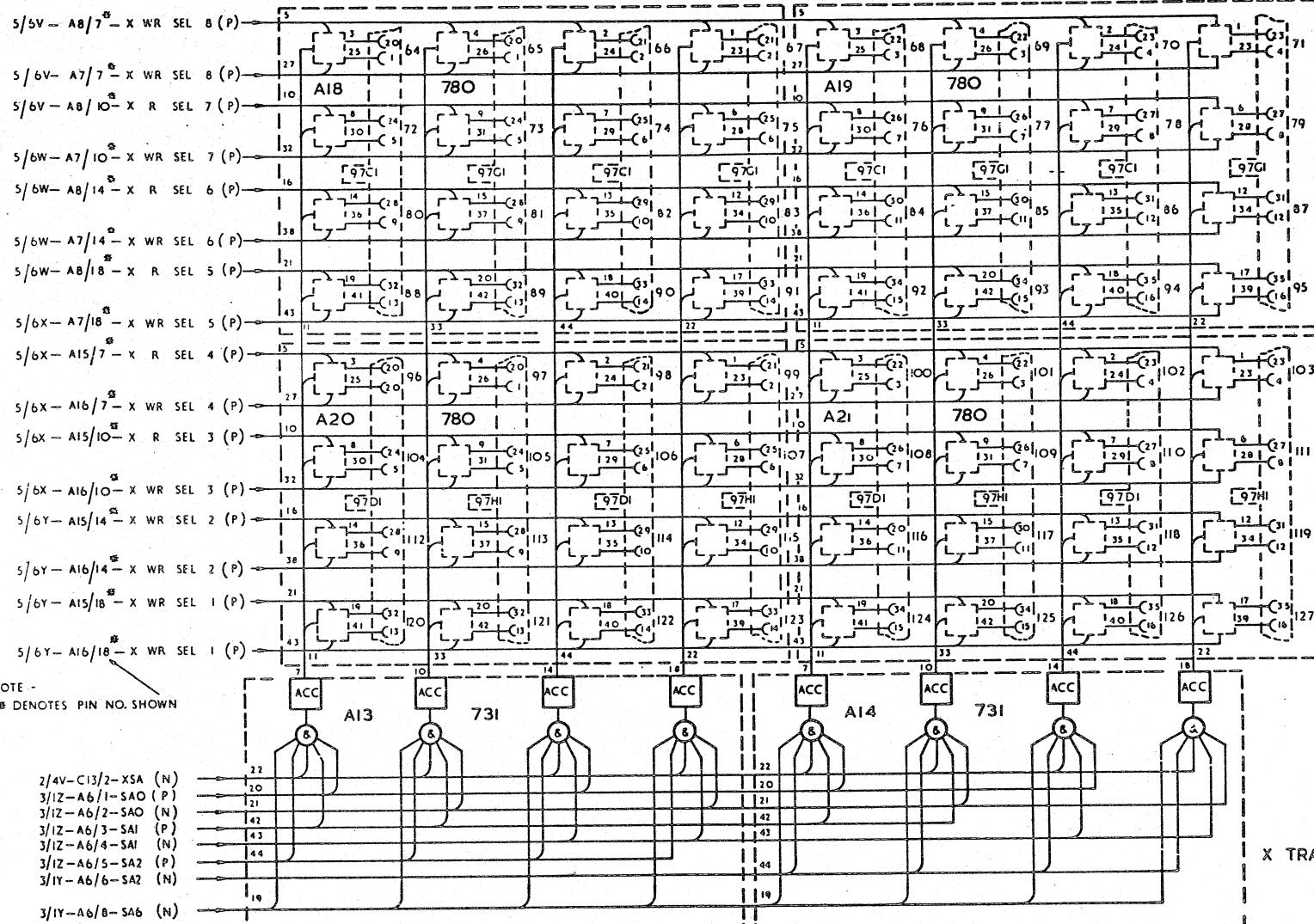
P 425/2

9-65

NOTES CHKD L.GARNER

DATE -8-65

DRAWN BY S COOPER

X TRANSFORMER MATRIX
(PART B)

LOGIC DIAGRAM

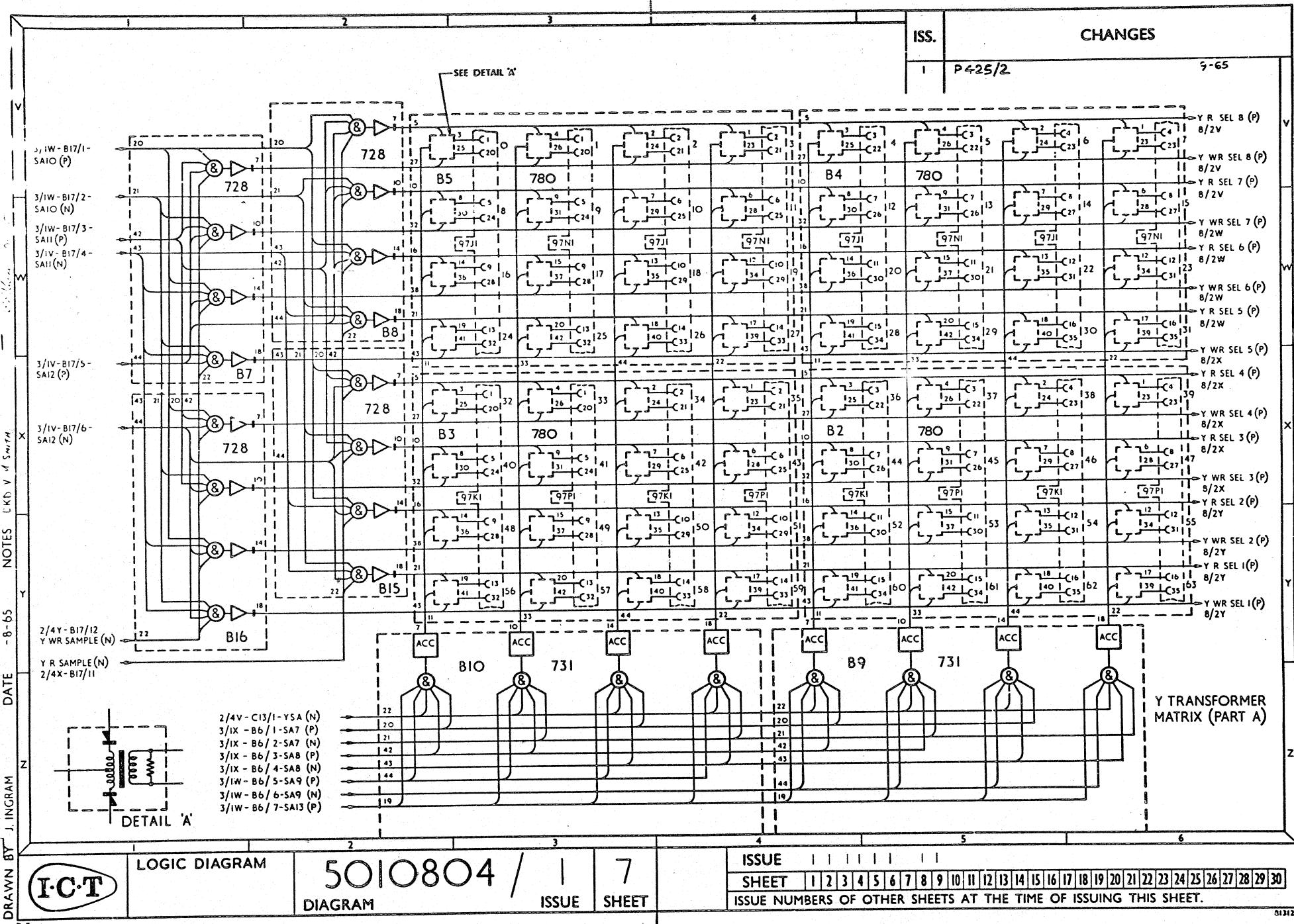
5010804 /

DIAGRAM ISSUE SHEET

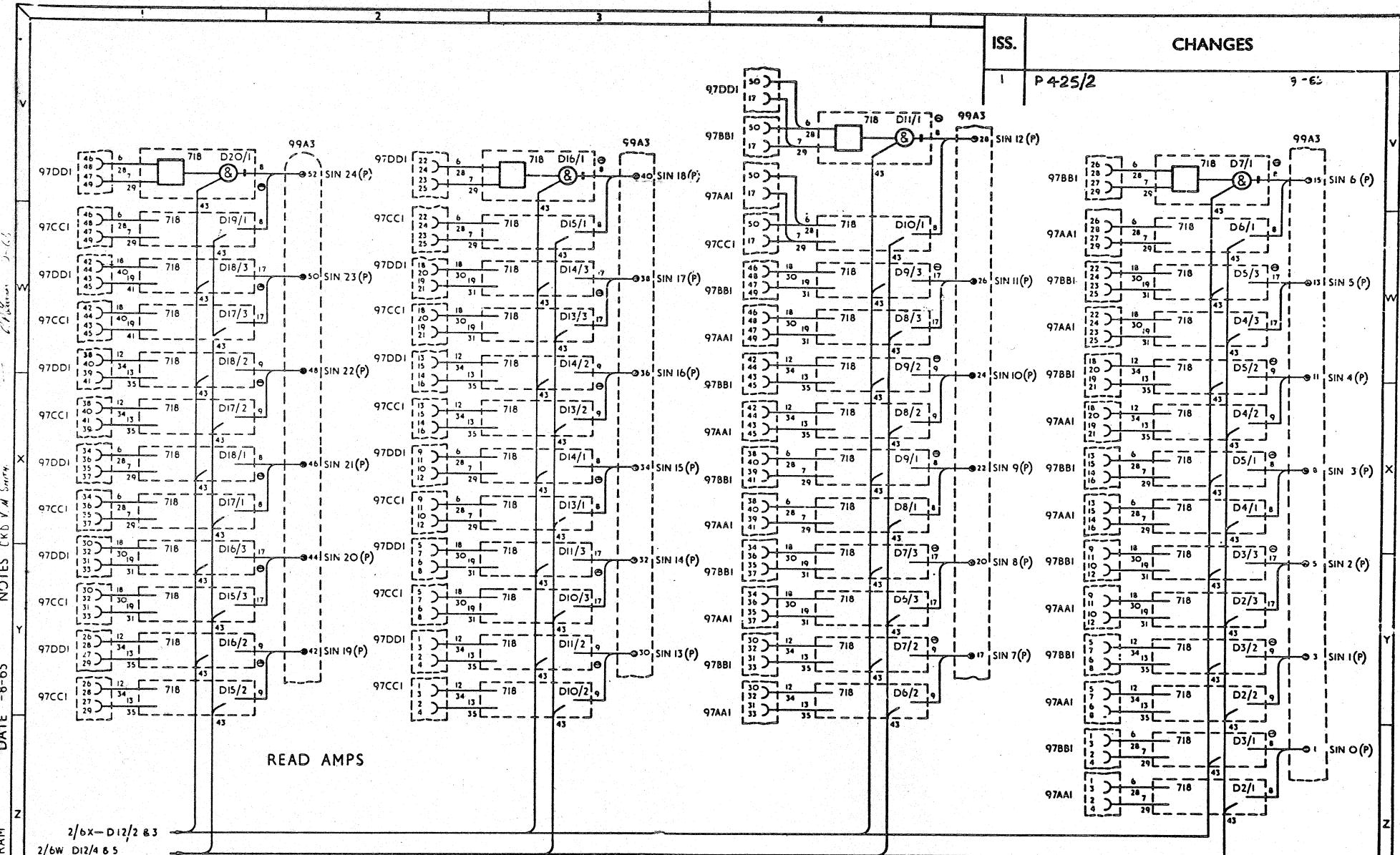
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SHEET | 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.



				ISS.	CHANGES	
				I P4-25/2		3-65
V						
W						
X						
Y						
Z						
NOTES CKD V.M.S.W.H.						
DATE -8-65						
DRAWN BY R SHILLDAY						
<p>NOTE * DENOTES PIN NO SHOWN</p> <p>2/4V-C13/1 - YSA(N) 3/IW-B6/1 - SA7(P) 3/IW-B6/2 - SA7(N) 3/IW-B6/3 - SA8(P) 3/IW-B6/4 - SA8(N) 3/IW-B6/5 - SA9(P) 3/IW-B6/6 - SA9(N) 3/IW-B6/8 - SA13(N)</p>				<p>Y TRANSFORMER MATRIX (PART B)</p>		
LOGIC DIAGRAM		5010804 /		ISSUE	ISSUE	
		DIAGRAM		SHEET	SHEET	
I.C.T				8	8	
					ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.	
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NOTE. NO CARDS FITTED IN POSITIONS MARKED THUS



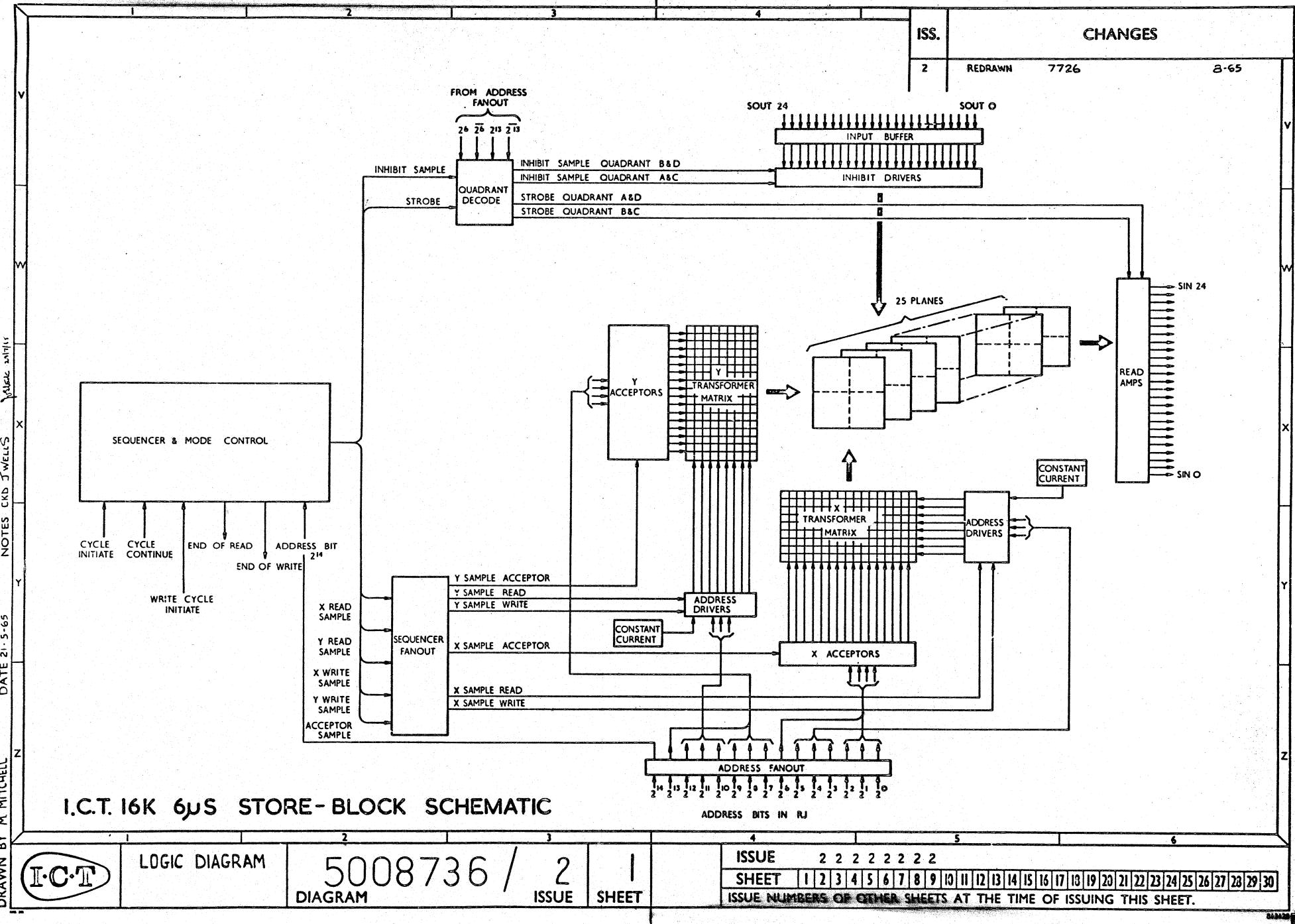
LOGIC DIAGRAM

5010804
DIAGRAM

1 | 9
ISSUE SHEET

ISSUE 1 1 1 1 1 1

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.



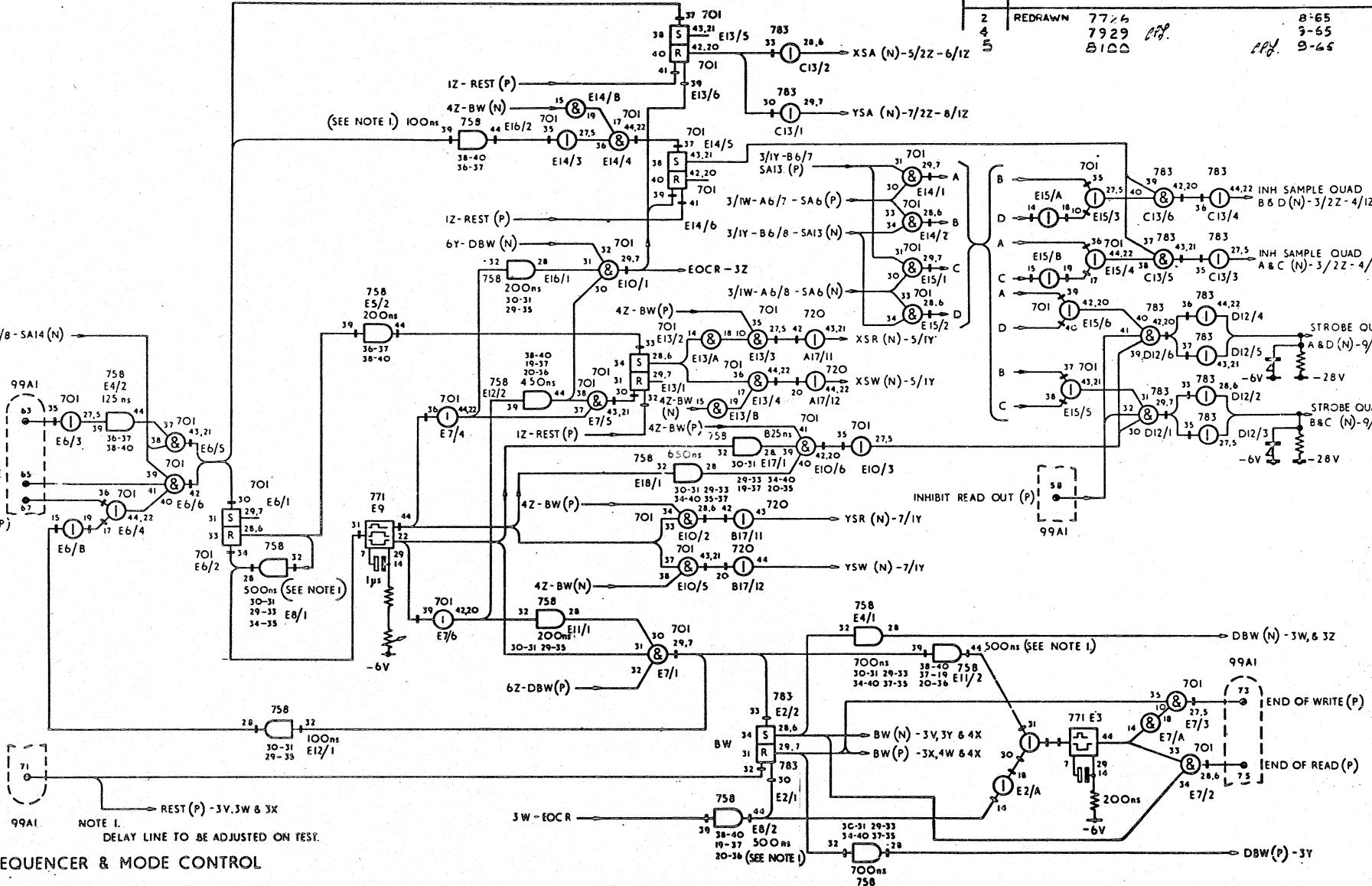
19

CHANGES

18

REDRAWN 77/6
7929
816A

8-65
9-65
ppd 9-65



SEQUENCER & MODE CONTROL

NOTE I.
DELAY LINE TO BE ADJUSTED ON TEST

DRAWN BY J. INGRAM



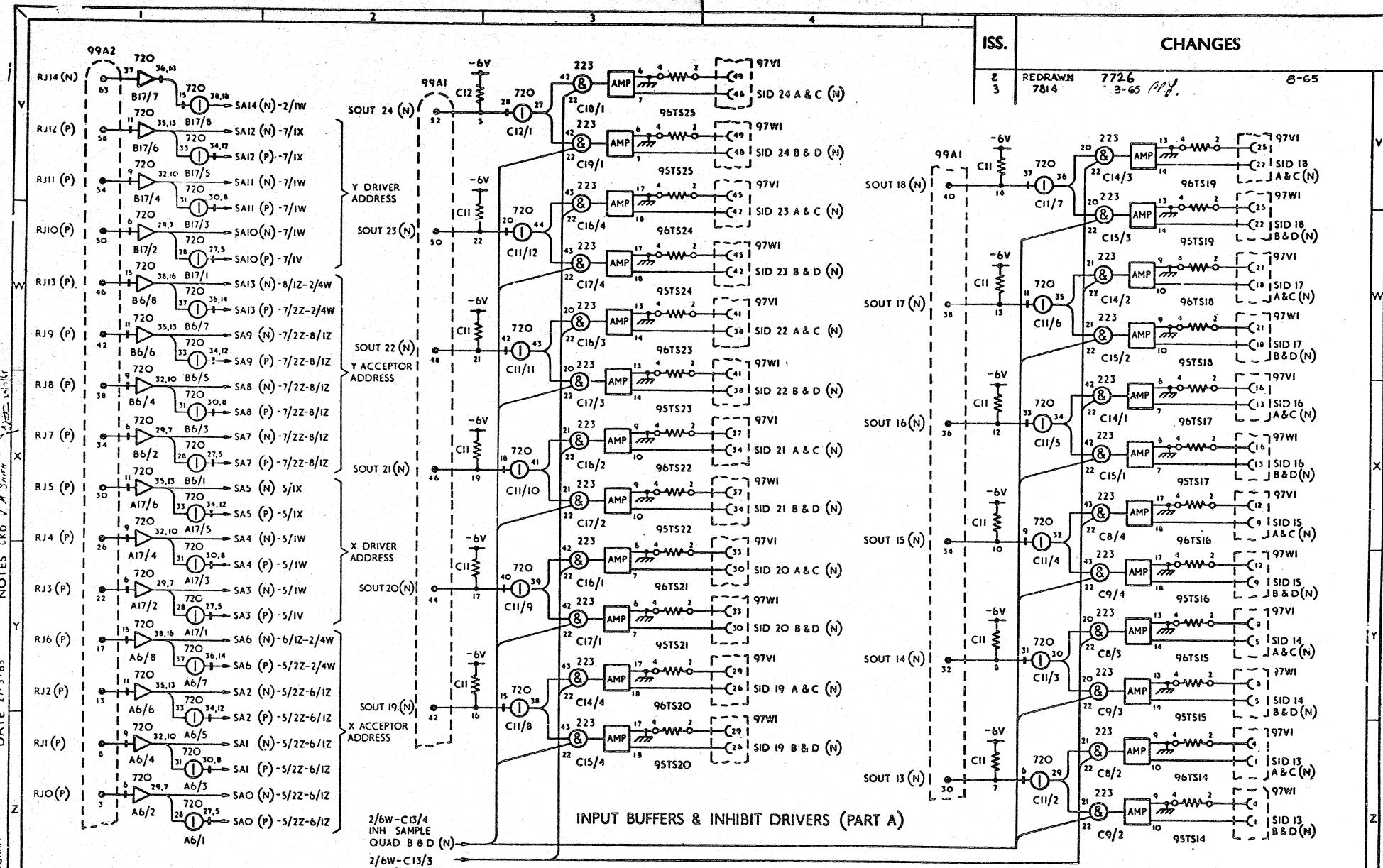
LOGIC DIAGRAM

5008736 / 5 2
DIAGRAM ISSUE SHEET

ISSUE 3 3 3 3 3 3 3

13308 2 3 3 3 3 3 3 2 .

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET



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INH SAMPLE
QUAD B & D (N) →
2/6W-C13/3
INH SAMPLE
QUAD A & D (N)

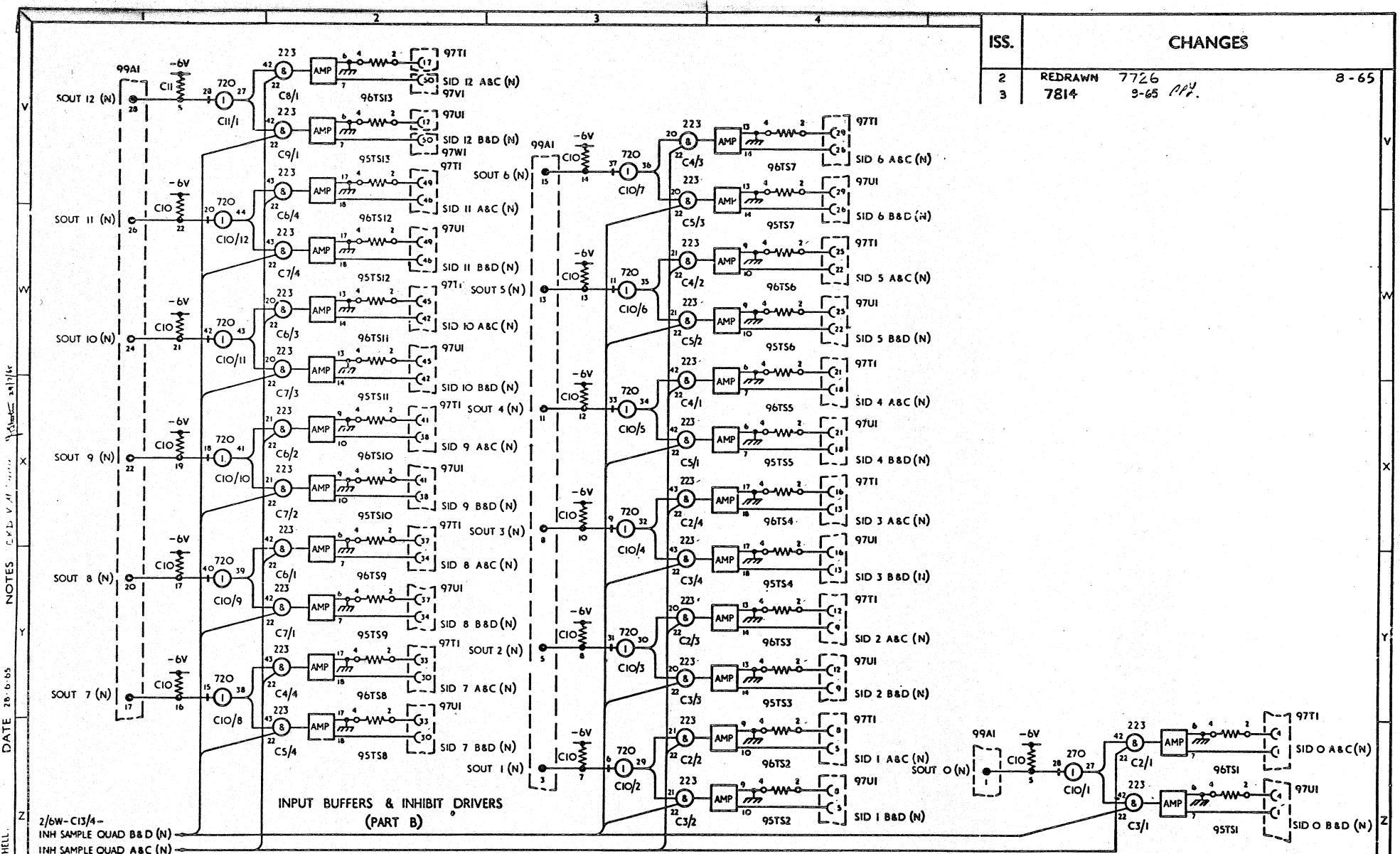
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DIAGRAM

3 | 3
ISSUE SHEET

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SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET



THE NBY MICHIGAN



LOGIC DIAGRAM

5008736/
DIAGRAM

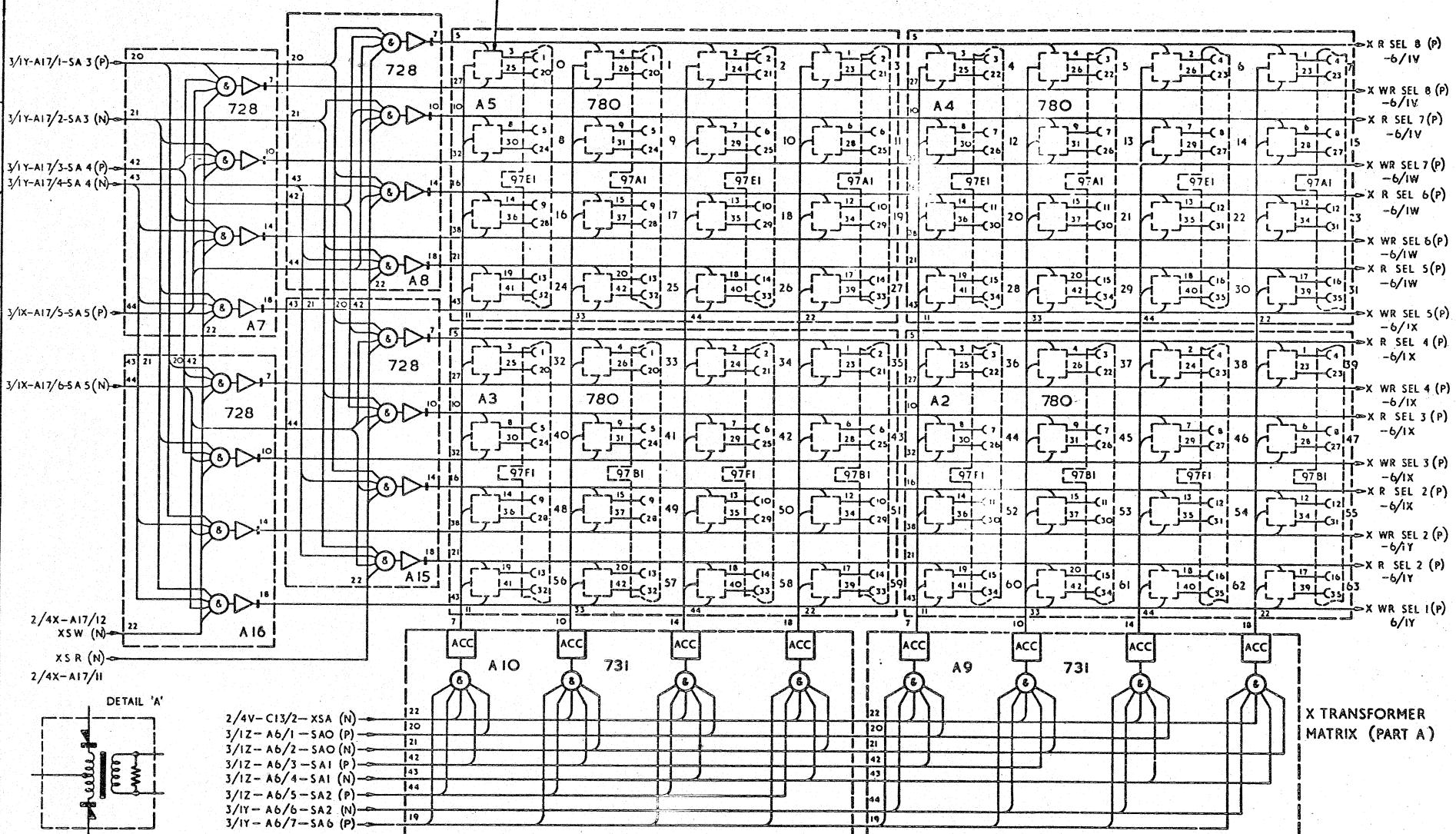
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ISSUE	SHEET

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SHEET	1	2	3	4	5	6	7	8	9
ISSUE NUMBERS OF OTHER SHEET									

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

ISS.

CHANGES

2 REDRAWN 7726
3 7814. CPY.B-65
9-65

LOGIC DIAGRAM

5008736 / 3

ISSUE 5 SHEET

ISSUE 2 2 3 3 3 3 3 2

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

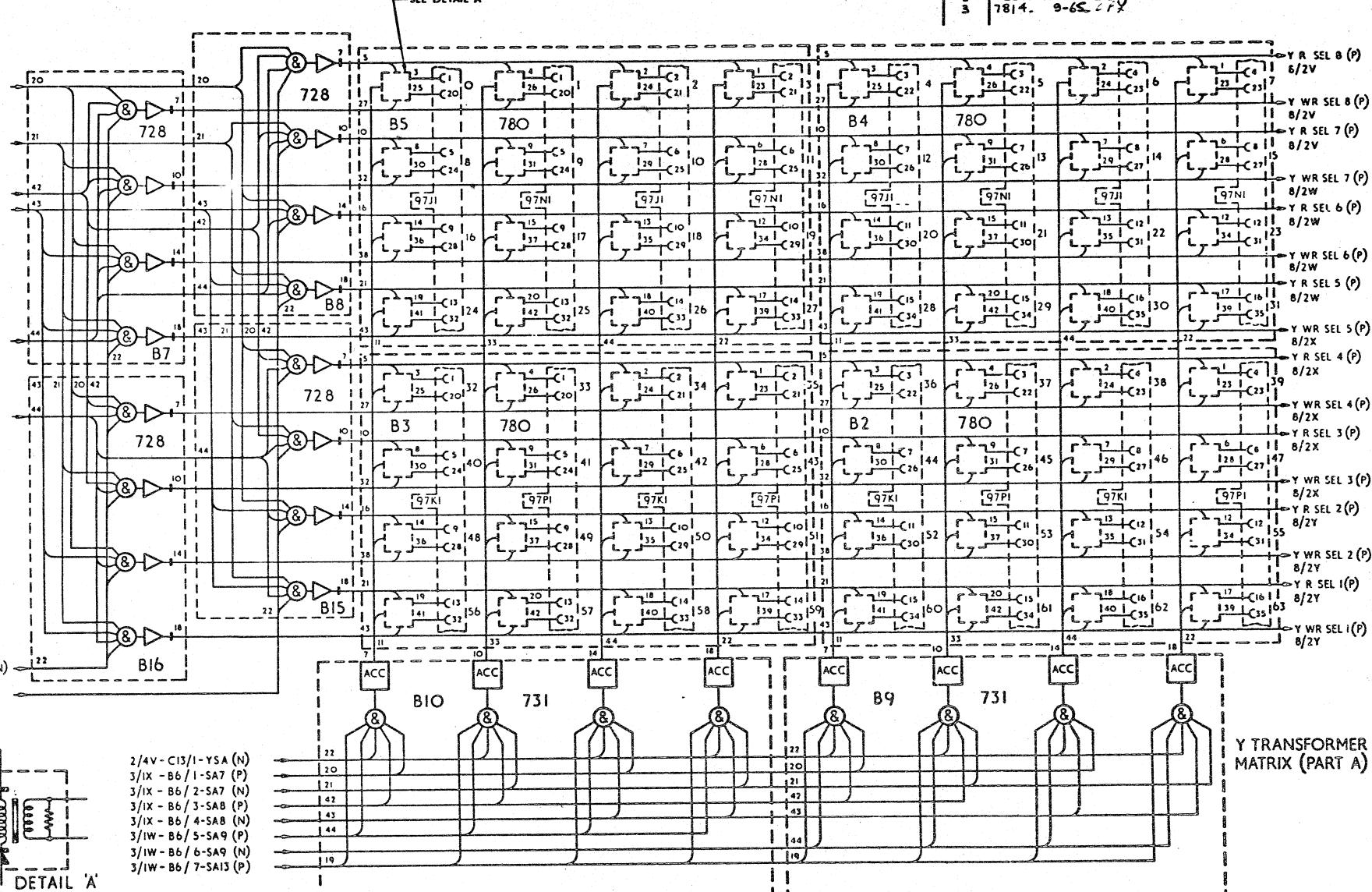
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ISS.

CHANGES

2 REDRAWN 7726
3 7814. 9-65

8-65



LOGIC DIAGRAM

5008736 / 3

DIAGRAM

ISSUE

SHEET

ISSUE 2 2 3 3 3 3 3 2

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

0331200

ISS. CHANGES

NOTES CKD V/S WITH DATE 29-7-65																				
DATE 25-5-65 DRAWN BY R SHILLDAY	2	3	4	5	6	ISS.	CHANGES													
						2 3	REDRAWN 7814. 9-66	7726												

7/6V-B8/743 - Y R SEL 8 (P) ←

7/6V-B8/27 * - YWR SEL 8 (P) ←

7/6V-B8/IO * - Y R SEL 7 (P) ←

7/6W-B8/32 * - YWR SEL 7 (P) ←

7/6W-B8/16 * - Y R SEL 6 (P) ←

7/6W-B8/38 * - YWR SEL 6 (P) ←

7/6W-B8/21 * - Y R SEL 5 (P) ←

7/6X-B8/43 * - YWR SEL 5 (P) ←

7/6X-B15/7 * - Y R SEL 4 (P) ←

7/6X-B15/27 * - YWR SEL 4 (P) ←

7/6X-B15/IO * - Y R SEL 3 (P) ←

7/6X-B15/32 * - YWR SEL 3 (P) ←

7/6Y-B15/16 * - Y R SEL 2 (P) ←

7/6Y-B15/38 * - YWR SEL 2 (P) ←

7/6Y-B15/21 * - Y R SEL 1 (P) ←

7/6Y-B15/43 * - YWR SEL 1 (P) ←

NOTE * DENOTES PIN NO SHOWN

Y TRANSFORMER MATRIX (PART B)

LOGIC DIAGRAM

DIAGRAM

5008736/

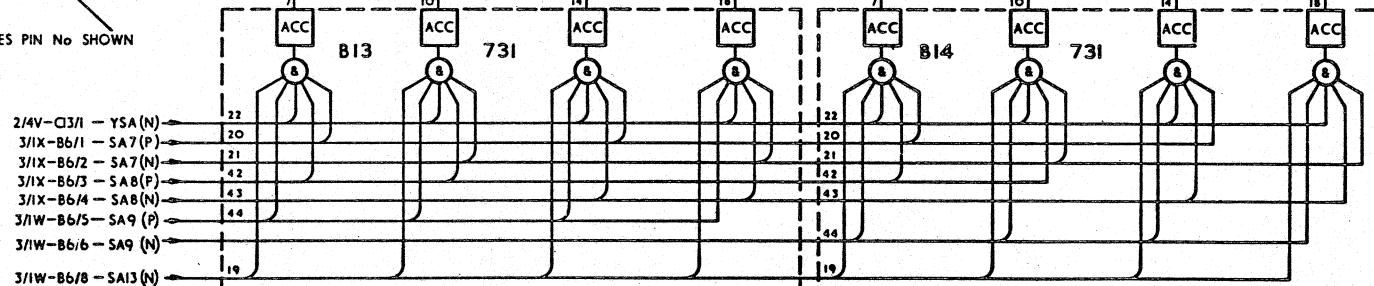
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

I.C.T.

NOTE
* DENOTES PIN NO SHOWN



Y TRANSFORMER MATRIX (PART B)

2/IV-B13/1 - YSA(N)-
 3/IX-B6/1 - SA7(P)-
 3/IX-B6/2 - SA7(N)-
 3/IX-B6/3 - SAB(P)-
 3/IX-B6/4 - SAB(N)-
 3/IW-B6/5 - SA9(P)-
 3/IW-B6/6 - SA9(N)-
 3/IW-B6/8 - SA13(N)

LOGIC DIAGRAM

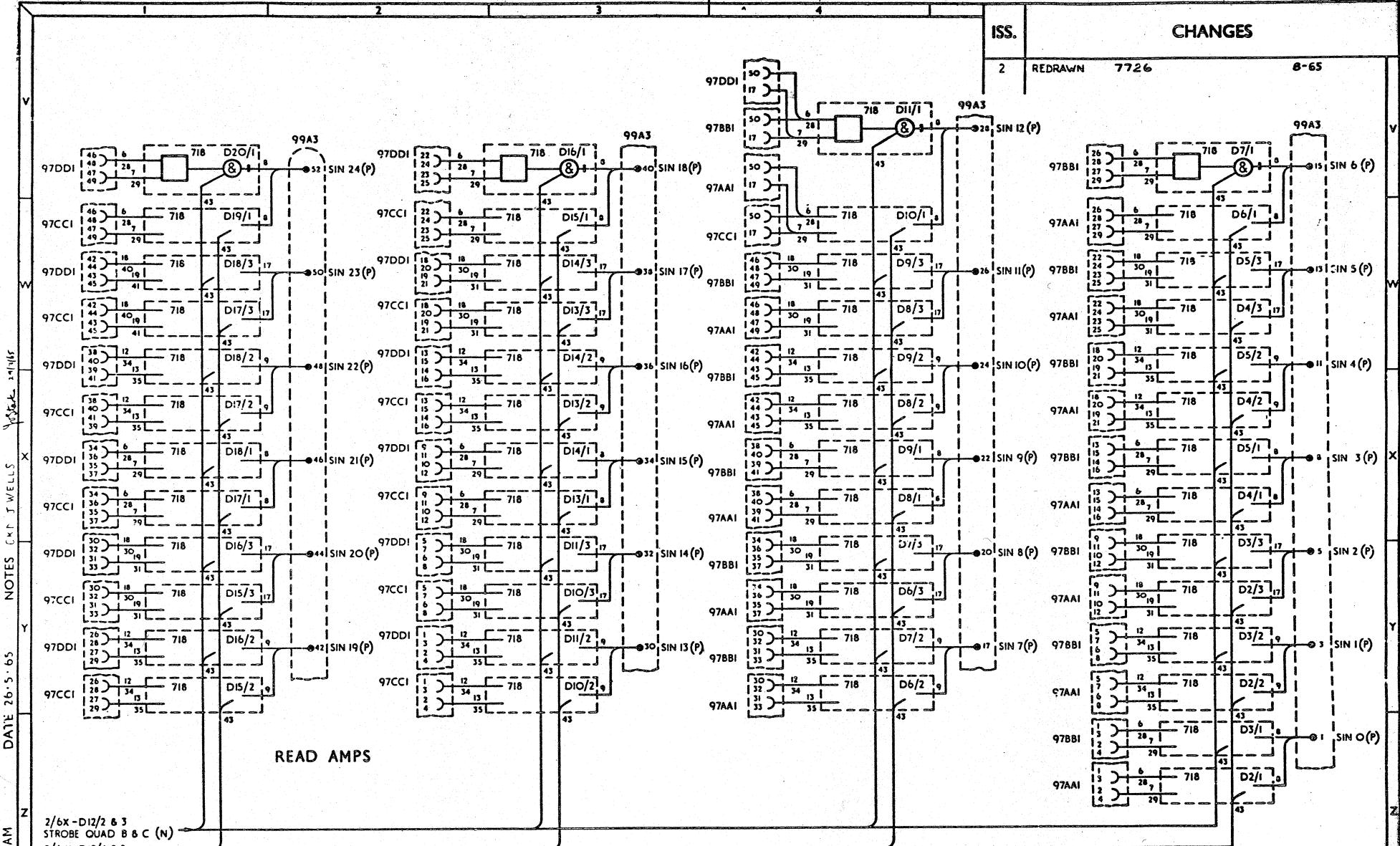
5008736
DIAGRAM

3 8
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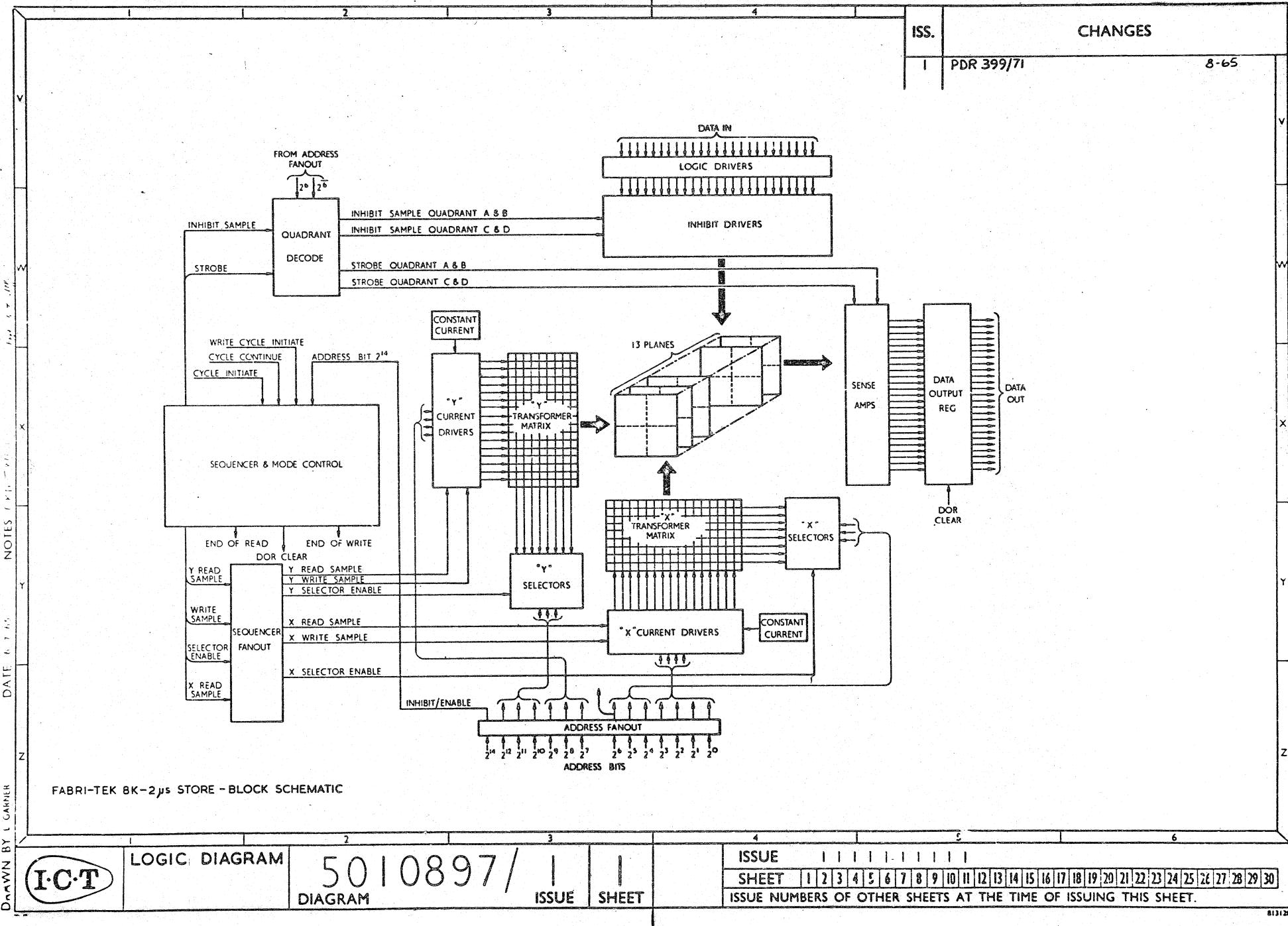
5008736 /
DIAGRAM

2
ISSUE
9
SHEET

ISSUE 2 2 2 2 2 2 2

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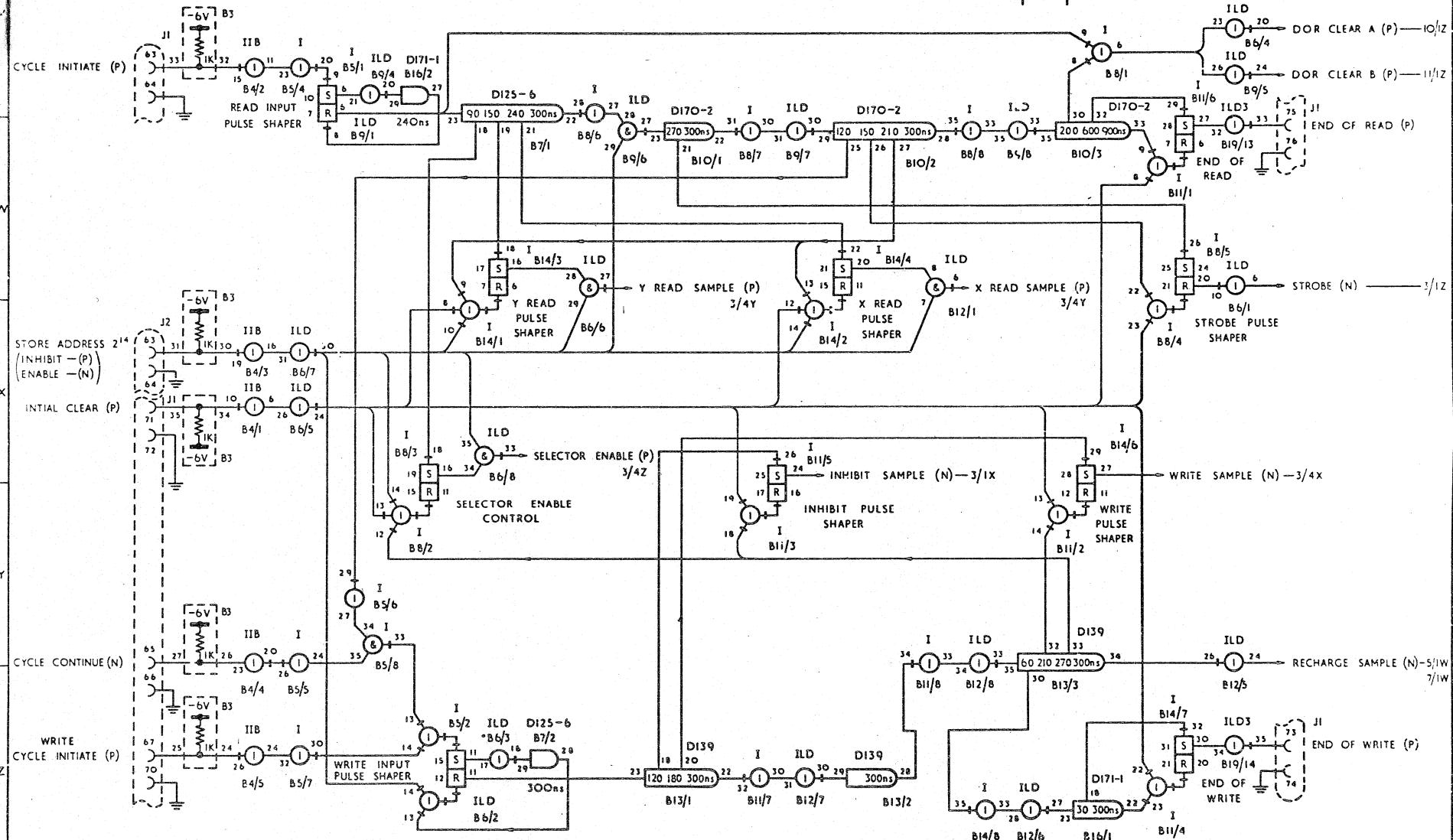
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ISS. CHANGES

I PDR 399/71

8-65



I.C.T.

LOGIC DIAGRAM

5010897/

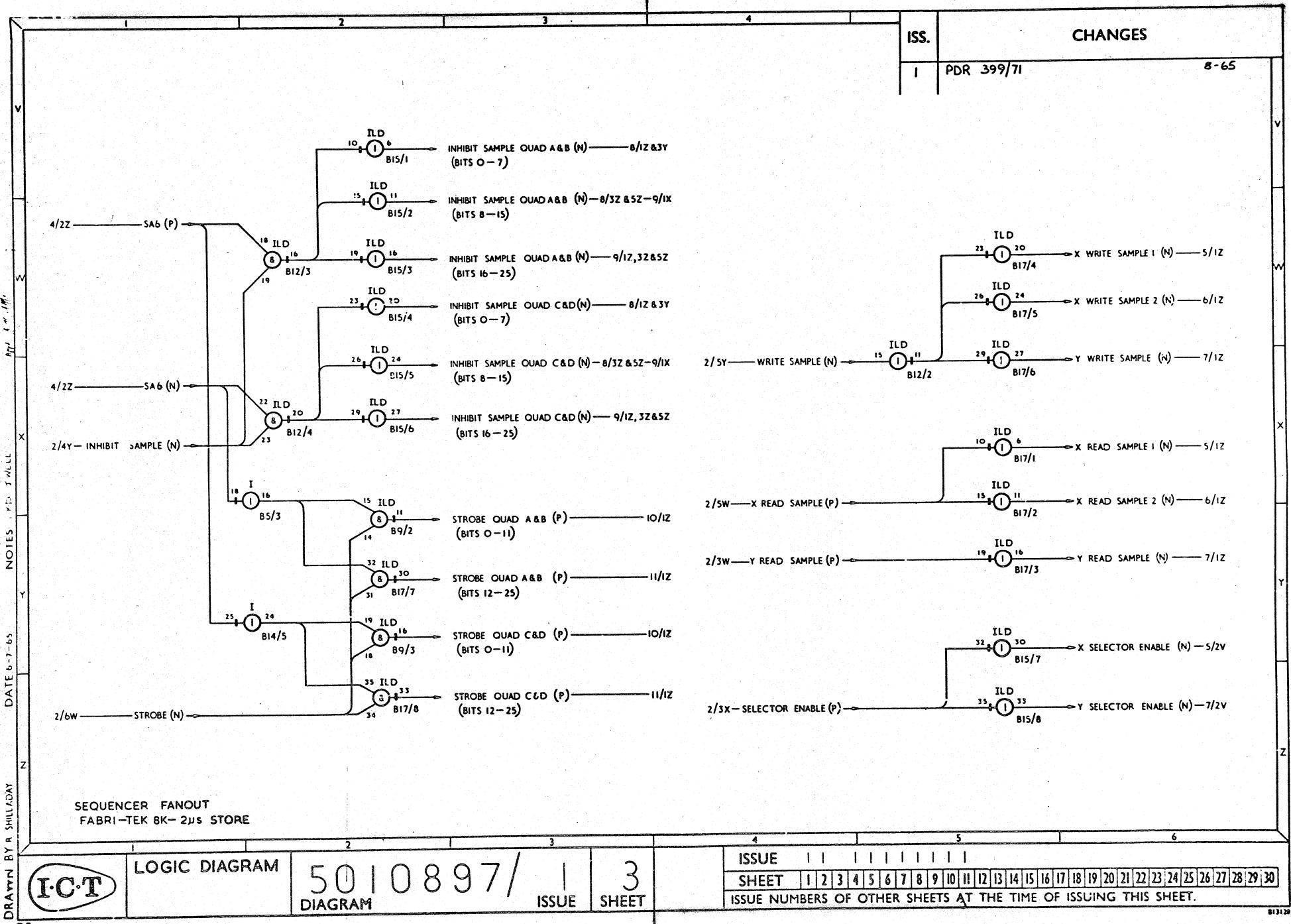
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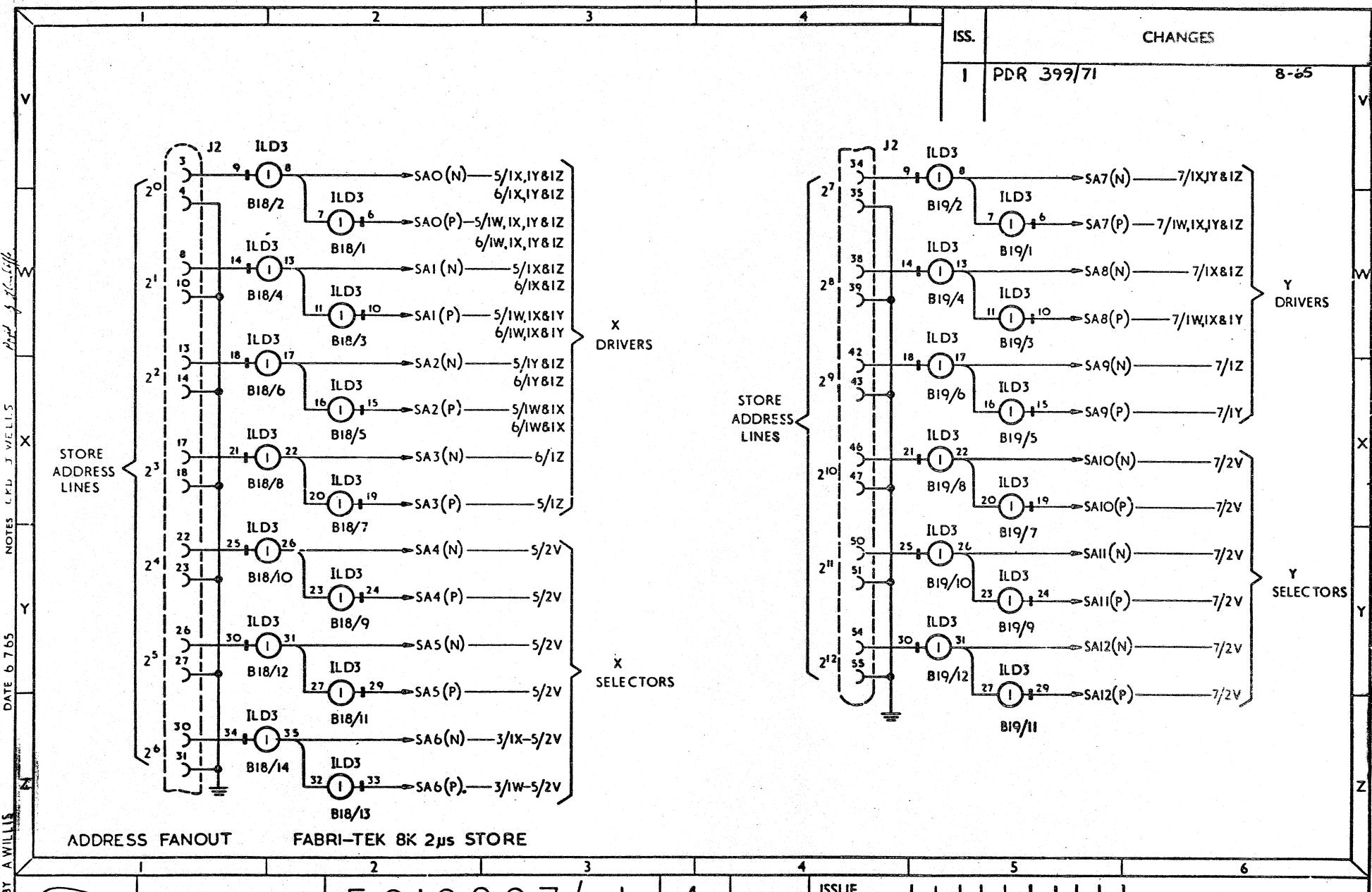
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SHEET | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 |

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.





I.C.T

LOGIC DIAGRAM

5010897/

DIAGRAM

ISSUE

4

SHEET

ISSUE

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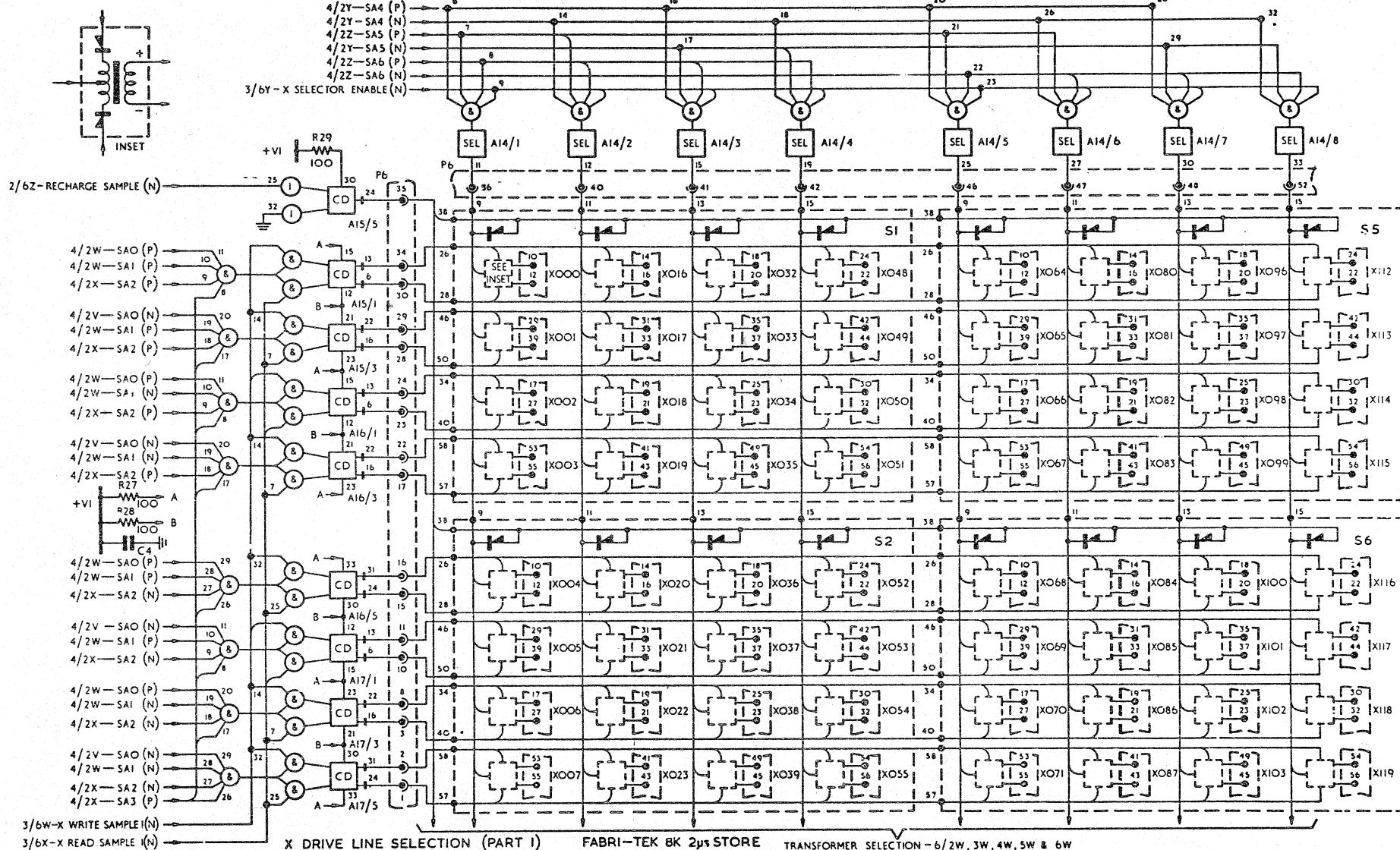
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ISS.

CHANGES

I PDR 399/71

8-65



LOGIC DIAGRAM

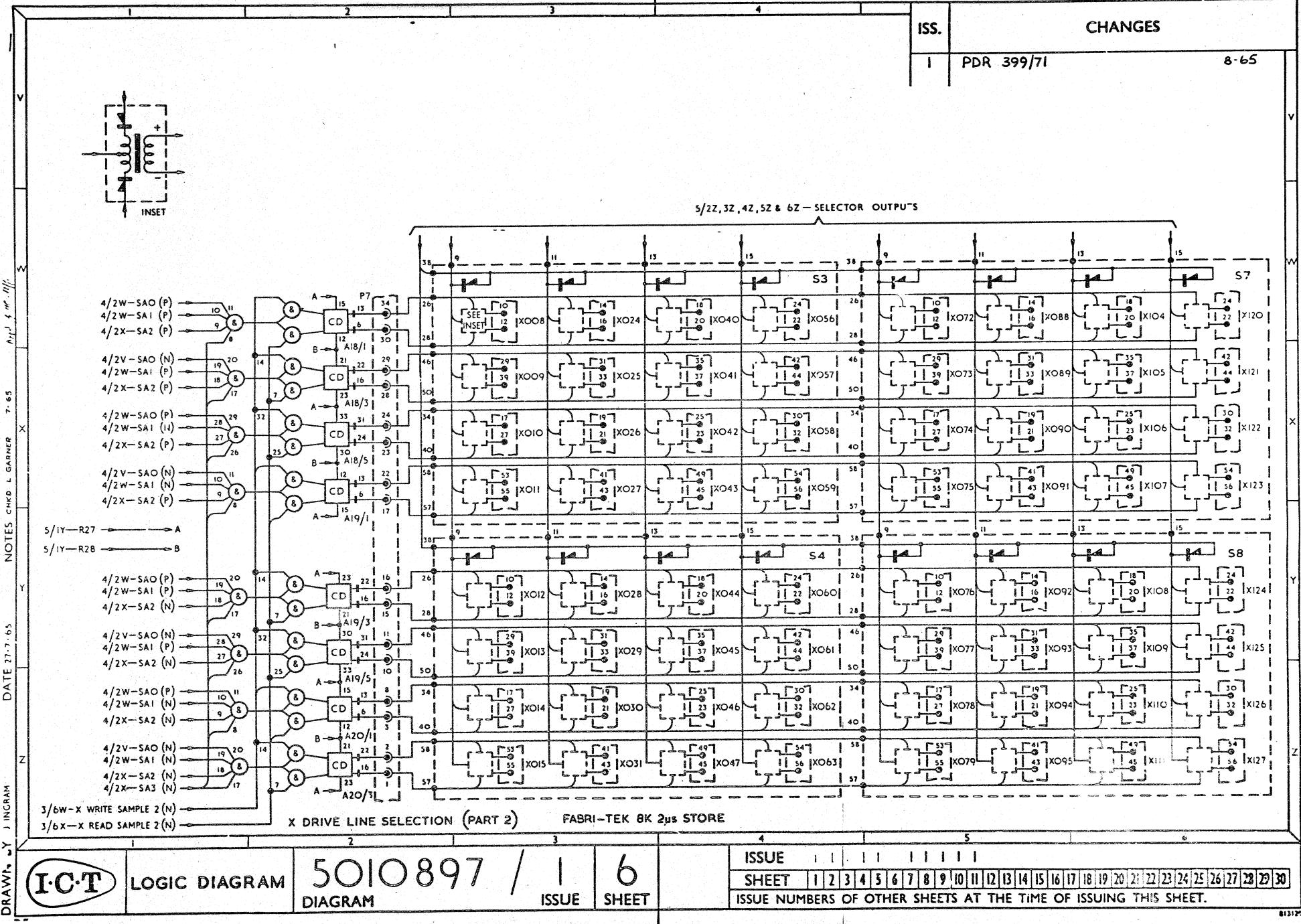
5010897 /

ISSUE SHEET

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I.C.T

LOGIC DIAGRAM

5010897 /

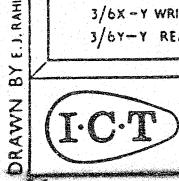
ISSUE

6

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.



LOGIC DIAGRAM

5010897 /

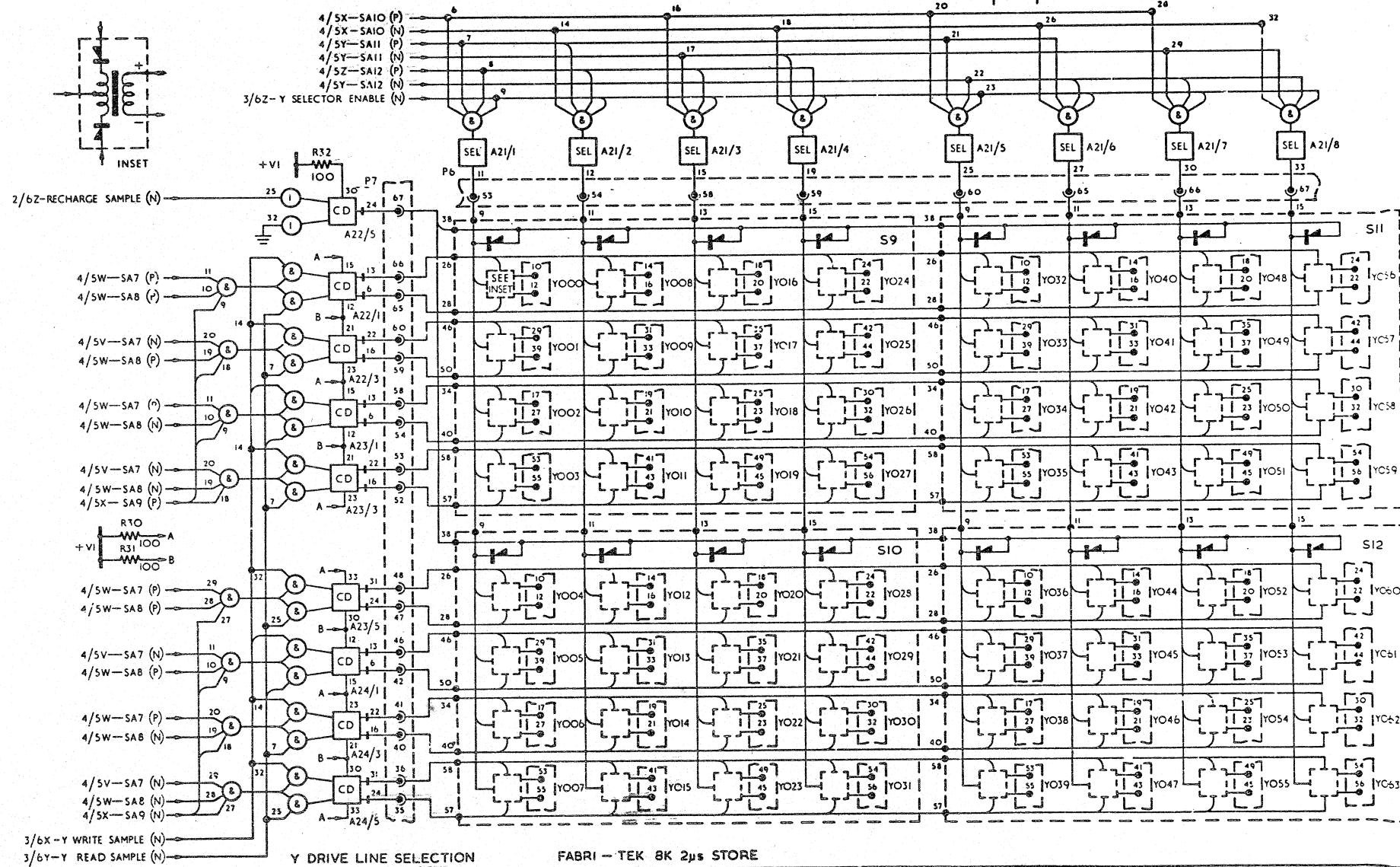
DIAGRAM

ISSUE 7 SHEET

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.						

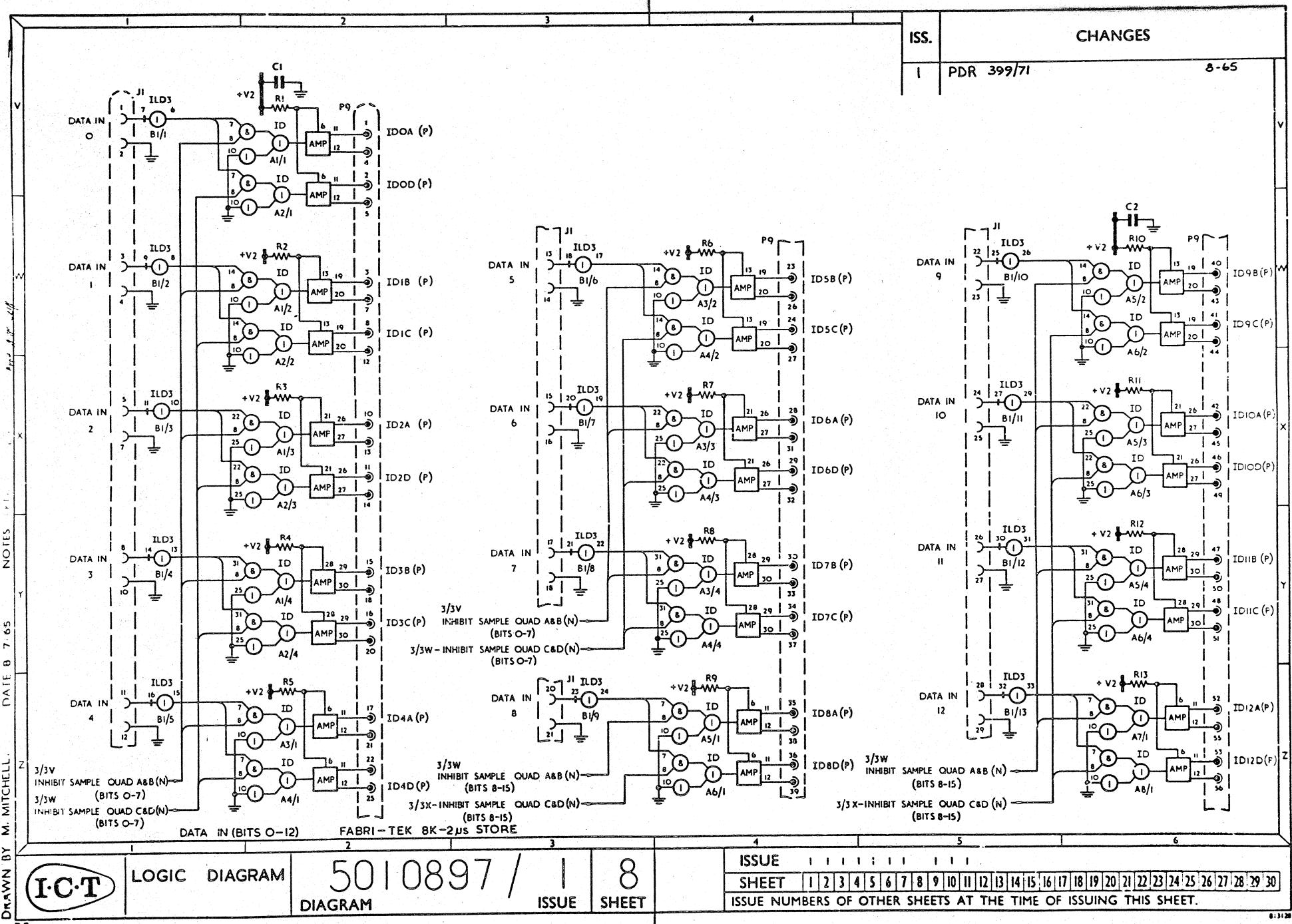
DRAWN BY E. J. RAHIM DATE 27.7.65

NOTES CHKD L GARNER



DRAWN BY M. MITCHELL

DATE 8/7/65 NOTES





LOGIC DIAGRAM

500897/

DIAGRAM

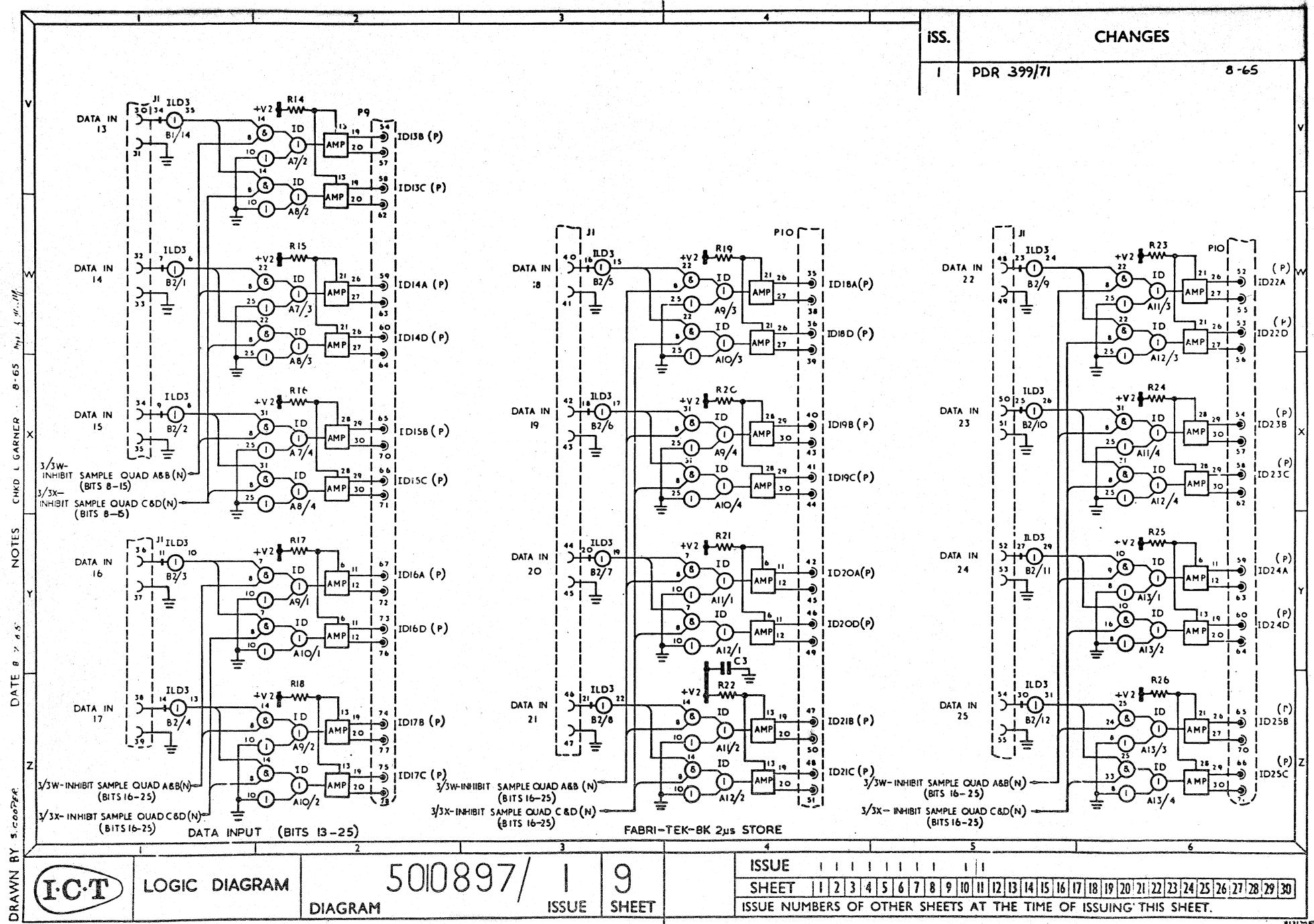
ISSUE

9 SHEET

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ISS. CHANGES

I PDR 399/71

8-65

V

W

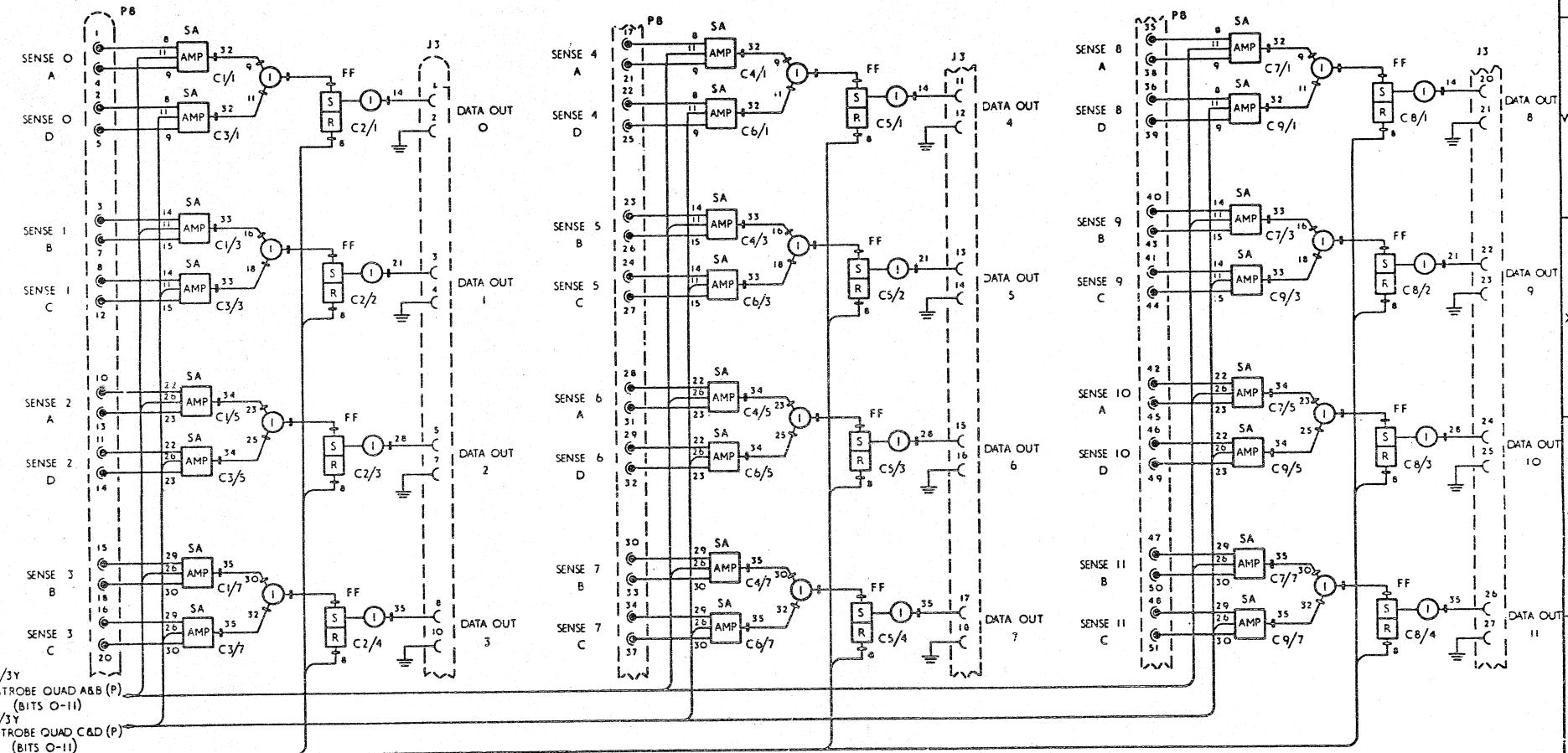
X

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Z

DATE

DRAWN BY G VACRAMAN



I.C.T.

LOGIC DIAGRAM

5010897 /

DIAGRAM

ISSUE

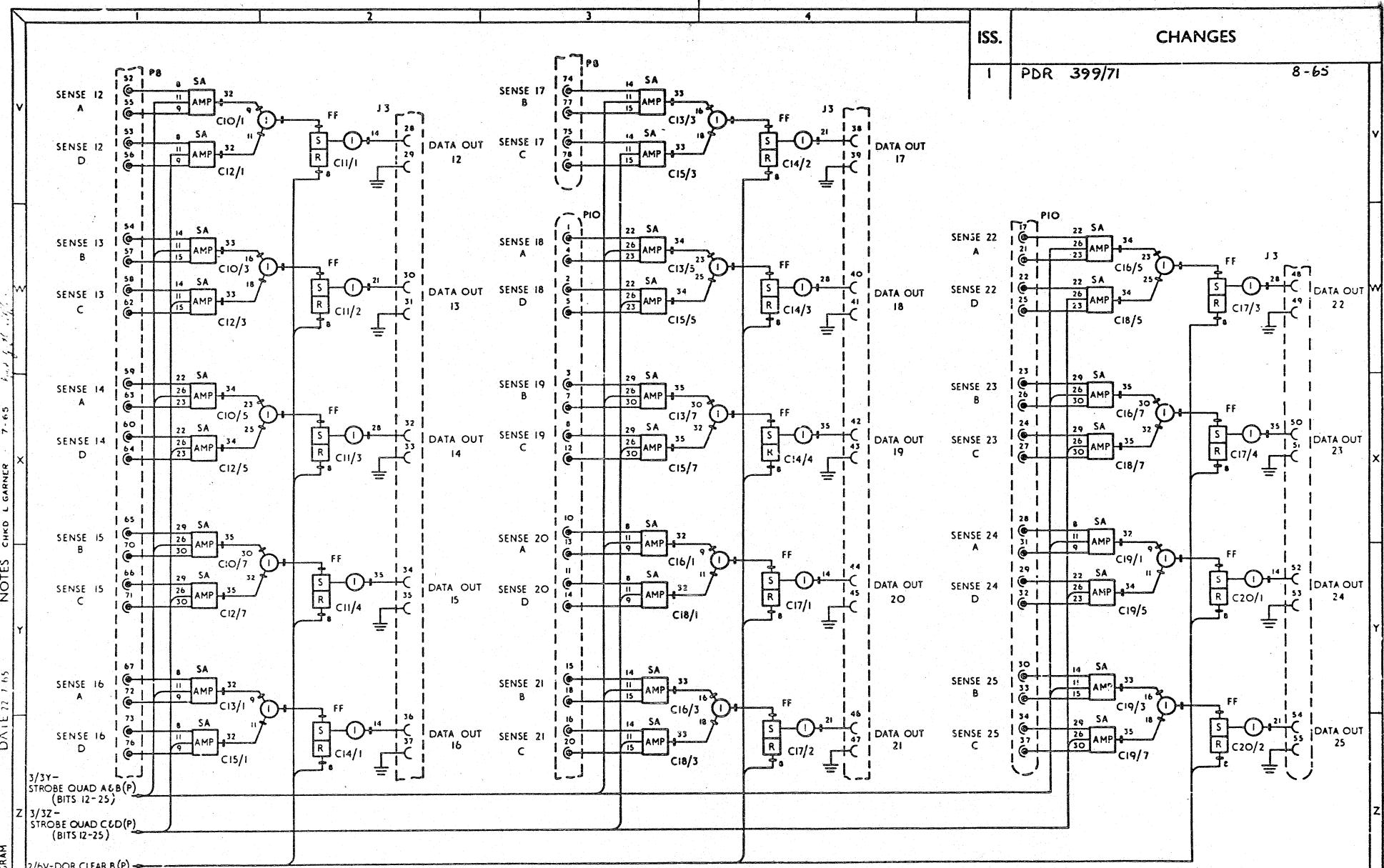
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

8-65



LOGIC DIAGRAM		5010897 /		ISSUE			
DIAGRAM		ISSUE		SHEET			
I.C.T.		1	2	3	4	5	6
		1	2	3	4	5	6

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

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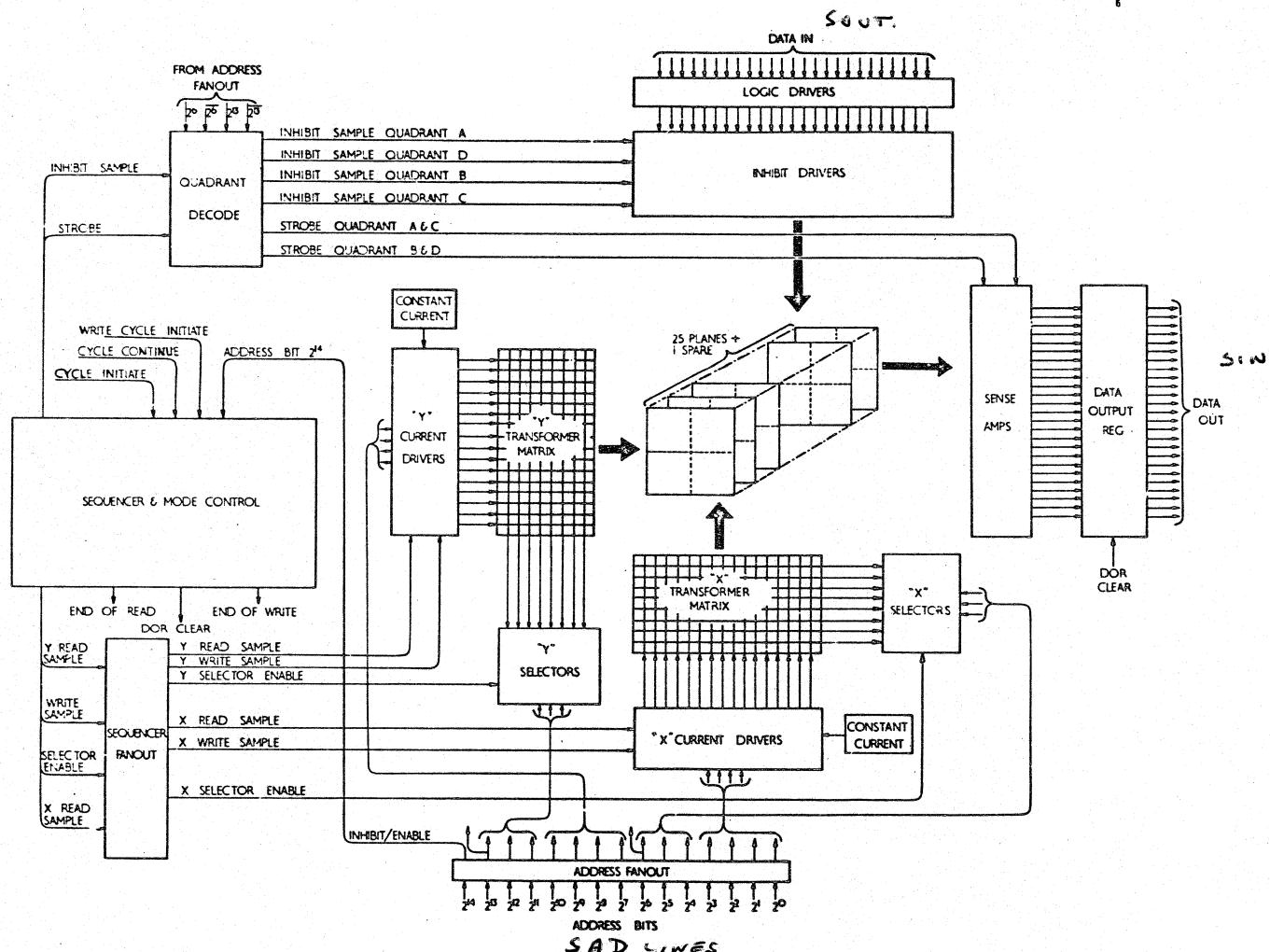
4

ISS

CHANGES

1 PDR 399/70

8-65



FABRI-TEK 16K 2μs STORE-BLOCK SCHEMATIC

LOGIC DIAGRAM

5010895 /

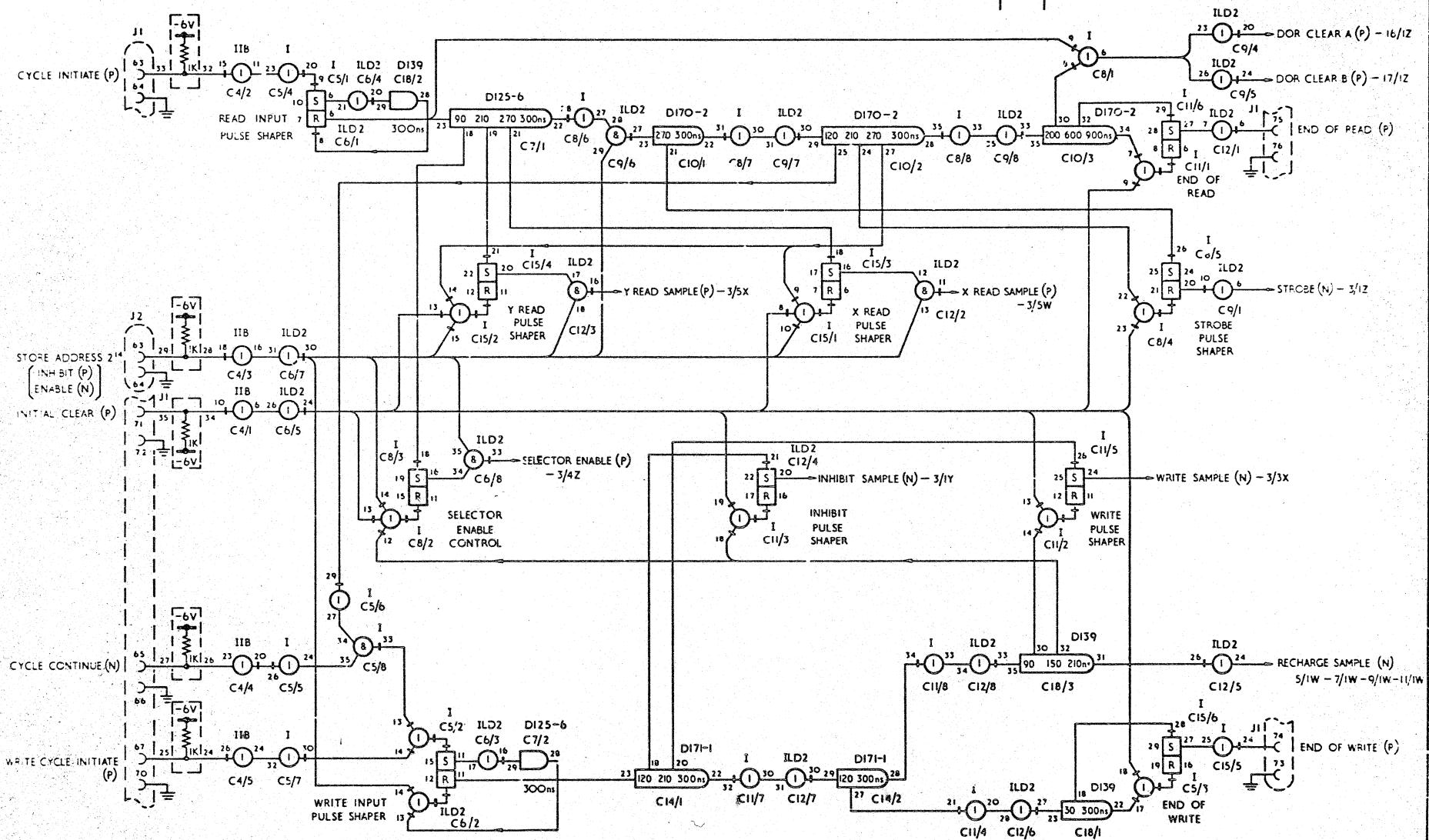
DIAGRAM

DRAWN TO
PROV. STD
7902101ISSUE
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

ISS

CHANGES

8 - 6



SEQUENCER AND MODE CONTROL FABRI-TEK 16K-2 μ s



LOGIC DIAGRAM

5010895

ISSUE

2
SHEET

DRAWN BY S COOPER DATE 4.6.65 NOTES

DRAWN
I.C.T.

SEQUENCER FANOUT FABRI - TEK 16K - 2 μ s

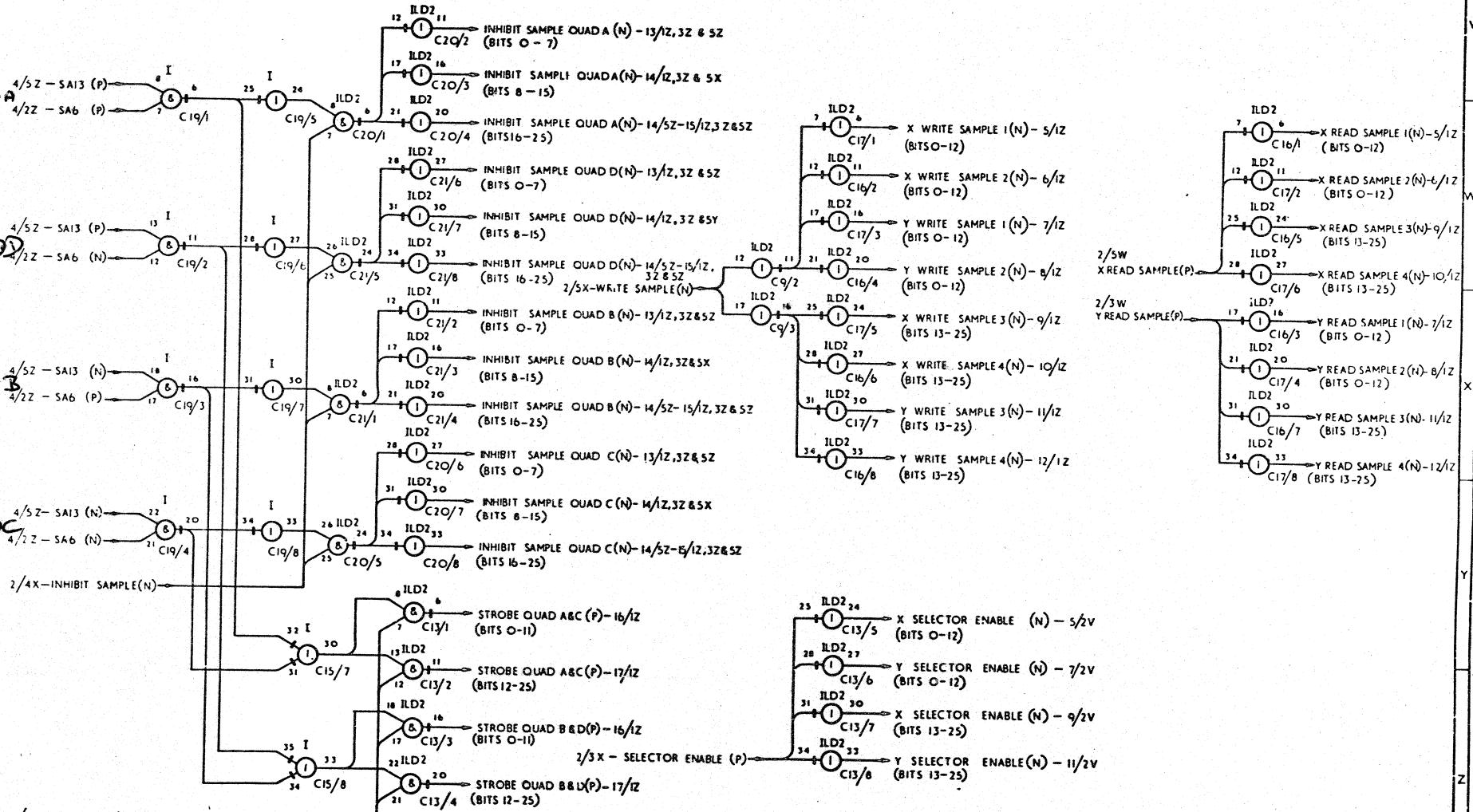
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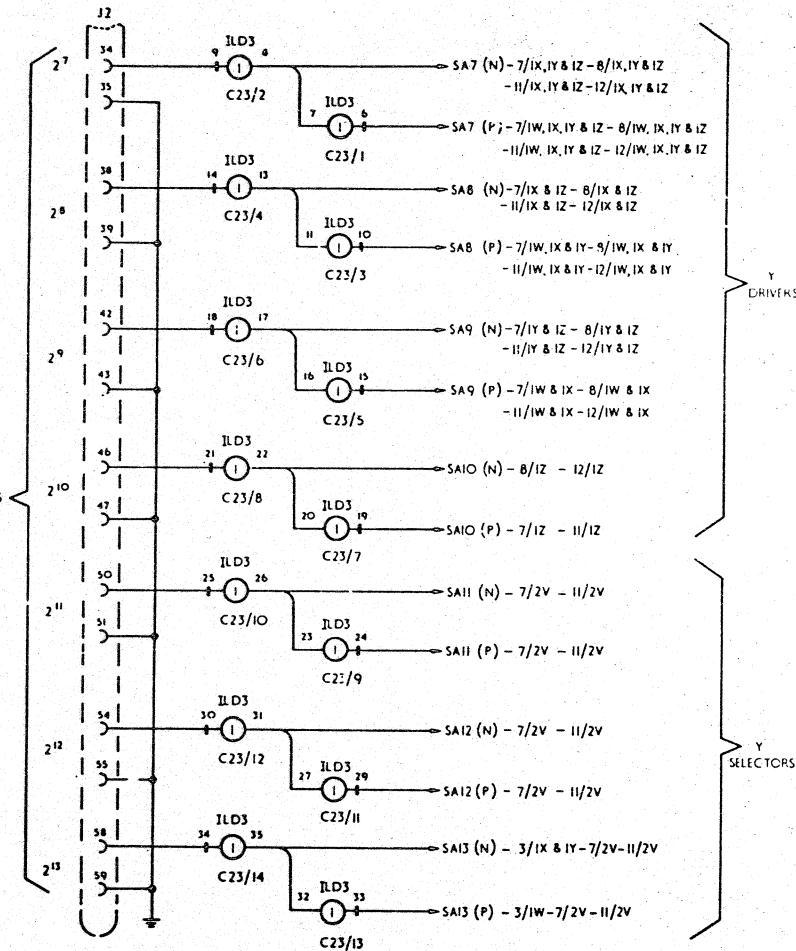
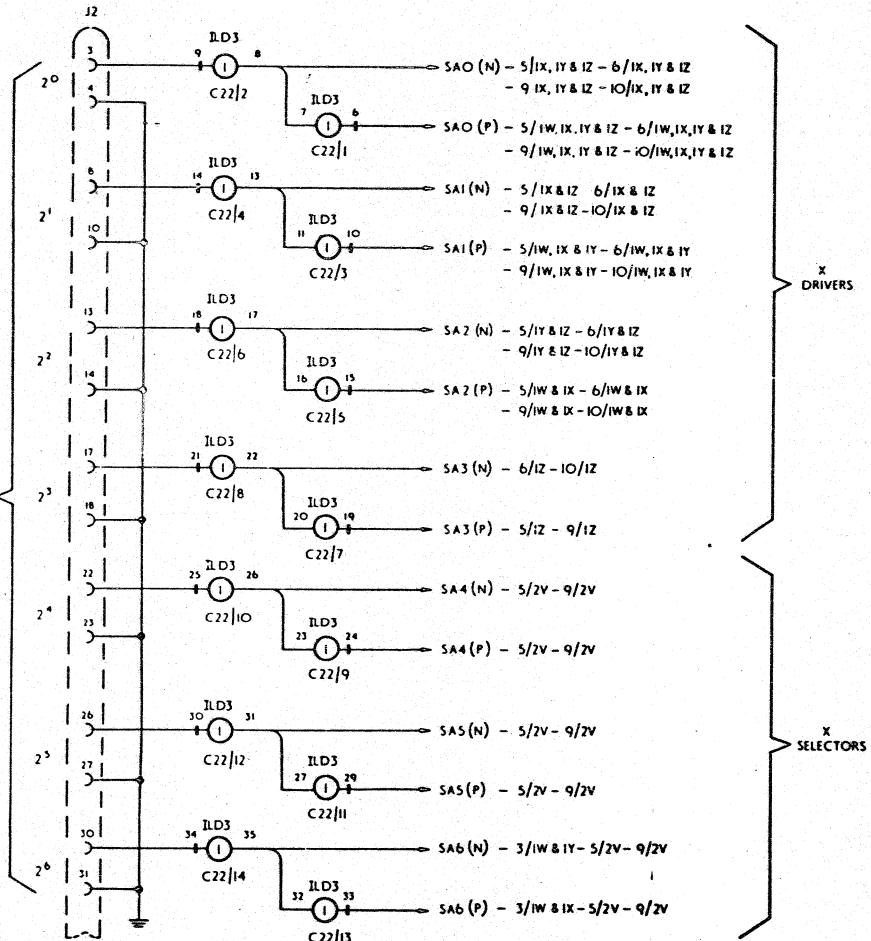
5010895 /
DIAGRAM

ISSUE 3 SHEET

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET





ADDRESS FANOUT FABRI-TEK 16K - 2 μ s

DRAWN BY
I.C.T.

LOGIC DIAGRAM

5010895/ | 4
DIAGRAM ISSUE SHEET

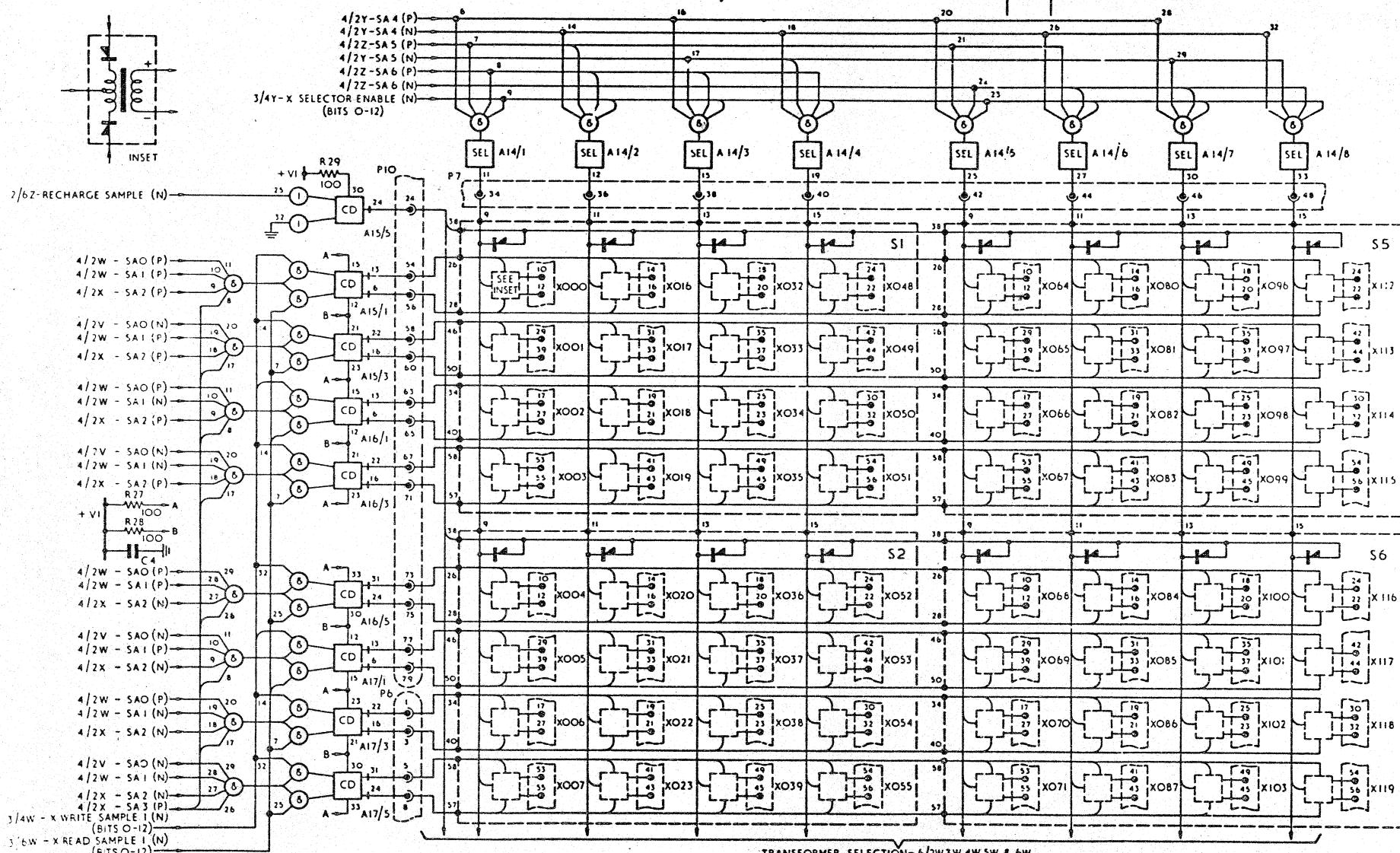
X DRIVE LINE SELECTION-BITS O-12 (PART 1)

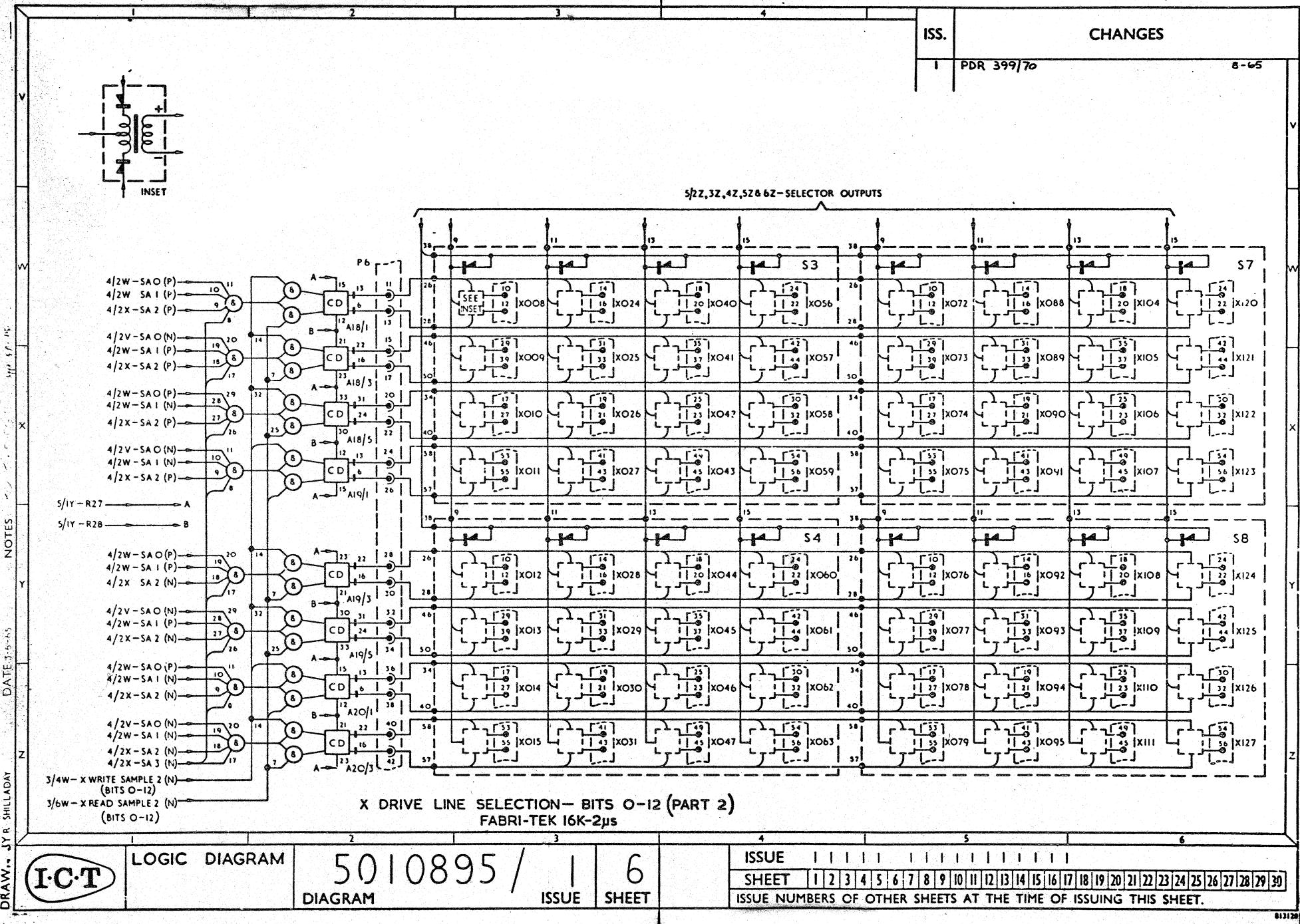
ISS.

CHANGES

1 PDR 399/70

8-65

FABRI-TEK 16K-2 μ s

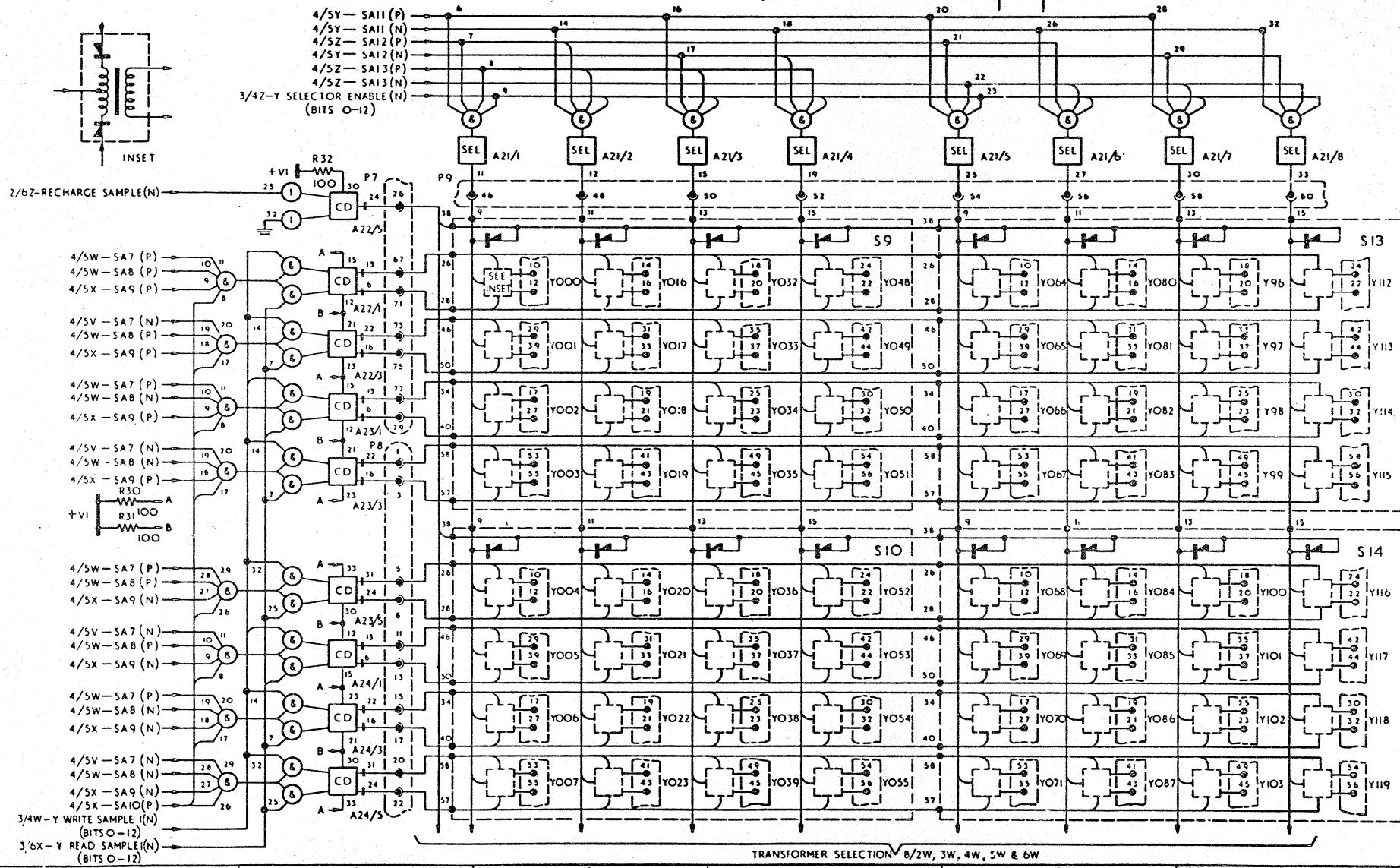


Y DRIVE LINE SELECTION - BITS 0-12 (PART I)
FABRI - TEK 16K - 2 μ s

ISS

CHANGES

8-65



TRANSFORMER SELECTION ✓ 8/2W, 3W, 4W, 5W & 6W

I.C.T

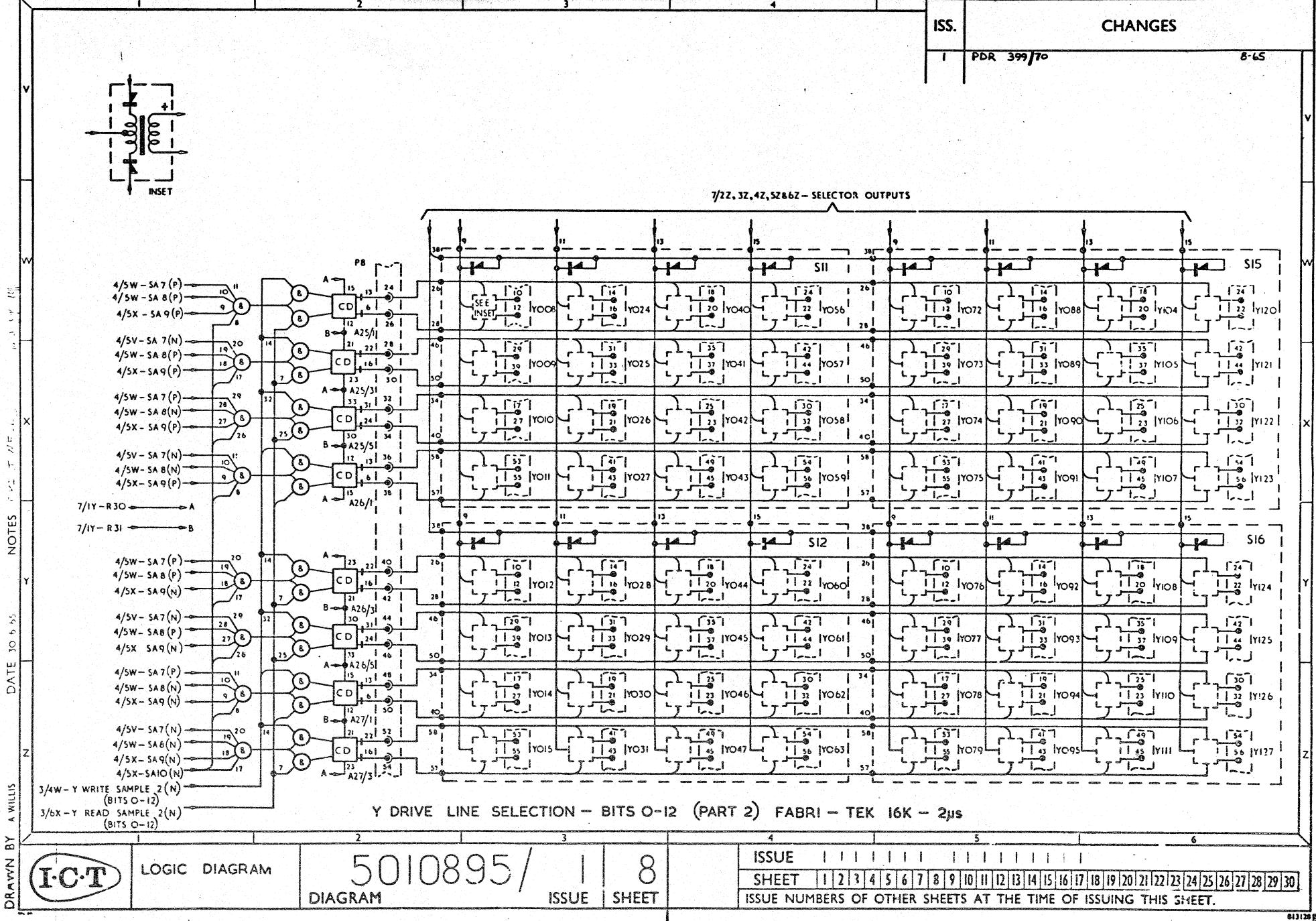
LOGIC DIAGRAM

5010895
DIAGRAM

1 7
ISSUE SHEET

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

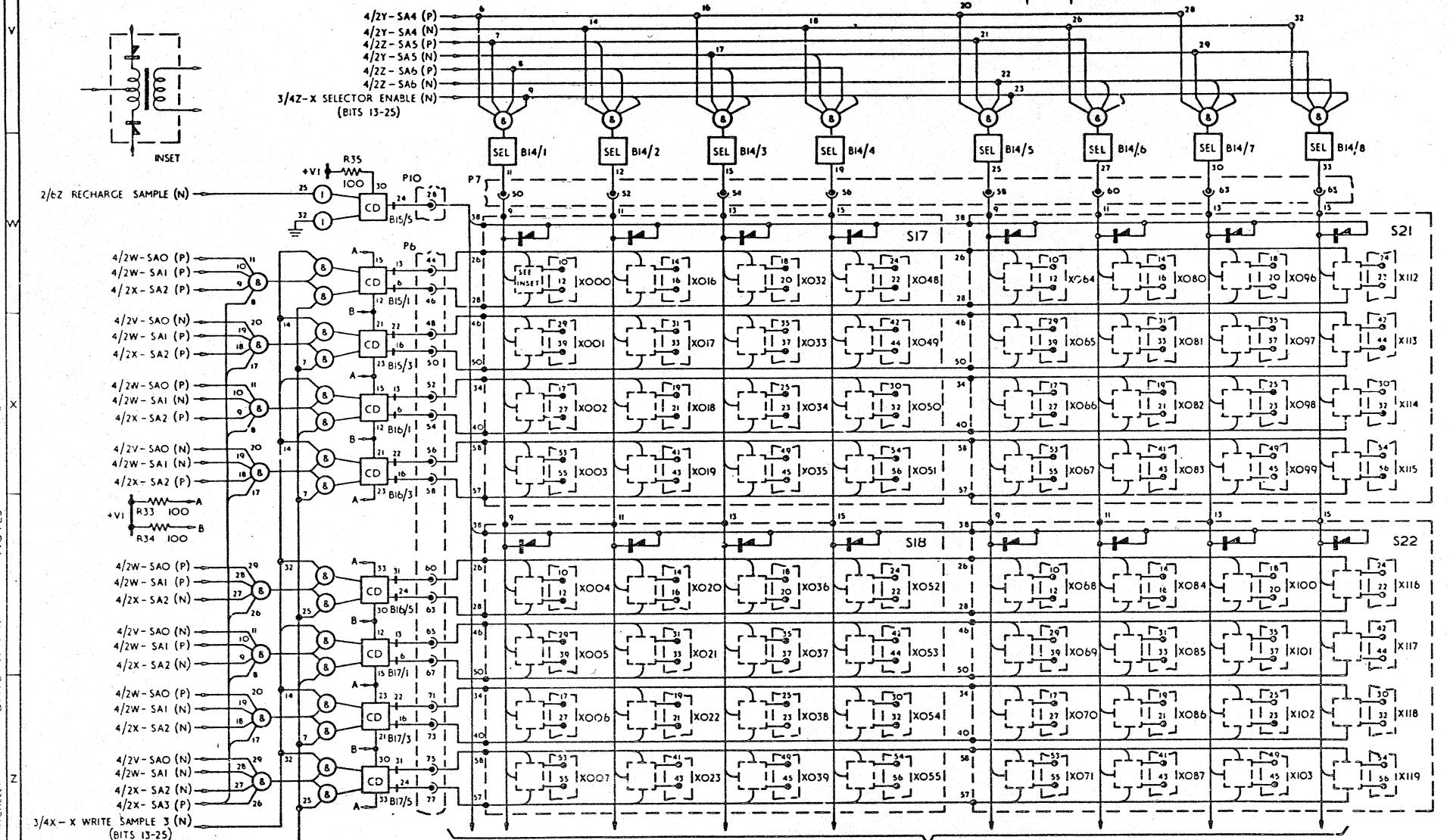


X DRIVE LINE SELECTION - BITS 13-25 (PART I)
FABRI-TEK 16K - 2 μ s

ISS.

CHANGES

8-6

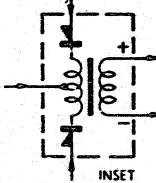


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I.C.T.

LOGIC DIAGRAM

5010895 /

| 9
ISSUE SHEET



INSET

9/2Z,3Z,4Z,5Z&6Z - SELECTOR OUTPUT

15

CHANGES

8-65

3/4X-X WRITE SAMPLE
(BITS 13-25)
3/6W-X READ SAMPLE
(BITS 13-25)

LOGIC DIAGRAM

5010895 /

10
ISSUE SHEET

ISSUE | | | | | | | | | | | | | | |

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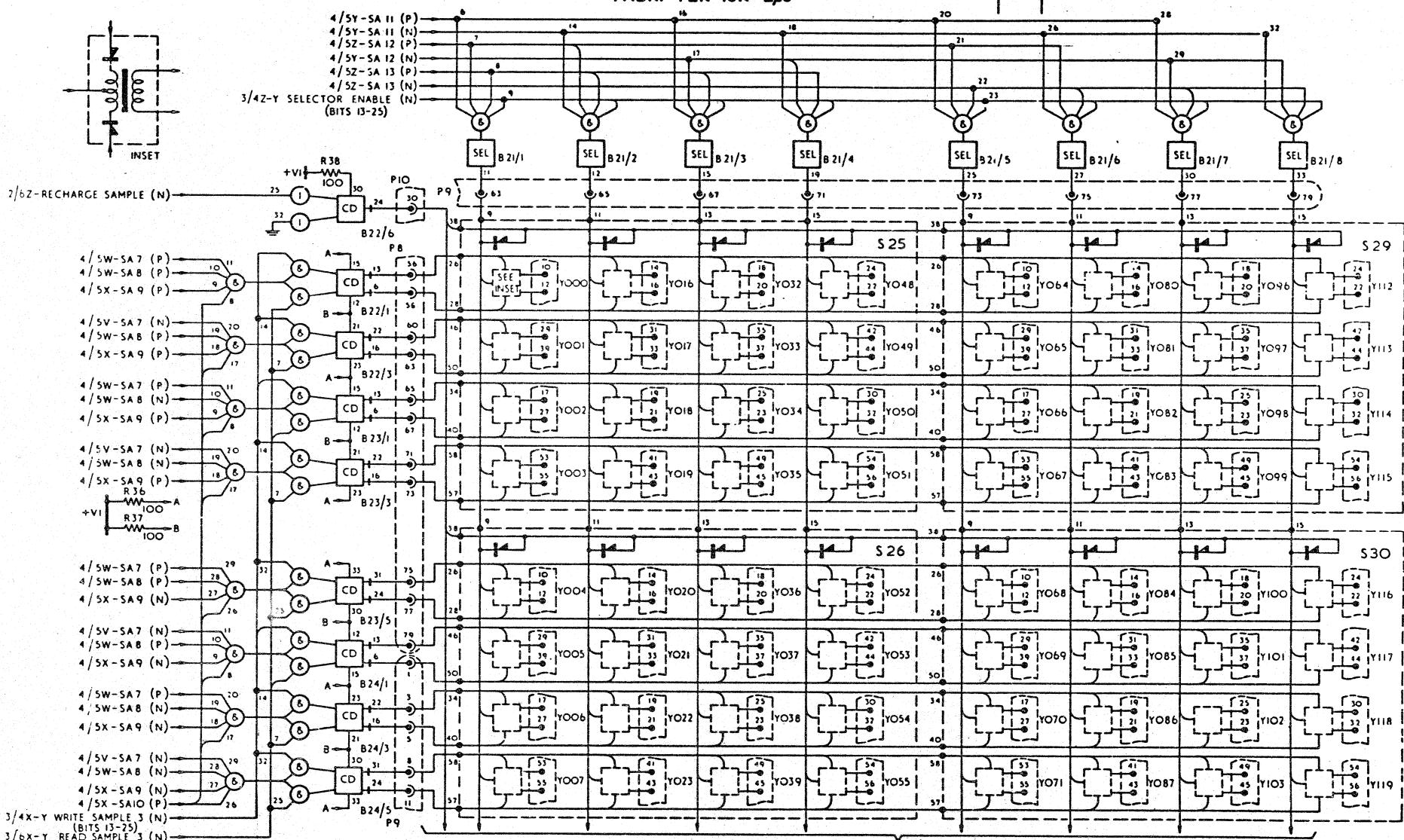
Y DRIVE LINE SELECTION - BITS I3-25 (PART I)
FABRI-TEK 16K-2 μ s

ISS.

CHANGES

I PDR 399/70

8-65



LOGIC DIAGRAM

5010895 /

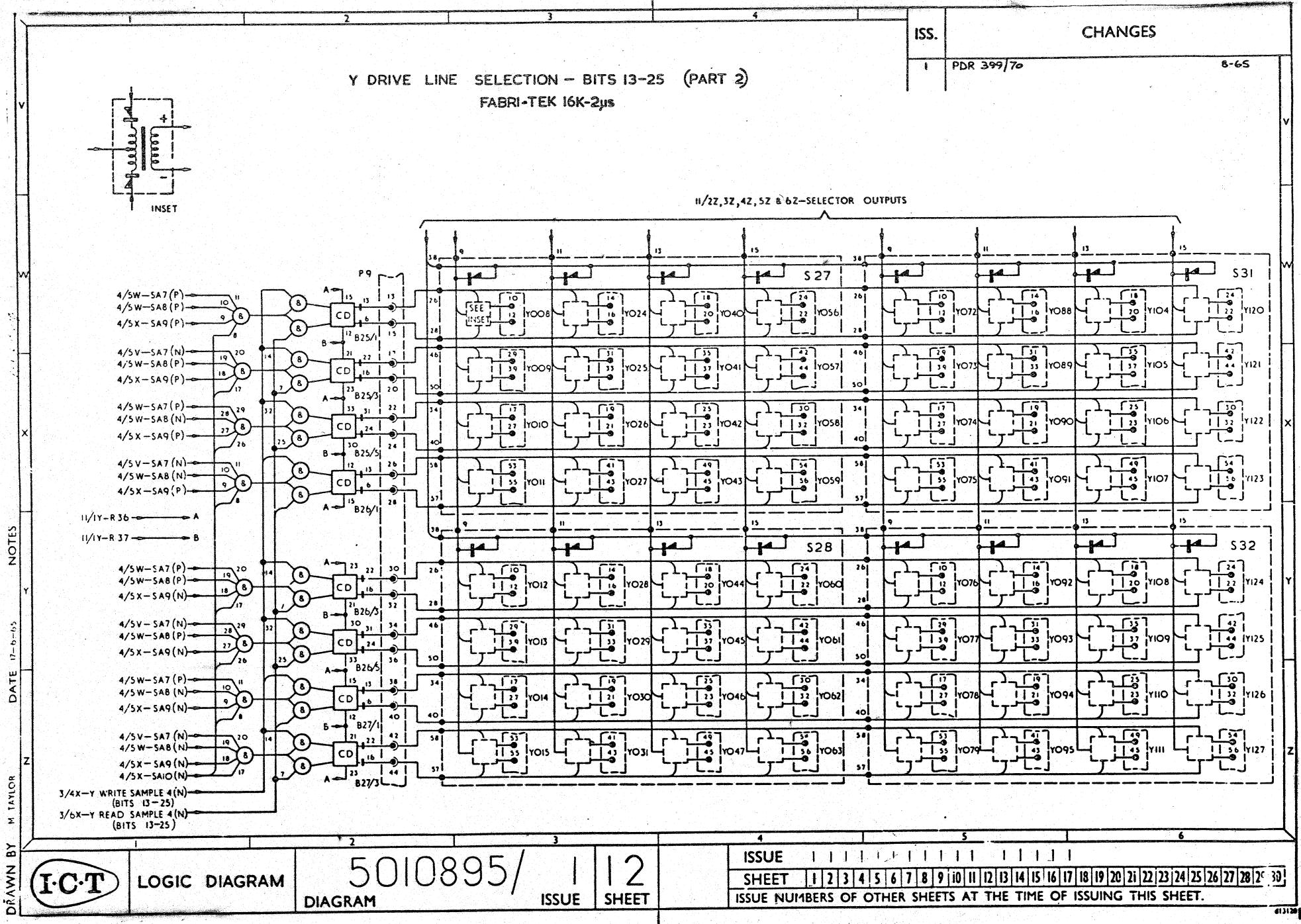
DIAGRAM

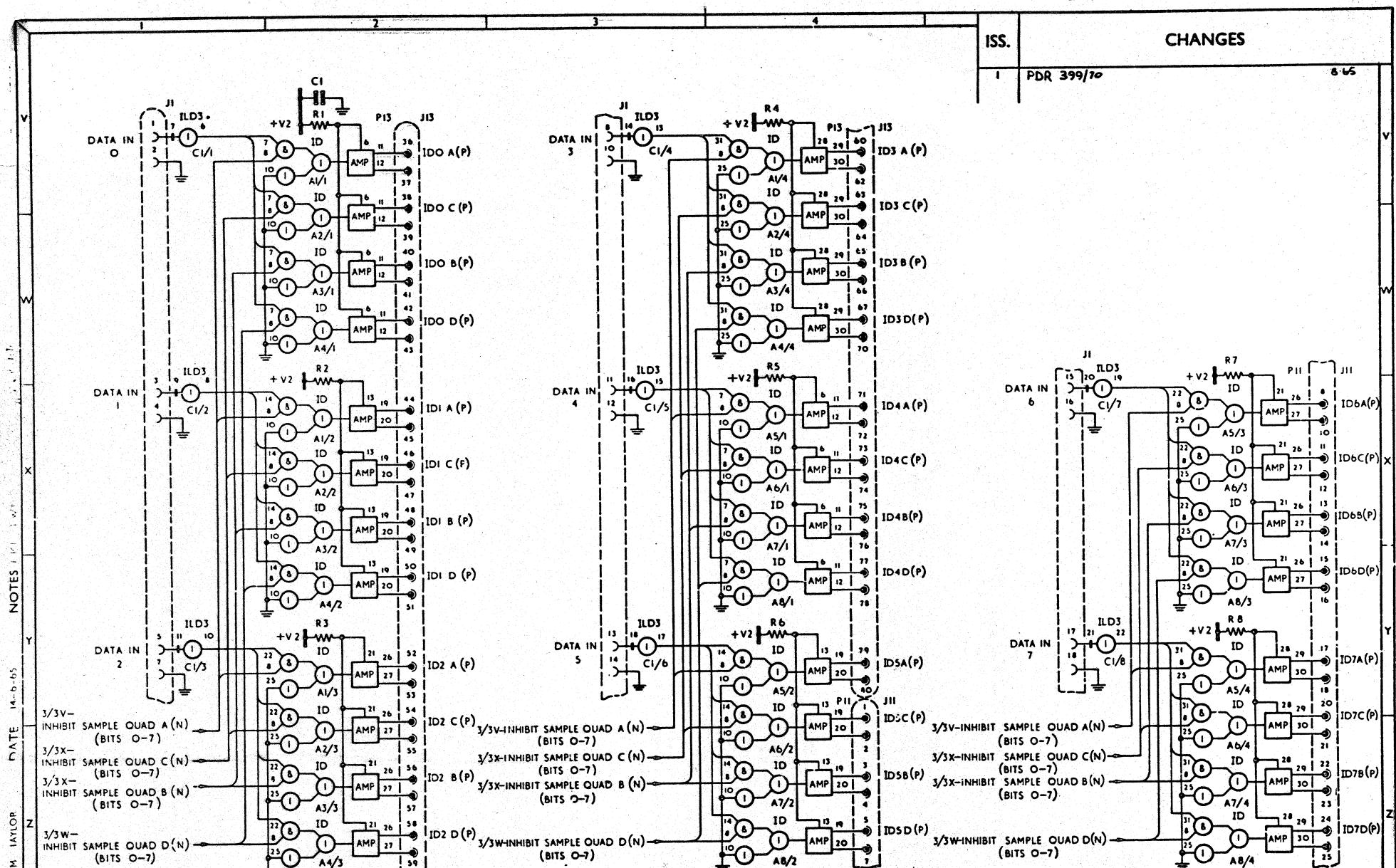
ISSUE SHEET

ISSUE

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.





DATA INPUT (BITS 0-7) FABRI-TEK 16K-2μs

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LOGIC DIAGRAM

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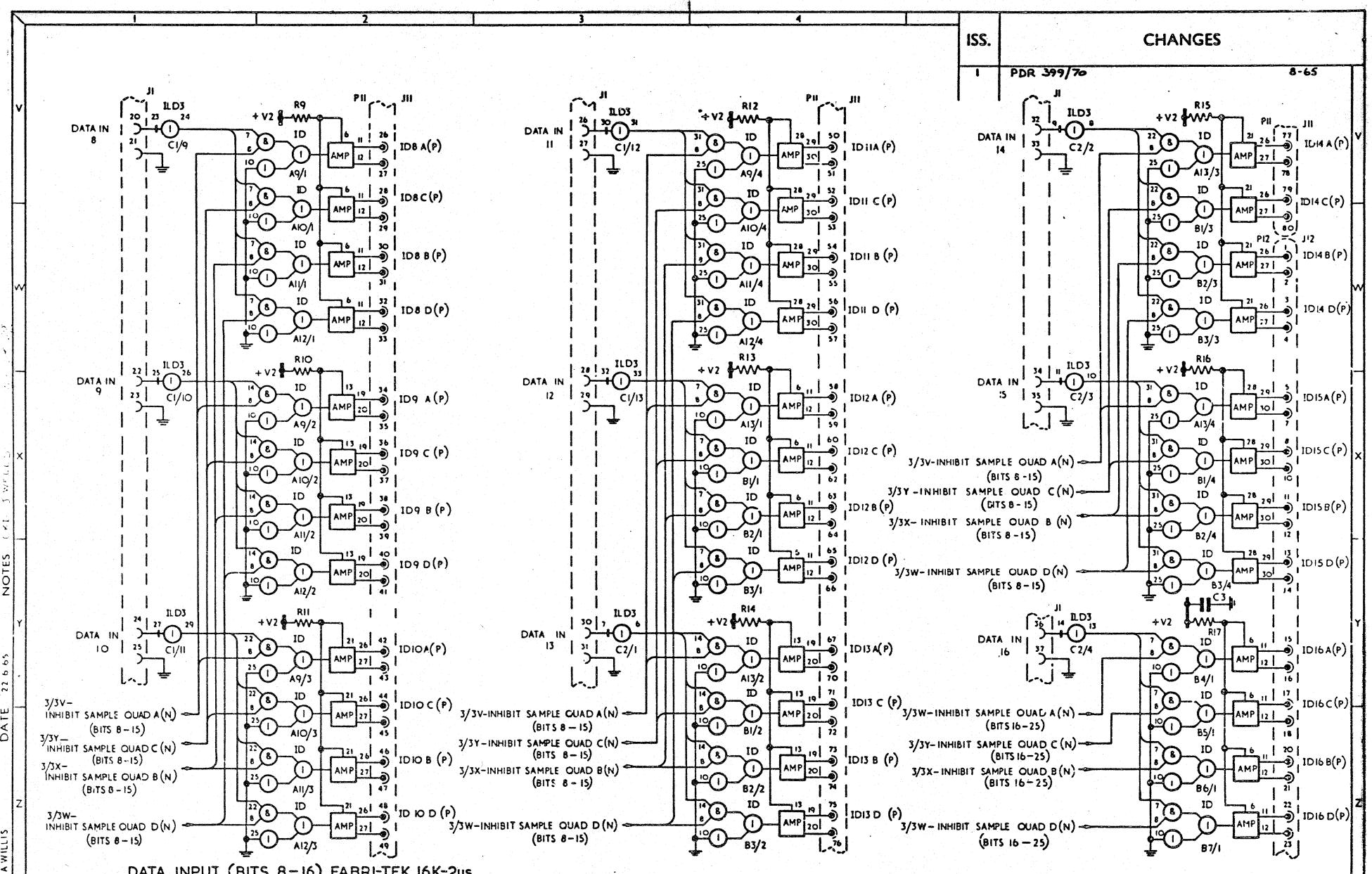
DIAGRAM

ISSUE SHEET

ISSUE

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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.



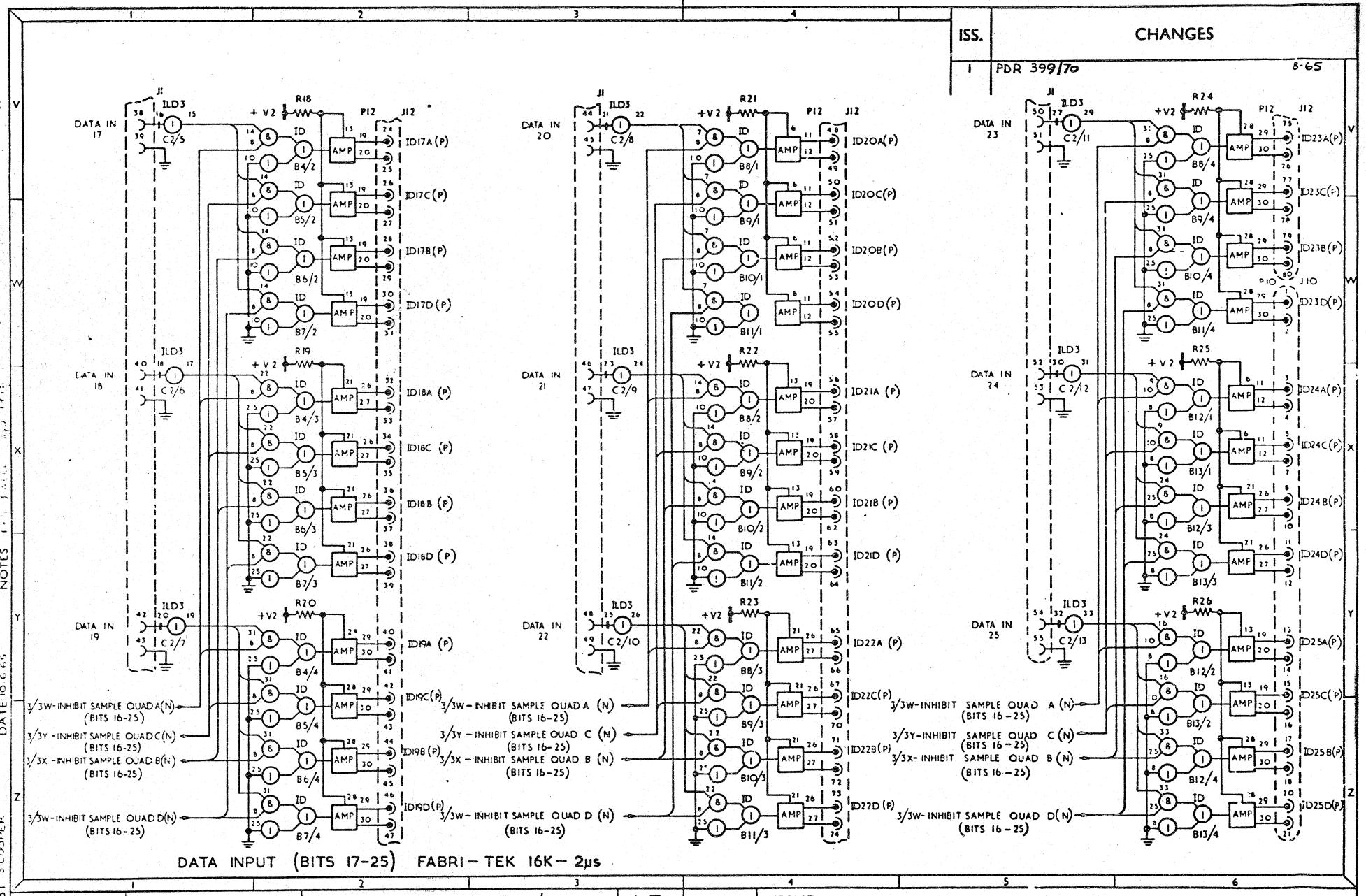
DATA INPUT (BITS 8-16) FABRI-TEK 16K-2μs



LOGIC DIAGRAM

5010895,
DIAGRAM

ISSUE SHEET



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I.C.T.

LOGIC DIAGRAM

5010895 /

ISSUE

15

SHEET

ISSUE

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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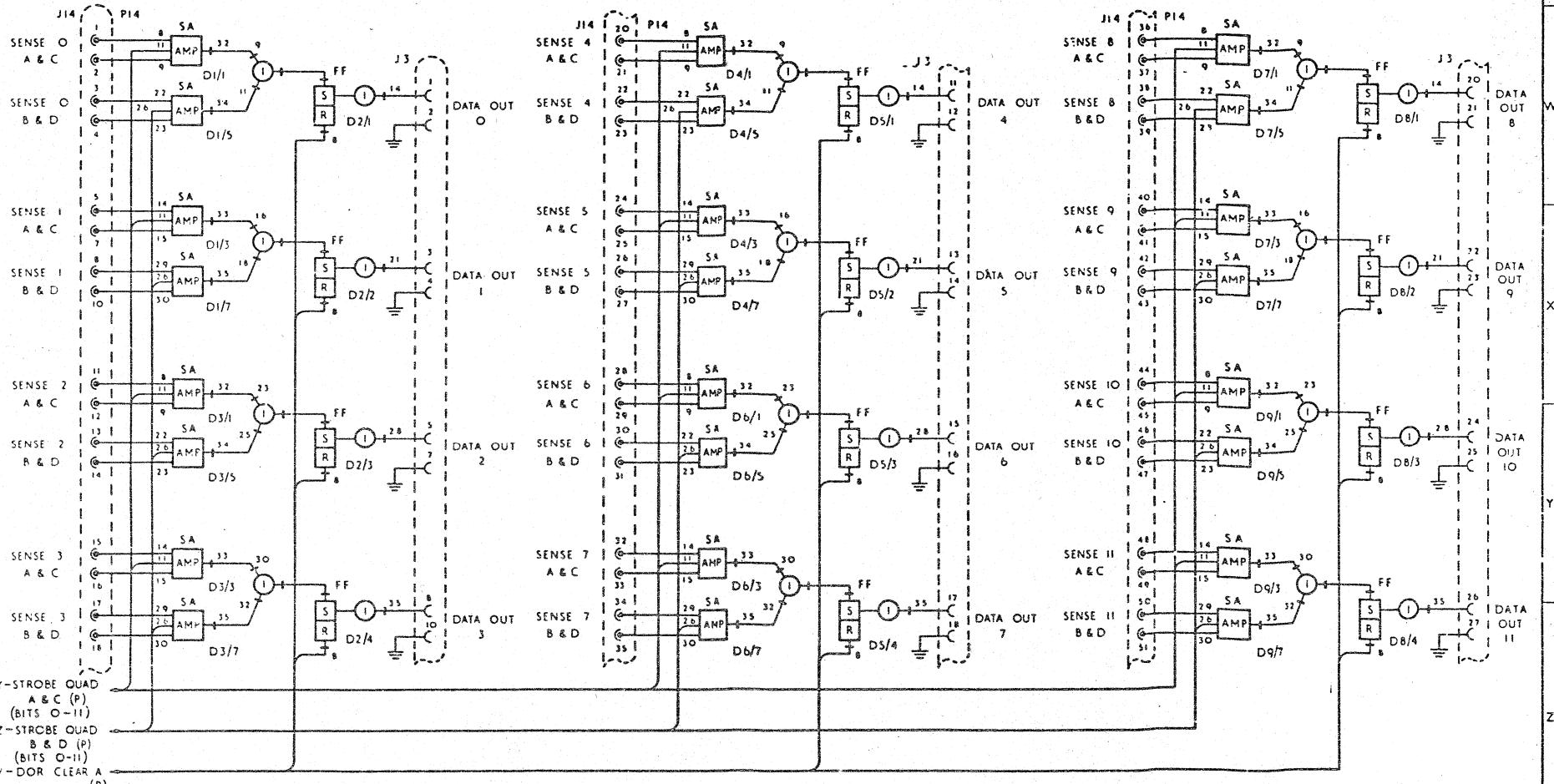
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

ISS.

CHANGES

I PDR 399/70

8-65



LOGIC DIAGRAM

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DIAGRAM

ISSUE

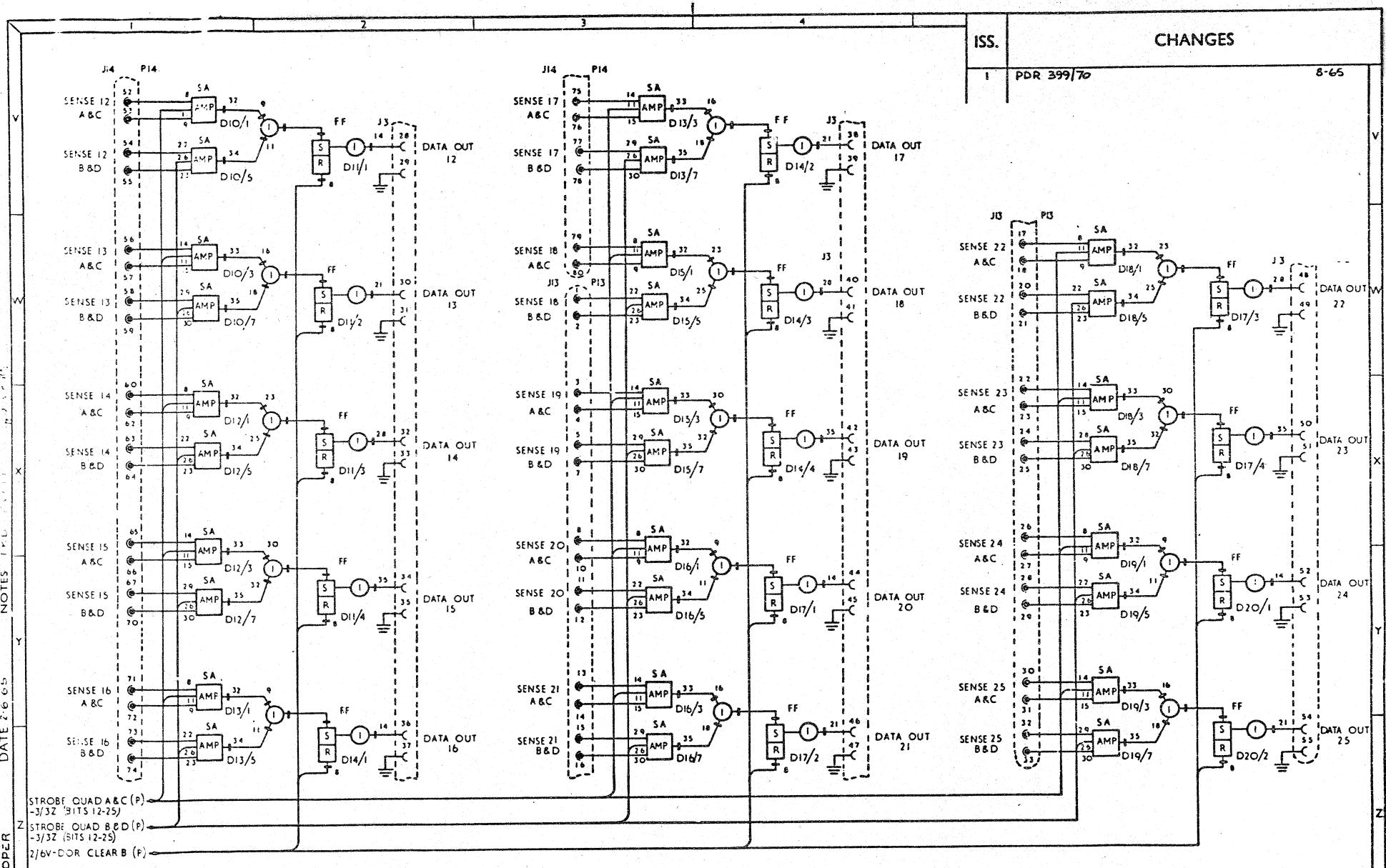
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SHEET

613126

ISSUE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.



DATA OUTPUT (BITS 12-25) FABRI-TEK 16K-2μs

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LOGIC DIAGRAM

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DIAGRAM

ISSUE SHEET

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.