

Historical Document

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FSD  
PARALLEL PROCESSOR  
DEVELOPMENT  
PROJECT

IBM

Federal Systems Division  
Los Angeles, California

IBM PROPRIETARY

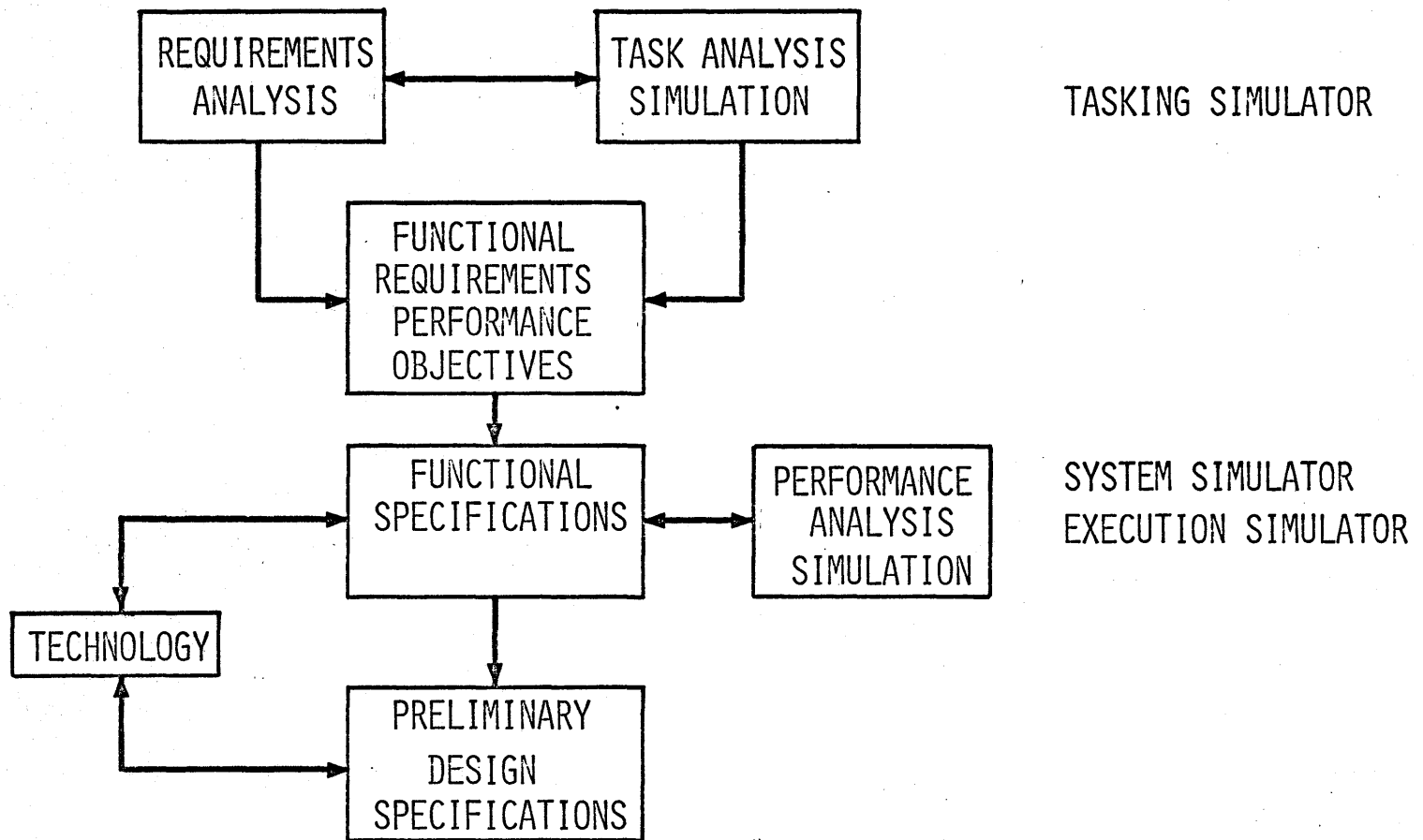
FSD  
PARALLEL PROCESSOR  
DEVELOPMENT  
PROJECT

*prepared in April 1968  
for presentation to Corp.  
Technical Committee.*

*Machine was never built.*

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Federal Systems Division  
INTERNATIONAL BUSINESS MACHINES CORPORATION  
8665 Wilshire Boulevard  
Beverly Hills, California 90211



TASKING SIMULATOR

SYSTEM SIMULATOR  
EXECUTION SIMULATOR

PROJECT PLAN - DESIGN BY SIMULATION

## PROBLEMS WITH INHERENT PARALLEL STRUCTURE

### LINEAR PROBLEMS

- o MATRIX ARITHMETIC
- o SYSTEMS OF EQUATIONS
- o LINEAR PROGRAMMING

### ORDINARY DIFFERENTIAL EQUATIONS

### PARTIAL DIFFERENTIAL EQUATIONS

### INTEGRAL TRANSFORMS

- o FOURIER ANALYSIS
- o AUTOCORRELATION AND SPECTRAL ANALYSIS

### OPTIMIZATION

### SIMULATION

## PROBLEM AREAS IN CONTINUUM MECHANICS

### FLUID MECHANICS

- METEOROLOGY
- OCEANOGRAPHY

### SOLID MECHANICS

- GEOPHYSICS
- SOIL MECHANICS

### NUCLEAR PHYSICS

- REACTOR DESIGN
- WEAPONS DESIGN

### MIXED PROBLEMS

- WEAPONS EFFECTS

PROBLEM RUNNING TIMES AND STORAGE

	<u>TYPE CODE</u>	<u>MESH SIZE</u>	<u>HOURS ON 6600</u>	<u>HOURS ON ILLIAC</u>	<u>STORAGE (KILOWORDS)</u>	<u>USE</u>
1.	2-D Plastic-Elastic (AFTON)	200 x 136	1,000	20	1,300	Ground Shock
2.	2-D Hydro (SHELL)	201 x 105	35	.7	130	Fireball Rise Shock Interactions
3.	3-D MHD	155 x 77 x 77 201 x 105 x 105	900 3,500	18 70	5,600 13,000	Fireball Rise Debris Distribution
4.	1-D Trans (no scatt, no ret)	150	100	2	104	Early Fireball
5.	1-D Trans scatt & ret	150	2,000	40	500	Weapon Output
6.	2-D Diffusion (RADISH5)	155 x 77	2,000	40	200	Debris Motion
7.	2-D Trans (TRASH) (no scatt, no ret)	100 x 50 (x2) 155 x 77 (x5)	1,500 22,000	30 440	$10^3$ $10^4$	Weapon Output Ground Interaction
8.	2-D Trans (TRASH) scatt & ret	100 x 50 (x2) 155 x 77 (x5)	30,000 440,000	600 8,800	$4 \times 10^4$ $4 \times 10^5$	Weapon Output Ground Interaction
9.	2-Body, 6-D Nonlocal Boltzmann Eq.	50 x 50 x 50 x 50 x 50 x 50	$10^{10}$ years		$10^8$	Geomagnetic Region Nonthermal Debris Expansion

## PROBLEM ANALYSIS STUDY PROCEDURE

### COLLECT PROBLEMS/CODES

- BREAK PROBLEMS DOWN TO TASK LEVEL
- CATALOGUE TASK PROPERTIES
- COMPARE PROBLEM FEATURES

### TASK PERFORMANCE SIMULATION

- STUDY PROBLEM MODELS
- DEVELOP TASK MANAGEMENT SCHEMES

### RESULTS

- GROSS SYSTEM SPECIFICATIONS
- GENERAL PROGRAM DESIGN TECHNIQUES
- PROGRAMMING/MACHINE TRADEOFF INFORMATION

## TASKING SIMULATOR USE

### PROBLEM ANALYSIS

- ① USE ACTUAL CODES
- ② IDENTIFY TASKS

### CODE TRACING

- ① EXECUTE CODE
- ② OBTAIN TASK INSTRUCTION COUNTS

### SIMULATION

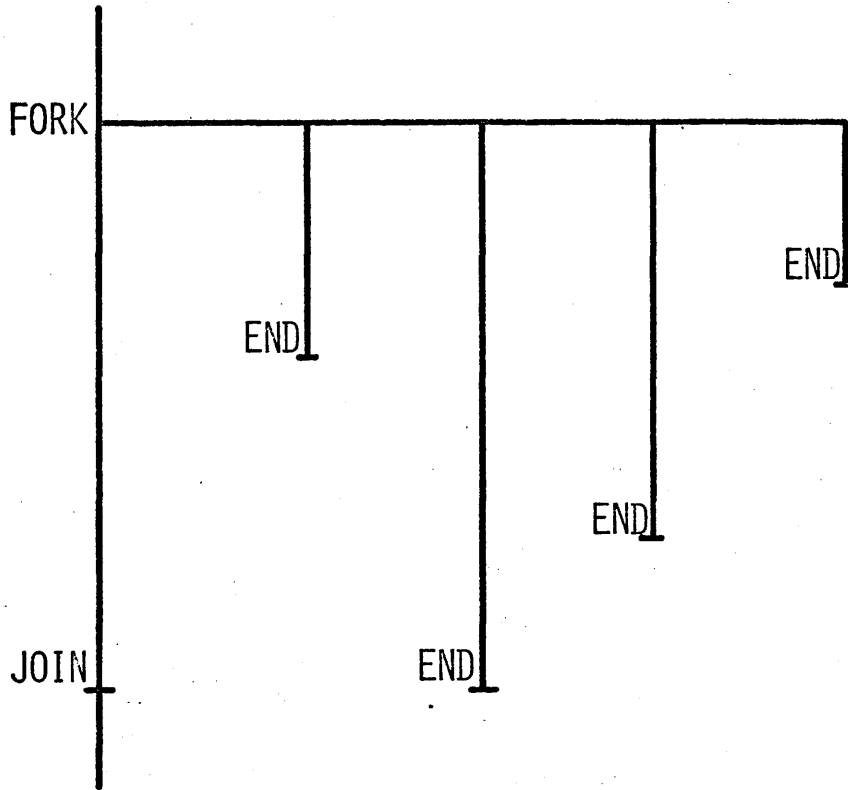
- ① WRITE PROGRAM
- ② RUN SIMULATOR, EXAMINE RESULTS

### TASKING SIMULATOR USED TO TEST

- ① APPLICATION PROGRAM STRATEGY
- ② TASK MANAGEMENT ALGORITHM



## FORK/JOIN PROCESS

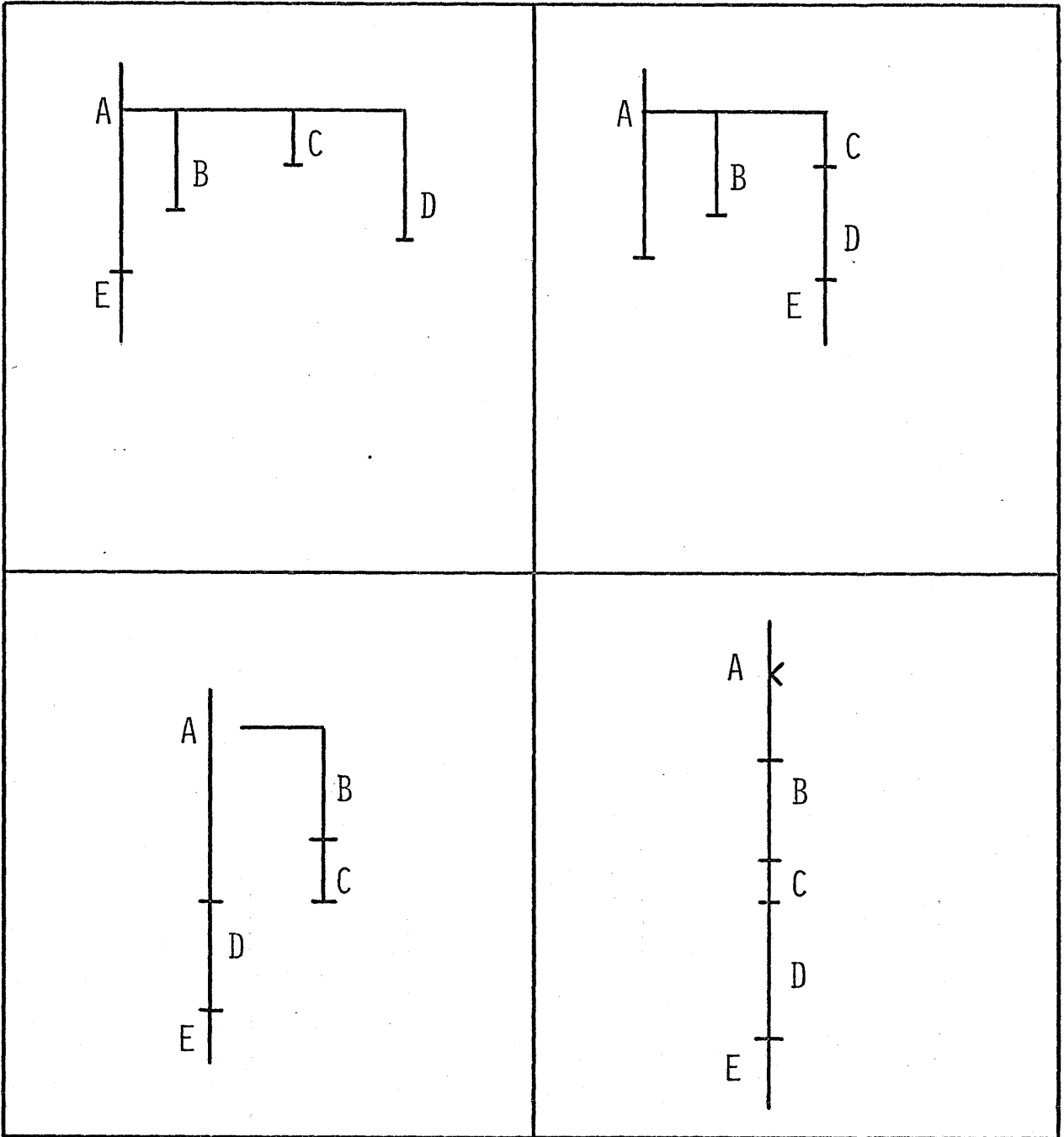


A FORK IS A CALL FOR THE ASYNCHRONOUS  
EXECUTION OF A SET OF TASKS

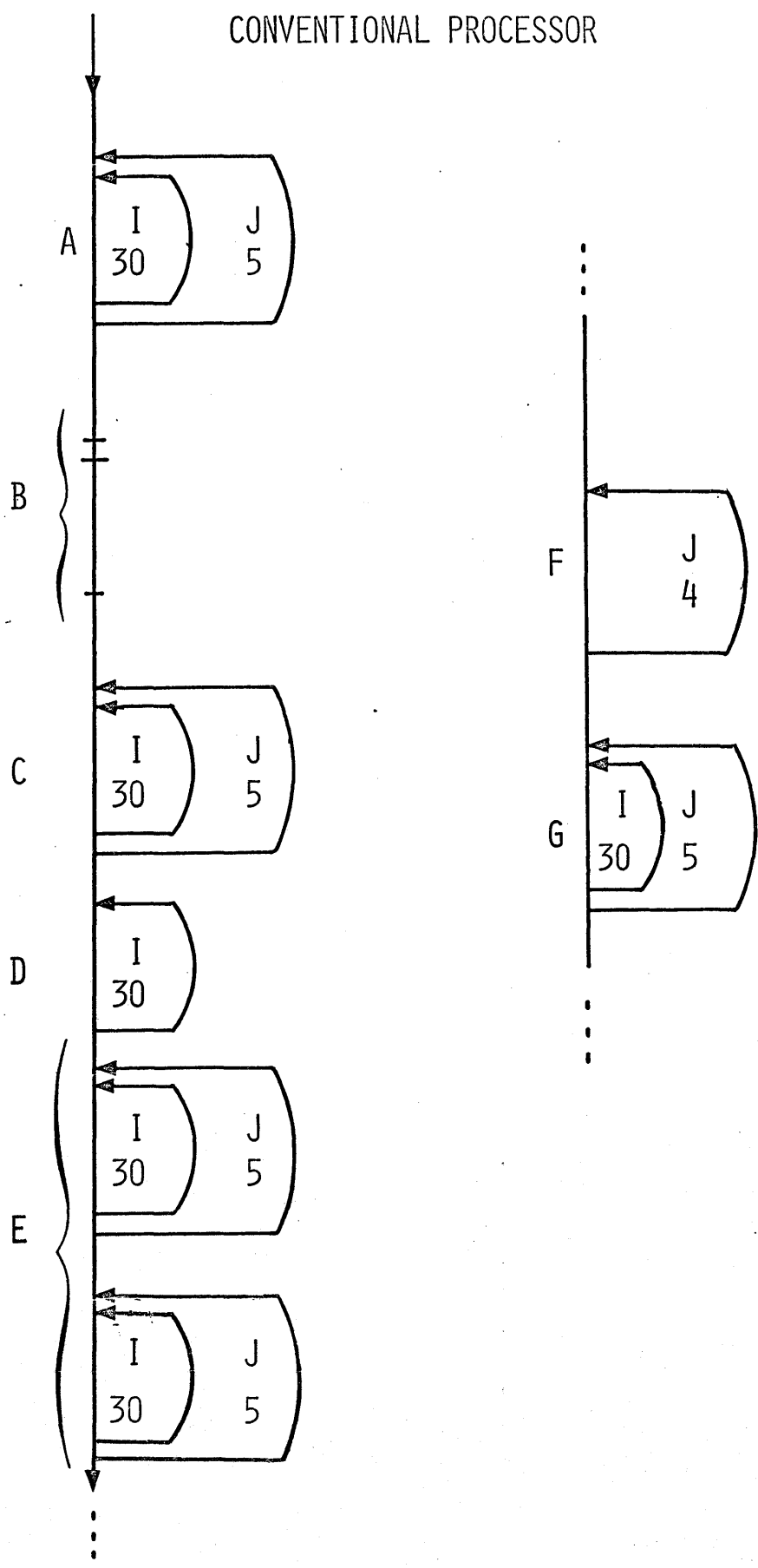
END DENOTES END OF A TASK

A JOIN IS A CALL FOR SYNCHRONIZATION  
ON COMPLETION OF A FORK

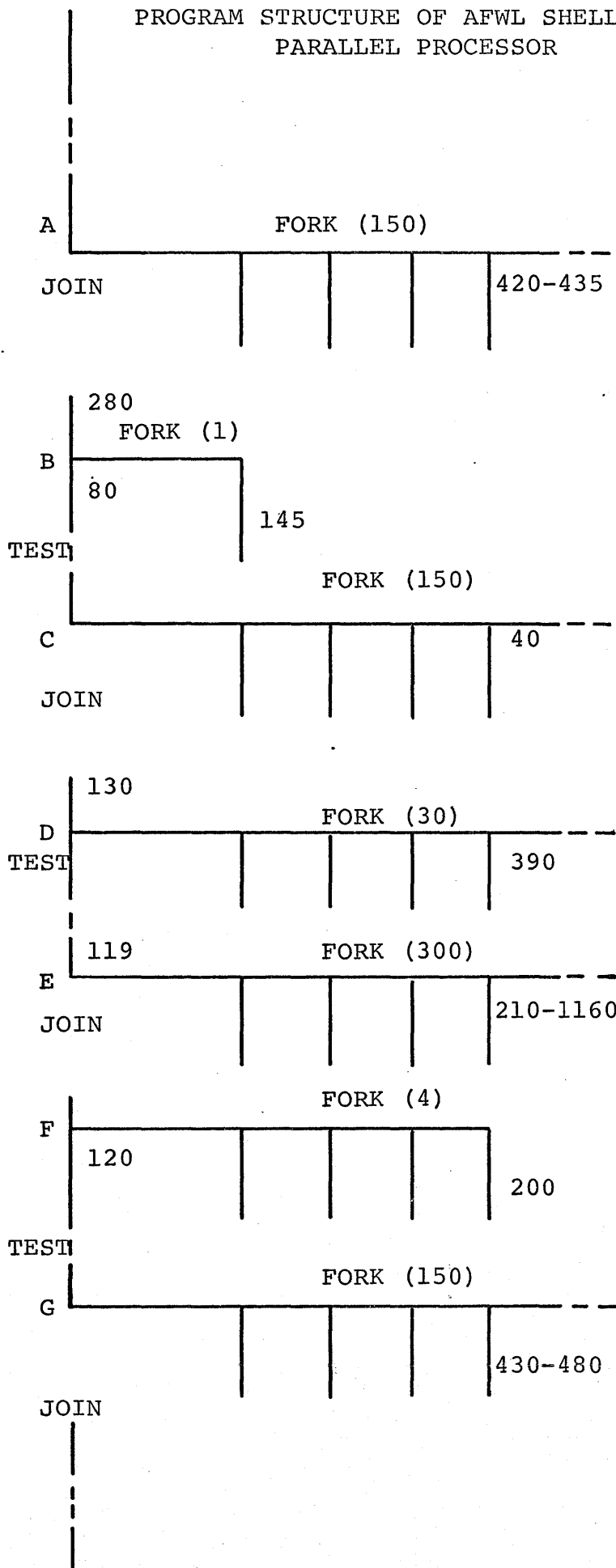
# SAMPLE TASK SCHEDULING ALGORITHM



SHELL CODE STRUCTURE  
CONVENTIONAL PROCESSOR



PROGRAM STRUCTURE OF AFWL SHELL CODE  
PARALLEL PROCESSOR



PROCESSOR EVENT TRACE

TIME	DELTA	TASKS	G/C	PROCESSORS
13938	1	0 I	I	AAAAAAAAAA AAAAA I
13939	1	0 I	I	AAAAAAAAAA .AAAA I
13940	1	0 I	I	AAAAAAAAAA AAAA I
13941	1	0 I	I	AAAAAAAAAA .AAA I
13942	1	0 I	I	AAAAAAAAAA AAA I
13943	1	0 I	I	AAAAAAAAAA .AA I
13944	1	0 I	I	AAAAAAAAAA AA I
13945	1	0 I	I	AAAAAAAAAA .A I
13946	1	0 I	I	AAAAAAAAAA A I
13947	1	0 I	I	AAAAAAAAAA . I
13948	1	0 I	I	AAAAAAAAAA I
13950	2	0 I	I	.AAAAAAAAA I
13951	1	0 I	I	AAAAAAAAA I
13952	1	0 I	I	.AAAAAAA I
13953	1	0 I	I	AAAAAAA I
13954	1	0 I	I	.AAAAAA I
13955	1	0 I	I	AAAAAA I
13956	1	0 I	I	.AAAAA I
13957	1	0 I	I	AAAAA I
13958	1	0 I	I	.AAAA I
13959	1	0 I	I	AAAA I
13960	1	0 I	I	.AAA I
13961	1	0 I	I	AAA I
13962	1	0 I	I	.A I
13963	1	0 I	I	A I
13964	1	0 I	I	. I
13965	1	0 I	I	. I
13966	1	1 I	I	. I
13967	1	1 I	I	. I
13968	1	0 I	I	. I
14228	28	0 I	I	A I
14950	50	0 I	I	. I
14951	1	150 I	I	..... I
14951	0	150 I	I	..... I
14952	1	150 I	I	..... I
14953	1	149 I	I	..... I
14954	1	148 I	I	..... I
14955	1	147 I	I	..... I
14956	1	146 I	I	..... I
14957	1	145 I	I	..... I
14958	1	144 I	I	..... I
14959	1	143 I	I	..... I
14960	1	142 I	I	.....A..... I
14961	1	141 I	I	.....AA..... I
14962	1	140 I	I	.....AAA..... I
14963	1	139 I	I	.....AAAA..... I
14964	1	138 I	I	.....AAAAA..... I
14965	1	137 I	I	.....AAAAAA..... I



SHELL INITIAL VERSION

BASIC PASSIVE MODEL

## PROCESSOR SUMMARY

PROC	<		FREE MODE TIME				>		<		>	
	AVAILABLE	TOTAL %	USER %	PROCESS %	EFF	SYSTEM %	OVERHEAD %	USER %				
1	19724	4998 25.	4492 23.	4492 23.	23.	506 10.	0 0.0	0 0.0				
2	19724	5079 26.	4574 23.	4574 23.	24.	505 10.	0 0.0	0 0.0				
3	19724	5038 26.	4520 23.	4520 23.	24.	518 10.	0 0.0	0 0.0				
4	19724	5093 26.	4574 23.	4574 23.	24.	519 10.	0 0.0	0 0.0				
5	19724	5052 26.	4520 23.	4520 23.	24.	532 11.	0 0.0	0 0.0				
6	19724	5107 26.	4568 23.	4568 23.	24.	539 11.	0 0.0	0 0.0				
7	19724	5085 26.	4574 23.	4574 23.	24.	511 10.	0 0.0	0 0.0				
8	19724	6257 32.	5248 27.	5248 27.	23.	1009 16.	0 0.0	0 0.0				
9	19724	5073 26.	4570 23.	4570 23.	24.	503 10.	0 0.0	0 0.0				
10	19724	5206 26.	4710 24.	4710 24.	24.	496 10.	0 0.0	0 0.0				
11	19724	5215 26.	4712 24.	4712 24.	25.	503 10.	0 0.0	0 0.0				
12	19724	5321 27.	4811 24.	4811 24.	25.	510 10.	0 0.0	0 0.0				
13	19724	5266 27.	4749 24.	4749 24.	25.	517 10.	0 0.0	0 0.0				
14	19724	5335 27.	4811 24.	4811 24.	25.	524 10.	0 0.0	0 0.0				
15	19724	5280 27.	4751 24.	4751 24.	25.	529 10.	0 0.0	0 0.0				
16	19724	5321 27.	4811 24.	4811 24.	25.	510 10.	0 0.0	0 0.0				
17	19724	6394 32.	5379 27.	5379 27.	29.	1015 16.	0 0.0	0 0.0				
18	19724	5294 27.	4806 24.	4806 24.	25.	488 9.	0 0.0	0 0.0				
19	19724	5084 26.	4590 23.	4590 23.	24.	494 10.	0 0.0	0 0.0				
20	19724	5092 26.	4591 23.	4591 23.	24.	501 10.	0 0.0	0 0.0				
21	19724	5193 26.	4690 24.	4690 24.	24.	508 10.	0 0.0	0 0.0				
22	19724	5143 26.	4628 23.	4628 23.	24.	515 10.	0 0.0	0 0.0				
23	19724	5212 26.	4690 24.	4690 24.	24.	522 10.	0 0.0	0 0.0				
24	19724	5157 26.	4628 23.	4628 23.	24.	529 10.	0 0.0	0 0.0				
25	19724	5226 26.	4691 24.	4691 24.	24.	535 10.	0 0.0	0 0.0				
26	19724	5204 26.	4690 24.	4690 24.	24.	514 10.	0 0.0	0 0.0				
27	19724	15032 76.	14021 71.	14021 71.	75.	1011 7.	0 0.0	0 0.0				
28	19724	5151 26.	4690 24.	4690 24.	24.	461 9.	0 0.0	0 0.0				
29	19724	5434 28.	4957 25.	4957 25.	26.	477 9.	0 0.0	0 0.0				
30	19724	7943 40.	7196 36.	7196 36.	38.	747 9.	0 0.0	0 0.0				
31	19724	5442 28.	4951 25.	4951 25.	26.	491 9.	0 0.0	0 0.0				
32	19724	4986 25.	4498 23.	4498 23.	23.	488 10.	0 0.0	0 0.0				
<hr/>												
	631168	190718 29.	162691 26.	162691 26.	27.	18027 10.	0 0.0	0 0.0				

SHELL INITIAL VERSION

BASIC PASSIVE MODEL

## PROCESSOR SUMMARY

PROC	AVAILABLE	FREE MODE TIME				OVERHEAD							
		TOTAL	%	USER	%	PROCESS	%	EFF	SYSTEM	%	USER	%	
1	11075	5961	54.	5322	48.	5322	48.	51.	639	11.	0	0.0	
2	11075	6039	55.	5612	51.	5612	51.	53.	427	7.	0	0.0	
3	11075	6152	56.	5759	52.	5759	52.	54.	393	6.	0	0.0	
4	11075	6262	57.	5858	53.	5858	53.	55.	404	6.	0	0.0	
5	11075	6211	56.	5796	52.	5796	52.	54.	415	7.	0	0.0	
6	11075	6284	57.	5858	53.	5858	53.	55.	426	7.	0	0.0	
7	11075	6233	56.	5798	52.	5798	52.	54.	435	7.	0	0.0	
8	11075	6279	57.	5858	53.	5858	53.	55.	421	7.	0	0.0	
9	11075	7352	66.	6423	58.	6423	58.	63.	929	13.	0	0.0	
10	11075	6257	56.	5850	53.	5850	53.	55.	407	7.	0	0.0	
11	11075	6050	55.	5631	51.	5631	51.	53.	419	7.	0	0.0	
12	11075	6062	55.	5632	51.	5632	51.	53.	430	7.	0	0.0	
13	11075	6172	56.	5728	52.	5728	52.	54.	444	7.	0	0.0	
14	11075	6121	55.	5666	51.	5666	51.	53.	455	7.	0	0.0	
15	11075	6194	56.	5722	52.	5722	52.	54.	472	8.	0	0.0	
16	11075	6143	55.	5660	51.	5660	51.	53.	483	8.	0	0.0	
17	11075	6218	56.	5723	52.	5723	52.	54.	495	8.	0	0.0	
18	11075	6117	55.	5650	51.	5650	51.	53.	467	8.	0	0.0	
19	11075	7958	72.	6500	59.	6500	59.	68.	1458	18.	0	0.0	
20	11075	6102	55.	5681	51.	5681	51.	53.	421	7.	0	0.0	
21	11075	6388	58.	5948	54.	5948	54.	56.	440	7.	0	0.0	
22	11075	6691	60.	5978	54.	5978	54.	58.	713	11.	0	0.0	
23	11075	6402	58.	5942	54.	5942	54.	56.	460	7.	0	0.0	
24	11075	5949	54.	5489	50.	5489	50.	52.	460	8.	0	0.0	
25	11075	6559	59.	5598	51.	5535	50.	55.	961	15.	63	1.	
26	11075	6036	55.	5599	51.	5599	51.	53.	437	7.	0	0.0	
27	11075	5844	53.	5401	49.	5401	49.	51.	443	8.	0	0.0	
28	11075	5911	53.	5466	49.	5466	49.	51.	445	8.	0	0.0	
29	11075	5865	53.	5404	49.	5404	49.	51.	461	8.	0	0.0	
30	11075	5932	54.	5463	49.	5463	49.	52.	469	8.	0	0.0	
31	11075	5904	53.	5461	49.	5461	49.	51.	443	8.	0	0.0	
32	11075	7090	64.	6160	56.	6160	56.	61.	930	13.	0	0.0	
-----		354400	200738	57.	183636	52.	183573	52.	54.	17102	9.	63	0.



SIMULATION OF SMALL SHELL (30 X 5 ARRAY) FINAL BREAKDOWN

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
0	1	I	I ..... I
300	119	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
600	87	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
900	86	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1200	55	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1500	23	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1800	22	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2100	0	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2400	0	I	I . I
2700	0	I	I A I
3000	126	I	I AAAAAAAAAA.....AAAAAAAA I
3300	0	I	I .AAAAAAA I
3600	0	I	I A I
3900	0	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4200	0	I	I A I
4500	269	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
4800	238	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
5100	207	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
5400	178	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
5700	148	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
6000	137	I	I AAAAAAAAAAAAAAA...AAA...AAAAAAAAA I
6300	82	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
6600	48	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
6900	0	I	I ...AAAAAAAAAAAAAAAAAAAAA.A I
7200	0	I	I . I
7500	0	I	I AAAAA I
7800	133	I	I ..AAAAAAAAAAAA..... I
8100	118	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
8400	87	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
8700	68	I	I AAAAAAAAAAAAAAAAAAAAAA...AAAAAAAAA I
9000	54	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
9300	23	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
9600	0	I	I AAAAAAAAAAAAAAAAAAAAAA AAAAA I
9900	0	I	I AAAAAAAAAAAAAAAAAAAAAA I

SIMULATION OF SMALL SHELL (30 X 5 ARRAY) FINAL BREAKDOWN

PROCESSOR SUMMARY

PROC	AVAILABLE	FREE MODE TIME						OVERHEAD					
		TOTAL	%	USEAGE		PROCESS		% EFF	SYSTEM	%	USER		
				USER	%	PROCESS	%	EFF			USER	%	
1	10400	7556	73.	6928	67.	6928	67.	71.	628	8.	0	0.0	
2	10400	8084	78.	7061	68.	6950	67.	74.	1023	13.	111	1.	
3	10400	7715	74.	7358	71.	7358	71.	73.	357	5.	0	0.0	
4	10400	8106	78.	7743	74.	7743	74.	77.	363	4.	0	0.0	
5	10400	8221	79.	7848	75.	7848	75.	78.	373	5.	0	0.0	
6	10400	8232	79.	7848	75.	7848	75.	78.	384	5.	0	0.0	
7	10400	7902	76.	7518	72.	7518	72.	75.	384	5.	0	0.0	
8	10400	7907	76.	7513	72.	7513	72.	75.	394	5.	0	0.0	
9	10400	7847	75.	7445	72.	7445	72.	74.	402	5.	0	0.0	
10	10400	7855	76.	7442	72.	7442	72.	75.	413	5.	0	0.0	
11	10400	7873	76.	7445	72.	7445	72.	75.	428	5.	0	0.0	
12	10400	7890	76.	7483	72.	7483	72.	75.	407	5.	0	0.0	
13	10400	7945	76.	7493	72.	7493	72.	75.	452	6.	0	0.0	
14	10400	7885	76.	7384	71.	7384	71.	75.	501	6.	0	0.0	
15	10400	7894	76.	7381	71.	7381	71.	75.	513	6.	0	0.0	
16	10400	7904	76.	7381	71.	7381	71.	75.	523	7.	0	0.0	
17	10400	7654	74.	7199	69.	7199	69.	72.	455	6.	0	0.0	
18	10400	8516	82.	7355	71.	7169	69.	78.	1161	14.	186	2.	
19	10400	7843	75.	7442	72.	7442	72.	74.	401	5.	0	0.0	
20	10400	8015	77.	7602	73.	7602	73.	76.	413	5.	0	0.0	
21	10400	8023	77.	7602	73.	7602	73.	76.	421	5.	0	0.0	
22	10400	8031	77.	7602	73.	7602	73.	76.	429	5.	0	0.0	
23	10400	8040	77.	7602	73.	7602	73.	76.	438	5.	0	0.0	
24	10400	9087	87.	7918	76.	7881	76.	85.	1169	13.	37	0.	
25	10400	7775	75.	7363	71.	7363	71.	74.	412	5.	0	0.0	
26	10400	7165	69.	6766	65.	6766	65.	68.	399	6.	0	0.0	
27	10400	7181	69.	6771	65.	6771	65.	68.	410	6.	0	0.0	
28	10400	7258	70.	6845	66.	6845	66.	69.	413	6.	0	0.0	
29	10400	7229	70.	6802	65.	6802	65.	68.	427	6.	0	0.0	
30	10400	7274	70.	6848	66.	6848	66.	69.	426	6.	0	0.0	
31	10400	7243	70.	6805	65.	6805	65.	68.	438	6.	0	0.0	
32	10400	7290	70.	6856	66.	6856	66.	69.	434	6.	0	0.0	
-----		332800	250440	75.	234649	71.	234315	70.	74.	15791	6.	334	0.

## TYPES OF PARALLEL EXECUTION

S. I. M. E. - SINGLE I MULTIPLE E.

- A SINGLE INSTRUCTION ~~DECODER~~ STREAM
- MANY ARITHMETIC UNITS
- ALL UNITS EXECUTE INSTRUCTIONS SYNCHRONOUSLY

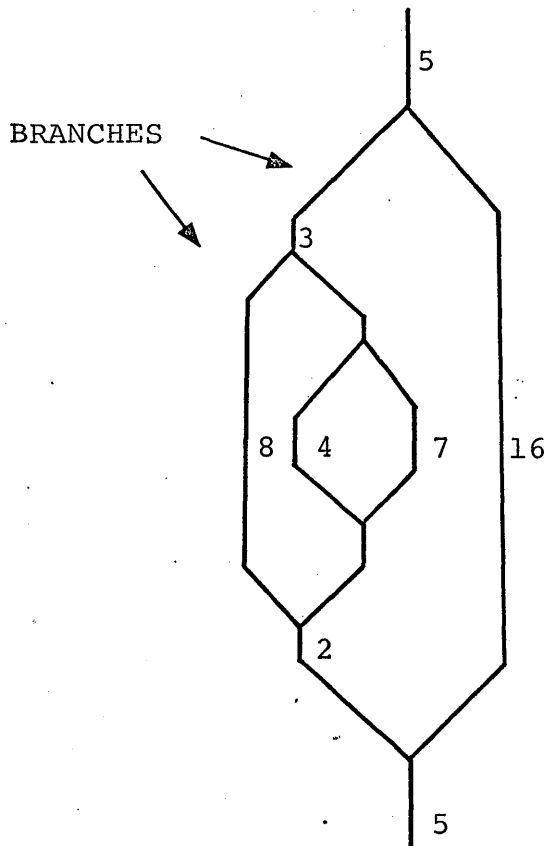
EXAMPLE: ILLIAC 4

M. I. M. E. - MULTIPLE I MULTIPLE E

- MANY INSTRUCTION ~~DECODERS~~ STREAMS
- MANY ARITHMETIC UNITS
- UNITS MAY EXECUTE ASYNCHRONOUSLY

EXAMPLE: FSDPP

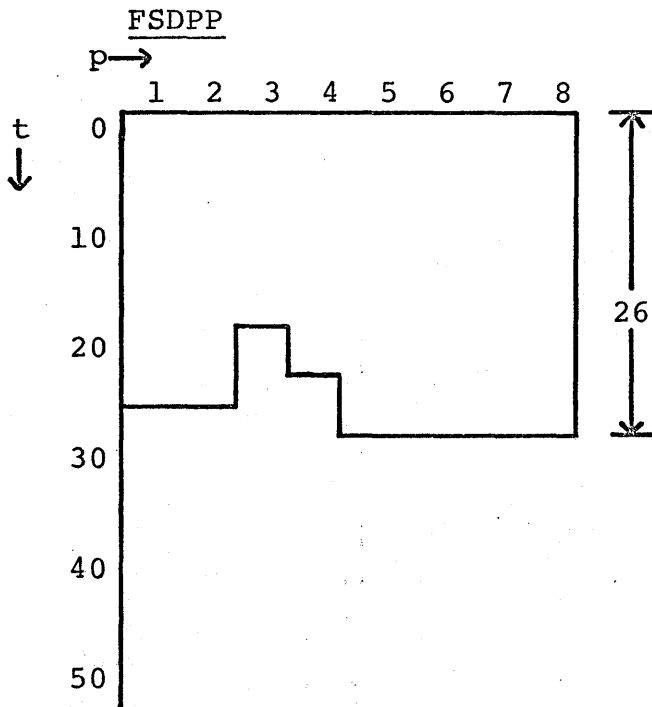
PERFORMANCE WITH DATA DEPENDENT EXECUTION



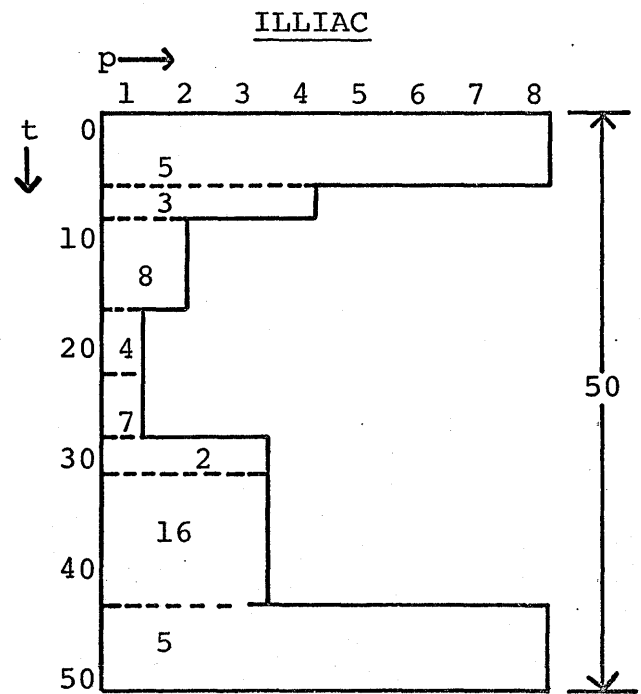
ASSUME EIGHT POINTS TO BE PROCESSED BY EIGHT PROCESSORS AND A 50% PROBABILITY OF TAKING EITHER LEG OF A SPLIT.

NUMBERS INDICATE RELATIVE LENGTHS OF LEGS.

PROCESSOR UTILIZATION MAPS

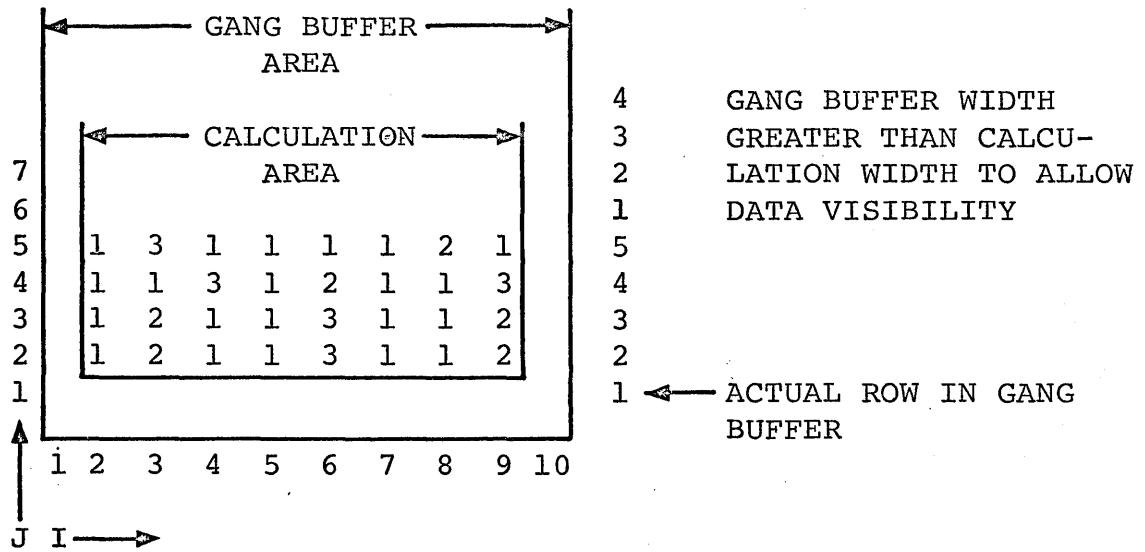


EFFECTIVENESS 92%



48%

PROCESSOR ALLOCATION TO ELIMINATE WAIT TIME



TIMES VARY FROM 1-3 AS SHOWN ABOVE

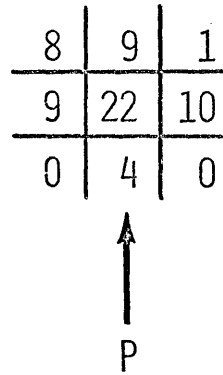
PROCESSOR CALCULATION ASSIGNMENTS

P=	1	2	3	4	5	6	7	8
t= 1	2,2	2,3	2,4	2,5	2,6	2,7	2,8	2,9
2	3,2	2,3	3,3	3,4	2,6	3,5	3,6	2,9
3	3,7	3,8	3,3	3,9	2,6	4,2	3,6	4,3
4	4,4	4,5	4,6	3,9	4,7	4,8	3,6	4,9
5	4,4	5,2	4,6	5,3	5,4	5,5	5,6	4,9
6	4,4	5,7	5,8	5,3	5,9	↓	↓	4,9
7	↓	↓	5,8	5,3	↓	↓	↓	↓
8	↓	↓	↓	↓	↓	↓	↓	↓

AT NO TIME ARE THERE MORE THAN THREE ROWS BEING WORKED ON. THUS, 5 BUFFER LINES PROVIDE SUFFICIENT SPACE TO ALLOW ALL PROCESSORS TO WORK CONTINUOUSLY.

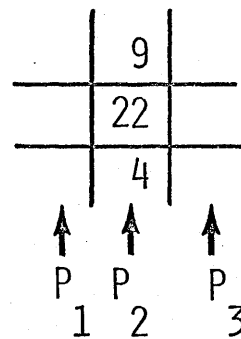
EXAMPLE OF DATA BUFFERING FROM WEATHER CODE

DATA REQUIREMENTS FOR MESH  
POINT COMPUTATION

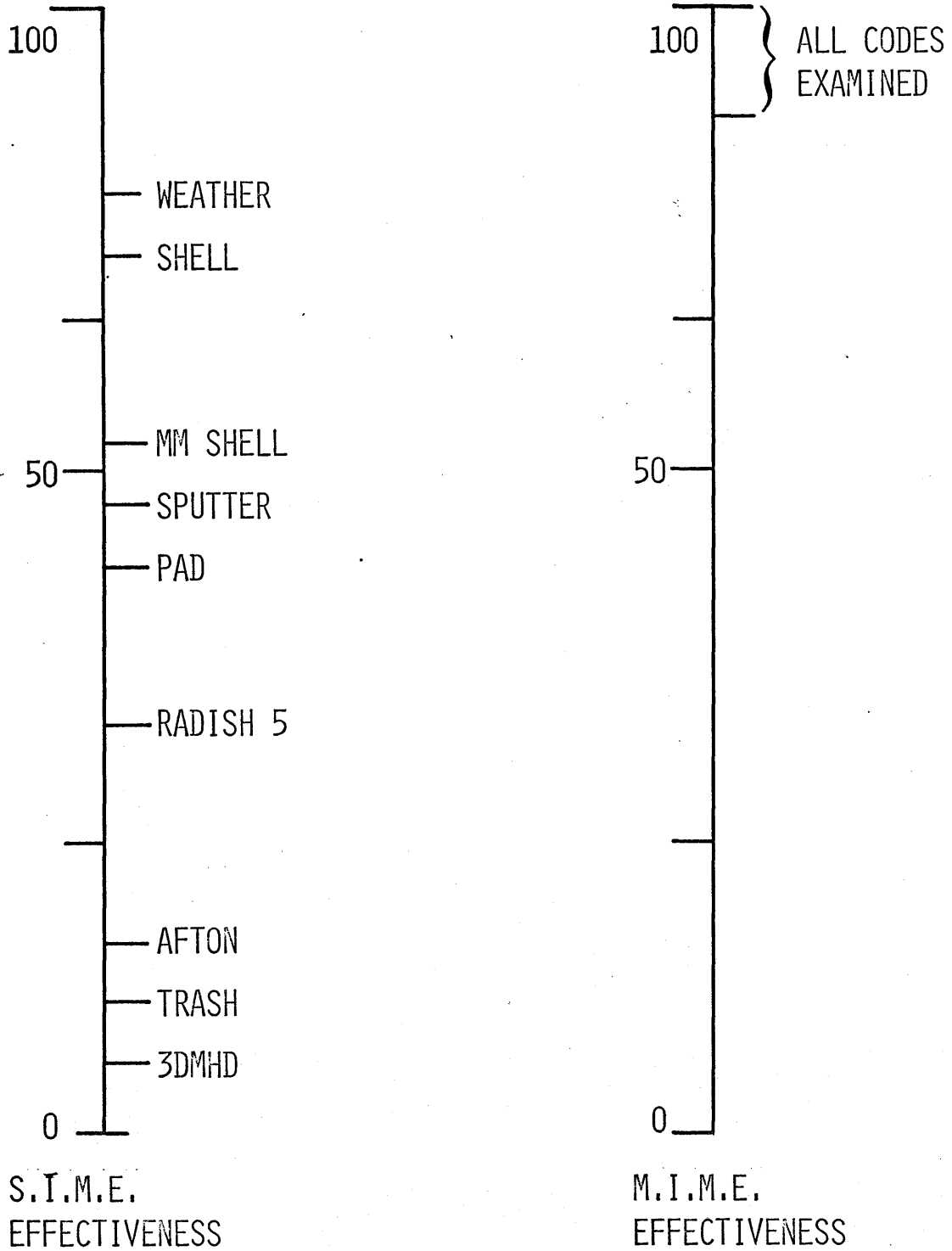


FREE MODE REQUIRES EACH  
PROCESSOR TO FETCH 63 ITEMS

GANG MODE REQUIRES EACH  
PROCESSOR TO FETCH 35 ITEMS



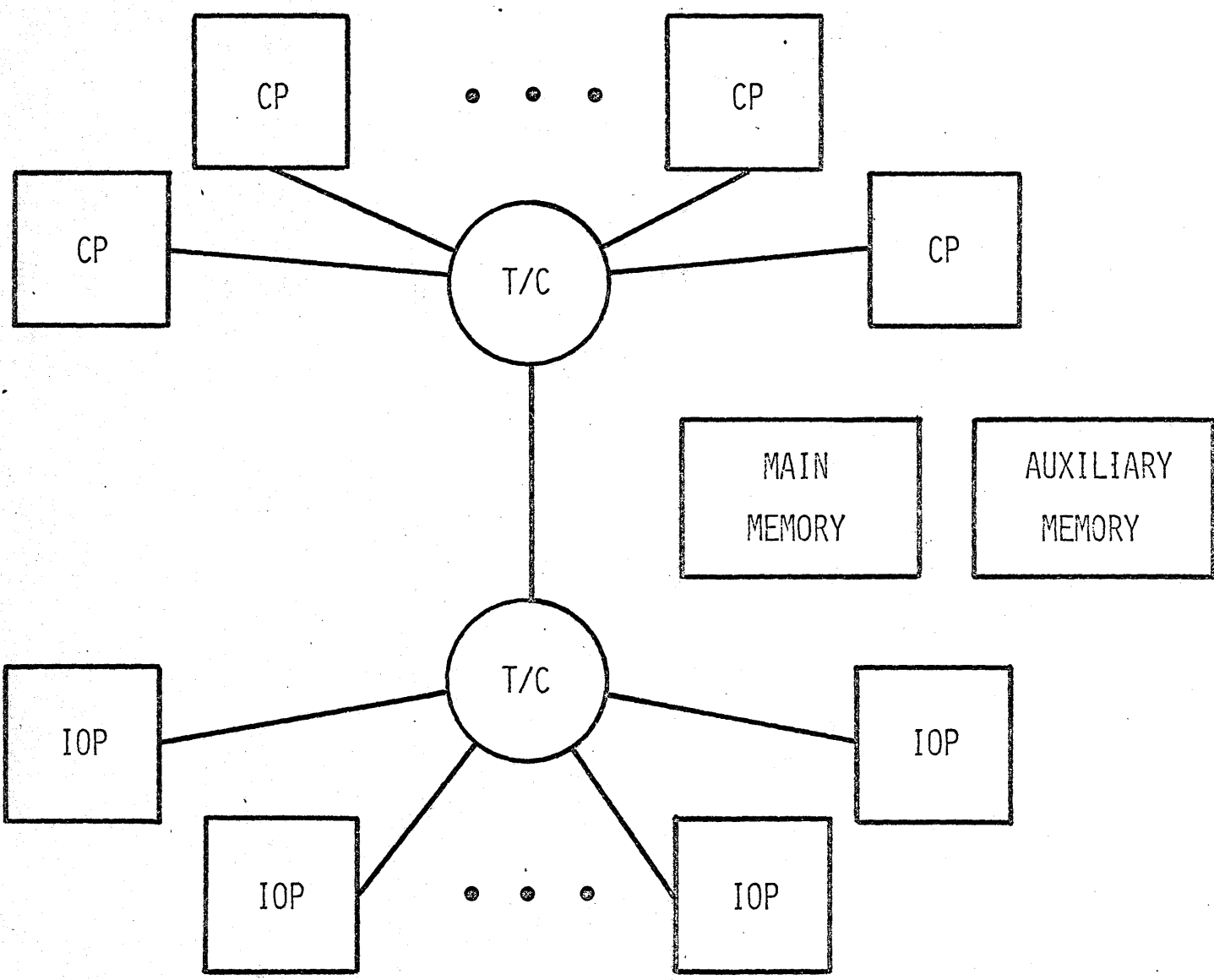
ESTIMATED PARALLEL PERFORMANCES



*of ILLIAC II  
2/19/54*

OPERATING  
SYSTEM





## APPLICATIONS

### LARGE SCIENTIFIC

- o DEMANDS ALL COMPUTE RESOURCES
- o LONG EXECUTION TIME
- o WELL DISCIPLINED, LOW INCIDENT CONTROL

### REAL TIME

- o DEMANDS ALL RESOURCES
- o LONG EXECUTION TIME
- o WELL DISCIPLINED, HIGH INCIDENT CONTROL

### MULTI-JOB

- o VARIABLE RESOURCE DEMAND
- o VARIABLE EXECUTION TIME
- o UNDISCIPLINED, HIGH INCIDENT CONTROL

## DESIGN OBJECTIVES

EXPLOIT FORK/JOIN

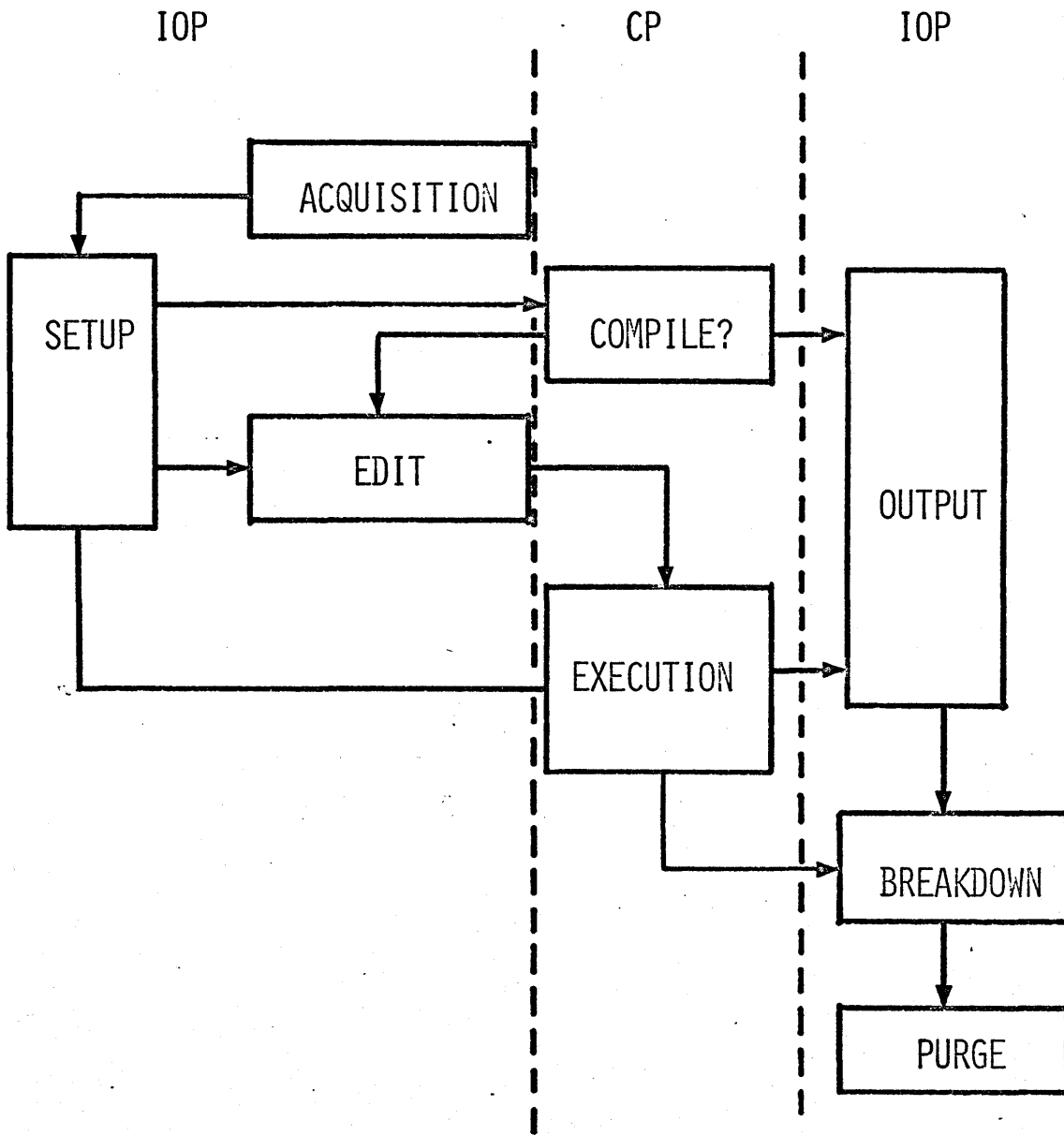
FORCE FUNCTIONS TO I/O PROCESSORS

EXPLICIT, DYNAMIC RESOURCE MANAGEMENT

CHANGE ABILITY

LESS THAN 5% OVERHEAD - 300 INSTRUCTION TASKS

GENERAL OPERATING SYSTEM STRUCTURE



## PROBLEM AREAS

### I/O BANDWIDTH

- FUNCTIONAL CHARACTERISTICS
- MINIMIZE TRAFFIC WITH EXTERNAL INTERFACES  
E.G., ONLINE DATA AND PROGRAM MAINTENANCE

### MEMORY MANAGEMENT

- HIERARCHY
- RELOCATION PHILOSOPHY

### COMPILE

- IMPLICIT VS EXPLICIT
- OPTIMIZATION

### DATA MANAGEMENT

### TASK COORDINATION

- SOFTWARE-HARDWARE TRADEOFFS

## SYSTEM SIMULATOR

### PURPOSE

- o SOFTWARE DESIGN
- o SOFTWARE IMPLEMENTATION

### METHOD

- o IMITATIVE AS OPPOSED TO ANALYTICAL TECHNIQUE
- o REFINED AS SOFTWARE DESIGN PROCEEDS

### BASIS

- o EXECUTION SIMULATOR
- o PROGRAM MODELING SYSTEM FOR OS/360

## EXECUTION SIMULATOR

### ENGINEERING DESIGN TOOL

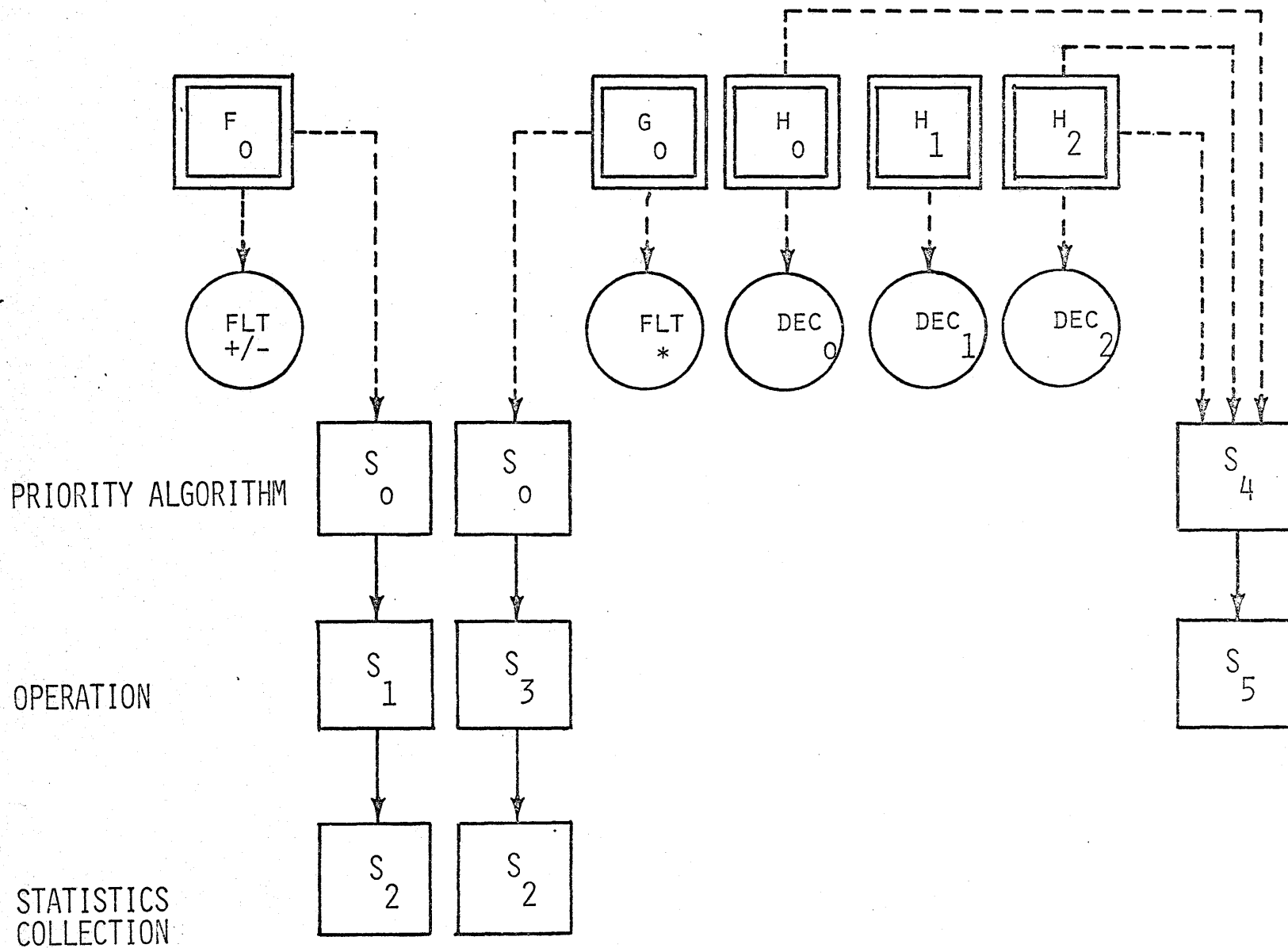
- o INTERPRETIVE EXECUTION - FLOW TRACE
- o INTERFERENCE ANALYSIS - PERFORMANCE

CONFIGURATION TABLE

PROCESSOR RESOURCE PROCESSOR	P <sub>0</sub>	P <sub>1</sub>	P <sub>2</sub> - - -
PROCESSOR	T <sub>0</sub>	T <sub>0</sub>	T <sub>0</sub>
⋮			
FLT ADD	F <sub>0</sub>	F <sub>0</sub>	F <sub>0</sub>
FLT SUB	F <sub>0</sub>	F <sub>0</sub>	F <sub>0</sub>
FLT MPY	G <sub>0</sub>	G <sub>0</sub>	G <sub>0</sub>
⋮			
DECODER	H <sub>0</sub>	H <sub>1</sub>	H <sub>2</sub>
⋮			
MEMORY MOD. 1			
2			
⋮			
MEMORY BUS			
⋮			



FLOW CONTROL TABLE



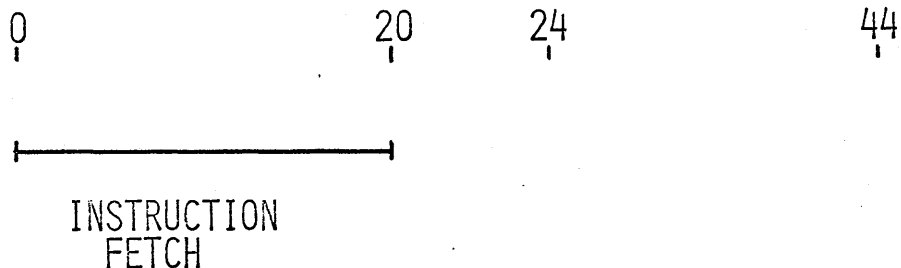
# INSTRUCTION UNIT

IU0

MASK0			
Y0		N0	
E	64	IU0	
S	IF	REQ	
E	20	IU1	

N0

Y0



IU1

MASK1			
Y1		N1	
E	64	IU1	
S	ID	REQ	
E	4	IU2	

N1

Y1



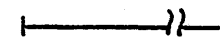
IU2

0			
Y2		Y2	
S	AG	REQ	
E	20	IU3	

Y2



IU3

## INPUT

### SIMULATION CONTROL

- o INITIALIZATION
- o TERMINATION
- o OUTPUT

### DESIGN PARAMETERS

- o CONFIGURATION
- o TIMING
- o ALGORITHM

### INSTRUCTION STREAM

LISTING OF PARAMETER CARDS AS READ

PROCESSOR NUMBER=32  
CHANGE RESOURCE=ADGEN, MASK=10, ACTSF=RELEASE  
CHANGE RESOURCE=FLDIV, NUMBER=1, TYPE=COMMON, ACTSE=EVENT,X  
ACTDT=0, ACTSF=EXFLD  
CHANGE RESOURCE=PKGC, PRIOT=448  
CHANGE FCTSTEP=XFCRK, ENTRY=3, FDT=40 CHANGING DELTA-TIME FIELD  
CHANGE FCTSTEP=PRIORITY, LENTRY=2, FSE=EVENT, FADDR=PRIORITY  
START STINST=1, STPRCC=31, X  
ILIMIT=20000  
TRACE LINES=0 COMMENT  
CHANGE FCTSTEP=PRIORITY, ENTRY=2, FDT=448  
PLOT PAGES=400  
ENDPARM MAIN KEYWORDS NEED NOT ALL START IN COL 4

END OF PARAMETERS. 13 CARDS READ.

LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

F01JAN68 4/04/68

C00000 462+INSTRC START 0 CONTROL SECTION FOR INPUT INSTRUCTIONS  
 00C000 000C0000 463+\*  
 464+IBEG DC A(0) FOR LENGTH OF INSTRUCTIONS

466 \*  
 467 \*  
 468 \*  
 469 \*  
 470 PRINT ON,NUGEN SUPRESS INSTRUCTION MACRO EXPANSIONS  
 471 \*  
 472 \* INSTRUCTIONS TO SIMULATE 'SUBROUTINE CMP108'  
 473 \*  
 474 \*  
 475 \*

PRIVATE STORE ASSIGNMENTS

VARIABLE	PRIVATE STORE LOCATION
J	0 (XR0)
IP1	1 (XR1)
I	2 (XR2)
IM	3 (XR3)
IM1	4 (XR4)
LOOP CNTRL	5 (XR5)
JM	6 (XR6)
MRCH	7 (XR7)
CONST 1	8
CONST 2	9
CONST 3	10
CONST 4	11
CONST 7	12
CONST 10	13
CONST 0.25	14
CONST 0.5	15
ARRAY PU	20
ARRAY P	40
ARRAY PV	60
ARRAY DXU	80
ARRAY V	100
.TEMP.	19

505 \* GET ALL PROCESSORS GOING ON TASK  
 506 \*  
 507 A FORK B  
 516 B FORK C  
 525 C FORK D  
 534 D FORK E  
 543 E FORK CMP108  
 552 \*

LOG OBJECT CODE      ADDR1 ADDR2      STMT      SOURCE STATEMENT

F01JAN68      4/04/68

```

553 CMP108    NOOP    ,                    ENTRY FOR SUBROUTINE CMP108
561 *
562 *    INITIALIZE REGISTERS FOR LOOP
563 *
564            LDX    X2,C5            SET I TO 5
574            LDX    X3,C31          SET IM TO 31
584            LDX    X6,C30          SET JM TO 30
594            LDX    X7,C2           SET MRCH TO 2 - THEN 3, 4, 2, 3, 4, ETC
604 *
605            LDX    X0,C0            START J AT 0
615            LDX    X5,C1            START LOOP CONTROL AT 1
625 *
626 *
627 *    INSTRUCTION COUNTING STATEMENTS, I.E. CTR=CTR+4, ARE NOT SIMULATED
628 *
629 LOOP      ATX    C1,X0,X0            J=J+1
640            TXH    SN10,X0,C2        IF(J.NE.2)
651            TXLE   SN10,X0,C1                            GO TO 10
662            ATX    C1,X2,X1            IP1=I+1
673            TXH    SKIP1,X2,X3        IF(I.EQ.IM) .... IP=1
684            SFX    C1,X3,X4            TEMP - X4=IM-1
695            TXLE   SKIP1,X2,X4        IF .LE. IM-1, THEN .NE. IM
706            LDX    X1,C1            IP1=1
716 SKIP1     LCX    X4,X2            IM1=
726            SFX    C1,X4,X4            I-1
737            TXH    SN10,X2,C1        IF(.NOT.(I.EQ.1))
748            TXLE   SN10,X2,C0                            GO TO 10
759            LDX    X4,X3            IM1=IM
769 *
770 SN10      NOOP    ,                    WAS CTR=CTR+4
778            LOD    A1,X5            SIMULATE SAVING 'CTR' SINCE
788            STO    A1,P19            IT DOES NOT AFFECT FLOW
798            TXH    SN2170,X0,X6      IF(J.EQ.JM)...GO TO 2175 - BRA IF HIGHER
809            SFX    C1,X6,X6            TEMP - JM=JM-1
820            TXLE   SKIP2,X0,X6        COMPARING J TO JM-1
831            ATX    C1,X6,X6            RESTORING JM
842            BRA    SN2175            GO TO 2175 (J.EQ.JM)
851 *
852 SKIP2     ATX    C1,X6,X6            RESTORING JM (HERE IF J.NE.JM)
863 *
864 SN2170    LOD    A1,P40            P(J,I)
874            FADD   A1,P40,A1                            + P(J,IP1)
885            FMPY   A1,P20,A1                            ABOVE QUANTITY * PU(J,I)
896            STO    A1,P20            PU(J,I)= ABOVE RESULT
906 *
907 SN2175    NOOP    ,                    WAS CTR=CTR+4
915            TXH    SN11,X7,C2        IF(.NOT.(MRCH.LE.2)) GO TO 11
926 *
927            LOD    A1,P100            V(J,I,L)
937            FADD   A1,P100,A1                            + V(J,IM1,L)
948            STU    A1,P19            SAVE IN TEMP
958            LUD    A1,P40            P(J,I)
968            FADD   A1,P40,A1                            + P(J-1,I)
979            FMPY   A1,P19,A1            * (V(J,I,L) + V(J,IM1,L))
990            FMPY   A1,P80,A1            * DXU(J)
    
```



## OUTPUT

### RESOURCE UTILIZATION

- o BUSY TIME
- o IDLE TIME

### REQUEST QUEUE

- o LENGTH
- o DELAY
- o REQUESTOR

### INSTRUCTION EXECUTION TRACE

- o INSTRUCTION FETCH
- o DECODE
- o ADDRESS GENERATION
- o OPERAND FETCH
- o EXECUTION



EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000064 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
-I-	-D-	-A-	-U-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6		
								0	1	2	3	4	4	5	6		
.000	.000	.000	.000	.000	0												
.000	.000	.000	.000	.000	1												
.000	.000	.000	.000	.000	2												
.000	.000	.000	.000	.000	3												
.000	.000	.000	.000	.000	4												
.000	.000	.000	.000	.000	5												
.000	.000	.000	.000	.000	6												
.000	.000	.000	.000	.000	7												
.000	.000	.000	.000	.000	8												
.000	.000	.000	.000	.000	9												
.000	.000	.000	.000	.000	10												
.000	.000	.000	.000	.000	11												
.000	.000	.000	.000	.000	12												
.000	.000	.000	.000	.000	13												
.000	.000	.000	.000	.000	14												
.000	.000	.000	.000	.000	15												
.000	.000	.000	.000	.000	16												
.000	.000	.000	.000	.000	17												
.000	.000	.000	.000	.000	18												
.000	.000	.000	.000	.000	19												
.000	.000	.000	.000	.000	20												
.000	.000	.000	.000	.000	21												
.000	.000	.000	.000	.000	22												
.000	.000	.000	.000	.000	23												
.000	.000	.000	.000	.000	24												
.000	.000	.000	.000	.000	25												
.000	.000	.000	.000	.000	26												
.000	.000	.000	.000	.000	27												
.000	.000	.000	.000	.000	28												
.000	.000	.000	.000	.000	29												
.000	.000	.000	.000	.000	30												
.312	.000	.000	.000	.000	31												

FORK

IIIIIIII

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00 FL.PT. ADD|SUB  
 C.00 FL.PT. MULTIPLY  
 C.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 C.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00000448 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for (STAGE TIME / TOTAL TIME) and (INSTRUCTION). Rows include instruction numbers (0-31), processor (PROC.), and instruction type (FETCH EXEC.). Includes timing data like .223 .C44 .C44 .C26 .000 and instruction details like NOOP, LDX.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.C0 FL.PT. ADD|SUB
0.00 FL.PT. MULTIPLY
C.C0 FL.PT. DIVIDE
0.C0 FX.PT. ADD|SUB
0.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00000512 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)														
-I-	-O-	-A-	-U-	-R-	PROC.	FETCH EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1	2	3	4	4	5	6					
.234	.046	.078	.046	.000	0	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.195	.039	.078	.046	.000	1	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.195	.039	.078	.046	.000	2	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.156	.031	.078	.046	.000	3	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.156	.031	.078	.046	.000	4	LDX NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.156	.031	.078	.046	.000	5	LDX NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.156	.031	.078	.046	.000	6	LDX NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	7	LDX NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	8	LDX NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	9	LDX NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	10	LDX NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	11	LDX NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	12	LDX NOOP	AAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	13	LDX NOOP	AAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.117	.023	.078	.046	.000	14	LDX NOOP	AAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.078	.015	.078	.023	.000	15	LDX NOOP	AAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA												
.078	.015	.078	.023	.000	16	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.078	.023	.000	17	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.078	.023	.000	18	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.078	.023	.000	19	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.039	.023	.000	20	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.039	.023	.000	21	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.039	.023	.000	22	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.015	.039	.023	.000	23	LDX NOOP	AAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAA												
.078	.007	.039	.023	.000	24	LDX NOOP	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIII												
.078	.007	.039	.023	.000	25	LDX NOOP	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIII												
.078	.007	.039	.023	.000	26	LDX NOOP	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIII												
.078	.007	.039	.023	.000	27	LDX NOOP	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIIIIIIIIIII												
.078	.007	.039	.023	.000	28	LDX NOOP	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIII												
.078	.007	.039	.023	.000	29	LDX NOOP	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIII												
.078	.007	.039	.023	.000	30	LDX NOOP	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIII												
.312	.054	.078	.046	.000	31	LDX LDX	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAA0000000000000000EEEEYYYYIIIIIIIIII												

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
C.00	FL.PT. DIVIDE
C.00	FX.PT. ADD SUB
C.00	FX.PT. MPY DIV

SYSTEM CLOCK TIME=00001600 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
Y=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for (STAGE TIME / TOTAL TIME) and (INSTRUCTION). Rows include instruction details like PROC., FETCH EXEC., and a sequence of characters (I, D, E, F, Y, A, /) representing execution status.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.50 FL.PT. ADD|SUB
0.00 FL.PT. MULTIPLY
0.00 FL.PT. DIVIDE
0.00 FX.PT. ADD|SUB
0.00 FX.PT. MPY|DIV

1111111133333333

SYSTEM CLOCK TIME=00001664 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
U=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for Stage Time / Total Time (I-, -D-, -A-, -U-, -R-), Instruction (PROC., FETCH EXEC.), and a trace of instruction execution (0-9) with binary representations of instruction types and addresses.

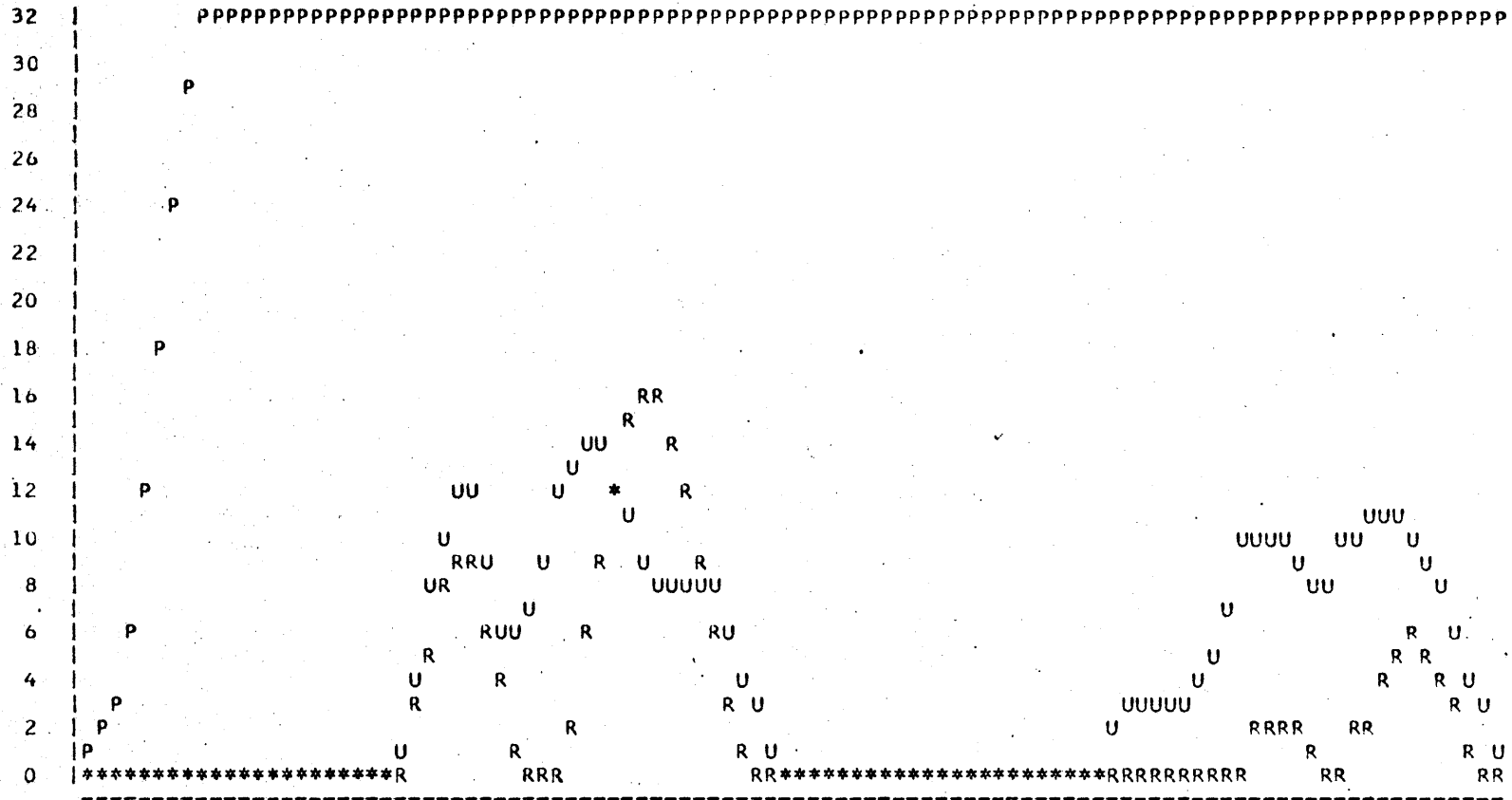
(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table showing execution unit utilization for FL.PT. ADDSUB, FL.PT. MULTIPLY, FL.PT. DIVIDE, FX.PT. ADDSUB, and FX.PT. MPY|DIV, with corresponding instruction counts.



SYSTEM CLOCK TIME=00006528 RING PRIORITY=(0,1,2,3)

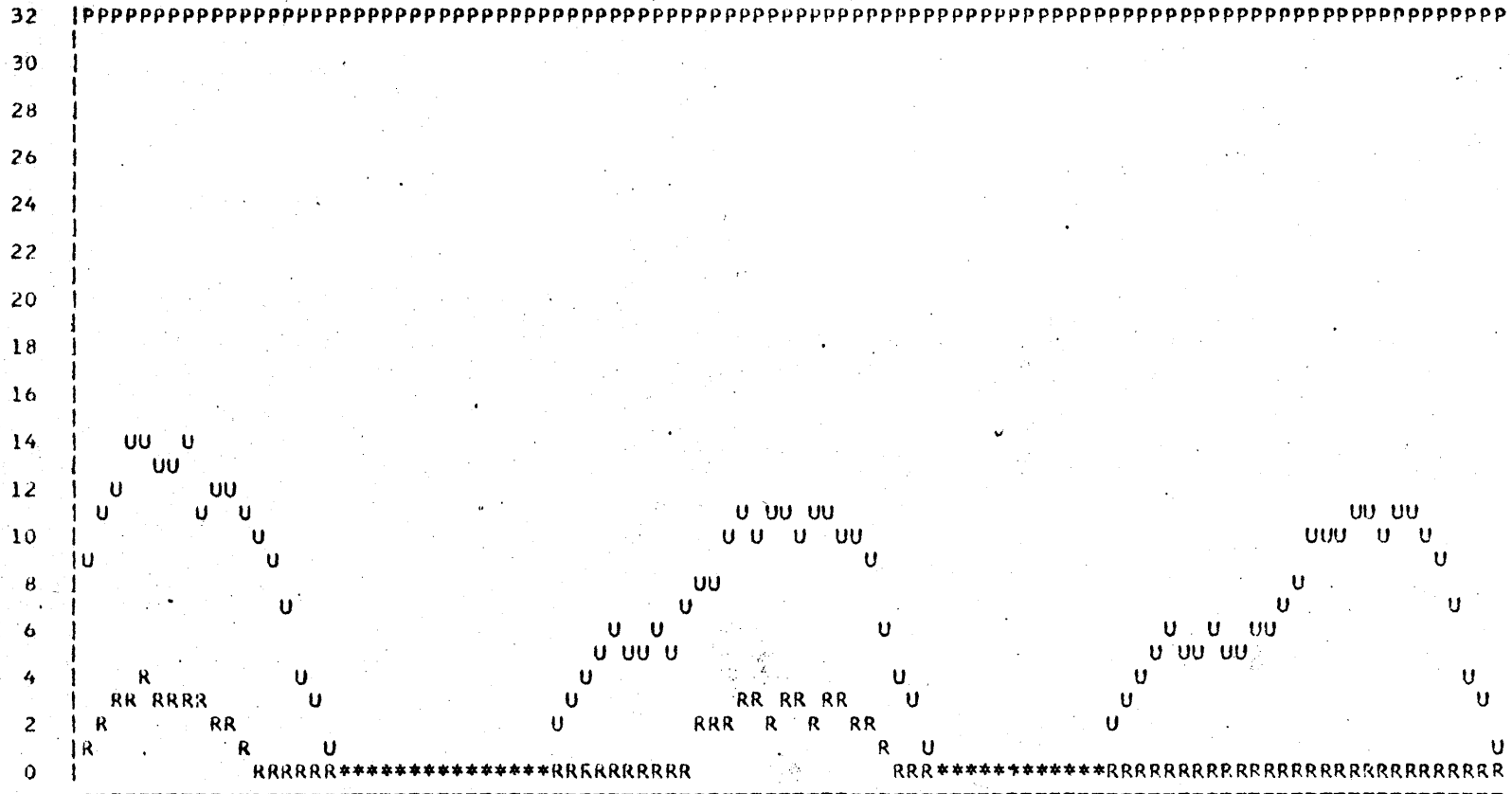
P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

SYSTEM CLOCK TIME=00032128 RING PRIORITY={0,1,2,3}

P = ACTIVE PROCESSORS  
U = ACTIVE EXECUTION UNIT STAGES  
R = REJECTED E-UNIT REQUESTS  
\* = ANY COMBINATION OF ABOVE



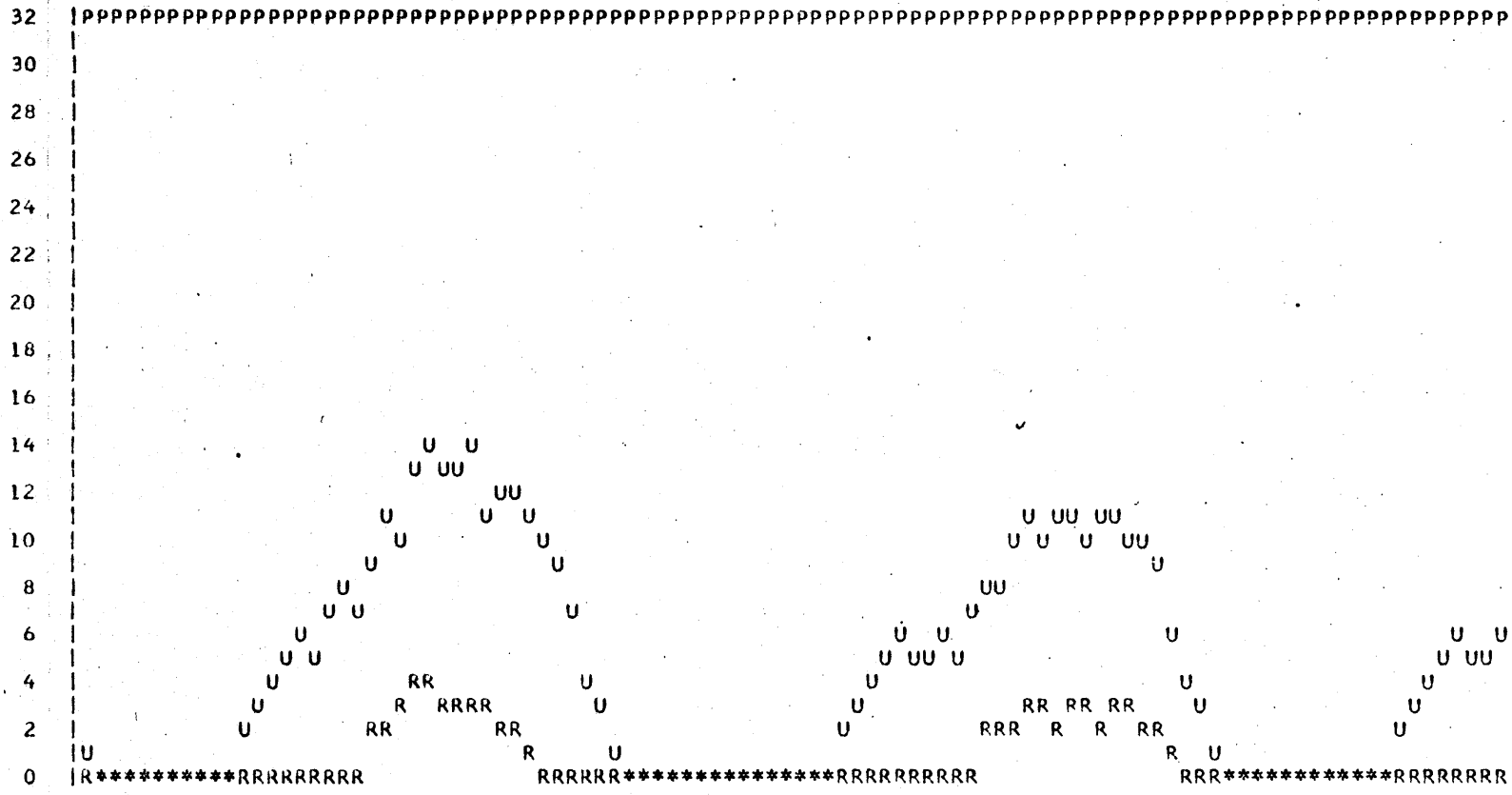
....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00



EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00038528 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

END OF SIMULATION - TIME USED= 0.0536 HOURS, PERFORMANCE = 6215 INSTRUCTIONS/MINUTE ( PLOT MODE )

\*\*\*\*\* EOF ON SYSIN - END OF RUN \*\*\*\*\*

LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

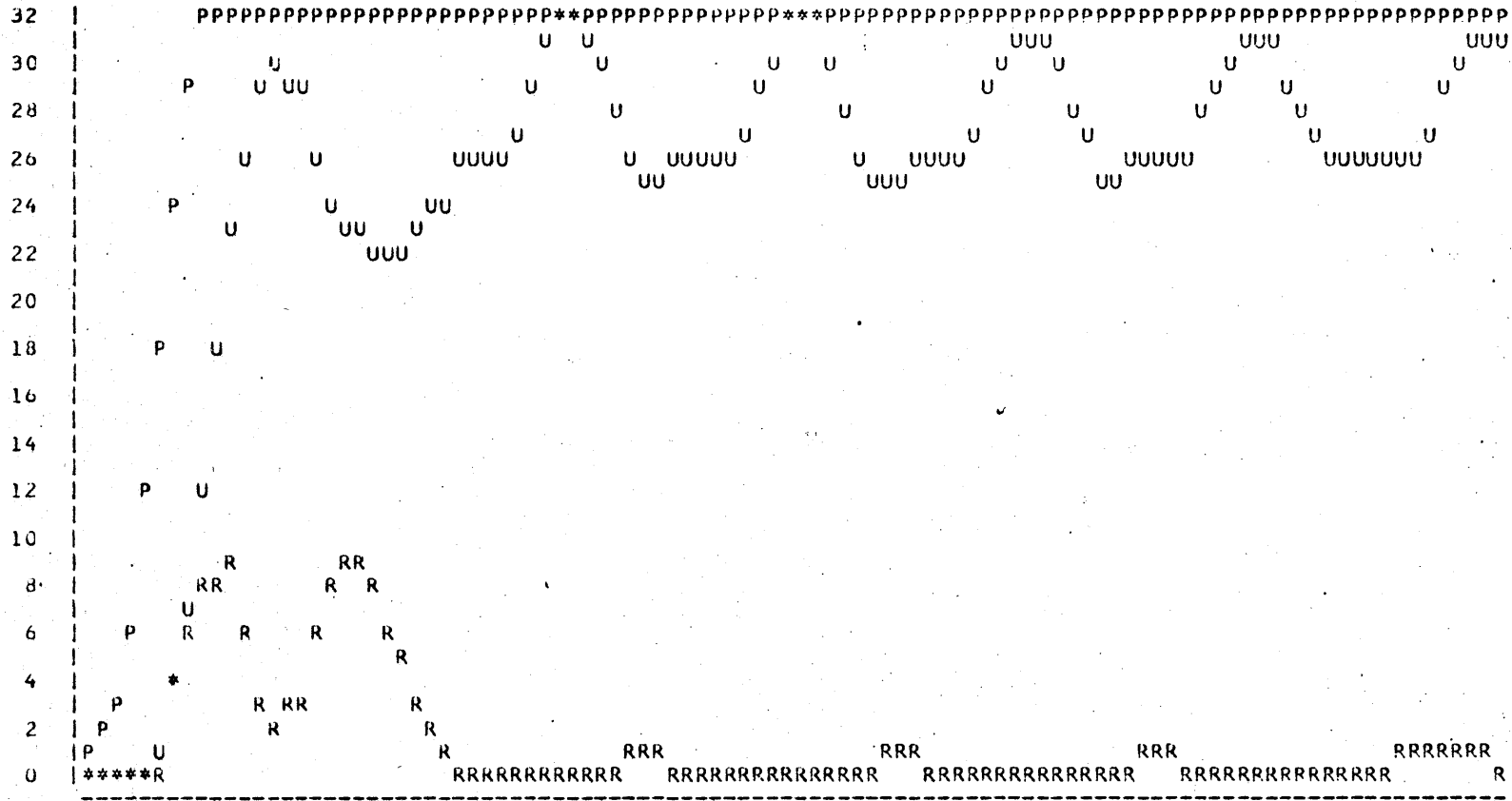
F01JAN68 3/18/68

CC0000	462+INSTRC	START 0 CONTROL SECTION FOR INPUT INSTRUCTIONS	
	463+*		
CCCC00 CCCC0000	464+IBEG	DC A(0) FOR LENGTH OF INSTRUCTIONS	
	466 *		
	467 *		
	468 *		
	469 *		
	470	PRINT ON,NOGEN	SUPRESS INSTRUCTION MACRO EXPANSIONS
	471	FORK DEX	
	480 DEX	FORK DEX2	
	489 DEX2	FORK DEX3	
	498 DEX3	FORK DEX4	
	507 DEX4	FORK TASKA	
	516 TASKA	LDX X2,C10	
	526 TK2	FADD A1,P1,A1	INST4710
	537	FSUB A1,P1,A1	INST4720
	548	FMPY A1,P1,A1	INST4730
	559	FDIV A1,P1,A1	INST4740
	570	ADDD A1,P1,A1	INST4750
	581	SUB A1,P1,A1	INST4760
	592	MPY A1,P1,A1	INST4770
	603	DIV A1,P1,A1	INST4780
	614	SFX C1,X2,X2	INST4790
	625	TXH TK2,X2,C0	INST4800
	636	TEND TASKA	INST4810
	645 *		INST4820
	646	TERM	CLEANUP
	683 *		INST4830
			INST4840

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00006528 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE

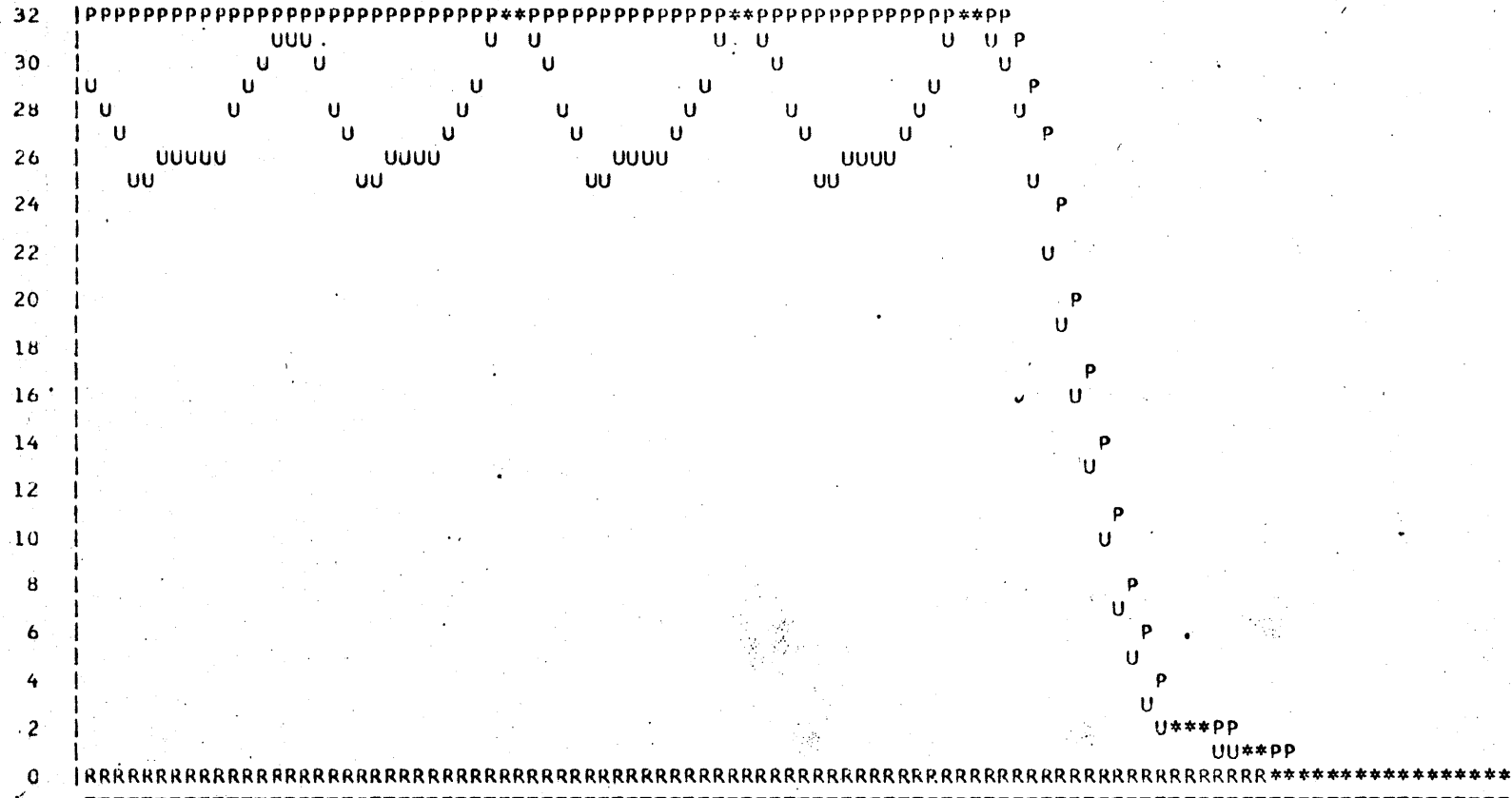


....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00012928 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

SIMULATION OF SMALL SHELL (30 X 5 ARRAY) FINAL BREAKDOWN

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
C	1	I	I .....
100	134	I	I .AAAAAAAAA..... I
200	119	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
300	119	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
400	118	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
500	118	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
600	87	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
700	87	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
800	86	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
900	86	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1000	58	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA...AA I
1100	55	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1200	55	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1300	54	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1400	41	I	I AAAAAAAAAAAAAA.....AAAAAAAAAAAA I
1500	23	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1600	23	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1700	22	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1800	22	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
1900	C	I	I A AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2000	C	I	I A AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2100	0	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2200	C	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2300	0	I	I . I
2400	C	I	I . I
2500	0	I	I . I
2600	0	I	I A I
2700	C	I	I A I
2800	0	I	I A I
2900	0	I	I .A I
3000	126	I	I AAAAAAAAA.....AAAAAAAA I
3100	82	I	I .....AAAAAAAAAAAAAAAAAAAAA... I
3200	33	I	I AAAAAAAAAAAAAA.....AAAAAAAA I
3300	0	I	I .AAAAAAA I
3400	0	I	I . I
3500	0	I	I . I
3600	C	I	I A I
3700	0	I	I A I
3800	0	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
3900	0	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4000	0	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4100	C	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4200	0	I	I A I
4300	269	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4400	269	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4500	269	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4600	268	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I
4700	238	I	I AAAAAAAAAAAAAAAAAAAAA .AAAAAAAAAAAA I



SIMULATION OF SMALL SHELL (30 X 5 ARRAY) FINAL BREAKDOWN

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
9600	C I	I	AAAAAAAAAAAAAAAAAAAAAAAAAAAAA AAAAA I
9700	C I	I	A AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
9800	C I	I	A AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
9900	C I	I	AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
10000	C I	I	AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
10100	C I	I	.
10200	C I	I	.
10300	C I	I	.

SIMULATION OF SMALL SHELL (30 X 5 ARRAY) FINAL BREAKDOWN

PROCESSOR SUMMARY

PROC	< AVAILABLE	FREE MODE TIME						>				
		< TOTAL	%	USEAGE USER	%	PROCESS	% EFF	< SYSTEM	%	OVERHEAD USER	%	
1	10400	7556	73.	6928	67.	6928	67.	71.	628	8.	0	0.0
2	10400	8084	78.	7061	68.	6950	67.	74.	1023	13.	111	1.
3	10400	7715	74.	7358	71.	7358	71.	73.	357	5.	0	0.0
4	10400	8106	78.	7743	74.	7743	74.	77.	363	4.	0	0.0
5	10400	8221	79.	7848	75.	7848	75.	78.	373	5.	0	0.0
6	10400	8232	79.	7848	75.	7848	75.	78.	384	5.	0	0.0
7	10400	7902	76.	7518	72.	7518	72.	75.	384	5.	0	0.0
8	10400	7907	76.	7513	72.	7513	72.	75.	394	5.	0	0.0
9	10400	7847	75.	7445	72.	7445	72.	74.	402	5.	0	0.0
10	10400	7855	76.	7442	72.	7442	72.	75.	413	5.	0	0.0
11	10400	7873	76.	7445	72.	7445	72.	75.	428	5.	0	0.0
12	10400	7890	76.	7483	72.	7483	72.	75.	407	5.	0	0.0
13	10400	7945	76.	7493	72.	7493	72.	75.	452	6.	0	0.0
14	10400	7885	76.	7384	71.	7384	71.	75.	501	6.	0	0.0
15	10400	7894	76.	7381	71.	7381	71.	75.	513	6.	0	0.0
16	10400	7904	76.	7381	71.	7381	71.	75.	523	7.	0	0.0
17	10400	7654	74.	7199	69.	7199	69.	72.	455	6.	0	0.0
18	10400	8516	82.	7355	71.	7169	69.	78.	1161	14.	186	2.
19	10400	7843	75.	7442	72.	7442	72.	74.	401	5.	0	0.0
20	10400	8015	77.	7602	73.	7602	73.	76.	413	5.	0	0.0
21	10400	8023	77.	7602	73.	7602	73.	76.	421	5.	0	0.0
22	10400	8031	77.	7602	73.	7602	73.	76.	429	5.	0	0.0
23	10400	8040	77.	7602	73.	7602	73.	76.	438	5.	0	0.0
24	10400	9087	87.	7918	76.	7881	76.	85.	1169	13.	37	0.
25	10400	7775	75.	7363	71.	7363	71.	74.	412	5.	0	0.0
26	10400	7165	69.	6766	65.	6766	65.	68.	399	6.	0	0.0
27	10400	7181	69.	6771	65.	6771	65.	68.	410	6.	0	0.0
28	10400	7258	70.	6845	66.	6845	66.	69.	413	6.	0	0.0
29	10400	7229	70.	6802	65.	6802	65.	68.	427	6.	0	0.0
30	10400	7274	70.	6848	66.	6848	66.	69.	426	6.	0	0.0
31	10400	7243	70.	6805	65.	6805	65.	68.	438	6.	0	0.0
32	10400	7290	70.	6856	66.	6856	66.	69.	434	6.	0	0.0
-----												
	332800	250440	75.	234649	71.	234315	70.	74.	15791	6.	334	0.



SIMULATION OF 54 X 54 ARRAY SHELL

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
C	1	I	I .....
1000	2825	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA.A.....A I
2000	2757	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
3000	2692	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
4000	2603	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAA..A I
5000	2533	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
6000	2468	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
7000	2378	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA.AA.AAAAAAAAAA..A I
8000	2309	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
9000	2244	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
10000	2153	I	I A.AAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAA..A I
11000	2085	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
12000	2020	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
13000	1930	I	I A.AAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAA..A I
14000	1861	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
15000	1790	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
16000	1700	I	I A.AAAAAAAAAAAAAAAAAA.AAAAAAAAAA.AAAAAAAAAA I
17000	1637	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
18000	1572	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
19000	1481	I	I A.AAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAA.A.A I
20000	1413	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
21000	1348	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
22000	1258	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA.AA.AAAAAAAAAA.A.A.A I
23000	1189	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
24000	1124	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
25000	1034	I	I A.AAAAAAAAAAAAAAAAAA.AA.AAAAAAAAAA.A.A.A I
26000	965	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
27000	900	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
28000	809	I	I A.AAAAAAAAAAAAAAAAAA.AAAAA.A.A I
29000	740	I	I .AAA I
30000	676	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
31000	584	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA..AAAAAAAAA I
32000	516	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
33000	452	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
34000	359	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA.A I
35000	292	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
36000	228	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
37000	134	I	I A.AAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAA I
38000	68	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
39000	4	I	I AAAAAAAAAAAAAAAAAAAAAAAAAA I
40000	0	I	I A I
41000	2499	I	I AAAAAAAAAAAAAA.....AAAAAAAAAA I
42000	2006	I	I ...AAAAAAAAAAAAAAAAAAAAAAAAA..... I
43000	1514	I	I AAA.....AAAAAAAAAAAAAAAAAAAA I
44000	1022	I	I AAAAAAAAAAAAAAAAAAAAAA.....AAAA I
45000	529	I	I .....AAAAAAAAAAAAAAAAAAAAA..... I
46000	37	I	I AAAAAAA.....AAAAAAAAAAAAA I
47000	0	I	I AAAAAAA .AAAAAAAAAAAAA I



SIMULATION OF 54 X 54 ARRAY SHELL

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
96000	383	1	I .AA I
97000	239	1	I AAAAAAAAAAAAAAAAAA.AAA.AAAAAAAAAAAAAAAAAA I
98000	93	1	I AAAAAAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
99000	0	1	I . I
100000	2885	1	I AAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
101000	2620	1	I AA I
102000	2756	1	I AA I
103000	2662	1	I AA I
104000	2597	1	I AA I
105000	2532	1	I AA I
106000	2460	1	I AAAAAAAAAAAAAAAAAA...AAAAAAAAAAAAAAAAAA I
107000	2373	1	I AA I
108000	2308	1	I AAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
109000	2244	1	I AA I
110000	2160	1	I A.A..A.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
111000	2086	1	I AA I
112000	2020	1	I AA I
113000	1956	1	I AA I
114000	1886	1	I .AAA I
115000	1797	1	I AA I
116000	1732	1	I AA I
117000	1663	1	I AAAAAAAAAAAAAAAAAAAAAA.AAA.AAAAAAAAAAAAAA I
118000	1575	1	I AA.AAA I
119000	1508	1	I AAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
120000	1444	1	I AA I
121000	1366	1	I AAA..AAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
122000	1287	1	I AA.AAA I
123000	1220	1	I AA I
124000	1156	1	I AA I
125000	1071	1	I .A.AAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
126000	998	1	I AA I
127000	932	1	I AA I
128000	864	1	I AAAAAAAAAAAAAA.AAAAAA.AAA.AAAAAAAAAAAAAA I
129000	778	1	I AA I
130000	708	1	I AA I
131000	644	1	I AA I
132000	570	1	I AAA.AAAAA.AAAA.AAAAAAAAAAAAAAAAAAAAAA I
133000	488	1	I AA I
134000	420	1	I AA I
135000	356	1	I AA I
136000	276	1	I .AAAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
137000	199	1	I AA I
138000	132	1	I AA I
139000	66	1	I AA I
140000	0	1	I AAAA AA A AA AAAAA AA AAA AA A I
141000	0	1	I A I

SIMULATION OF 54 X 54 ARRAY SHELL

PROCESSOR SUMMARY

PRCC	< AVAILABLE	FREE MODE TIME						> SYSTEM	OVERHEAD			
		< TOTAL	%	USAGE USER	%	PROCESS	% EFF		%	USER	%	
1	141743	137708	97.	131817	93.	131817	93.	97.	5891	4.	0	0.0
2	141743	137372	97.	131640	93.	131640	93.	97.	5732	4.	0	0.0
3	141743	137752	97.	132105	93.	132105	93.	97.	5647	4.	0	0.0
4	141743	137288	97.	131618	93.	131618	93.	97.	5670	4.	0	0.0
5	141743	137222	97.	131539	93.	131539	93.	97.	5683	4.	0	0.0
6	141743	138831	98.	132433	93.	132190	93.	98.	6393	5.	243	0.
7	141743	138421	98.	132761	94.	132761	94.	98.	5660	4.	0	0.0
8	141743	137885	97.	132230	93.	132230	93.	97.	5655	4.	0	0.0
9	141743	138447	98.	132329	93.	132292	93.	98.	6113	4.	37	0.
10	141743	137593	97.	131939	93.	131939	93.	97.	5654	4.	0	0.0
11	141743	136812	97.	131163	93.	131163	93.	96.	5649	4.	0	0.0
12	141743	136789	97.	131157	93.	131157	93.	96.	5632	4.	0	0.0
13	141743	136877	97.	131245	93.	131245	93.	96.	5632	4.	0	0.0
14	141743	137307	97.	131648	93.	131648	93.	97.	5659	4.	0	0.0
15	141743	136909	97.	131193	93.	131198	93.	96.	5711	4.	0	0.0
16	141743	137031	97.	131349	93.	131349	93.	97.	5682	4.	0	0.0
17	141743	138311	98.	131722	93.	131353	93.	97.	6589	5.	369	0.
18	141743	137278	97.	131704	93.	131704	93.	97.	5574	4.	0	0.0
19	141743	137244	97.	131664	93.	131664	93.	97.	5580	4.	0	0.0
20	141743	137304	97.	131721	93.	131721	93.	97.	5583	4.	0	0.0
21	141743	137280	97.	131667	93.	131667	93.	97.	5613	4.	0	0.0
22	141743	137182	97.	131558	93.	131558	93.	97.	5624	4.	0	0.0
23	141743	137360	97.	131737	93.	131737	93.	97.	5623	4.	0	0.0
24	141743	137135	97.	131488	93.	131488	93.	97.	5647	4.	0	0.0
25	141743	137287	97.	131644	93.	131644	93.	97.	5643	4.	0	0.0
26	141743	139122	98.	133200	94.	133200	94.	98.	5922	4.	0	0.0
27	141743	137273	97.	131608	93.	131608	93.	97.	5665	4.	0	0.0
28	141743	137257	97.	131592	93.	131592	93.	97.	5665	4.	0	0.0
29	141743	137493	97.	131846	93.	131846	93.	97.	5647	4.	0	0.0
30	141743	137381	97.	131690	93.	131690	93.	97.	5691	4.	0	0.0
31	141743	137346	97.	131631	93.	131631	93.	97.	5715	4.	0	0.0
32	141743	137635	97.	131920	93.	131920	93.	97.	5715	4.	0	0.0
	4535776	4400132	97.	4216563	93.	4215914	93.	97.	183569	4.	649	0.

COMPLETE RUN OF MINTZ-ARAKAWA WEATHER MODEL (1000 POINTS) FIRST RUN WITH

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
0	1	I	I ..... I
1000	0	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2000	133	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAA I
3000	64	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
4000	0	I	I A AA AAAAA AAAA AA AAAAA AAAA AA I
5000	87	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
6000	0	I	I . I
7000	0	I	I AA AA I
8000	129	I	I AAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
9000	19	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
10000	0	I	I .A I
11000	1828	I	I AAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
12000	1670	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
13000	1510	I	I AAAAAAAAAAAAAA.A.AA.AAAAAAAAAAAAA I
14000	1352	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
15000	1192	I	I AAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
16000	1033	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
17000	1045	I	I AA.AAAA.AAAAAAAAAAAAAAAAAAAAA.AA I
18000	913	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
19000	780	I	I .AA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
20000	646	I	I AAAAAA.AAAAAAAAAAAAAAAAAAAAA.AAAA I
21000	511	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
22000	375	I	I AAAAAAAAA.AAAAAAAAAAAAA.AAAAAAAAAAAAA I
23000	245	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
24000	86	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAA..AAAA I
25000	129	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
26000	27	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
27000	0	I	I AA .AAAAAA AAAA I
28000	0	I	I A A I
29000	95	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
30000	0	I	I A A A AA AAAA AA AAA A AA A I
31000	1929	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAA I
32000	1858	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
33000	1786	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
34000	1700	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.A I
35000	1619	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
36000	1542	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
37000	1460	I	I A.AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
38000	1380	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
39000	1300	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
40000	1218	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
41000	1138	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
42000	1057	I	I AAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
43000	977	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
44000	1026	I	I .AAAAA.AAAAAAAAAAAAA.AAA..AAAAAAAAAAAA I
45000	893	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
46000	757	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
47000	624	I	I AAAAAAAAAAAAAAAAAAAAA.AAA.AAAAAAAAAAAAA I

COMPLETE RUN OF MINIZ-ARAKAWA WEATHER MODEL (1000 POINTS) FIRST RUN WIT

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
48000	489	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
49000	356	I	I AAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAA I
50000	221	I	I AAA.A.AAAAAAAAAAAAAAAAAAAAAAAAAA I
51000	51	I	I AAAAAAAAAAAAAAAAA.AAAAAAA.AAAAAAAA I
52000	0	I	I AAAAA I
53000	2	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
54000	1087	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
55000	994	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
56000	1014	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
57000	867	I	I AAAAAAAAA.AAAAAAAAAAAAAAAAA.AAAAA I
58000	727	I	I AAAAAAAAA.AAAAAAAAA.AA.AAA.AAA I
59000	581	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
60000	435	I	I AAA..AAAAA.AAAAAAAAAAAAAAAAAAAAAA I
61000	297	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
62000	150	I	I AAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
63000	43	I	I AAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAA I
64000	0	I	I . I
65000	56	I	I A.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA.A. I
66000	0	I	I . I
67000	60	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
68000	0	I	I A A I
69000	82	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
70000	0	I	I AA A A AA AA A AAAAAAAAAA I
71000	833	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
72000	601	I	I ...A.AAAAAAAAAAAAAAAAAAAAAAAAAA I
73000	359	I	I AAAAA.A.AAAAAAAAAAAAAAAAAAAAAAA I
74000	128	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
75000	129	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
76000	82	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
77000	24	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
78000	0	I	I A A A A A I
79000	0	I	I AAAAAAAAAAAAAAAAAAAAA AAAAAAAA I
80000	0	I	I AAAAA AA AA I
81000	0	I	I AAAAA AA AA I
82000	0	I	I AA A A I
83000	885	I	I AAA.....AAAAAAAAAAAAAAAAAAAAAAAAA I
84000	654	I	I AAAAAAAAAAAAAAAAA..AAAAAAAAAAAAA I
85000	422	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.A.AAA I
86000	192	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
87000	0	I	I . I
88000	772	I	I AAAAAAAAA.AAAAAAAAA...A.AAAAA I
89000	544	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
90000	310	I	I AAA..AAAAAAAAAAAAAAAAAAAAAAAAAAAA I
91000	66	I	I AAAAAAAAA.AAAAAAAAA..AAA.AAAAA I
92000	121	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
93000	62	I	I AAAAAAA.AAA.AAAAAAAAA.AAAAAAAA I
94000	7	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
95000	0	I	I . I

COMPLETE RUN OF MINIZ-ARAKAWA WEATHER MODEL (1000 POINTS) FIRST RUN WITH

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
96000	0	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AA I
97000	0	I	I AAAA AA AA I
98000	0	I	I AA A A I
99000	0	I	I AA A A I
100000	833	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
101000	607	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA..AA I
102000	376	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
103000	143	I	I A....AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
104000	0	I	I A I
105000	9	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
106000	9	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
107000	0	I	I .AAAAAAAAA I
108000	95	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
109000	31	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
110000	0	I	I AAA A A A I
111000	99	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
112000	29	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
113000	159	I	I ..... I
114000	5	I	I AAAAAAAAAAAAAAAAAA AAAAA.AAA.AAAA I
115000	0	I	I AAAAAA AAAAAA I
116000	0	I	I A A I
117000	61	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
118000	0	I	I . I
119000	1881	I	I AAAAAAAAAAAAAAAAAA.....AAAAAAAAA I
120000	1722	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
121000	1558	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
122000	1401	I	I AAAAAAAAAAAAAAAAAA AAAAA.AAAAAAAAAA I
123000	1242	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
124000	1083	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
125000	928	I	I AAAAAAAAAAAAAAAAAA.AAAAAAAAAA AAAAAAAAAA I
126000	956	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA.AAA..AAAA I
127000	822	I	I AAAAAAAAAAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
128000	688	I	I AAAA.AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
129000	553	I	I AAAAAAAAAA.AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
130000	417	I	I .AAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
131000	283	I	I AAAA.AAAAAAAAAA AAAAAAAAAA.AAAAAAAAAA I
132000	151	I	I AAAAA.AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
133000	0	I	I . I
134000	60	I	I AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
135000	0	I	I . I
136000	0	I	I AA AA I
137000	122	I	I AAAAAAAAAA AAAAA.AAA.AAAAAAAAAA I
138000	3	I	I AAAAAAAAAA AAAAAAAAAA.AAAAAAAAAA I
139000	0	I	I A I
140000	1865	I	I AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
141000	1790	I	I AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
142000	1714	I	I AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA I
143000	1627	I	I AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA AAAAAAAAAA. I

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
144000	1549	I	I AA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
145000	1468	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
146000	1386	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
147000	1308	I	I AAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
148000	1226	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
149000	1146	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
150000	1065	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
151000	983	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
152000	1038	I	I AAAAAAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAA I
153000	903	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
154000	770	I	I AAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
155000	633	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AA.AAA I
156000	501	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAAAAAAAAAA I
157000	367	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAA I
158000	232	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAA.AAAA I
159000	68	I	I AA.AAAAAAAAAAAAAAAAAAAAA.AA.AAA I
160000	0	I	I .AAAA I
161000	29	I	I AAAAAAAAAAAAAAAAAAAAA.AAAAA I
162000	1095	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
163000	1003	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
164000	1026	I	I AAAAAAAAAAAAA.AAAAAAAAAAAAAAAAAAAAA I
165000	880	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
166000	735	I	I AAAAA.AAAAAAAAAAAAAAAAAAAAA I
167000	590	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
168000	452	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
169000	305	I	I AA.AAAAAAAAA.AAAAAAAAAAAAA.A I
170000	159	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
171000	51	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
172000	0	I	I . I
173000	60	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
174000	0	I	I A AA A A I
175000	81	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAA I
176000	0	I	I A AA A I
177000	82	I	I AAAAAAAAAAAAA.AAAAAAAAAAAAA I
178000	0	I	I A A AA AAAAA AAAAAAAAAA AA I
179000	848	I	I AA.AAAAA.A.AAAAAAAAAAAAAAAAAAAAA I
180000	608	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
181000	360	I	I AA.AAAAAAAAAAAAA.AAAA.AAAAAAA I
182000	128	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAA I
183000	129	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA.AAAAAAA I
184000	82	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
185000	7	I	I AAAAA.AAA.AAAAAAAAAAAAAAAAAAAAA I
186000	0	I	I . I
187000	0	I	I AAAAAAAAAAAAA AAAAAAA AAAAA I
188000	0	I	I AAAA AA AA I
189000	0	I	I AA A A I
190000	0	I	I AA A A I
191000	832	I	I AAAAAAAAAAAAAAAAAAAAAAAAAAAAA I





COMPLETE RUN OF MINIZ-ARAKAWA WEATHER MODEL (1000 POINTS) FIRST RUN WIT

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
240000	545	I	I AA I
241000	523	I	I AA I
242000	509	I	I AA I
243000	488	I	I AA I
244000	474	I	I AA I
245000	455	I	I AA I
246000	434	I	I AA I
247000	420	I	I AA I
248000	398	I	I AA I
249000	385	I	I AA I
250000	365	I	I AA I
251000	352	I	I AAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
252000	330	I	I AA I
253000	315	I	I AA I
254000	294	I	I AA I
255000	278	I	I AA I
256000	261	I	I AAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
257000	242	I	I AA I
258000	226	I	I AA I
259000	204	I	I AA I
260000	192	I	I AA I
261000	173	I	I AA I
262000	158	I	I AA I
263000	140	I	I AAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
264000	118	I	I AA I
265000	106	I	I AA I
266000	88	I	I AA I
267000	69	I	I AA I
268000	49	I	I AA I
269000	32	I	I AAAAAA.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
270000	16	I	I AA I
271000	0	I	I AAAAAAAAAAAAAA AA AAAAAA AAAAAAA I
272000	0	I	I AA A AAAA A AAA I
273000	0	I	I . I
274000	653	I	I AAAAAAAAAAAAAAAAAAAAAA.AAAA.AAAAAAAAAA I
275000	288	I	I AA.A.A.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
276000	0	I	I . I

COMPLETE RUN OF MINTZ-ARAKAWA WEATHER MODEL (1000 POINTS) FIRST RUN WITH COMP3

PROCESSOR SUMMARY

PROC	FREE MODE TIME											
	< AVAILABLE	< TOTAL	%	USEAGE USER	%	PROCESS	%	> EFF	< SYSTEM	%	OVERHEAD USER	> %
1	276200	232123	84.	221318	80.	220458	80.	83.	10805	5.	860	0.
2	276200	229961	83.	220410	80.	220410	80.	83.	9551	4.	0	0.0
3	276200	230596	83.	220961	80.	220779	80.	83.	9635	4.	182	0.
4	276200	235325	85.	226179	82.	226179	82.	85.	9146	4.	0	0.0
5	276200	236389	86.	227727	82.	227727	82.	85.	8662	4.	0	0.0
6	276200	235834	85.	226431	82.	226431	82.	85.	9403	4.	0	0.0
7	276200	233031	84.	224413	81.	224413	81.	84.	8618	4.	0	0.0
8	276200	232604	84.	223395	81.	223395	81.	84.	9209	4.	0	0.0
9	276200	235438	85.	224983	81.	224801	81.	85.	10455	4.	182	0.
10	276200	233176	84.	224495	81.	224495	81.	84.	8681	4.	0	0.0
11	276200	235282	85.	225949	82.	225949	82.	85.	9333	4.	0	0.0
12	276200	236955	86.	228176	83.	228176	83.	85.	8779	4.	0	0.0
13	276200	234435	85.	224428	81.	224428	81.	84.	10007	4.	0	0.0
14	276200	236748	86.	226256	82.	226256	82.	85.	10492	4.	0	0.0
15	276200	238482	86.	227955	83.	227955	83.	86.	10527	4.	0	0.0
16	276200	238774	86.	228840	83.	228840	83.	86.	9934	4.	0	0.0
17	276200	237084	86.	225468	82.	224176	81.	85.	11616	5.	1292	1.
18	276200	235798	85.	225493	82.	225493	82.	85.	10305	4.	0	0.0
19	276200	235839	85.	226587	82.	226587	82.	85.	9252	4.	0	0.0
20	276200	234983	85.	225512	82.	225512	82.	85.	9471	4.	0	0.0
21	276200	240863	87.	229968	83.	229968	83.	87.	10895	5.	0	0.0
22	276200	238760	86.	228080	83.	228080	83.	86.	10680	4.	0	0.0
23	276200	239103	87.	226428	82.	225318	82.	86.	12675	5.	1110	0.
24	276200	232666	84.	222424	81.	222424	81.	84.	10242	4.	0	0.0
25	276200	236215	86.	226574	82.	226574	82.	85.	9641	4.	0	0.0
26	276200	233576	85.	224389	81.	224389	81.	84.	9187	4.	0	0.0
27	276200	229203	83.	220490	80.	220490	80.	82.	8713	4.	0	0.0
28	276200	233165	84.	222771	81.	222589	81.	84.	10394	4.	182	0.
29	276200	231254	84.	220572	80.	220463	80.	83.	10682	5.	109	0.
30	276200	229679	83.	221257	80.	221257	80.	83.	8422	4.	0	0.0
31	276200	232758	84.	223659	81.	223659	81.	84.	9099	4.	0	0.0
32	276200	234787	85.	224473	81.	224473	81.	84.	10314	4.	0	0.0
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	8838400	7510886	85.	7196061	81.	7192144	81.	84.	314825	4.	3917	0.







FOURTH BREAK-DOWN OF RADISH5 4 APRIL 1968 (1600 PDINTS)

PROCESSOR SUMMARY

PROC	FREE MODE TIME											
	AVAILABLE	TOTAL	%	USER	%	PROCESS	%	EFF	SYSTEM	%	USER	%
1	1413825	1163030	82.	1142098	81.	1142098	81.	82.	20932	2.	0	0.0
2	1413825	1161976	82.	1139296	81.	1139296	81.	82.	22680	2.	0	0.0
3	1413825	1189512	84.	1144446	81.	1122828	79.	82.	45066	4.	21618	2.
4	1413825	1172152	83.	1152611	82.	1152611	82.	83.	19541	2.	0	0.0
5	1413825	1176462	83.	1145568	81.	1138053	80.	82.	30694	3.	7515	1.
6	1413825	1180593	83.	1143035	81.	1128696	80.	82.	37353	3.	14339	1.
7	1413825	1167421	83.	1147325	81.	1147325	81.	82.	20096	2.	0	0.0
8	1413825	1175732	83.	1146501	81.	1139301	81.	82.	29231	2.	7200	1.
9	1413825	1160680	82.	1140051	81.	1140051	81.	82.	20629	2.	0	0.0
10	1413825	1175596	83.	1145749	81.	1138549	81.	82.	29847	3.	7200	1.
11	1413825	1210752	86.	1172092	83.	1157715	82.	84.	38660	3.	14377	1.
12	1413825	1204573	85.	1176378	83.	1169163	83.	84.	28195	2.	7215	1.
13	1413825	1211994	86.	1184042	84.	1176842	83.	85.	27952	2.	7200	1.
14	1413825	1215720	86.	1194226	84.	1194226	84.	86.	21494	2.	0	0.0
15	1413825	1201839	85.	1181601	84.	1181601	84.	85.	20233	2.	0	0.0
16	1413825	1201909	85.	1180731	84.	1180731	84.	85.	21173	2.	0	0.0
17	1413825	1130583	84.	1168495	83.	1168495	83.	84.	22083	2.	0	0.0
18	1413825	1218566	86.	1180039	83.	1165674	82.	85.	38477	3.	14415	1.
19	1413825	1230041	87.	1199546	85.	1192346	84.	86.	30495	2.	7200	1.
20	1413825	1206768	85.	1176815	83.	1169606	83.	85.	29953	2.	7209	1.
21	1413825	1216466	86.	1187140	84.	1179950	83.	85.	29326	2.	7190	1.
22	1413825	1225215	87.	1196496	85.	1189259	84.	86.	28719	2.	7237	1.
23	1413825	1222611	86.	1193835	84.	1186676	84.	86.	28726	2.	7209	1.
24	1413825	1230043	87.	1202315	85.	1195115	85.	86.	27723	2.	7200	1.
25	1413825	1211960	86.	1191838	84.	1191838	84.	86.	20122	2.	0	0.0
26	1413825	1215721	86.	1194595	84.	1194595	84.	86.	21126	2.	0	0.0
27	1413825	1206730	85.	1186082	84.	1186082	84.	85.	20648	2.	0	0.0
28	1413825	1198456	84.	1166509	83.	1166509	83.	84.	21947	2.	0	0.0
29	1413825	1175676	83.	1154723	82.	1154723	82.	83.	20953	2.	0	0.0
30	1413825	1191452	84.	1162606	82.	1155406	82.	83.	28346	2.	7200	1.
31	1413825	1188718	84.	1168860	83.	1168860	83.	84.	19858	2.	0	0.0
32	1413825	1179666	83.	1158494	82.	1158494	82.	83.	21172	2.	0	0.0
	45242400	38268413	85.	37424238	83.	37272714	82.	84.	844175	2.	151524	0.







INITIAL BREAK-DOWN OF MONTE-CARLO METHOD FOR 25000 POINTS ON 32 V.P.'S

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
960000	C I	I	AA I
970000	O I	I	AA I
980000	O I	I	AA I
990000	C I	I	AA I
1000000	O I	I	AA I
1010000	C I	I	AA I
1020000	O I	I	AA I
1030000	C I	I	AA I
1040000	C I	I	AA I
1050000	C I	I	AA I
1060000	C I	I	AA I
1070000	C I	I	AA I
1080000	C I	I	AA I
1090000	O I	I	AA I
1100000	C I	I	AA I
1110000	O I	I	AA I
1120000	C I	I	AA I
1130000	O I	I	AA I
1140000	O I	I	AA I
1150000	C I	I	AA I
1160000	O I	I	AA I
1170000	C I	I	AA I
1180000	O I	I	AA I
1190000	C I	I	AAAAAA AAAAAAAAAAAAAAAAAAAAAAAAAA AAAAA I
1200000	O I	I	AAAAA A AAAAAA A AAAAAAAAAAAAA AAAAA I
1210000	C I	I	A A A AAAAA A AAAAAA A AAA I
1220000	O I	I	A AA A A AA A A I
1230000	C I	I	A A A I

INITIAL BREAK-DOWN OF MONTE-CARLO METHOD FOR 25000 POINTS ON 32 V.P.'S

PROCESSOR SUMMARY

PROC	FREE MODE TIME									
	AVAILABLE	TOTAL	%	USER	%	PROCESS	% EFF	SYSTEM	OVERHEAD	USER
1	1243355	1170797	94.	1170518	94.	1170518	94. 94.	279	0.	0 0.0
2	1243355	1218550	98.	1218539	98.	1218539	98. 98.	11	0.	0 0.0
3	1243355	1205438	97.	1205426	97.	1205426	97. 97.	12	0.	0 0.0
4	1243355	1228844	99.	1228831	99.	1228831	99. 99.	13	0.	0 0.0
5	1243355	1203812	97.	1203798	97.	1203798	97. 97.	14	0.	0 0.0
6	1243355	1199327	96.	1199312	96.	1199312	96. 96.	15	0.	0 0.0
7	1243355	1216961	98.	1216945	98.	1216945	98. 98.	16	0.	0 0.0
8	1243355	1186036	95.	1186019	95.	1186019	95. 95.	17	0.	0 0.0
9	1243355	1220162	98.	1220144	98.	1220144	98. 98.	18	0.	0 0.0
10	1243355	1227799	99.	1227780	99.	1227780	99. 99.	19	0.	0 0.0
11	1243355	1218511	98.	1218491	98.	1218491	98. 98.	20	0.	0 0.0
12	1243355	1212950	98.	1212929	98.	1212929	98. 98.	21	0.	0 0.0
13	1243355	1209774	97.	1209752	97.	1209752	97. 97.	22	0.	0 0.0
14	1243355	1199782	96.	1199759	96.	1199759	96. 96.	23	0.	0 0.0
15	1243355	1223840	98.	1223816	98.	1223816	98. 98.	24	0.	0 0.0
16	1243355	1197958	96.	1197933	96.	1197933	96. 96.	25	0.	0 0.0
17	1243355	1205132	97.	1205106	97.	1205106	97. 97.	26	0.	0 0.0
18	1243355	1226687	99.	1226660	99.	1226660	99. 99.	27	0.	0 0.0
19	1243355	1219548	98.	1219520	98.	1219520	98. 98.	28	0.	0 0.0
20	1243355	1234491	99.	1234462	99.	1234462	99. 99.	29	0.	0 0.0
21	1243355	1222008	98.	1221978	98.	1221978	98. 98.	30	0.	0 0.0
22	1243355	1217266	98.	1217235	98.	1217235	98. 98.	31	0.	0 0.0
23	1243355	1204781	97.	1204749	97.	1204749	97. 97.	32	0.	0 0.0
24	1243355	1203227	97.	1203194	97.	1203194	97. 97.	33	0.	0 0.0
25	1243355	1206077	97.	1206043	97.	1206043	97. 97.	34	0.	0 0.0
26	1243355	1219935	98.	1219900	98.	1219900	98. 98.	35	0.	0 0.0
27	1243355	1179072	95.	1179036	95.	1179036	95. 95.	36	0.	0 0.0
28	1243355	1235569	99.	1235532	99.	1235532	99. 99.	37	0.	0 0.0
29	1243355	1214770	98.	1214732	98.	1214732	98. 98.	38	0.	0 0.0
30	1243355	1243347	100.	1243044	100.	1243044	100.100.	303	0.	0 0.0
31	1243355	1202411	97.	1202371	97.	1202371	97. 97.	40	0.	0 0.0
32	1243355	1209223	97.	1209182	97.	1209182	97. 97.	41	0.	0 0.0
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	39787360	38784085	97.	38782736	97.	38782736	97. 97.	1349	0.	0 0.0

INITIAL BREAK-DOWN OF LOS ALAMOS PAD (PISTON ACTUATED DETONATION)

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
0	1 I	I	I .....
50	0 I	I	I A
100	5 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAA.....
150	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
200	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
250	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
300	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
350	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
400	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
450	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
500	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
550	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
600	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
650	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
700	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
750	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
800	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
850	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
900	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
950	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1000	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1050	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1100	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1150	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1200	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1250	0 I	I	I .
1300	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1350	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1400	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1450	0 I	I	I .....
1500	0 I	I	I .AAAAA
1550	0 I	I	I A
1600	5 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAA.....
1650	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1700	26 I	I	I .....
1750	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1800	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1850	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1900	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
1950	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2000	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2050	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2100	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2150	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2200	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2250	0 I	I	I .AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
2300	0 I	I	I . . . . . AA . . . . .
2350	0 I	I	I .....

INITIAL BREAK-DOWN OF LOS ALAMOS PAD (PISTON ACTUATED DETONATION)

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
2400	0 I	I	.AAAAA I
2450	0 I	I	A I
2500	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA... I
2550	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2600	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2650	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2700	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2750	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2800	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2850	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2900	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
2950	0 I	I	.AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA I
3000	0 I	I	.AA .AAAAAAAAAAAAAAAAAAAAAAAA I
3050	0 I	I	.AA I
3100	0 I	I	.AA I
3150	0 I	I	.AA I
3200	0 I	I	.AA I
3250	0 I	I	.AA I
3300	0 I	I	.AA I
3350	0 I	I	.AA I
3400	0 I	I	.AA I
3450	0 I	I	.AA I
3500	0 I	I	.AA I
3550	0 I	I	.AA I
3600	0 I	I	.AA I
3650	0 I	I	.AA I
3700	0 I	I	.AA I
3750	0 I	I	.AA I
3800	0 I	I	.AA I
3850	0 I	I	.AA I
3900	0 I	I	.AA I
3950	0 I	I	.AA I
4000	0 I	I	.AA I
4050	0 I	I	.AA I
4100	0 I	I	.AA I
4150	0 I	I	.AA I
4200	0 I	I	.A I
4250	0 I	I	.A I
4300	0 I	I	.A I
4350	0 I	I	.A I
4400	0 I	I	.A I
4450	0 I	I	.A I
4500	0 I	I	.A I
4550	0 I	I	.A I
4600	0 I	I	.A I
4650	0 I	I	.A I
4700	0 I	I	.A I
4750	0 I	I	A I

INITIAL BREAK-DOWN OF LOS ALAMOS PAD (PISTON ACTUATED DETONATION)

PROCESSOR TIME TRACE

TIME	TASKS	G/C	PROCESSORS
4800	0 I	I	.AA I
4850	0 I	I	.AA I
4900	0 I	I	.AA I
4950	0 I	I	.AA I
5000	0 I	I	. .AAAA I
5050	0 I	I	.A.. I
5100	0 I	I	.AAA I
5150	0 I	I	.AAA I
5200	0 I	I	A I
5250	0 I	I	A I

INITIAL BREAK-DOWN OF LOS ALAMOS PAD (PISTON ACTUATED DETONATION)

PROCESSOR SUMMARY

PROC	<		FREE MODE TIME				>			
	AVAILABLE	TOTAL %	USER %	PROCESS %	EFF	SYSTEM %	OVERHEAD %	USER %		
1	5290	5290100.	2609 49.	363 7.	14.	2681 51.		2246 42.		
2	5290	4727 89.	4629 88.	4629 88.	89.	98 2.		0 0.0		
3	5290	4181 79.	4075 77.	4075 77.	79.	106 3.		0 0.0		
4	5290	3034 57.	2910 55.	2910 55.	56.	124 4.		0 0.0		
5	5290	2916 55.	2793 53.	2793 53.	54.	123 4.		0 0.0		
6	5290	2926 55.	2793 53.	2793 53.	54.	133 5.		0 0.0		
7	5290	2792 53.	2673 51.	2673 51.	52.	119 4.		0 0.0		
8	5290	2798 53.	2673 51.	2673 51.	52.	125 4.		0 0.0		
9	5290	2804 53.	2673 51.	2673 51.	52.	131 5.		0 0.0		
10	5290	2810 53.	2673 51.	2673 51.	52.	137 5.		0 0.0		
11	5290	2816 53.	2673 51.	2673 51.	52.	143 5.		0 0.0		
12	5290	2822 53.	2664 50.	2664 50.	52.	158 6.		0 0.0		
13	5290	2828 53.	2667 50.	2667 50.	52.	161 6.		0 0.0		
14	5290	2835 54.	2661 50.	2661 50.	52.	174 6.		0 0.0		
15	5290	2812 53.	2652 50.	2652 50.	52.	160 6.		0 0.0		
16	5290	2818 53.	2652 50.	2652 50.	52.	166 6.		0 0.0		
17	5290	2824 53.	2652 50.	2652 50.	52.	172 6.		0 0.0		
18	5290	2830 53.	2652 50.	2652 50.	52.	178 6.		0 0.0		
19	5290	2836 54.	2652 50.	2652 50.	52.	184 6.		0 0.0		
20	5290	2842 54.	2652 50.	2652 50.	52.	190 7.		0 0.0		
21	5290	2848 54.	2652 50.	2652 50.	52.	196 7.		0 0.0		
22	5290	2854 54.	2652 50.	2652 50.	52.	202 7.		0 0.0		
23	5290	2860 54.	2652 50.	2652 50.	52.	208 7.		0 0.0		
24	5290	2866 54.	2652 50.	2652 50.	52.	214 7.		0 0.0		
25	5290	2872 54.	2652 50.	2652 50.	52.	220 8.		0 0.0		
26	5290	2878 54.	2652 50.	2652 50.	52.	226 8.		0 0.0		
27	5290	2884 55.	2652 50.	2652 50.	52.	232 8.		0 0.0		
28	5290	2890 55.	2652 50.	2652 50.	52.	238 8.		0 0.0		
29	5290	2899 55.	2652 50.	2652 50.	53.	247 9.		0 0.0		
30	5290	2242 42.	1970 37.	1970 37.	39.	272 12.		0 0.0		
31	5290	1117 21.	861 16.	861 16.	17.	256 23.		0 0.0		
32	5290	1123 21.	861 16.	861 16.	17.	262 23.		0 0.0		
-----										
	169280	92874 55.	84638 50.	82392 49.	51.	8236 9.		2246 2.		

LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

F01JAN68 4/04/68

C00000 462+INSTRC START 0 CONTRCL SECTION FOR INPUT INSTRUCTIONS  
 000000 00000000 463+\*  
 464+IBEG DC A(0) FOR LENGTH OF INSTRUCTIONS

466 \*  
 467 \*  
 468 \*  
 469 \*  
 470 PRINT ON,NUGEN SUPRESS INSTRUCTION MACRO EXPANSIONS  
 471 \*  
 472 \* INSTRUCTIONS TO SIMULATE 'SUBROUTINE CMP108'  
 473 \*  
 474 \*

P R I V A T E S T O R E A S S I G N M E N T S

VARIABLE	PRIVATE STORE LOCATION
J	0 (XR0)
IP1	1 (XR1)
I	2 (XR2)
IM	3 (XR3)
IM1	4 (XR4)
LOOP CNTRL	5 (XR5)
JM	6 (XR6)
MRCH	7 (XR7)
CONST 1	8
CONST 2	9
CONST 3	10
CONST 4	11
CONST 7	12
CONST 10	13
CONST 0.25	14
CONST 0.5	15
ARRAY PU	20
ARRAY P	40
ARRAY PV	60
ARRAY DXU	80
ARRAY V	100
.TEMP.	19

503 \*  
 504 \*  
 505 \* GET ALL PROCESSORS GOING ON TASK  
 506 \*  
 507 A FORK B  
 516 B FORK C  
 525 C FORK D  
 534 D FORK E  
 543 E FORK CMP108  
 552 \*



LLC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

FO1JAN68 4/04/68

```

553 CMP108 NOOP , ENTRY FOR SUBROUTINE CMP108
561 *
562 * INITIALIZE REGISTERS FOR LOOP
563 *
564 LDX X2,C5 SET I TO 5
574 LDX X3,C31 SET IM TO 31
584 LDX X6,C30 SET JM TO 30
594 LDX X7,C2 SET MRCH TO 2 - THEN 3, 4, 2, 3, 4, ETC
604 *
605 LDX X0,C0 START J AT 0
615 LDX X5,C1 START LOOP CONTROL AT 1
625 *
626 *
627 * INSTRUCTION COUNTING STATEMENTS, I.E. CTR=CTR+4, ARE NOT SIMULATED
628 *
629 LOOP ATX C1,X0,X0 J=J+1
640 TXH SN10,X0,C2 IF(J.NE.2)
651 TXLE SN10,X0,C1 GO TO 10
662 ATX C1,X2,X1 IP1=I+1
673 TXH SKIP1,X2,X3 IF(I.EQ.IM) .... IP=1
684 SFX C1,X3,X4 TEMP - X4=IM-1
695 TXLE SKIP1,X2,X4 IF .LE. IM-1, THEN .NE. IM
706 LDX X1,C1 IP1=1
716 SKIP1 LDX X4,X2 IM1=
726 SFX C1,X4,X4 I-1
737 TXH SN10,X2,C1 IF(.NOT.(I.EQ.1))
748 TXLE SN10,X2,C0 GO TO 10
759 LDX X4,X3 IM1=IM
769 *
770 SN10 NOOP , WAS CTR=CTR+4
778 LOD A1,X5 SIMULATE SAVING 'CTR' SINCE
788 STO A1,P19 IT DOES NOT AFFECT FLOW
798 TXH SN2170,X0,X6 IF(J.EQ.JM)...GO TO 2175 - BRA IF HIGHER
809 SFX C1,X6,X6 TEMP - JM=JM-1
820 TXLE SKIP2,X0,X6 COMPARING J TO JM-1
831 ATX C1,X6,X6 RESTORING JM
842 BRA SN2175 GO TO 2175 (J.EQ.JM)
851 *
852 SKIP2 ATX C1,X6,X6 RESTORING JM (HERE IF J.NE.JM)
863 *
864 SN2170 LOD A1,P40 P(J,I)
874 FADD A1,P40,A1 + P(J,IP1)
885 FMPY A1,P20,A1 ABOVE QUANTITY * PU(J,I)
896 STO A1,P20 PU(J,I)= ABOVE RESULT
906 *
907 SN2175 NOOP , WAS CTR=CTR+4
915 TXH SN11,X7,C2 IF(.NOT.(MRCH.LE.2)) GO TO 11
926 *
927 LOD A1,P100 V(J,I,L)
937 FADD A1,P100,A1 + V(J,IM1,L)
948 STO A1,P19 SAVE IN TEMP
958 LOD A1,P40 P(J,I)
968 FADD A1,P40,A1 + P(J-1,I)
979 FMPY A1,P19,A1 * (V(J,I,L) + V(J,IM1,L))
990 FMPY A1,P80,A1 * DXU(J)
    
```

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT		FO1JAN68	4/04/68
1001				FMPY	A1,P14,A1	* 0.25		
1012				STO	A1,P60	PV(J,I) = ABOVE PRODUCT		
1022	*							
1023	*							
1024	SN11			NOOP	,	WAS CTR=CTR+4		
1032				TXH	SN12,X7,C3	IF(.NOT.(MRCH.EQ.3))		
1043				TXLE	SN12,X7,C2	GO TO 12		
1054	*							
1055				LDD	A1,P40	P(J,I)		
1065				FADD	A1,P40,A1	+ P(J-1,I)		
1076				FMPY	A1,P100,A1	* V(J,I,L)		
1087				FMPY	A1,P80,A1	* DXU(J)		
1098				FMPY	A1,P15,A1	*0.5		
1109				STO	A1,P60	PV(J,I) = ABOVE PRODUCT		
1119	*							
1120	SN12			NOOP	,	WAS CTR=CTR+4		
1128				TXH	SN13,X7,C4	IF(.NOT.(MRCH.EQ.4))		
1139				TXLE	SN13,X7,C3	GO TO 13		
1150	*							
1151				LDD	A1,P40	P(J,I)		
1161				FADD	A1,P40,A1	+ P(J-1,I)		
1172				FMPY	A1,P100,A1	* V(J,IM1,L)		
1183				FMPY	A1,P80,A1	* DXU(J)		
1194				FMPY	A1,P15,A1	*0.5		
1205				STO	A1,P60	PV(J,I) = ABOVE PRODUCT		
1215	*							
1216	SN13			NOOP	,	WAS CTR=CTR+4		
1224				TXH	SN2181,X0,X6	IF(J.NE.JM) .... GO TO 2181		
1235				SFX	C1,X6,X6	TEMP - JM=JM-1		
1246				TXLE	SKIP3,X0,X6	BRANCH IF J.LE. JM-1 (.NE. JM)		
1257				ATX	C1,X6,X6	RESTORING JM ( J.EQ. JM )		
1268				BRA	SKIP4	BRANCH AROUND EQUAL CASE		
1277	*							
1278	SKIP3			ATX	C1,X6,X6	RESTORING JM - J.NE. JM		
1289				BRA	SN2181	GO TO 2181		
1298	*							
1299	SKIP4			ATX	C1,X2,X2	I=I+1		
1310				LDD	X0,C1	J=1		
1320	*							
1321	SN2181			NOOP	SN2181	CONTINUE		
1330	*							
1331				ATX	C1,X5,X5	LOOP COUNTER UP BY ONE		
1342				TXH	TENDCMP,X5,C30	BRANCH TO TEND IF DONE		
1353	*							
1354				ATX	C1,X7,X7	MRCH=MRCH+1		
1365				TXLE	LOOP,X7,C4	BRANCH BACK IF MRCH STILL LE 4		
1376	*							
1377				LDD	X7,C2	RESET MRCH TO 2		
1387				BRA	LOOP			
1396	*							
1397	*							
1398	TENDCMP			TEND	CMP108			
1407				TERM		CLEANUP		
1528	*							
1529	*							

PRINTOUT OF ASSEMBLED INPUT INSTRUCTIONS

INSTRUCTION NUMBER	1	FORK	,,12
INSTRUCTION NUMBER	2	FURK	,,13
INSTRUCTION NUMBER	3	FORK	,,14
INSTRUCTION NUMBER	4	FORK	,,15
INSTRUCTION NUMBER	5	FORK	,,16
INSTRUCTION NUMBER	6	NGCP	,,
INSTRUCTION NUMBER	7	LDX	C5,,X2
INSTRUCTION NUMBER	8	LDX	C31,,X3
INSTRUCTION NUMBER	9	LDX	C30,,X6
INSTRUCTION NUMBER	10	LDX	C2,,X7
INSTRUCTION NUMBER	11	LDX	C0,,X0
INSTRUCTION NUMBER	12	LDX	C1,,X5
INSTRUCTION NUMBER	13	ATX	C1,X0,X0
INSTRUCTION NUMBER	14	TXH	X0,C2,I26
INSTRUCTION NUMBER	15	TXLE	X0,C1,I26
INSTRUCTION NUMBER	16	ATX	C1,X2,X1
INSTRUCTION NUMBER	17	TXH	X2,X3,I21
INSTRUCTION NUMBER	18	SFX	C1,X3,X4
INSTRUCTION NUMBER	19	TXLE	X2,X4,I21
INSTRUCTION NUMBER	20	LDX	C1,,X1
INSTRUCTION NUMBER	21	LDX	X2,,X4
INSTRUCTION NUMBER	22	SFX	C1,X4,X4
INSTRUCTION NUMBER	23	TXH	X2,C1,I26
INSTRUCTION NUMBER	24	TXLE	X2,C0,I26
INSTRUCTION NUMBER	25	LDX	X3,,X4
INSTRUCTION NUMBER	26	NOOP	,,
INSTRUCTION NUMBER	27	LOD	X5,,A1
INSTRUCTION NUMBER	28	STO	A1,,P19
INSTRUCTION NUMBER	29	TXH	X0,X6,I35

INSTRUCTION NUMBER	30	STX	C1,X6,X6
INSTRUCTION NUMBER	31	TXLE	X6,X6,I54
INSTRUCTION NUMBER	32	ATX	C1,X6,X6
INSTRUCTION NUMBER	33	BRA	,,I39
INSTRUCTION NUMBER	34	ATX	C1,X6,X6
INSTRUCTION NUMBER	35	LOD	P40,,A1
INSTRUCTION NUMBER	36	FADD	A1,P40,A1
INSTRUCTION NUMBER	37	FMPY	A1,P20,A1
INSTRUCTION NUMBER	38	STO	A1,,P20
INSTRUCTION NUMBER	39	NOCP	,,
INSTRUCTION NUMBER	40	TXH	X7,C2,I50
INSTRUCTION NUMBER	41	LOD	P100,,A1
INSTRUCTION NUMBER	42	FADD	A1,P100,A1
INSTRUCTION NUMBER	43	STC	A1,,P19
INSTRUCTION NUMBER	44	LOD	P40,,A1
INSTRUCTION NUMBER	45	FADD	A1,P40,A1
INSTRUCTION NUMBER	46	FMPY	A1,P19,A1
INSTRUCTION NUMBER	47	FMPY	A1,P80,A1
INSTRUCTION NUMBER	48	FMPY	A1,P14,A1
INSTRUCTION NUMBER	49	STO	A1,,P60
INSTRUCTION NUMBER	50	NOOP	,,
INSTRUCTION NUMBER	51	TXH	X7,C3,I59
INSTRUCTION NUMBER	52	TXLE	X7,C2,I59
INSTRUCTION NUMBER	53	LOD	P40,,A1
INSTRUCTION NUMBER	54	FADD	A1,P40,A1
INSTRUCTION NUMBER	55	FMPY	A1,P100,A1
INSTRUCTION NUMBER	56	FMPY	A1,P80,A1
INSTRUCTION NUMBER	57	FMPY	A1,P15,A1
INSTRUCTION NUMBER	58	STO	A1,,P60
INSTRUCTION NUMBER	59	NCCP	,,
INSTRUCTION NUMBER	60	TXH	X7,C4,I68
INSTRUCTION NUMBER	61	TXLE	X7,C3,I68
INSTRUCTION NUMBER	62	LOD	P40,,A1

INSTRUCTION NUMBER	83	PADD	A1,P40,A1
INSTRUCTION NUMBER	64	FMPY	A1,P100,A1
INSTRUCTION NUMBER	65	FMPY	A1,P80,A1
INSTRUCTION NUMBER	66	FMPY	A1,P15,A1
INSTRUCTION NUMBER	67	STO	A1,,P60
INSTRUCTION NUMBER	68	NOOP	,,
INSTRUCTION NUMBER	69	TXH	X0,X6,I78
INSTRUCTION NUMBER	70	SFX	C1,X6,X6
INSTRUCTION NUMBER	71	TXLE	X0,X6,I74
INSTRUCTION NUMBER	72	ATX	C1,X6,X6
INSTRUCTION NUMBER	73	BRA	,,I76
INSTRUCTION NUMBER	74	ATX	C1,X6,X6
INSTRUCTION NUMBER	75	BRA	,,I78
INSTRUCTION NUMBER	76	ATX	C1,X2,X2
INSTRUCTION NUMBER	77	LDX	C1,,X0
INSTRUCTION NUMBER	78	NOOP	,,I78
INSTRUCTION NUMBER	79	ATX	C1,X5,X5
INSTRUCTION NUMBER	80	TXH	X5,C30,I85
INSTRUCTION NUMBER	81	ATX	C1,X7,X7
INSTRUCTION NUMBER	82	TXLE	X7,C4,I13
INSTRUCTION NUMBER	83	LDX	C2,,X7
INSTRUCTION NUMBER	84	BRA	,,I13
INSTRUCTION NUMBER	85	TEND	,,I6

CALLED RDPARM

LISTING OF PARAMETER CARDS AS READ

PROCESSOR NUMBER=32  
CHANGE RESOURCE=ADGEN, MASK=10, ACTSF=RELEASE  
CHANGE RESOURCE=FLDIV, NUMBER=1, TYPE=COMMON, ACTSE=EVENT,X  
ACTDT=0, ACTSF=EXFLD  
CHANGE RESOURCE=PROC, PRIDT=448  
CHANGE FCTSTEP=XFCRK, ENTRY=3, FDT=40 CHANGING DELTA-TIME FIELD  
CHANGE FCTSTEP=PRIORITY, ENTRY=2, FSE=EVENT, FADDR=PRIORITY  
START STINST=1, STPROC=31, X  
ILIMIT=20000  
TRACE LINES=0 COMMENT  
CHANGE FCTSTEP=PRIORITY, ENTRY=2, FDT=448  
PLOT PAGES=400  
ENDPARM MAIN KEYWORDS NEED NOT ALL START IN COL 4

END OF PARAMETERS. 13 CARDS READ.

EXECUTION UNITS

FIXED POINT

ADD/SUB 1

MPY/DIV 1

FLOATING POINT

ADD/SUB 2

MPY 1

DIV 1

TIME = 1.45828, DATE = 03/18/68

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000064 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6		
								.....	.....	.....	.....	.....	.....	.....	.....		
.000	.000	.000	.000	.000	0												
.000	.000	.000	.000	.000	1												
.000	.000	.000	.000	.000	2												
.000	.000	.000	.000	.000	3												
.000	.000	.000	.000	.000	4												
.000	.000	.000	.000	.000	5												
.000	.000	.000	.000	.000	6												
.000	.000	.000	.000	.000	7												
.000	.000	.000	.000	.000	8												
.000	.000	.000	.000	.000	9												
.000	.000	.000	.000	.000	10												
.000	.000	.000	.000	.000	11												
.000	.000	.000	.000	.000	12												
.000	.000	.000	.000	.000	13												
.000	.000	.000	.000	.000	14												
.000	.000	.000	.000	.000	15												
.000	.000	.000	.000	.000	16												
.000	.000	.000	.000	.000	17												
.000	.000	.000	.000	.000	18												
.000	.000	.000	.000	.000	19												
.000	.000	.000	.000	.000	20												
.000	.000	.000	.000	.000	21												
.000	.000	.000	.000	.000	22												
.000	.000	.000	.000	.000	23												
.000	.000	.000	.000	.000	24												
.000	.000	.000	.000	.000	25												
.000	.000	.000	.000	.000	26												
.000	.000	.000	.000	.000	27												
.000	.000	.000	.000	.000	28												
.000	.000	.000	.000	.000	29												
.000	.000	.000	.000	.000	30												
.312	.000	.000	.000	.000	31												

FORK

IIIIIIII

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV



EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000128 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6		
								0	8	6	4	2	0	8	6	3	
.000	.000	.000	.000	.000	0												
.000	.000	.000	.000	.000	1												
.000	.000	.000	.000	.000	2												
.000	.000	.000	.000	.000	3												
.000	.000	.000	.000	.000	4												
.000	.000	.000	.000	.000	5												
.000	.000	.000	.000	.000	6												
.000	.000	.000	.000	.000	7												
.000	.000	.000	.000	.000	8												
.000	.000	.000	.000	.000	9												
.000	.000	.000	.000	.000	10												
.000	.000	.000	.000	.000	11												
.000	.000	.000	.000	.000	12												
.000	.000	.000	.000	.000	13												
.000	.000	.000	.000	.000	14												
.000	.000	.000	.000	.000	15												
.000	.000	.000	.000	.000	16												
.000	.000	.000	.000	.000	17												
.000	.000	.000	.000	.000	18												
.000	.000	.000	.000	.000	19												
.000	.000	.000	.000	.000	20												
.000	.000	.000	.000	.000	21												
.000	.000	.000	.000	.000	22												
.000	.000	.000	.000	.000	23												
.000	.000	.000	.000	.000	24												
.000	.000	.000	.000	.000	25												
.000	.000	.000	.000	.000	26												
.000	.000	.000	.000	.000	27												
.000	.000	.000	.000	.000	28												
.000	.000	.000	.000	.000	29												
.000	.000	.000	.000	.000	30												
.312	.031	.000	.000	.000	31	FORK		I	I	I	I	I	I	I	I	I	

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000192 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6		
.104	.020	.000	.000	.000	0	FORK		I	I	I	I	I	I	I	I		
.000	.000	.000	.000	.000	1												
.000	.000	.000	.000	.000	2												
.000	.000	.000	.000	.000	3												
.000	.000	.000	.000	.000	4												
.000	.000	.000	.000	.000	5												
.000	.000	.000	.000	.000	6												
.000	.000	.000	.000	.000	7												
.000	.000	.000	.000	.000	8												
.000	.000	.000	.000	.000	9												
.000	.000	.000	.000	.000	10												
.000	.000	.000	.000	.000	11												
.000	.000	.000	.000	.000	12												
.000	.000	.000	.000	.000	13												
.000	.000	.000	.000	.000	14												
.000	.000	.000	.000	.000	15												
.000	.000	.000	.000	.000	16												
.000	.000	.000	.000	.000	17												
.000	.000	.000	.000	.000	18												
.000	.000	.000	.000	.000	19												
.000	.000	.000	.000	.000	20												
.000	.000	.000	.000	.000	21												
.000	.000	.000	.000	.000	22												
.000	.000	.000	.000	.000	23												
.000	.000	.000	.000	.000	24												
.000	.000	.000	.000	.000	25												
.000	.000	.000	.000	.000	26												
.000	.000	.000	.000	.000	27												
.000	.000	.000	.000	.000	28												
.000	.000	.000	.000	.000	29												
.000	.000	.000	.000	.000	30												
.312	.041	.000	.000	.000	31	FORK		I	I	I	I	I	I	I	I		

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
0.00	FL.PT. DIVIDE
0.00	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000256 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
I-	O-	A-	O-	R-	PROC.	FETCH EXEC.	0	1	2	3	4	4	5	6			
.156	.031	.000	.000	.000	0	FORK	IIIIIIIIIIIIIIIIIIIDDDD.....										
.078	.015	.000	.000	.000	1	FORK	IIIIIIIIIIIIIIIIIIIDDDD.....										
.078	.015	.000	.000	.000	2	FORK	IIIIIIIIIIIIIIIIIIIDDDD.....										
.000	.000	.000	.000	.000	3												
.000	.000	.000	.000	.000	4												
.000	.000	.000	.000	.000	5												
.000	.000	.000	.000	.000	6												
.000	.000	.000	.000	.000	7												
.000	.000	.000	.000	.000	8												
.000	.000	.000	.000	.000	9												
.000	.000	.000	.000	.000	10												
.000	.000	.000	.000	.000	11												
.000	.000	.000	.000	.000	12												
.000	.000	.000	.000	.000	13												
.000	.000	.000	.000	.000	14												
.000	.000	.000	.000	.000	15												
.000	.000	.000	.000	.000	16												
.000	.000	.000	.000	.000	17												
.000	.000	.000	.000	.000	18												
.000	.000	.000	.000	.000	19												
.000	.000	.000	.000	.000	20												
.000	.000	.000	.000	.000	21												
.000	.000	.000	.000	.000	22												
.000	.000	.000	.000	.000	23												
.000	.000	.000	.000	.000	24												
.000	.000	.000	.000	.000	25												
.000	.000	.000	.000	.000	26												
.000	.000	.000	.000	.000	27												
.000	.000	.000	.000	.000	28												
.000	.000	.000	.000	.000	29												
.000	.000	.000	.000	.000	30												
.312	.046	.000	.000	.000	31	FORK	IIIIIIIIIIIIIDDDD.....IIIIIII										

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
C.00	FL.PT. DIVIDE
0.00	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

SYSTEM CLOCK TIME=00000320 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)																	
-I-	-O-	-A-	-O-	-R-	PROC.	FETCH EXEC.	0	1	2	3	4	4	5	6								
							.....	8	.....	6	.....	4	.....	2	.....	0	.....	8	.....	6	.....	3
.187	.037	.000	.000	.000	0	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.125	.025	.000	.000	.000	1	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.125	.025	.000	.000	.000	2	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.062	.012	.000	.000	.000	3	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.062	.012	.000	.000	.000	4	FORK																
.062	.012	.000	.000	.000	5	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.062	.012	.000	.000	.000	6	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I
.000	.000	.000	.000	.000	7																	
.000	.000	.000	.000	.000	8																	
.000	.000	.000	.000	.000	9																	
.000	.000	.000	.000	.000	10																	
.000	.000	.000	.000	.000	11																	
.000	.000	.000	.000	.000	12																	
.000	.000	.000	.000	.000	13																	
.000	.000	.000	.000	.000	14																	
.000	.000	.000	.000	.000	15																	
.000	.000	.000	.000	.000	16																	
.000	.000	.000	.000	.000	17																	
.000	.000	.000	.000	.000	18																	
.000	.000	.000	.000	.000	19																	
.000	.000	.000	.000	.000	20																	
.000	.000	.000	.000	.000	21																	
.000	.000	.000	.000	.000	22																	
.000	.000	.000	.000	.000	23																	
.000	.000	.000	.000	.000	24																	
.000	.000	.000	.000	.000	25																	
.000	.000	.000	.000	.000	26																	
.000	.000	.000	.000	.000	27																	
.000	.000	.000	.000	.000	28																	
.000	.000	.000	.000	.000	29																	
.000	.000	.000	.000	.000	30																	
.312	.050	.000	.000	.000	31	FORK	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000384 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)										
-I-	-O-	-A-	-U-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6
.208	.041	.000	.000	.000	0	FORK		I	I	I	I	I	I	I	I
.156	.031	.000	.000	.000	1	FORK		I	I	I	I	I	I	I	I
.156	.031	.000	.000	.000	2	FORK		I	I	I	I	I	I	I	I
.104	.020	.000	.000	.000	3	FORK		I	I	I	I	I	I	I	I
.104	.020	.000	.000	.000	4	FORK									
.104	.020	.000	.000	.000	5	FORK									
.104	.020	.000	.000	.000	6	FORK									
.052	.010	.000	.000	.000	7	FORK									
.052	.010	.000	.000	.000	8	FORK									
.052	.010	.000	.000	.000	9	FORK									
.052	.010	.000	.000	.000	10	FORK									
.052	.010	.000	.000	.000	11	FORK									
.052	.010	.000	.000	.000	12	FORK									
.052	.010	.000	.000	.000	13	FORK									
.052	.010	.000	.000	.000	14	FORK									
.000	.000	.000	.000	.000	15										
.000	.000	.000	.000	.000	16										
.000	.000	.000	.000	.000	17										
.000	.000	.000	.000	.000	18										
.000	.000	.000	.000	.000	19										
.000	.000	.000	.000	.000	20										
.000	.000	.000	.000	.000	21										
.000	.000	.000	.000	.000	22										
.000	.000	.000	.000	.000	23										
.000	.000	.000	.000	.000	24										
.000	.000	.000	.000	.000	25										
.000	.000	.000	.000	.000	26										
.000	.000	.000	.000	.000	27										
.000	.000	.000	.000	.000	28										
.000	.000	.000	.000	.000	29										
.000	.000	.000	.000	.000	30										
.312	.052	.000	.000	.000	31	NOOP		I	I	I	I	I	I	I	I

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000448 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)												
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6		
								.....8	.....6	.....4	.....2	.....0	.....8	.....6	.....3		
.223	.044	.044	.026	.000	0	NOOP	NOOP	I	I	I	I	I	I	I	I		
.178	.035	.044	.026	.000	1	NOOP	NOOP	I	I	I	I	I	I	I	I		
.178	.035	.044	.026	.000	2	NOOP	NOOP	I	I	I	I	I	I	I	I		
.133	.026	.044	.026	.000	3	NOOP	NOOP	I	I	I	I	I	I	I	I		
.133	.026	.044	.026	.000	4	NOOP		.....I	I	I	I	I	I	I	I		
.133	.026	.044	.026	.000	5	NOOP		.....I	I	I	I	I	I	I	I		
.133	.026	.044	.026	.000	6	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.026	.000	7	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.026	.000	8	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.026	.000	9	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.026	.000	10	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.026	.000	11	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.000	.000	12	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.000	.000	13	NOOP		.....I	I	I	I	I	I	I	I		
.089	.017	.044	.000	.000	14	NOOP		.....I	I	I	I	I	I	I	I		
.044	.008	.044	.000	.000	15	NOOP		.....I	I	I	I	I	I	I	I		
.044	.008	.044	.000	.000	16	NOOP											
.044	.008	.044	.000	.000	17	NOOP											
.044	.008	.044	.000	.000	18	NOOP											
.044	.008	.044	.000	.000	19	NOOP											
.044	.008	.000	.000	.000	20	NOOP											
.044	.008	.000	.000	.000	21	NOOP											
.044	.008	.000	.000	.000	22	NOOP											
.044	.008	.000	.000	.000	23	NOOP											
.044	.000	.000	.000	.000	24	NOOP											
.044	.000	.000	.000	.000	25	NOOP											
.044	.000	.000	.000	.000	26	NOOP											
.044	.000	.000	.000	.000	27	NOOP											
.044	.000	.000	.000	.000	28	NOOP											
.044	.000	.000	.000	.000	29	NOOP											
.044	.000	.000	.000	.000	30	NOOP											
.312	.053	.044	.026	.000	31	LDX	NOOP	I	I	I	I	I	I	I	I		

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

- C.00 FL.PT. ADD|SUB
- C.00 FL.PT. MULTIPLY
- C.00 FL.PT. DIVIDE
- C.00 FX.PT. ADD|SUB
- C.00 FX.PT. MPY|DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000512 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)										
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6
								.....	.....	.....	.....	.....	.....	.....	.....
.234	.046	.078	.046	.000	0	LDX	LDX	I	I	I	I	I	I	I	I
.195	.039	.078	.046	.000	1	LDX	LDX	I	I	I	I	I	I	I	I
.195	.039	.078	.046	.000	2	LDX	LDX	I	I	I	I	I	I	I	I
.156	.031	.078	.046	.000	3	LDX	LDX	I	I	I	I	I	I	I	I
.156	.031	.078	.046	.000	4	LDX	NOOP	E	E	E	E	E	E	E	E
.156	.031	.078	.046	.000	5	LDX	NOOP	E	E	E	E	E	E	E	E
.156	.031	.078	.046	.000	6	LDX	NOOP	E	E	E	E	E	E	E	E
.117	.023	.078	.046	.000	7	LDX	NOOP	E	E	E	E	E	E	E	E
.117	.023	.078	.046	.000	8	LDX	NOOP	G	G	G	G	G	G	G	G
.117	.023	.078	.046	.000	9	LDX	NOOP	O	O	O	O	O	O	O	O
.117	.023	.078	.046	.000	10	LDX	NOOP	O	O	O	O	O	O	O	O
.117	.023	.078	.046	.000	11	LDX	NOOP	O	O	O	O	O	O	O	O
.117	.023	.078	.023	.000	12	LDX	NOOP	A	A	A	A	A	A	A	A
.117	.023	.078	.023	.000	13	LDX	NOOP	A	A	A	A	A	A	A	A
.117	.023	.078	.023	.000	14	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.078	.023	.000	15	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.078	.023	.000	16	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.078	.023	.000	17	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.078	.023	.000	18	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.078	.023	.000	19	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.039	.023	.000	20	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.039	.023	.000	21	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.039	.023	.000	22	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.015	.039	.023	.000	23	LDX	NOOP	A	A	A	A	A	A	A	A
.078	.007	.039	.023	.000	24	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	25	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	26	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	27	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	28	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	29	LDX	NOOP	I	I	I	I	I	I	I	I
.078	.007	.039	.023	.000	30	LDX	NOOP	I	I	I	I	I	I	I	I
.312	.054	.078	.046	.000	31	LDX	LDX	I	I	I	I	I	I	I	I

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00 FL.PT. ADD|SUB  
 C.00 FL.PT. MULTIPLY  
 C.00 FL.PT. DIVIDE  
 C.00 FX.PT. ACC|SUB  
 C.00 FX.PT. MPY|DIV





SYSTEM CLOCK TIME=00000640 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)									
-I-	-O-	-A-	-U-	-R-	PROC.	FETCH EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1	2	3	4	4	5	6
.250	.050	.125	.075	.000	0	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.218	.043	.125	.075	.000	1	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.218	.043	.125	.075	.000	2	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.187	.037	.125	.075	.000	3	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.187	.037	.125	.075	.000	4	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.187	.037	.125	.075	.000	5	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.187	.037	.125	.075	.000	6	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.075	.000	7	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.075	.000	8	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.075	.000	9	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.075	.000	10	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.075	.000	11	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAA	0	0	0	0	0	0	0
.156	.031	.125	.056	.000	12	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.156	.031	.125	.056	.000	13	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.156	.031	.125	.056	.000	14	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.125	.056	.000	15	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.125	.056	.000	16	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.125	.056	.000	17	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.125	.056	.000	18	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.125	.056	.000	19	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.093	.056	.000	20	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.093	.056	.000	21	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.093	.056	.000	22	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.025	.093	.056	.000	23	LDX LDX	AAAAOCCOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIII	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	24	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	25	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	26	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	27	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	28	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	29	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.125	.018	.093	.056	.000	30	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0
.312	.056	.125	.075	.000	31	LDX LDX	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA	0	0	0	0	0	0	0

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
0.00	FL.PT. DIVIDE
0.00	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

SYSTEM CLOCK TIME=00000704 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)									
I-	O-	A-	U-	R-	PROC.	FETCH EXEC.	0	1	2	3	4	4	5	6
							0.....8.....6.....4.....2.....0.....8.....6.....3							
.255	.051	.142	.085	.000	0	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOOEEEEYYYY							
.227	.045	.142	.085	.000	1	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOOEEEEYYYY							
.227	.045	.142	.085	.000	2	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOOEEEEYYYY							
.198	.039	.142	.085	.000	3	LDX LDX	IIIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOOEEEEYYYY							
.198	.039	.142	.085	.000	4	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOO							
.198	.039	.142	.085	.000	5	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOO							
.198	.039	.142	.085	.000	6	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOO							
.170	.034	.142	.085	.000	7	LDX LDX	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCCOOOOOOOOO							
.170	.034	.142	.085	.000	8	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCC							
.170	.034	.142	.085	.000	9	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCC							
.170	.034	.142	.085	.000	10	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCC							
.170	.034	.142	.085	.000	11	LDX LDX	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAACCC							
.170	.034	.142	.068	.000	12	LDX LDX	AAAAOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAA							
.170	.034	.142	.068	.000	13	LDX LDX	AAAAOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAA							
.170	.034	.142	.068	.000	14	LDX LDX	AAAAOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAA							
.142	.028	.142	.068	.000	15	LDX LDX	AAAAOCCOCCOCCOCCOCCOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDDAAAAAAAAAAAAA							
.142	.028	.142	.068	.000	16	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.142	.068	.000	17	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.142	.068	.000	18	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.142	.068	.000	19	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.113	.068	.000	20	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.113	.068	.000	21	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.113	.068	.000	22	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.028	.113	.068	.000	23	LDX LDX	AAAAAAAAAAAAAACCOC							
.142	.022	.113	.068	.000	24	LDX LDX	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	25	LDX LDX	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	26	LDX LDX	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	27	LDX LDX	IIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	28	LDX LDX	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	29	LDX LDX	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOC							
.142	.022	.113	.068	.000	30	LDX LDX	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOC							
.312	.056	.142	.085	.000	31	LDX LDX	IIIIIIIIIIIIIDDDDAAAAAAAAAAAAAAAAAAAAAAACCOCOCOCOCOCOCOCOCOCOCOC							

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00000768 KING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)																																								
I-	O-	A-	O-	R-	PKOC.	FETCH	EXEC.	0	1	2	3	4	4	5	6																														
								.....	.....	.....	.....	.....	.....	.....	.....																														
.260	.C52	.156	.C93	.000	0	LDX	LDX	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I	I															

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00 FL.PT. ADD|SUB  
 0.00 FL.PT. MULTIPLY  
 0.00 FL.PT. DIVIDE  
 0.00 FX.PT. ADD|SUB  
 0.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00000832 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns: (STAGE TIME / TOTAL TIME) and (INSTRUCTION). Rows include instruction details like PROC., FETCH EXEC., and a hex-like trace output.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

- C.00 FL.PT. ADD|SUB
0.00 FL.PT. MULTIPLY
0.00 FL.PT. DIVIDE
0.00 FX.PT. ADD|SUB
0.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00000896 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH O=OPERAND FETCH R=EXECUTION UNIT REQUEST REJECTED Y=EXECUTION UNIT REQUEST ACCEPTED E=EXECUTION UNIT REQUEST /=DIVISION IN PROGRESS A=ADDRESS GENERATION D=INSTRUCTION DECODE

Table with columns for stage time (I, O, A, R), instruction type (PROC., FETCH, EXEC.), and a detailed trace of instructions (1-31) with their binary representations.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table showing average utilization for various operations: C.OO FL.PT. ADD|SUB, O.OO FL.PT. MULTIPLY, C.OO FL.PT. DIVIDE, O.OO FX.PT. ADD|SUB, O.OO FX.PT. MPY|DIV.

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00000960 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)														
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6				
								.....8	.....6	.....4	.....2	.....0	.....8	.....6	.....3				
.270	.054	.187	.112	.000	0	NOOP	NOOP	I	I	I	I	I	I	I	I				
.250	.050	.187	.112	.000	1	NOOP	NOOP	I	I	I	I	I	I	I	I				
.250	.050	.187	.112	.000	2	NOOP	NOOP	I	I	I	I	I	I	I	I				
.229	.045	.187	.112	.000	3	NOOP	NOOP	I	I	I	I	I	I	I	I				
.229	.045	.187	.112	.000	4	NOOP	ATX	E	E	E	E	E	E	E	E				
.229	.045	.187	.112	.000	5	NOOP	ATX	E	E	E	E	E	E	E	E				
.229	.045	.187	.112	.000	6	NOOP	ATX	E	E	E	E	E	E	E	E				
.208	.041	.187	.112	.000	7	NOOP	ATX	E	E	E	E	E	E	E	E				
.208	.041	.187	.112	.000	8	NOOP	ATX	O	O	O	O	O	O	O	O				
.208	.041	.187	.112	.000	9	NOOP	ATX	O	O	O	O	O	O	O	O				
.208	.041	.187	.112	.000	10	NOOP	ATX	O	O	O	O	O	O	O	O				
.208	.041	.187	.112	.000	11	NOOP	ATX	O	O	O	O	O	O	O	O				
.208	.041	.187	.100	.000	12	NOOP	ATX	A	A	A	A	A	A	A	A				
.208	.041	.187	.100	.000	13	NOOP	ATX	A	A	A	A	A	A	A	A				
.208	.041	.187	.100	.000	14	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.187	.100	.000	15	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.187	.100	.000	16	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.187	.100	.000	17	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.187	.100	.000	18	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.187	.100	.000	19	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.166	.100	.000	20	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.166	.100	.000	21	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.166	.100	.000	22	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.037	.166	.100	.000	23	NOOP	ATX	A	A	A	A	A	A	A	A				
.187	.033	.166	.100	.000	24	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	25	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	26	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	27	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	28	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	29	NOOP	ATX	I	I	I	I	I	I	I	I				
.187	.033	.166	.100	.000	30	NOOP	ATX	I	I	I	I	I	I	I	I				
.312	.058	.187	.112	.000	31	BRA	NOOP	I	I	I	I	I	I	I	I				

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
0.00	FL.PT. DIVIDE
0.00	Fx.PT. ADD SUB
0.00	Fx.PT. MPY DIV

SYSTEM CLOCK TIME=00001024 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, total time, instruction type, and execution details. Includes rows for instructions 0-31 with various opcodes like BRA, NOOP and execution status indicators.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

- C.00 FL.PT. ADD|SUB
C.00 FL.PT. MULTIPLY
C.00 FL.PT. DIVIDE
C.00 FX.PT. ADD|SUB
0.00 FX.PT. MPY|DIV





SYSTEM CLOCK TIME=00001152 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)									
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1	2	3	4	4	5	6
.277	.C55	.208	.125	.000	0	LOAD LOAD	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEEYYYY							
.260	.C52	.208	.125	.000	1	LOAD LOAD	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEEYYYY							
.260	.C52	.208	.125	.000	2	LOAD LOAD	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEEYYYY							
.243	.C48	.208	.125	.000	3	LOAD LOAD	IIIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEEYYYY							
.243	.C48	.208	.125	.000	4	LOAD NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000							
.243	.C48	.208	.125	.000	5	LOAD NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000							
.243	.C48	.208	.125	.000	6	LOAD NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000							
.225	.C45	.208	.125	.000	7	LOAD NOOP	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000							
.225	.C45	.208	.125	.000	8	LOAD NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA0000							
.225	.C45	.208	.125	.000	9	LOAD NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA0000							
.225	.C45	.208	.125	.000	10	LOAD NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA0000							
.225	.C45	.208	.125	.000	11	LOAD NOOP	00000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA0000							
.225	.C45	.208	.114	.000	12	LOAD NOOP	AAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAA							
.225	.C45	.208	.114	.000	13	LOAD NOOP	AAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAA							
.225	.C45	.208	.114	.000	14	LOAD NOOP	AAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAA							
.208	.C41	.208	.114	.000	15	LOAD NOOP	AAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAA							
.208	.C41	.208	.114	.000	16	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.208	.114	.000	17	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.208	.114	.000	18	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.208	.114	.000	19	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.190	.114	.000	20	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.190	.114	.000	21	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.190	.114	.000	22	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C41	.190	.114	.000	23	LOAD NOOP	AAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAA							
.208	.C38	.190	.114	.000	24	LOAD NOOP	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIII							
.208	.C38	.190	.114	.000	25	LOAD NOOP	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIII							
.208	.C38	.190	.114	.000	26	LOAD NOOP	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIII							
.208	.C38	.190	.114	.000	27	LOAD NOOP	IIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIIIIIIIII							
.208	.C38	.190	.114	.000	28	LOAD NOOP	IIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIII							
.208	.C38	.190	.114	.000	29	LOAD NOOP	IIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIII							
.208	.C38	.190	.114	.000	30	LOAD NOOP	IIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIII							
.312	.C59	.208	.125	.000	31	STO LOAD	IIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAA000000000000EEEEYYYYIIIIIIII							

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.C0	FL.PT. ADD SUB
C.C0	FL.PT. MULTIPLY
C.C0	FL.PT. DIVIDE
C.C0	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

EXECUTION SIMULATOR INSTRUCTION TRACE OUTPUT

SYSTEM CLOCK TIME=00001216 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)										
I-	O-	A-	O-	R-	PROC.	FETCH	EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1	2	3	4	4	5	6
.275	.C55	.213	.128	.000	0	STO	STO	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAAOCCCCCCCCOOEEEYYYY							
.263	.052	.213	.128	.000	1	STO	STO	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAAOCCCCCCCCOOEEEYYYY							
.263	.C52	.213	.128	.000	2	STO	STO	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAAOCCCCCCCCOOEEEYYYY							
.246	.049	.213	.128	.000	3	STO	STO	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAAAAOCCCCCCCCOOEEEYYYY							
.246	.C49	.213	.128	.000	4	STO	LOAD	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAODDDOOOOOOO							
.246	.049	.213	.128	.000	5	STO	LOAD	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAODDDOOOOOOO							
.246	.C49	.213	.128	.000	6	STO	LOAD	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAODDDOOOOOOO							
.230	.046	.213	.128	.000	7	STO	LOAD	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAODDDOOOOOOO							
.230	.C46	.213	.128	.000	8	STO	LOAD	OOCCCCOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAO							
.230	.046	.213	.128	.000	9	STO	LOAD	OOCCCCOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAO							
.230	.C46	.213	.128	.000	10	STO	LOAD	OOCCCCOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAO							
.230	.C46	.213	.128	.000	11	STO	LOAD	OOCCCCOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAO							
.230	.C46	.213	.118	.000	12	STO	LOAD	AAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAA							
.230	.046	.213	.118	.000	13	STO	LOAD	AAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAA							
.230	.C46	.213	.118	.000	14	STO	LOAD	AAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAA							
.213	.C42	.213	.118	.000	15	STO	LOAD	AAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAA							
.213	.042	.213	.118	.000	16	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAA							
.213	.042	.213	.118	.000	17	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAA							
.213	.C42	.213	.118	.000	18	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAA							
.213	.C42	.213	.118	.000	19	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAA							
.213	.C42	.197	.118	.000	20	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDD							
.213	.042	.197	.118	.000	21	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDD							
.213	.042	.197	.118	.000	22	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDD							
.213	.C42	.197	.118	.000	23	STO	LOAD	AAAAAAAAAAAAAAGCCCCOOOOOOOEEEEEEYYYIIIIIIIIIIIIIIIIIIIDDD							
.213	.039	.197	.118	.000	24	STO	LOAD	IIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIIIIIIIII							
.213	.039	.197	.118	.000	25	STO	LOAD	IIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIIIIIIIII							
.213	.039	.197	.118	.000	26	STO	LOAD	IIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIIIIIIIII							
.213	.039	.197	.118	.000	27	STO	LOAD	IIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIIIIIIIII							
.213	.039	.197	.118	.000	28	STO	LOAD	IIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIII							
.213	.039	.197	.118	.000	29	STO	LOAD	IIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIII							
.213	.C39	.197	.118	.000	30	STO	LOAD	IIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIII							
.312	.059	.213	.128	.000	31	NOOP	STO	IIIIIIIIIIIDDDCAAAAAAAAAAAAAAAAAAAAAAAAAAOCCOCCCCOOOEEEEEEYYYIIIIIIII							

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00 FL.PT. ADD|SUB  
 C.00 FL.PT. MULTIPLY  
 C.00 FL.PT. DIVIDE  
 C.00 FX.PT. ADD|SUB  
 C.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00001280 RING PRIORITY=(2,3,0,1)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)									
-I-	-O-	-A-	-U-	-R-	PROC.	FETCH	EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1.....2.....3.....4.....4.....5.....6					
.281	.056	.218	.131	.000	0	NOOP	NOOP	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAACOOOOOOOOOEEEEYYYY						
.265	.053	.218	.131	.000	1	NOOP	NOOP	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYY						
.265	.053	.218	.131	.000	2	NOOP	NOOP	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAOOOOOOOOOOEEEEYYYY						
.250	.050	.218	.131	.000	3	NOOP	NOOP	IIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAOOOOOOOOOOEEEEYYYY						
.250	.050	.218	.131	.000	4	NOOP	STO	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOOOOOOOO						
.250	.050	.218	.131	.000	5	NOOP	STO	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOOOOOOOO						
.250	.050	.218	.131	.000	6	NOOP	STO	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOOOOOOOO						
.234	.046	.218	.131	.000	7	NOOP	STO	EEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOOOOOOOO						
.234	.046	.218	.131	.000	8	NOOP	STO	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOO						
.234	.046	.218	.131	.000	9	NOOP	STO	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOO						
.234	.046	.218	.131	.000	10	NOOP	STO	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOO						
.234	.046	.218	.131	.000	11	NOOP	STO	OOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAOOOO						
.234	.046	.218	.121	.000	12	NOOP	STO	AAAAOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAA						
.234	.046	.218	.121	.000	13	NOOP	STO	AAAAOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAA						
.234	.046	.218	.121	.000	14	NOOP	STO	AAAAOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAA						
.218	.043	.218	.121	.000	15	NOOP	STO	AAAAOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAAAAAAAAAA						
.218	.043	.218	.121	.000	16	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAA						
.218	.043	.218	.121	.000	17	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAA						
.218	.043	.218	.121	.000	18	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAA						
.218	.043	.218	.121	.000	19	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDDAAAAAAAA						
.218	.043	.203	.121	.000	20	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDD						
.218	.043	.203	.121	.000	21	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDD						
.218	.043	.203	.121	.000	22	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDD						
.218	.043	.203	.121	.000	23	NOOP	STO	AAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIIIIIIIIDDD						
.218	.040	.203	.121	.000	24	NOOP	STO	IIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIII						
.218	.040	.203	.121	.000	25	NOOP	STO	IIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIII						
.218	.040	.203	.121	.000	26	NOOP	STO	IIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIII						
.218	.040	.203	.121	.000	27	NOOP	STO	IIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIIIIIIIIIII						
.218	.040	.203	.121	.000	28	NOOP	STO	IIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIII						
.218	.040	.203	.121	.000	29	NOOP	STO	IIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIII						
.218	.040	.203	.121	.000	30	NOOP	STO	IIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIII						
.312	.059	.218	.131	.000	31	SFX	NOOP	IIIIIIIIIIIDDDAAAAAAAAAAAAAAAAAAAAAAGOOOOOOOOOEEEEYYYYIIIIII						

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
0.00	FL.PT. DIVIDE
0.00	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

SYSTEM CLOCK TIME=00001344 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction type (PROC., FETCH, EXEC.), and a detailed instruction trace. Includes headers like (STAGE TIME / TOTAL TIME) and (INSTRUCTION).

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table for execution unit utilization with columns for unit type (C.CO, FL.PT., FX.PT.) and operation (ADD|SUB, MULTIPLY, DIVIDE, MPY|DIV).

SYSTEM CLOCK TIME=00001408 RING PRIORITY={3,0,1,2}

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for (STAGE TIME / TOTAL TIME) and (INSTRUCTION). Rows include instruction details like PROC., FETCH EXEC., and a sequence of characters (I, O, E, A, Y, /, D) representing instruction execution stages.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.OO FL.PT. ADD|SUB
C.OO FL.PT. MULTIPLY
C.OO FL.PT. DIVIDE
0.00 FX.PT. ADD|SUB
0.00 FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00001472 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction type (PROC., FETCH, EXEC.), and a binary trace of instruction execution (I, O, A, U, R, D, E, Y, F, A, D).

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.OO FL.PT. ADD|SUB
C.OO FL.PT. MULTIPLY
C.OO FL.PT. DIVIDE
C.OO FX.PT. ADD|SUB
C.OO FX.PT. MPY|DIV

SYSTEM CLOCK TIME=00001536 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
 O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)									
-I-	-D-	-A-	-O-	-R-	PROC.	FETCH EXEC.	0.....8.....6.....4.....2.....0.....8.....6.....3	1	2	3	4	4	5	6
.286	.057	.234	.140	.000	0	LOAD LOAD	IIIIIIIIIIIIIIIIIIII	I	D	D	D	A	A	A
.273	.054	.234	.140	.000	1	LOAD LOAD	IIIIIIIIIIIIIIIIIIII	I	D	D	D	A	A	A
.273	.054	.234	.140	.000	2	LOAD LOAD	IIIIIIIIIIIIIIIIIIII	I	D	D	D	A	A	A
.260	.052	.234	.140	.000	3	LOAD LOAD	IIIIIIIIIIIIIIIIIIII	I	D	D	D	A	A	A
.260	.052	.234	.140	.000	4	LOAD ATX	EEEEYYYYIIIIIIIIIIII	I	D	D	D	A	A	A
.260	.052	.234	.140	.000	5	LOAD ATX	EEEEYYYYIIIIIIIIIIII	I	D	D	D	A	A	A
.260	.052	.234	.140	.000	6	LOAD ATX	EEEEYYYYIIIIIIIIIIII	I	D	D	D	A	A	A
.247	.049	.234	.140	.000	7	LOAD ATX	EEEEYYYYIIIIIIIIIIII	I	D	D	D	A	A	A
.247	.049	.234	.140	.000	8	LOAD ATX	OCOCOCOCOCOCOCOCOC	O	C	C	C	O	C	C
.247	.049	.234	.140	.000	9	LOAD ATX	OCOCOCOCOCOCOCOCOC	O	C	C	C	O	C	C
.247	.049	.234	.140	.000	10	LOAD ATX	OCOCOCOCOCOCOCOCOC	O	C	C	C	O	C	C
.247	.049	.234	.140	.000	11	LOAD ATX	OCOCOCOCOCOCOCOCOC	O	C	C	C	O	C	C
.247	.049	.234	.132	.000	12	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.247	.049	.234	.132	.000	13	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.247	.049	.234	.132	.000	14	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.234	.132	.000	15	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.234	.132	.000	16	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.234	.132	.000	17	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.234	.132	.000	18	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.234	.132	.000	19	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.221	.132	.000	20	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.221	.132	.000	21	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.221	.132	.000	22	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.046	.221	.132	.000	23	LOAD ATX	AAAAAGCOCOCOCOCOCOC	A	A	A	A	A	A	A
.234	.044	.221	.132	.000	24	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	25	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	26	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	27	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	28	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	29	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.234	.044	.221	.132	.000	30	LOAD ATX	IIIIIDDDCAAAAAAAAAA	I	I	I	I	D	D	D
.312	.059	.234	.140	.000	31	FADD LOAD	IIIIIIIIIIIIIIIIIIII	I	I	I	I	I	I	I

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

C.00	FL.PT. ADD SUB
0.00	FL.PT. MULTIPLY
0.00	FL.PT. DIVIDE
C.00	FX.PT. ADD SUB
0.00	FX.PT. MPY DIV

SYSTEM CLOCK TIME=00001600 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
U=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

(STAGE TIME / TOTAL TIME)					(INSTRUCTION)													
-I-	-U-	-A-	-O-	-R-	PROC.	FETCH	EXEC.	0	1	2	3	4	4	5	6			
								.....	.....	.....	.....	.....	.....	.....	.....			
.287	.057	.237	.142	.000	0	FADD	FADD	I	I	I	I	I	I	I	I			
.275	.055	.237	.142	.037	1	FADD	FADD	I	I	I	I	I	I	I	I			
.275	.055	.237	.142	.037	2	FADD	FADD	I	I	I	I	I	I	I	I			
.262	.052	.237	.142	.000	3	FADD	FADD	I	I	I	I	I	I	I	I			
.262	.052	.237	.142	.000	4	FADD	LOAD	E	E	E	E	E	E	E	E			
.262	.052	.237	.142	.000	5	FADD	LOAD	E	E	E	E	E	E	E	E			
.262	.052	.237	.142	.000	6	FADD	LOAD	E	E	E	E	E	E	E	E			
.250	.050	.237	.142	.000	7	FADD	LOAD	E	E	E	E	E	E	E	E			
.250	.050	.237	.142	.000	8	FADD	LOAD	O	O	O	O	O	O	O	O			
.250	.050	.237	.142	.000	9	FADD	LOAD	O	O	O	O	O	O	O	O			
.250	.050	.237	.142	.000	10	FADD	LOAD	O	O	O	O	O	O	O	O			
.250	.050	.237	.142	.000	11	FADD	LOAD	O	O	O	O	O	O	O	O			
.250	.050	.237	.135	.000	12	FADD	LOAD	A	A	A	A	A	A	A	A			
.250	.050	.237	.135	.000	13	FADD	LOAD	A	A	A	A	A	A	A	A			
.250	.050	.237	.135	.000	14	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.237	.135	.000	15	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.237	.135	.000	16	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.237	.135	.000	17	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.237	.135	.000	18	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.237	.135	.000	19	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.225	.135	.000	20	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.225	.135	.000	21	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.225	.135	.000	22	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.047	.225	.135	.000	23	FADD	LOAD	A	A	A	A	A	A	A	A			
.237	.045	.225	.135	.000	24	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	25	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	26	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	27	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	28	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	29	FADD	LOAD	I	I	I	I	I	I	I	I			
.237	.045	.225	.135	.000	30	FADD	LOAD	I	I	I	I	I	I	I	I			
.312	.060	.237	.142	.000	31	FMPY	FADD	I	I	I	I	I	I	I	I			

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

0.50 FL.PT. ADD|SUB  
0.00 FL.PT. MULTIPLY  
0.00 FL.PT. DIVIDE  
0.00 FX.PT. ADD|SUB  
0.00 FX.PT. MPY|DIV

1111111133333333



SYSTEM CLOCK TIME=00001664 RING PRIORITY=(3,0,1,2)

I=INSTRUCT. FETCH K=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
U=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction type (PROC, FETCH, EXEC.), and instruction content. Rows show instruction 0 through 31, including operations like FMPY, FADD, and STO.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

11.50
0.37
0.00
0.00
0.00

FL.PT. ADD|SUB
FL.PT. MULTIPLY
FL.PT. DIVIDE
FX.PT. ADD|SUB
FX.PT. MPY|DIV

55555555777777779999999988888888DDDDDDDDFF FFFF FGGGGGGGGGGGGGGG
1111111122222222





SYSTEM CLOCK TIME=00001856 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION  
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction type (PROC, FETCH, EXEC), and a sequence of characters (I, R, E, A, O, Y, /, D) representing instruction execution status. Rows are numbered 0-31.

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table showing utilization for C.OO, FL.PT. ADD|SUB, FL.PT. MULTIPLY, FL.PT. DIVIDE, FX.PT. ADD|SUB, and FX.PT. MPY|DIV.



SYSTEM CLOCK TIME=00001984 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction type (PROC, FETCH, EXEC), and a binary trace of execution stages (0-31). Includes sub-headers for (STAGE TIME / TOTAL TIME) and (INSTRUCTION).

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table showing average utilization for units: FL.PT. ADD|SUB, FL.PT. MULTIPLY, FL.PT. DIVIDE, FX.PT. ADD|SUB, FX.PT. MPY|DIV. Includes a long string of digits representing instruction counts.



SYSTEM CLOCK TIME=00002112 RING PRIORITY=(0,1,2,3)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERANC FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for (STAGE TIME / TOTAL TIME) and (INSTRUCTION). Rows include instruction details like PROC., FETCH EXEC., and a binary trace of instruction execution stages (0-6).

(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

Summary table showing utilization for FL.PT. ADD|SUB, FL.PT. MULTIPLY, FL.PT. DIVIDE, FX.PT. ADD|SUB, and FX.PT. MPY|DIV with corresponding digit counts.





SYSTEM CLOCK TIME=00002240 RING PRIORITY=(1,2,3,0)

I=INSTRUCT. FETCH R=EXECUTION UNIT REQUEST REJECTED E=EXECUTION UNIT REQUEST A=ADDRESS GENERATION
O=OPERAND FETCH Y=EXECUTION UNIT REQUEST ACCEPTED /=DIVISION IN PROGRESS D=INSTRUCTION DECODE

Table with columns for stage time, instruction, and a binary trace. Rows represent instructions 0 through 31, showing various operations like FADD, LOAD, STO, and FMPY.

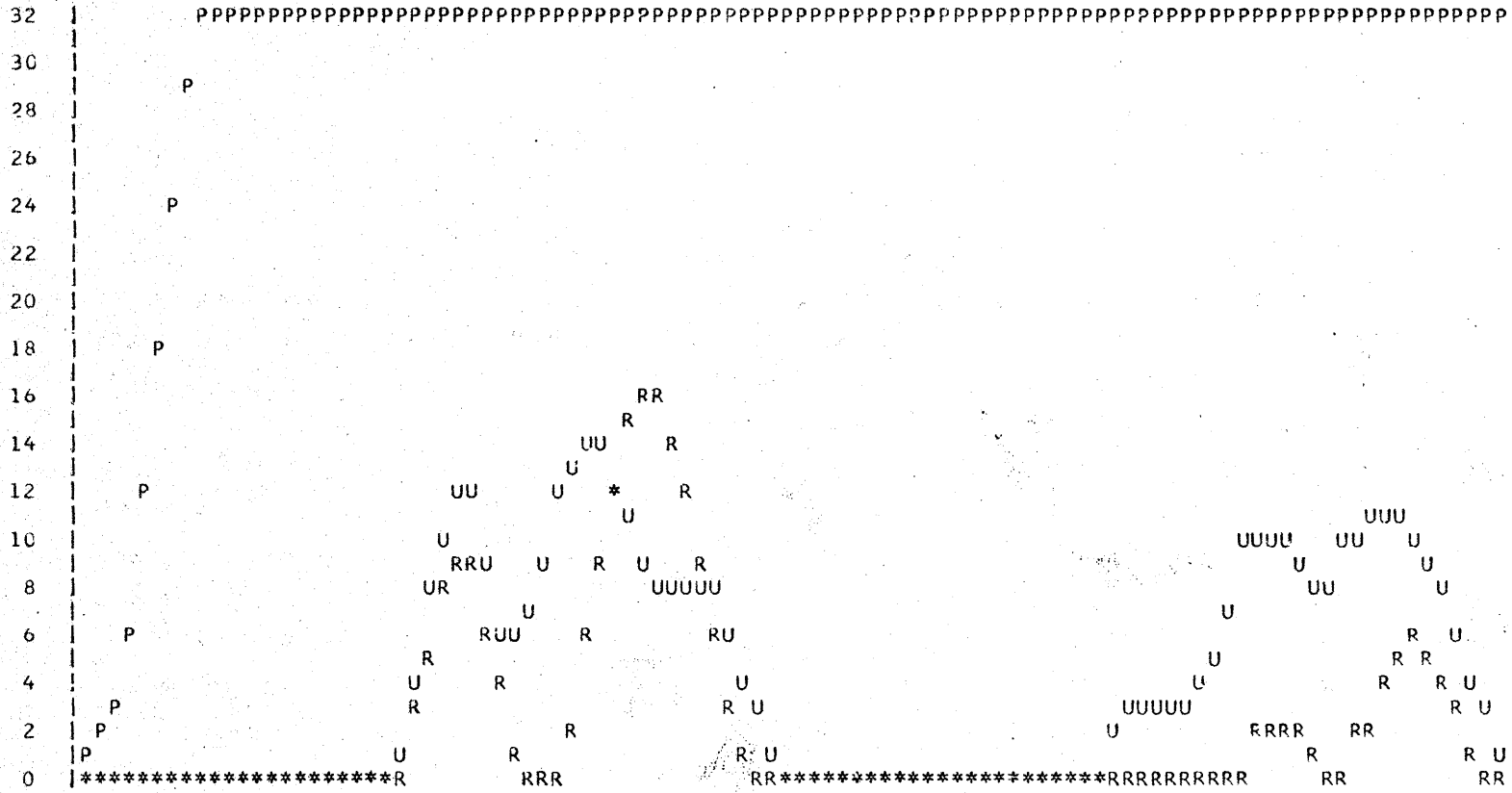
(AVERAGE) EXECUTION UNIT UTILIZATION. (DIGITS INDICATE # OF INSTRUCTIONS IN UNIT)

13.75 FL.PT. ADD|SUB BBBBRRBCCCCCDDDDDDDEEEEEEEFFFFFFFGGGGGGGFFFFFFFEEEEEEE
0.37 FL.PT. MULTIPLY 111111122222222
0.00 FL.PT. DIVIDE
0.00 FX.PT. ADD|SUB
0.00 FX.PT. MPLY|DIV

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00006528 RING PRIORITY=(0,1,2,3)

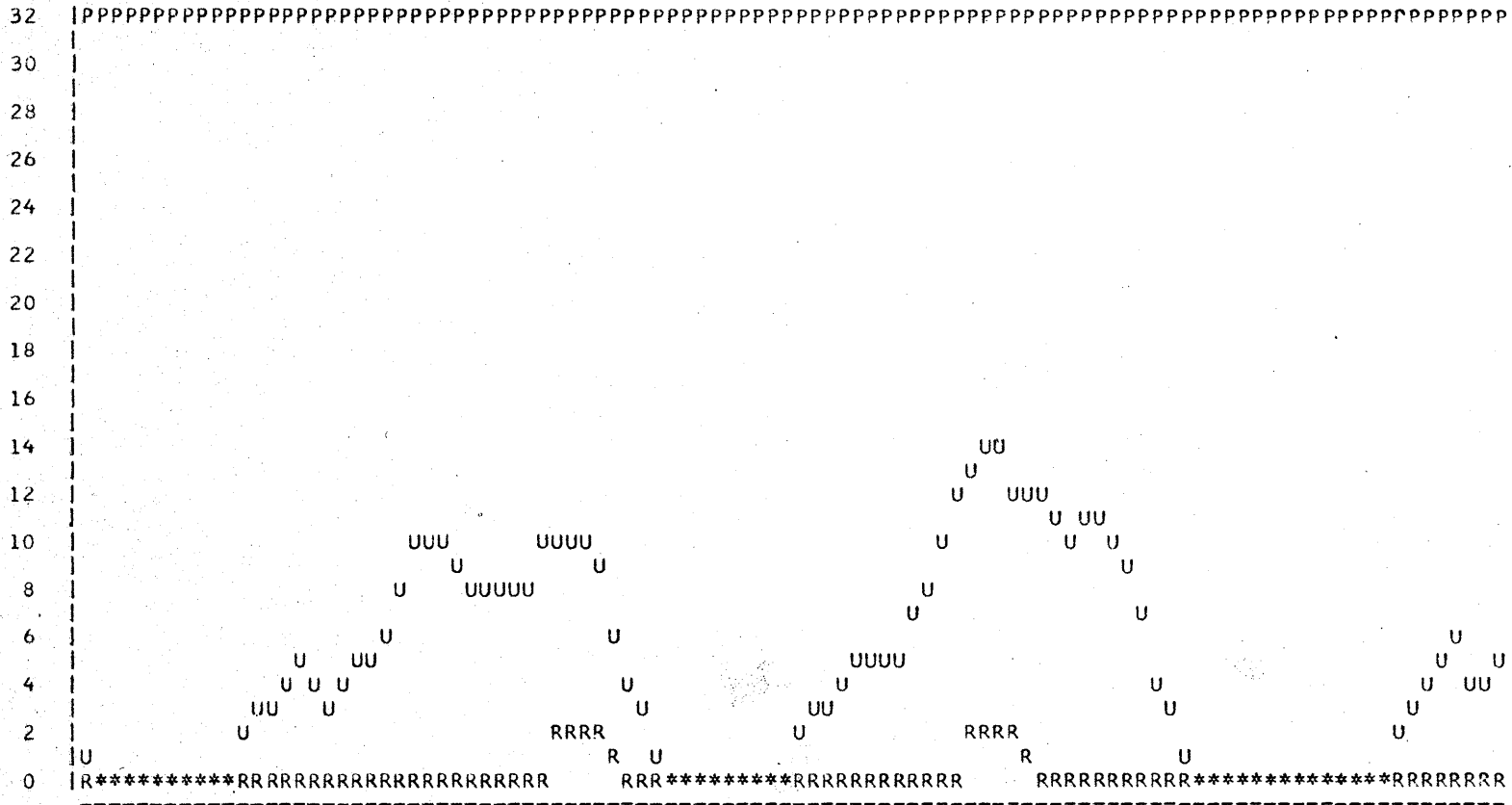
P = ACTIVE PROCESSORS  
U = ACTIVE EXECUTION UNIT STAGES  
R = REJECTED E-UNIT REQUESTS  
\* = ANY COMBINATION OF ABOVE



...5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

SYSTEM CLOCK TIME=00012928 RING PRIORITY=(0,1,2,3)

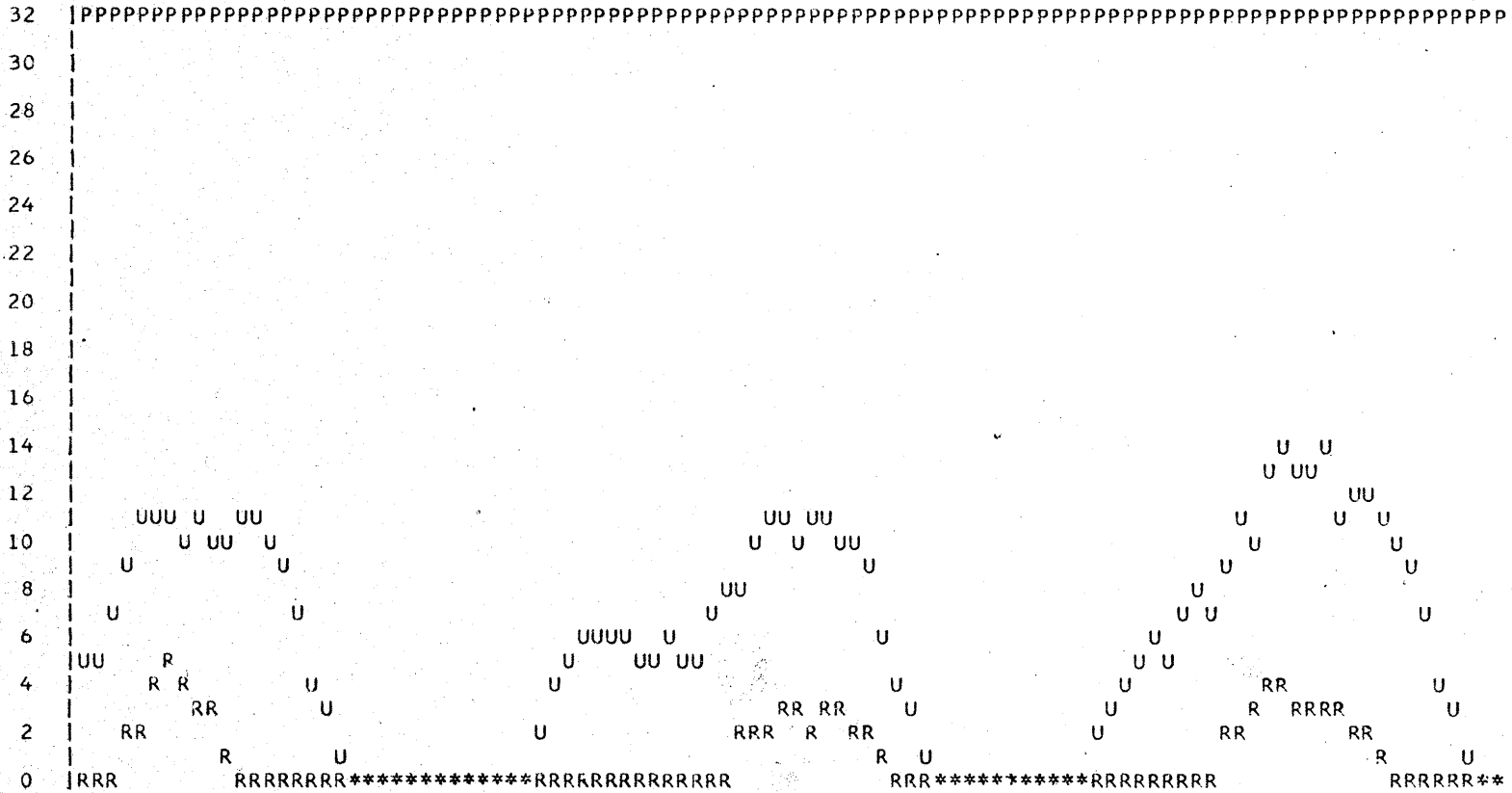
P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

SYSTEM CLOCK TIME=00019328 RING PRIORITY=(0,1,2,3)

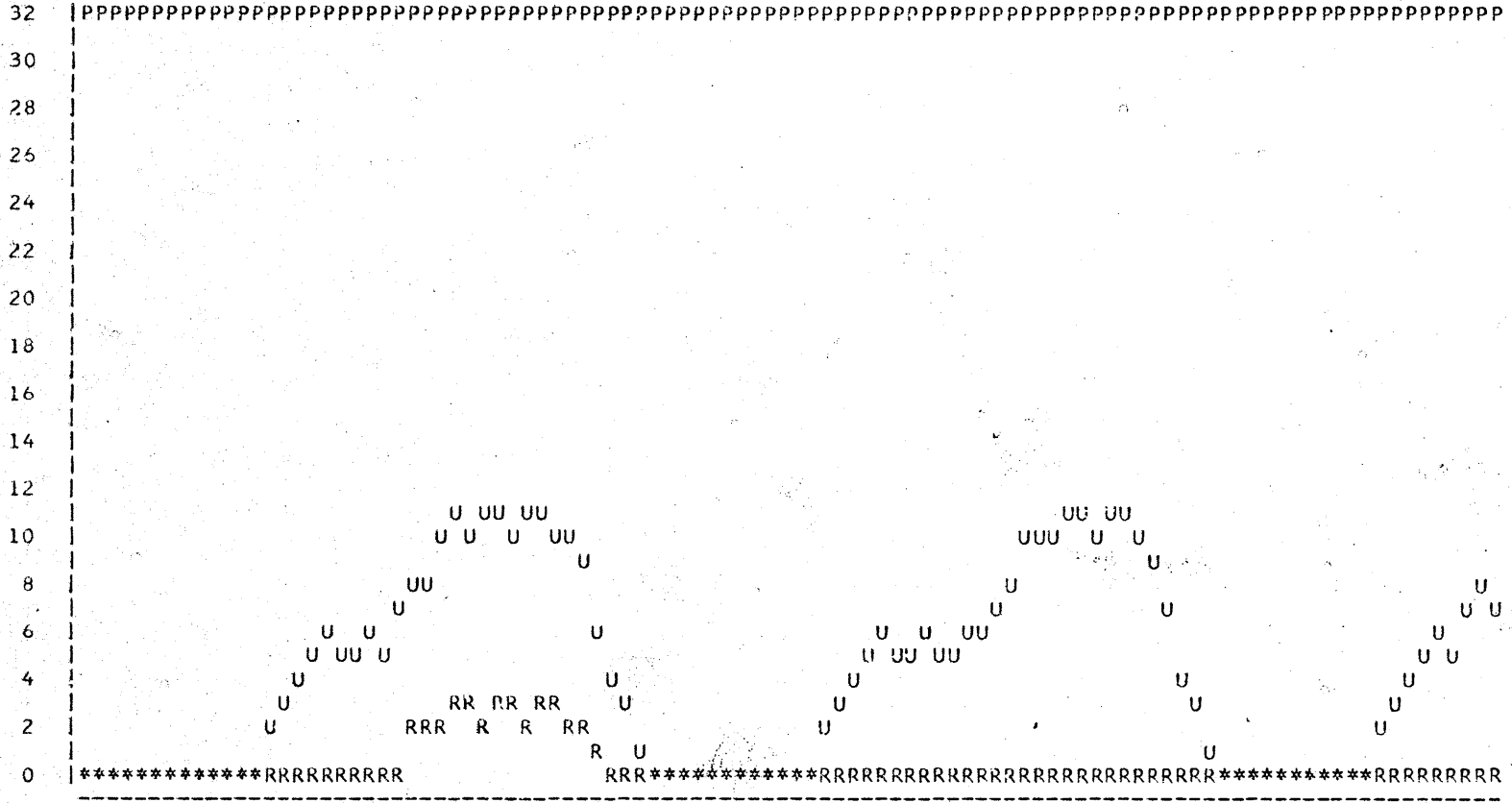
P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



.....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

SYSTEM CLOCK TIME=00025728 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS  
U = ACTIVE EXECUTION UNIT STAGES  
R = REJECTED E-UNIT REQUESTS  
\* = ANY COMBINATION OF ABOVE

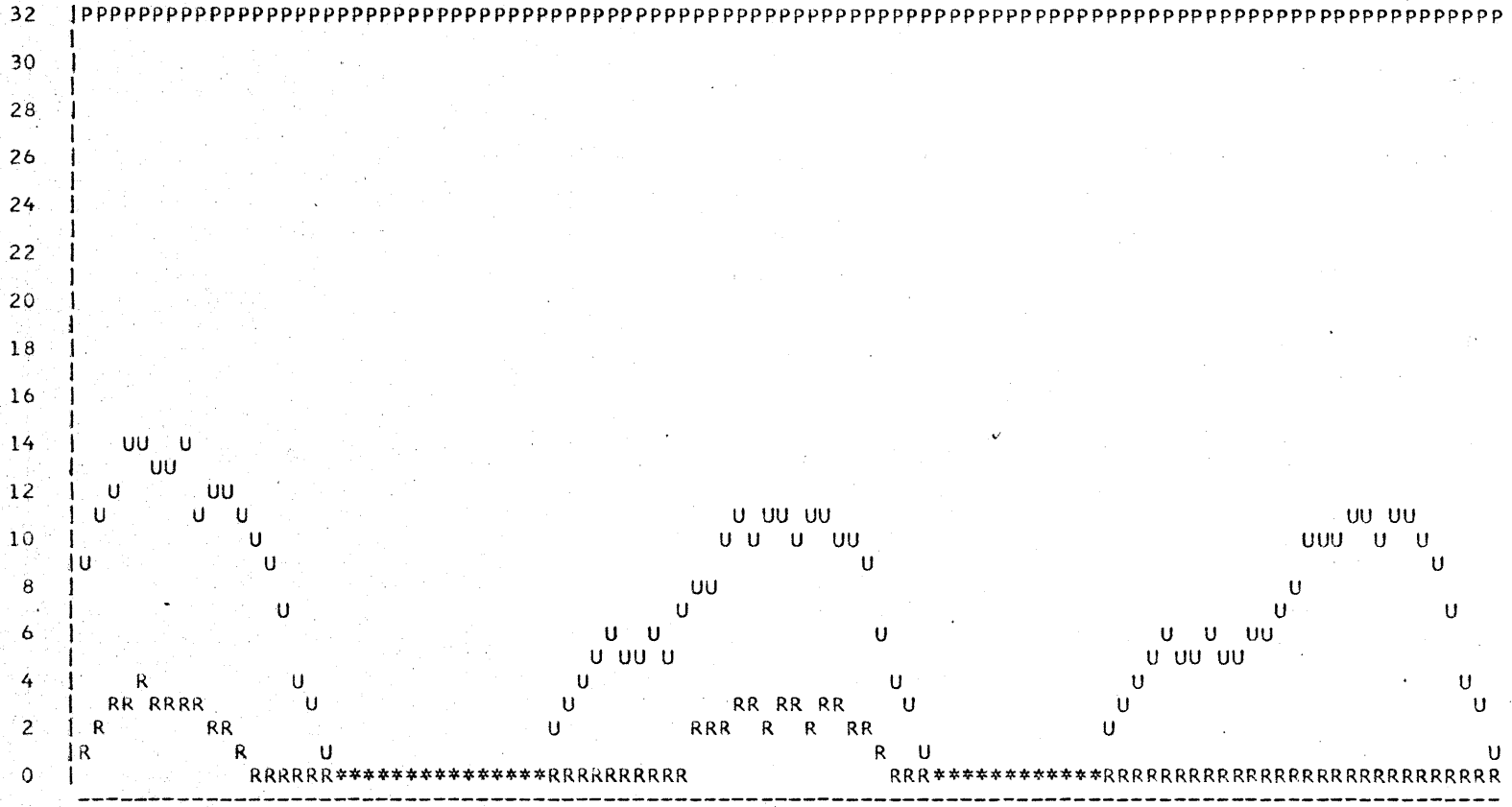


....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00032128 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS  
 U = ACTIVE EXECUTION UNIT STAGES  
 R = REJECTED E-UNIT REQUESTS  
 \* = ANY COMBINATION OF ABOVE

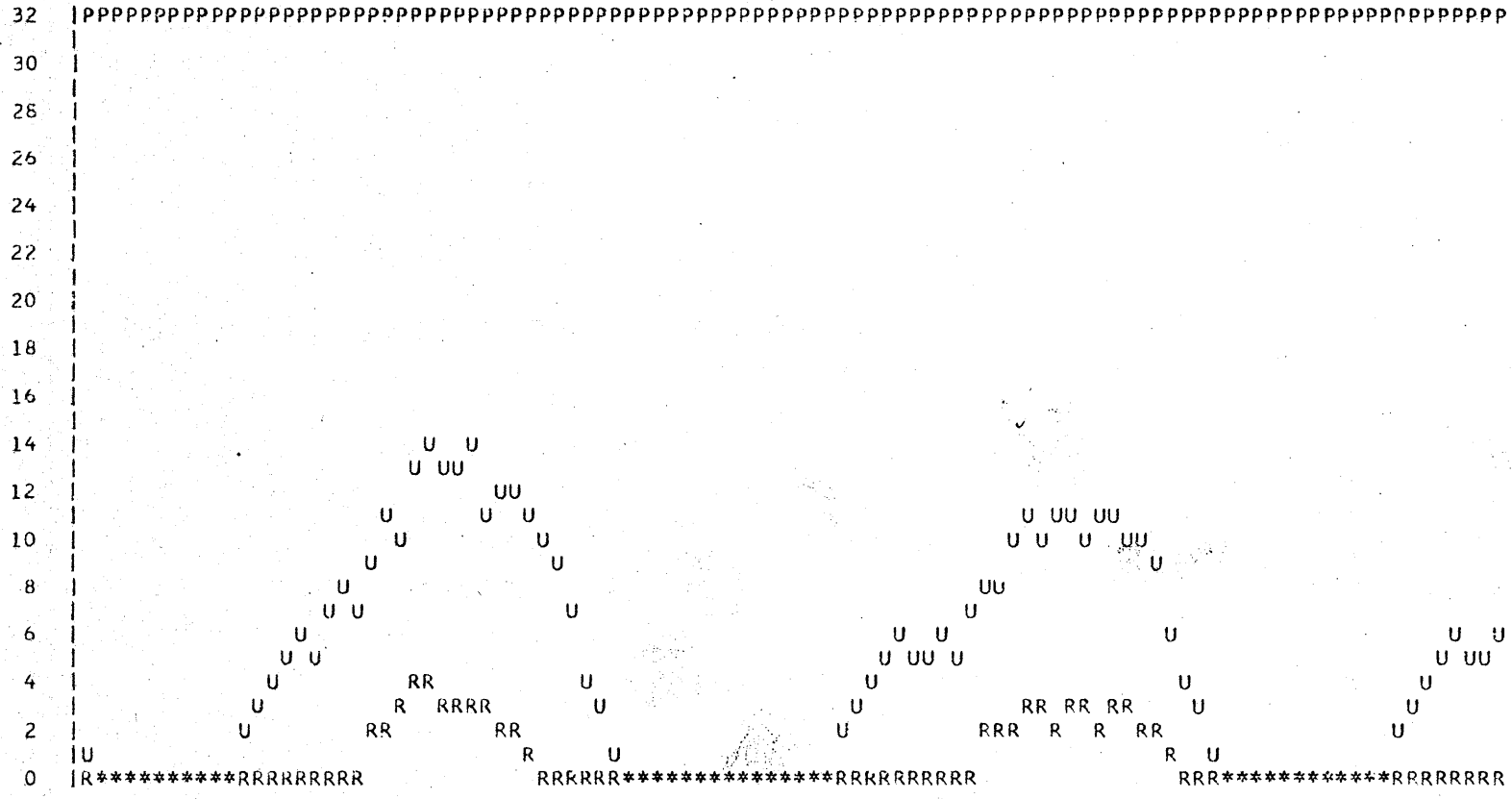


....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00038528 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

END OF SIMULATION - TIME USED= 0.0536 HOURS, PERFORMANCE = 6215 INSTRUCTIONS/MINUTE ( PLOT MODE )

\*\*\*\*\* EOF ON SYSIN - END OF RUN \*\*\*\*\*



LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

F01JAN68 3/18/68

```

000000      462+INSTRC  START 0 CONTROL SECTION FOR INPUT INSTRUCTIONS
000000      463+*
000000 00000000 464+IBEG   DC    A(0) FOR LENGTH OF INSTRUCTIONS

466 *
467 *
468 *
469 *
470          PRINT ON,NOGEN          SUPRESS INSTRUCTION MACRO EXPANSIONS
471          FORK DEX
480 DEX      FORK DEX2
489 DEX2     FORK DEX3
498 DEX3     FORK DEX4
507 DEX4     FORK TASKA
516 TASKA    LDX  X2,C10
526 TK2      FADD A1,P1,A1           INST4710
537          FSUB A1,P1,A1           INST4720
548          FMPY A1,P1,A1           INST4730
559          FDIV A1,P1,A1           INST4740
570          ADDD A1,P1,A1           INST4750
581          SUB  A1,P1,A1           INST4760
592          MPY  A1,P1,A1           INST4770
603          DIV  A1,P1,A1           INST4780
614          SFX  C1,X2,X2           INST4790
625          TXH  TK2,X2,C0           INST4800
636          TEND TASKA              INST4810
645 *
646          TERM                    CLEANUP          INST4820
683 *
684 *
685 * EQUATED INSTRUCTION OP-CODES          INST4850
686 *
687 *
688 * OP-CODES FOR ARITHMETIC TYPE INST HAVING REGULAR SOURCE-SOURCE-TARG- INST4880
689 * ET OPERANDS                          INST4890
690 *
000001 691 FADCOD EQU 1
000002 692 FSBCOD EQU 2
000003 693 FMPCOD EQU 3
000004 694 FDCVCD EQU 4
000005 695 ACDCOD EQU 5
000006 696 SUBCOD EQU 6
000007 697 MPYCOD EQU 7
000008 698 DIVCOD EQU 8
000009 699 ANDCOD EQU 9
00000A 700 IGRCOD EQU 10
00000B 701 EGRCOD EQU 11
00000C 702 SHRCOD EQU 12
00000D 703 SHLCOD EQU 13
704 *
705 * GAP LEFT HERE FOR MORE ARITHMETIC OP-CODES          INST5050
INST5060

```

PRINTOUT OF ASSEMBLED INPUT INSTRUCTIONS

INSTRUCTION NUMBER	1	FORK	,,I2
INSTRUCTION NUMBER	2	FORK	,,I3
INSTRUCTION NUMBER	3	FORK	,,I4
INSTRUCTION NUMBER	4	FORK	,,I5
INSTRUCTION NUMBER	5	FORK	,,I6
INSTRUCTION NUMBER	6	LDX	C10,,X2
INSTRUCTION NUMBER	7	FADD	A1,P1,A1
INSTRUCTION NUMBER	8	FSUB	A1,P1,A1
INSTRUCTION NUMBER	9	EMPY	A1,P1,A1
INSTRUCTION NUMBER	10	FDIV	A1,P1,A1
INSTRUCTION NUMBER	11	ADDD	A1,P1,A1
INSTRUCTION NUMBER	12	SUB	A1,P1,A1
INSTRUCTION NUMBER	13	MPY	A1,P1,A1
INSTRUCTION NUMBER	14	DIV	A1,P1,A1
INSTRUCTION NUMBER	15	SFX	C1,X2,X2
INSTRUCTION NUMBER	16	TXH	X2,C0,I7
INSTRUCTION NUMBER	17	TEND	,,I6

LISTING OF PARAMETER CARDS AS READ

TRACE        LINES=0                    COMMENT  
PROCESSOR    NUMBER=32  
CHANGE       RESOURCE=ADGEN,        MASK=10,        ACTSF=RELEASE  
CHANGE       RESOURCE=FLDIV,        NUMBER=1,        TYPE=COMMON,    ACTSE=EVENT,X  
              ACTDT=0,        ACTSF=EXFLD  
CHANGE       RESOURCE=PROC,        PRIDT=448  
CHANGE       FCTSTEP=XFCRK,        ENTRY=3,        FDT=40        CHANGING DELTA-TIME FIELD  
CHANGE       FCTSTEP=PRIORITY,        ENTRY=2,        FSE=EVENT,        FADDR=PRIORITY  
START        STINST=1,                    STPROC=31,                    X  
              ILIMIT=20000  
PLOT        PAGES=1000        SHORT RUN FOR OVERLAY TEST  
ENDPARM                    MAIN KEYWORDS NEED NOT ALL START IN COL 4

END OF PARAMETERS. 12 CARDS READ.

EXECUTION UNITS

FIXED POINT

ADD/SUB 1

MPY/DIV 1

FLOATING POINT

ADD/SUB 2

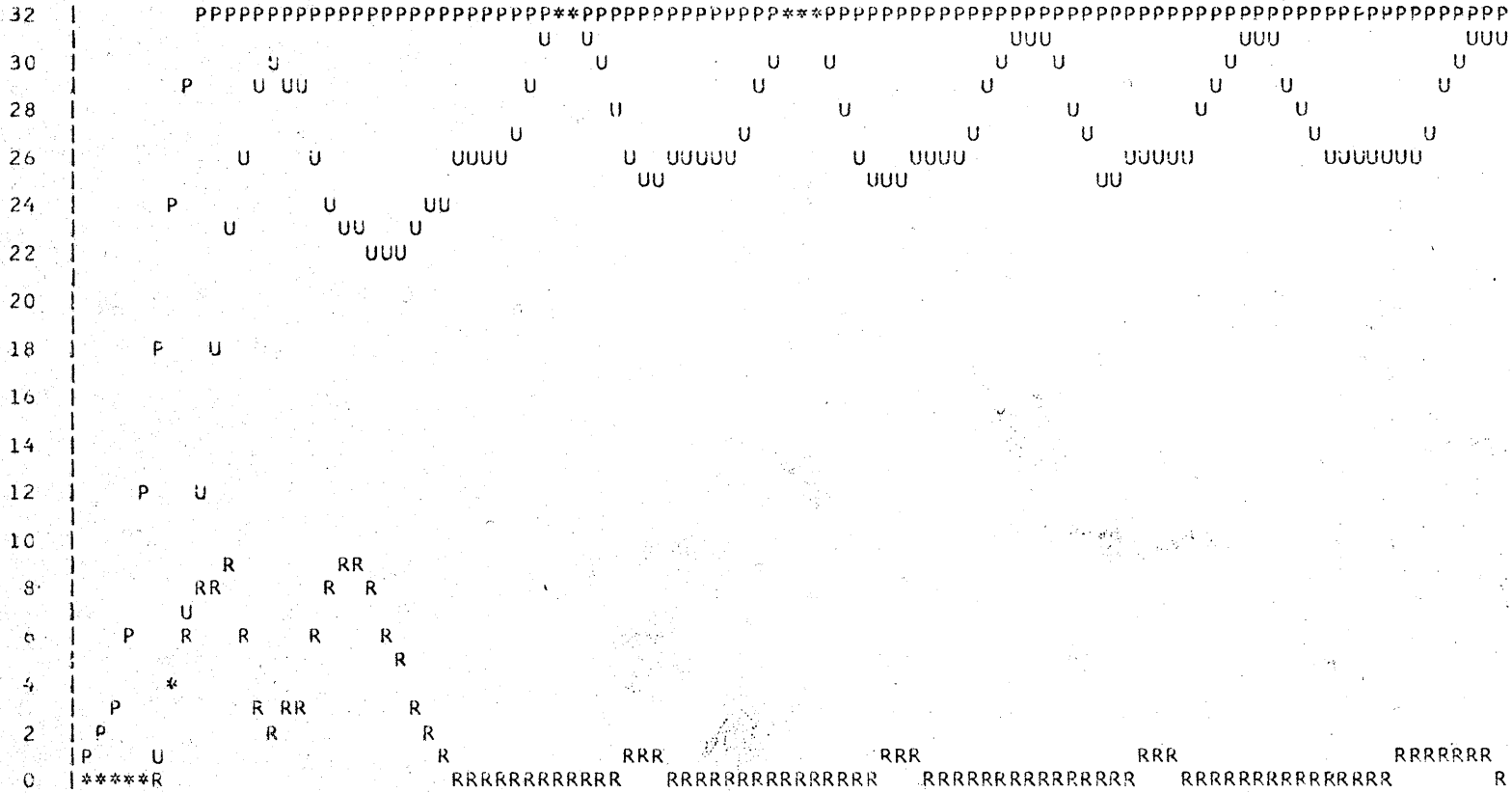
MPY 1

DIV 1

TIME = 23.4734C, DATE = 04/01/68

SYSTEM CLOCK TIME=00006528 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE

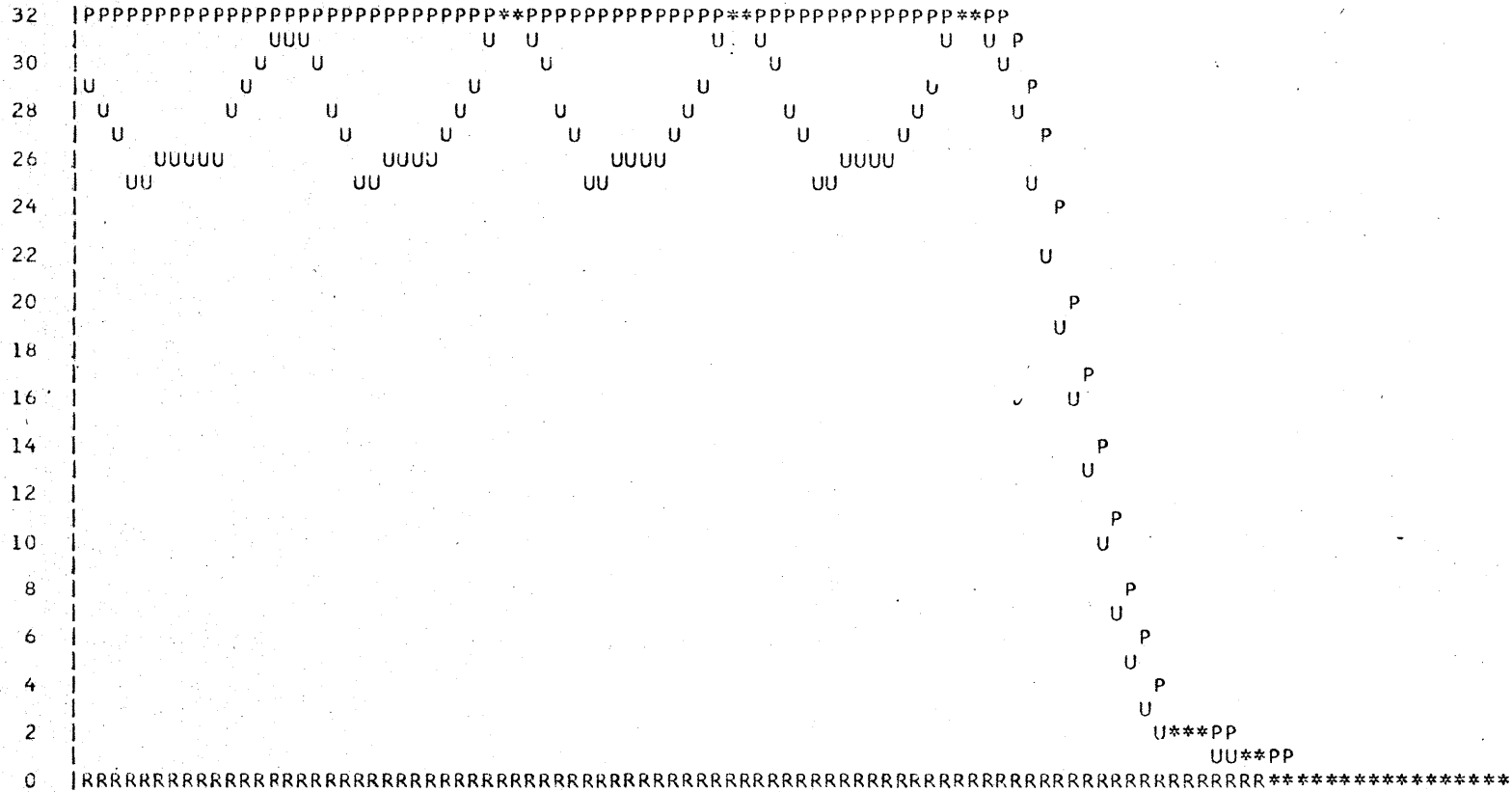


....5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00

EXECUTION UNIT UTILIZATION

SYSTEM CLOCK TIME=00012928 RING PRIORITY=(0,1,2,3)

P = ACTIVE PROCESSORS
U = ACTIVE EXECUTION UNIT STAGES
R = REJECTED E-UNIT REQUESTS
\* = ANY COMBINATION OF ABOVE



...5...1|0..1|5..2|0..2|5..3|0..3|5..4|0..4|5..5|0..5|5..6|0..6|5..7|0..7|5..8|0..8|5..9|0..9|5..1|00