

D-FD-M8315-Ø-24	FLOW DIAGRAM
D-FD-M8315-Ø-25	FLOW DIAGRAM
D-FD-M8315-Ø-26	FLOW DIAGRAM
D-FD-M8315-Ø-27	FLOW DIAGRAM
E-FD-PDP8/E-Ø-Ø6	PROCESSOR FLOW CHART
D-TD-PDP8/E-Ø-Ø5	TIMING (PDP8/E)
E-CS-M83ØØ-Ø-1	MAJOR REGISTERS
E-CS-M831Ø-Ø-1	MAJOR REGISTERS CONTROL
E-CS-M832Ø-Ø-1	BUS LOADS
E-CS-M833Ø-Ø-1	TIMING GENERATOR
A-SP-KM8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8317-Ø-1	OPTION BOARD #2
D-UA-M8317-Ø-Ø	UNIT ASSY
B-PL-M8317-Ø-Ø	PARTS LIST
D-TD-KM8-A-4	AUTO RESTART/BOOT SEQUENCE
D-TD-KM8-A-5	BOOTSTRAP TIMING
D-FD-KM8-A-6	FLOW CHART OPTION #2
A-SP-KM8-A-7	ROM PROG. INST.
A-SP-DKCS-A-1	FIELD INST. AND ACCEPTANCE PROCEDURE
D-CS-M8316-Ø-1	OPTION BOARD #1
D-UA-M8316-Ø-Ø	UNIT ASSY
B-PL-M8316-Ø-Ø	PARTS LIST
E-UA-KCS-A-Ø	BEZEL ASSY
D-AD-7010644-Ø-Ø	KEYBOARD ASSY
D-CS-5411241-Ø-1	INDICATOR DISPLAY
D-CS-5411316-Ø-1	REGISTERS AND CONTROL
A-SP-KT8-A	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-UA-M8416-Ø-Ø	KT8-A UNIT ASSY
B-PL-M8416-Ø-Ø	PARTS LIST
D-CS-M8416-Ø-1	MEMORY MANAGEMENT OPTION
D-CS-M9Ø2Ø-Ø-1	KT8-A TERMINATOR CARD
A-SP-MS8-C-Ø	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8417-Ø-1	PDP-8 MOS MEMORY
D-UA-M8417-Ø-Ø	UNIT ASSY
B-PL-M8417-Ø-Ø	PARTS LIST
A-SP-MR8-F-2	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8349-Ø-1	1K PROM
A-SP-MS8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8311-Ø-1	4K X 12 MOS MEMORY

TITLE	SIZE	CODE	NUMBER	REV.
8A SEMICONDUCTOR MEMORY FAMILY	B	TC	8A-1-1	A
SHEET 2 OF 2				

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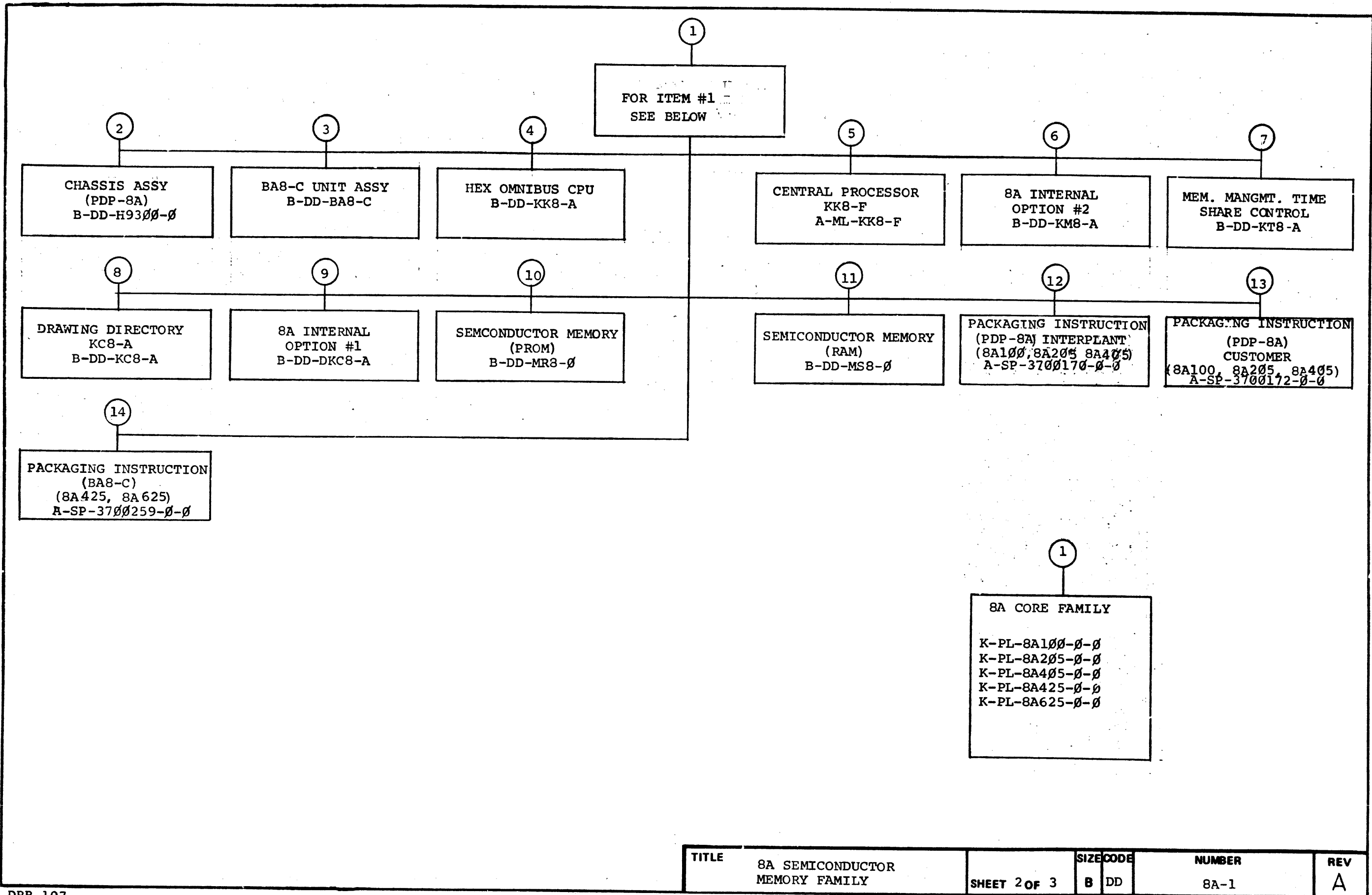
FOR FIELD MAINTENANCE PRINT SET SEE
B-TC-8A-1-1

UNIT VARIATIONS	
VAR	TITLE
8A100	12 SLOT OMNIBUS W/KK8A CPU
8A205	12 SLOT OMNIBUS W/KK8A CPU
	OPTION #2 AND G8016 REGULATOR
8A405	12 SLOT OMNIBUS W/KK8A CPU
	OPTION #2 AND G8018 REGULATOR
8A425	20 SLOT OMNIBUS W/KK8A CPU
	AND OPTION #2
8A625	20 SLOT OMNIBUS W/KK8-F
	CPU AND OPTION #2

REVISIONS	REV.	A	USED ON OPTION/MODEL	DRN.	M.P. DUGGAN	DATE	12 77 MAY	TITLE			8A SEMICONDUCTOR MEMORY FAMILY	digital	
	CHANGE NO.	8A-1-MK02C		CHK'D.	<i>Larry Naclio</i>	DATE	11- JUL-77	SIZE	CODE	NUMBER			REV
	CHK	✓		PROJ. ENG.	<i>Larry Naclio</i>	DATE	11- JUL-77	B	DD	8A-1			A
				PROB.	<i>Jim Kane</i>	DATE	25 77	DIST.					
				SHEET 1 OF 3									

DRB 106A

MK



TITLE	SIZE CODE	NUMBER	REV
8A SEMICONDUCTOR MEMORY FAMILY	B DD	8A-1	A

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
1	MP00415	FIELD MAINTENANCE PRINT SET (MP)	-	9	B-DD-DK8-A	8A INTERNAL OPTION #1	-
	B-TC-8A-1-1	FIELD MAINTENANCE PRINT SET (TC)	-				
	K-PL-8A100-0-0	8A SEMICONDUCTOR MEMORY FAMILY (8A100)	E/M				
	K-PL-8A205-0-0	8A CORE FAMILY (8A205)	E/M				
	K-PL-8A405-0-0	8A CORE FAMILY (8A405)	E/M				
	K-PL-8A425-0-0	8A CORE FAMILY (8A425)	E/M	10	B-DL-MR8-0	SEMICONDUCTOR MEMORY (PROM)	-
	K-PL-8A625-0-0	8A CORE FAMILY (8A625)	E/M				
2	B-DD-H9300	CHASSIS ASSY (PDP-8A)	-				
3	B-DD-BA8-C	BA8-C UNIT ASSY	-	11	B-DD-MS8-0	SEMICONDUCTOR MEMORY (RAM)	-
				12	A-SP-3700170-0-0	PACKAGING INSTRUCTION, INTERPLANT (PDP-8A)	-
4	B-DD-KK8-A	HEX OMNIBUS CPU	-				
				13	A-SP-3700172-0-0	PACKAGING INSTRUCTION, CUSTOMER (PDP-8A)	-
5	A-ML-KK8-F	CENTRAL PROCESSOR KK8-F	-				
				14	A-SP-3700259-0-0	PACKAGING INSTRUCTION, INTERPLANT W/CONSOLE (BA8-C)	-
6	B-DD-KM8-A	8A INTERNAL OPTION #2	-				
7	B-DD-KT3-A	MEM MANAGEMENT AND TIME SHARE CONTROL	-				
8	B-DD-KC8-A	DRAWING DIRECTORY KC8-A	-				

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL



TITLE 8A SEMICONDUCTOR MEMORY FAMILY
SHEET 3 OF 3 SIZE CODE B DD NUMBER 8A-1 REV A

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					AA	AB	AC	AD	AE	AF	AK	AL	AM	AN	AP	AR	
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	1	0	1	0	1	0	0	0	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	1	0	1	0	0	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	0	0	0	0	1	0	1	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	0	0	0	0	0	1	0	1
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CFU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
12	12	A-PL-MS8-C-0	00MS8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
13	13	A-PL-MS8-A-0	00MS8-AA	PDP8A RAM 1K	0	0	1	1	0	0	0	0	0	0	0	1	1
14	14	A-PL-MS8-A-0	00MS8-AB	PDP8A RAM 2K	0	0	0	0	1	1	0	0	0	0	0	0	0
15	15	A-PL-MS8-A-0	00MS8-AD	PDP8A RAM 4K	0	0	0	0	0	0	1	1	0	0	0	0	0
16	16	C-UA-MR8-F-0	00MR8-FB	1KX12 CONTENT ALTERABLE ROM & 25	0	0	0	0	0	0	0	0	0	0	0	0	0
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY			BASIC PART NO: 8A100			DRN: M DUGGAN			DATE: 12-MAY-77			DBP			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF B	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE			PARTS LIST						
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX	DES.ENG.:	L NARHI	DATE:	8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY			8A100						
			[A] AA,AB,AC,AD,AE,AF,AK,AL,AM,AN,AP,AR	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER									
			[B] AS,AT,AU,AV,FA,FB,FC,FD	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV						
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		K	PL	8A100-0-0	B						
			[D]			FILE NAME:				MK0384.PLS	EDIT #						
			[E]			#B-DD-8A-1					7						
			[F]														

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MK

AUTOMATED BY PRTLST.3P(44)

P A R T S L I S T

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				AA	AB	AC	AD	AE	AF	AK	AL	AM	AN	AP	AR		
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	SECTION A	OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A SEMICONDUCTOR MEMORY FAMILY					8A100-0-0	
							8A100			K	PL		B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION								
				AS	AT	AU	AV	FA	FB	FC	FD	
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	0	0	0	0	1	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	1	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	1	0	1	0	0	0	1	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	0	0	1
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
9	9	A-PL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
11	11	A-PL-MSB-C-0	00MSB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
12	12	A-PL-MSB-C-0	00MSB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
13	13	A-PL-MSB-A-0	00MSB-AA	PDP8A RAM 1K	0	0	0	0	0	0	0	0
14	14	A-PL-MSB-A-0	00MSB-AB	PDP8A RAM 2K	1	1	0	0	0	0	0	0
15	15	A-PL-MSB-A-0	00MSB-AD	PDP8A RAM 4K	0	0	1	1	0	0	0	0
16	16	C-UA-MRB-F-0	00MRB-FB	1KX12 CONTENT ALTERABLE ROM & 25	0	0	0	0	1	1	1	1
17	17	A-PL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
18	18	A-PL-DKCB-A-0	00DKCB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
19	19	E-UA-KCB-A-0	00KCB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
21	21	A-PL-BA-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-

REVISION HISTORY		BASIC PART NO: 8A100		DRN:	M DUGGAN	DATE:	12-MAY-77	DBP DIGITAL			
ENG!	ECO NUMBER	REV	SECTION B OF B	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE	PARTS LIST		
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX					8A SEMICONDUCTOR MEMORY FAMILY	8A100		
			[A] AA,AB,AC,AD,AE,AF,AK,AL,AM,AN,AP,AR	DES.ENG.:	L NARHI	DATE:	8-NOV-77		DOCUMENT NUMBER		
			[B] AS,AT,AU,AV,FA,FB,FC,FD	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV
			[C]	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	K	PL	8A100-0-0	B
			[D]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #		
			[E]			#B-DD-8A-1		MK0384.PLS	7		
			[F]								

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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION								
				AS	AT	AU	AV	FA	FB	FC	FD	
31	31	A-PL-KM8-A-0	00KM8-AB *** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC *** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
33	33	A-PL-KM8-A-0	00KM8-AD *** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION B OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A100			K	PL	8A100-0-0	B

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
					AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU	BV
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	0	0	1	0	1	0	0	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	0	0	1	0	1	0	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	1	0	0	0	0	0	1	0	1	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	1	0	0	0	0	0	1	0	1
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	0	0	1	1	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	1	1	0	0	1	1
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	00DKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	0	0	0
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY			BASIC PART NO: 8A205			DRN: M DUGGAN			DATE: 12-MAY-77			DBP			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF C	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE			PARTS LIST						
DF	BA-1-MK002B	B	SECTION, VARIATION INDEX	DES.ENG.:	L NARHI	DATE:	8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY			8A205						
			[A] AA,AB,AC,AD,AM,BN, BP,BR,BS,BT,BU,BV	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER									
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV						
			[C] DS,DT,DU,DV	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	#B-DD-8A-1	K	PL	8A205-0-0	B						
			[D]	FILE NAME:		EDIT #:				MK0385.PLS	5						
			[E]	*THIS DRAWING AND SPECIFICATIONS HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT (C) 1981, DIGITAL EQUIPMENT CORPORATION *													

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AUTOMATED BY PRTLST.3P(44)

P A R T S L I S T

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
				AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU	BV	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV
							8A205			K	PL	8A205-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR		
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	1	0	0	0	0	0	0	1	0	1	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	0	0	0	0	0	1	0	1
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	1	0	1	0	0	0	0	0	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	1	0	1	0	0	0	0	0
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0	0	0
12	12	A-PL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1	0	0	1	1	0	0	1	1	1
13	13	A-PL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKCB-A-0	00DKCB-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KCB-A-0	00KCB-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	1	1	1	1
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-BA-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY		BASIC PART NO: 8A205		DRN: M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF C	CHK'D: L NARHI	DATE: 8-NOV-77	TITLE PARTS LIST							
DF	BA-1-MK002B	B	SECTION, VARIATION INDEX	DES.ENG.: L NARHI	DATE: 8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A205							
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV	RESP.ENG.: L NARHI	DATE: 8-NOV-77	DOCUMENT NUMBER							
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR			SIZE	CODE	NUMBER	REV				
			[C] DS,DT,DU,DV	MFG.ENG.: J V KANE	DATE: 8-NOV-77	K	PL	8A205-0-0	B				
			[D]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #:					
			[E]		#B-DD-8A-1	MK0385.PLS		5					
			[F]										

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION B OF C	SIZE	CODE	DOCUMENT NUMBER	REV
							8A205			K	PL	8A205-0-0	B

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
					DS	DT	DU	DV
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	0	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	1	0	1	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-
9	9	A-PL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-
11	11	A-PL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0
12	12	A-PL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1
13	13	A-PL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-
14	14	A-PL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-
15	15	A-PL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-
17	17	A-PL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-
18	18	A-PL-DKC8-A-0	00DKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1
19	19	E-UA-KCB-A-0	00KCB-AA	PROGRAMMER'S CONSOLE	1	1	1	1
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-

REVISION HISTORY			BASIC PART NO: 8A205		DRN: M DUGGAN		DATE: 12-MAY-77		DBP DIGITAL	
ENG	ECO NUMBER	REV	SECTION C OF C	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE	PARTS LIST	
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	DATE:	8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY	8A205	
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV	DES.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER		
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	SIZE	CODE	NUMBER
			[C] DS,DT,DU,DV	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	K	PL	8A205-0-0
			[D]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #	
			[E]			#B-DD-8A-1		MK0385.PLS	5	
			[F]							

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
					DS	DT	DU	DV
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION C OF C	SIZE	CODE	DOCUMENT NUMBER	REV
							8A205			K	PL	8A205-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION														
				AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU	BV			
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-FL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-FL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	0	0	1	1	0	0	0	0
12	12	A-FL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	1	1	0	0	1	1	0	0
13	13	A-FL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-FL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-FL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-FL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-FL-DKCB-A-0	00DKCB-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KCB-A-0	00KCB-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	0	0	0	0	0
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-FL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	0	0	0	0	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 8 AMP 115V 60HZ	1	0	0	0	1	0	1	0	0	0	0	0	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	1	0	0	0	1	0	1	0	0	0	0	0	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	0	0	1	0	0	0	0	0	1	0	1	0	1	0
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	0	0	1	0	0	0	0	0	1	0	1	0	1

REVISION HISTORY			BASIC PART NO: 8A405		DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF C	CHK'D:	L NARHI	DATE: 8-NOV-77	TITLE	PARTS LIST							
DF	BA-1-MK002B	B	SECTION. VARIATION INDEX	DES.ENG.:	L NARHI	DATE: 8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A405								
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77	DOCUMENT NUMBER								
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR				SIZE	CODE	NUMBER	REV					
			[C] DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	8A405-0-0	B					
			[D]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:			EDIT #:					
			[E]			#B-DD-8A-1	MK0386.PLS			4					
			[F]												

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MK

PARTS LIST

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
					AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU	BV
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	0	0	0	0	0	0	0	0

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV
							8A405			K	PL	8A405-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR		
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0	0	0
12	12	A-PL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1	0	0	1	1	0	0	1	1	1
13	13	A-PL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKCB-A-0	00DKCB-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KCB-A-0	00KCB-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	1	1	1	1	1
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-BA-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	0	0	0	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 3 AMP 115V 60HZ	1	0	1	0	0	0	0	0	1	0	1	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	1	0	1	0	0	0	0	0	1	0	1	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	0	0	0	0	1	0	1	0	0	0	0	0	0
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	0	0	0	0	1	0	1	0	0	0	0	0

REVISION HISTORY			BASIC PART NO: 8A405		DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECD NUMBER	REV	SECTION B OF C	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE PARTS LIST								
DF	BA-1-MK002B	B	SECTION, VARIATION INDEX	DES.ENG.:	L NARHI	DATE:	8-NOV-77	BA SEMICONDUCTOR MEMORY FAMILY 8A405								
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER								
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV					
			[C] DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		K	PL	8A405-0-0	B					
			[D]			#B-DD-8A-1						FILE NAME:	EDIT #			
			[E]									MK0386.PLS	4			
			[F]													

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AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET B2 OF B2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	0	0	0	0	0	0	0	0	0

! D ! I ! G ! I ! T ! A ! L !	! TITLE	! SIZE ! CODE !	! DOCUMENT NUMBER	! REV !
! ! ! ! ! ! ! !	8A SEMICONDUCTOR MEMORY FAMILY	! SECTION B OF C !	! ! ! !	! ! ! !
! ! ! ! ! ! ! !	8A405	! ! ! !	! K ! PL ! 8A405-0-0 !	! B !

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				DS	DT	DU	DV	LM	LN	LP	LR	LS	LT	LU	LV		
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-FL-KKB-A-0	00KKB-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KKB-F-0	00KKB-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-FL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0	0	0
12	12	A-FL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1	0	0	1	1	0	0	1	1	1
13	13	A-FL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-FL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-FL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-FL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-FL-DKCB-A-0	00DKCB-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KCB-A-0	00KCB-AA	PROGRAMMER'S CONSOLE	1	1	1	1	0	0	0	0	0	0	0	0	0
20	20	D-UA-KTB-A-0	00KTB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-FL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	0	0	0	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 8 AMP 115V 60HZ	0	0	0	0	1	0	1	0	0	0	0	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	0	0	0	0	1	0	1	0	0	0	0	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	1	0	1	0	0	0	0	0	1	0	1	0	0
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	1	0	1	0	0	0	0	0	1	0	1	1

REVISION HISTORY			BASIC PART NO: BA405			DRN: M DUGGAN			DATE: 12-MAY-77			DBP			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION C OF C	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE			PARTS LIST						
DF	BA-1-MK002B	B	SECTION, VARIATION INDEX	[A]	AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV	[B]	CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	[C]	DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	[D]		[E]		[F]			
				[E]		[F]		RESP.ENG.: L NARHI			DATE: 8-NOV-77			DOCUMENT NUMBER			
				[G]		[H]		MFG.ENG.: J V KANE			DATE: 8-NOV-77			K	PL	BA405-0-0	REV
				[I]		[J]		ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:			FILE NAME:			EDIT #
								#B-DD-8A-1			MK0386.PLS						4

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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
				DS	DT	DU	DV	LM	LN	LP	LR	LS	LT	LU	LV	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	0	0	0	0	0	0	0	0
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	1	1	1	1	1	1	1	1

D	I	G	I	T	A	L	TITLE	SECTION C OF C	SIZE	CODE	DOCUMENT NUMBER	REV
							8A SEMICONDUCTOR MEMORY FAMILY					
							8A405					
									K	PL	8A405-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
				AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP	BR	
1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	E-UA-BAB-C-0	00BAB-CA	20-SLOT OMNIBUS, 10.5 X 21", 2 GB0	1	0	0	0	1	0	0	0	1	0	1	0	0
6	E-UA-BAB-C-0	00BAB-CB	20-SLOT OMNIBUS, 10.5 X 21", 2 GB0	0	1	0	0	0	1	0	0	0	0	1	0	1
7	E-UA-BAB-C-0	00BAB-CH	SAME AS BAB-CB EXCEPT 240V 60HZ	0	0	1	0	0	0	1	0	0	0	0	0	0
8	E-UA-BAB-C-0	00BAB-CJ	SAME AS BAB-CA EXCEPT 120V 50HZ	0	0	0	1	0	0	0	1	0	0	0	0	0
9	A-FL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	A-FL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	1	1	0	0	0	0	0
12	A-FL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	0	0	1	1	2	2	2
13	A-FL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	A-FL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	A-FL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	A-FL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	A-FL-DK8-A-0	00DK8-AA	OPTION BOARD #1: SLU, XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0	0	0
19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
20	D-UA-KT8-A-0	00KT8-A	MEM MAN OPTION FOR KT8-A SYS	0	0	0	0	0	0	0	0	0	0	1	1	1
21	A-FL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	A-SP-3700259-0-0	3700259-01	INSTR PKG BAB-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1	1	1
26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	1	1	1	1	1	1	1	1	1	1	1	1	1
27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY		BASIC PART NO: 8A425		DRN: M DUGGAN	DATE: 12-MAY-77	DBP DIGITAL				
ENG	ECO NUMBER	REV	SECTION A OF B	CHK'D: L NARHI	DATE: 8-NOV-77	TITLE PARTS LIST				
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX	DES. ENG.: L NARHI	DATE: 8 NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A425				
			[A] AA, AB, AC, AD, BH, BJ, BK, BL, BM, BN, BP, BR	RESP. ENG.: L NARHI	DATE: 8-NOV-77	DOCUMENT NUMBER				
			[B] BS, BT, BU, BV, CM, CN, CP, CR, CS, CT, CU, CV	MFG. ENG.: J V KANE	DATE: 8-NOV-77	SIZE	CODE	NUMBER	REV	
			[C]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	PL	8A425-0-0	B	
			[D]		#B-DD-8A-1	FILE NAME:	EDIT #			
			[E]			MK0387.PLS	4			
			[F]							

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP	BR	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A425			K	PL	8A425-0-0	B

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					BS	BT	BU	BV	CM	CN	CP	CR	CS	CT	CU	CV	
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	20-SLOT OMNIBUS,10.5 X 21",2 G80	0	0	0	0	1	0	1	0	0	0	0	0	0
6	6	E-UA-BAB-C-0	00BAB-CB	20-SLOT OMNIBUS,10.5 X 21",2 G80	0	0	0	0	0	1	0	1	0	0	0	0	0
7	7	E-UA-BAB-C-0	00BAB-CH	SAME AS BAB-CB EXCEPT 240V 60HZ	1	0	1	0	0	0	0	0	0	1	0	1	0
8	8	E-UA-BAB-C-0	00BAB-CJ	SAME AS BAB-CA EXCEPT 120V 50HZ	0	1	0	1	0	0	0	0	0	0	1	0	1
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	0	0	0	0	0	0	0	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	1	1	2	2	1	1	2	2	1	1	2	2	2
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DK8-A-0	00DK8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	1	1	1	1	1	1	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	MEM MAN OPTION FOR KT8-A SYS	0	0	1	1	0	0	1	1	0	0	1	1	1
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
23	23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	INSTR PKG BAB-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1	1	1
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	1	1	1	1	1	1	1	1	1	1	1	1	1
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY			BASIC PART NO: 8A425			DRN: M DUGGAN			DATE: 12-MAY-77			DBP			DIGITAIL		
ENG	ECD NUMBER	REV	SECTION B OF B	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE PARTS LIST									
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX	DES.ENG.:	L NARHI	DATE:	8 NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A425									
			[A] AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER									
			[B] BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV						
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		K	PL	8A425-0-0	B						
			[D]			FILE NAME:				MK0387.PLS	EDIT #						
			[E]			#B-DD-8A-1					4						
			[F]														

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AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				BS	BT	BU	BV	CM	CN	CP	CR	CS	CT	CU	CV		
31	31	A-PL-KMB-A-0	00KMB-AB	*** THIS ITEM IS NOT USED	***	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KMB-A-0	00KMB-AC	8A INTERNAL OPTION 2		1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KMB-A-0	00KMB-AD	*** THIS ITEM IS NOT USED	***	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION B OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A425			K	PL	8A425-0-0	B

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP	BR	
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	20-SLOT OMNIBUS,10.5 X 21",2 G80	1	0	0	0	1	0	0	0	1	0	1	0	0
6	6	E-UA-BAB-C-0	00BAB-CB	20-SLOT OMNIBUS,10.5 X 21",2 G80	0	1	0	0	0	1	0	0	0	0	1	0	1
7	7	E-UA-BAB-C-0	00BAB-CH	SAME AS BAB-CB EXCEPT 240V 60HZ	0	0	1	0	0	0	1	0	0	0	0	0	0
8	8	E-UA-BAB-C-0	00BAB-CJ	SAME AS BAB-CA EXCEPT 120V 50HZ	0	0	0	1	0	0	0	1	0	0	0	0	0
9	9	A-PL-KK8-A-0	00KK8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
10	10	D-UA-KK8-F-0	00KK8-F	CENTRAL PROCESSOR [8E]	1	1	1	1	1	1	1	1	1	1	1	1	1
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	1	1	1	0	0	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	0	0	1	1	2	2	2
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DK8-A-0	00DK8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	MEM MAN OPTION FOR KT8-A SYS	0	0	0	0	0	0	0	0	0	0	1	1	1
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
23	23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	INSTR PKG BAB-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1	1	1
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	1	1	1	1	1	1	1	1	1	1	1	1	1
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY			BASIC PART NO: 8A625		DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L	
ENG	ECD NUMBER	REV	SECTION A	OF B	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE	PARTS LIST							
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX		[A]	AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR	DES.ENG.:	L NARHI	DATE:	8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A625						
			[B]		[C]	BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	RESP.ENG.:	L NARHI	DATE:	8-NOV-77	DOCUMENT NUMBER						
			[D]		[E]		MFG.ENG.:	J V KANE	DATE:	8-NOV-77	SIZE	CODE	NUMBER	REV			
			[F]		[G]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		K	PL	8A625-0-0	B	FILE NAME:	EDIT #	
									#B-DD-8A-1						MK0388.PLS	6	

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MK

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
					AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP	BR	
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A625			K	PL	8A625-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				BS	BT	BU	BV	CM	CN	CP	CR	CS	CT	CU	CV		
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	20-SLOT OMNIBUS,10.5 X 21",2 GB0	0	0	0	0	1	0	1	0	0	0	0	0	0
6	6	E-UA-BAB-C-0	00BAB-CB	20-SLOT OMNIBUS,10.5 X 21",2 GB0	0	0	0	0	0	1	0	1	0	0	0	0	0
7	7	E-UA-BAB-C-0	00BAB-CH	SAME AS BAB-CB EXCEPT 240V 60HZ	1	0	1	0	0	0	0	0	0	1	0	1	0
8	8	E-UA-BAB-C-0	00BAB-CJ	SAME AS BAB-CA EXCEPT 120V 50HZ	0	1	0	1	0	0	0	0	0	0	1	0	1
9	9	A-PL-KKB-A-0	00KKB-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
10	10	D-UA-KKB-F-0	00KKB-F	CENTRAL PROCESSOR [8E]	1	1	1	1	1	1	1	1	1	1	1	1	1
11	11	A-PL-MSB-C-0	00MSB-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	0	0	0	0	0	0	0	0	0
12	12	A-PL-MSB-C-0	00MSB-CB	32K 12 BIT MOS RAM 4K CHIPS	1	1	2	2	1	1	2	2	1	1	2	2	2
13	13	A-PL-MSB-A-0	00MSB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MSB-A-0	00MSB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MSB-A-0	00MSB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MRB-F-0	00MRB-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KMB-A-0	00KMB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKCB-A-0	0DKCB-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	1	1	1	1	1	1	1	1	1
19	19	E-UA-KCB-A-0	00KCB-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KTB-A-0	00KTB-A	MEM MAN OPTION FOR KTB-A SYS	0	0	1	1	0	0	1	1	0	0	1	1	1
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
23	23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	INSTR PKG BAB-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1	1	1
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER BAB20	1	1	1	1	1	1	1	1	1	1	1	1	1
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY		BASIC PART NO: 8A625		DRN: M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF B	CHK'D: L NARHI	DATE: 8-NOV-77	TITLE		PARTS LIST					
DF	EA-1-MK002B	B	SECTION. VARIATION INDEX	DES.ENG.: L NARHI	DATE: 8 NOV-77	8A SEMICONDUCTOR MEMORY FAMILY		8A625					
			[A] AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR	RESP.ENG.: L NARHI	DATE: 8-NOV-77	DOCUMENT NUMBER							
			[B] BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	MFG.ENG.: J V KANE	DATE: 8-NOV-77	SIZE	CODE	NUMBER	REV				
			[C]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	FL	8A625-0-0	B	FILE NAME:	EDIT #:		
			[D]		#B-DD-8A-1			MK0388.PLS	6				
			[E]	*THIS DRAWING AND SPECIFICATIONS HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT (C) 1981, DIGITAL EQUIPMENT CORPORATION *									
			[F]										

AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				BS	BT	BU	BV	CM	CN	CP	CR	CS	CT	CU	CV		
31	31	A-PL-KMB-A-0	00KMB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KMB-A-0	00KMB-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KMB-A-0	00KMB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION B OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A625			K	PL	8A625-0-0	B

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS					QUANTITY VARIATION													
PARTS LIST					8A100	8A205	8A405	8A425	8A625									
MADE BY	M. DUGGAN	CHECKED	SECTION															
DATE	12 MAY 77	DATE	11-JUL-77															
ENG	Jerry Harke	PROD	J. Kane	ISSUED SECT.														
DATE	11-JUL-77	DATE	25 OCT 77															
ITEM NO.	DWG NO./PART NO.	DESCRIPTION																
1	EK-8A002-OP	PDP-8A OPERATOR'S HANDBOOK			1	1	1	0	0									
2	MP00415	8A SEMICONDUCTOR FAMILY MAINTENANCE PRINT SET			1	1	1	1	1									
3	A-PL-8A-1-3	8A SEMICONDUCTOR FAMILY SOFTWARE LIST			1	1	1	1	1									
4	EK-8A002-MM	PDP-8A USER'S MANUAL			1	1	1	1	0									
5	EK-H9300-IP	H9300 ILLUSTRATED PARTS BREAKDOWN			1	1	1	0	0									
6	EK-BA8C-IP	BA8-C ILLUSTRATED PARTS BREAKDOWN			0	0	0	1	1									
7	EK-KC8A-IP	KC8-A ILLUSTRATED PARTS BREAKDOWN			0	1	1	1	1									
TITLE				ASSY NO.	SIZE	CODE	NUMBER	REV	ECO NO.									
SHIPPING LIST 8A SEMICONDUCTOR MEMORY FAMILY (8A100, 8A205, 8A405, 8A425, 8A625)				C-PL-8A-1-0	A	PL	8A-1-2											
				SHEET 1 OF 1	DIST													

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS PARTS LIST				QUANTITY / VARIATION																		
MADE BY M. DUGGAN		CHECKED L. NARHI		SECTION																		
DATE 12-MAY-77		DATE 11-JUL-77		1																		
ENG LARRY. NARHI		PROD J. KANE		ISSUED SECT.																		
DATE 11-JUL-77		DATE 25-OCT-77																				
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION				8A100	8A205	8A405	8A425	8A625												
1	ZF006-RB	4K BASIC SOFTWARE				Ø	1	1	1	1												
2	ZF209-RB	4K OPTION #2 SOFTWARE				Ø	1	1	1	1												
3	ZF208-RB	4K OPTION #1 SOFTWARE				Ø	1	1	1	1												
4	ZF235-RB	MEMORY MANAGEMENT OPTION SOFTWARE				Ø	Ø	Ø	1	1												
TITLE SOFTWARE LIST 8A SEMICONDUCTOR		ASSY NO. B-DD-8A-1		SIZE CODE A PL		NUMBER 8A-1-3		REV. A		ECO NO. 8A-1												
MEMORY FAMILY (8A100, 8A205, 8A405, 8A425, 8A625)		SHEET 1 OF 1		DIST.																		

DEC FORM DEC 16 (325)-1031-N870
DRA 110

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
 DATE 26-apr-77

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG *J. Malin* 11-Nov-77 APPD *Clay Placke* 11-11-77 SIZE CODE A SP NUMBER 8A-1-4 REV 1 of 5
 DEC FORM NO EN-01022-16-N70(JAN) DRA 108

ENGINEERING SPECIFICATION

TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

- I. GENERAL
- II. UNPACKING
- III. INSPECTION
- IV. INSTALLATION PROCEDURE
- V. ACCEPTANCE PROCEDURE

DEC FORM NO EN-01022-16-N70(JAN) DRA 108
 SIZE CODE A SP NUMBER 8A-1-4 REV 2 OF 6

ENGINEERING SPECIFICATION

TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION ACCEPTANCE PROCEDURE

- I. GENERAL
 Installation of the basic 8A100, 205, 405, 425 and 625 computer requires no special tools or equipment. Normal hand tools are all that are required.
- II. UNPACKING
 Unpack and inspect the equipment using the procedure provided in the Operator's Handbook.
- III. INSPECTION
 After removing the equipment packing material, inspect the equipment.
 1. Inspect the external surface of the chassis for surface, bezel, switch and light damage.
 2. Internally inspect the 8A enclosure and console for damage, loose nuts, bolts, screws, etc.
 3. Inventory all hardware against shipping list.
 4. Inventory all software against software list, if ordered.
 5. Inventory all prints against shipping list if ordered.
- IV. INSTALLATION PROCEDURE
 Install the equipment using the following procedure:
 1. Turn off the power switch of the Limited Function Console.

WARNING

 DO NOT TOUCH THE COMPUTER AFTER PLUGGING IT IN UNTIL IT IS CHECKED FOR THE PROPER GROUNDING.
 2. Insure that all power is received from the same source.
 3. Plug in the power cord.

DEC FORM NO EN-01022-16-N70(JAN) DRA 108
 SIZE CODE A SP NUMBER 8A-1-4 REV 3 OF 6

ENGINEERING SPECIFICATION

TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION ACCEPTANCE PROCEDURE

4. Before touching the computer, check frame to ground to insure that no AC voltage is present.
5. Unplug power cord.
6. Turn "on" Power ON/OFF switch and set regulator circuit breaker to the "ON" position. (Behind the Limited Function Console on the 8A100, 8A205, and 8A425 or inside the rear panel on the 8A625 and 8A625.)
7. Repeat Steps 3 and 4.
8. Power should now be applied to the 8A; fans should be running, and the power light on the Limited Function Console should be "ON". The light labeled 'Battery charging' should be illuminated on the 8A205 and 8A625 indicating that DC power is okay.
 If none of the above occur, remove the Limited Function panel and check the Master/Slave switch located below the ON/OFF switch on the Limited Function Board.
9. The Run light should not be on. If it is, switch Power Off via the ON/OFF switch.
 Remove the M8315 CPU module (on all except 8A625) and set switches as indicated below. Then insert CPU in the first slot in the OMNIBUS and turn power back on. The Run light should remain off.
 S1-1 thru S1-6, S1-8 set to "OFF" position.
 S1-7 set to "ON" position.
10. Check modules to insure they are located in their proper position in the OMNIBUS. Refer to the Operator's Handbook.
11. Check the operation of the Programmer's Console.
12. Manually load, deposit and examine to insure that memory modules are associated with the correct memory fields. If not, turn the unit off and reconfigure the memory modules to the correct fields.

DEC FORM NO EN-01022-16-N70(JAN) DRA 108
 SIZE CODE A SP NUMBER 8A-1-4 REV 4 OF 6

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE 8A100,205,405,425,625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

V. ACCEPTANCE PROCEDURE

Perform the acceptance tests referred to in table A. If abnormal indications are encountered, refer to the diagnostic listings for error descriptions. Refer to the operators handbook and the diagnostic listings for instructions on loading diagnostics.

Equipment required

1. 8A100,205,405,425,625 with 1-32K of semiconductor memory.
2. Programmer's Console (KC8A and DKC8A)
3. Paper tape input device.
4. Diagnostics and listings.

NOTE: If programmer's panel and paper tape input device are not available as part of the system being installed, they must be provided by the customer in good working order. If semiconductor memory is 1K PROM only, refer to the MR8-F Engineering Spec.

Table A

Acceptance 8A100,205,405,425,625

<u>Program Name</u>	<u>MAINDEC #</u>	<u>Accept Time</u>
PDP8A Central Processor Test	08-DJKKA	20 Minutes
1-32K Random Exerciser	08-DJEXA	20 Minutes
MS8-A or MS8-C/D MOS Memory	Refer to Acceptance Procedure for MS8-C/D and MS8-A.	
KT8-A Memory Management Option	Refer to Acceptance Procedure for KT8-A.	

SIZE A	CODE Sp	NUMBER 8A-1-4	REV
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ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE 8A100,205,405,425,625 FIELD INSTALLATION ACCEPTANCE PROCEDURE

DKC8A Option One Refer to Acceptance Procedure for DKC8A.

KM8A Option Two Refer to Acceptance Procedure for KM8A.

SIZE A	CODE SP	NUMBER -8A-1-4	REV
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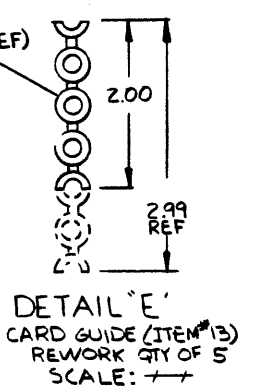
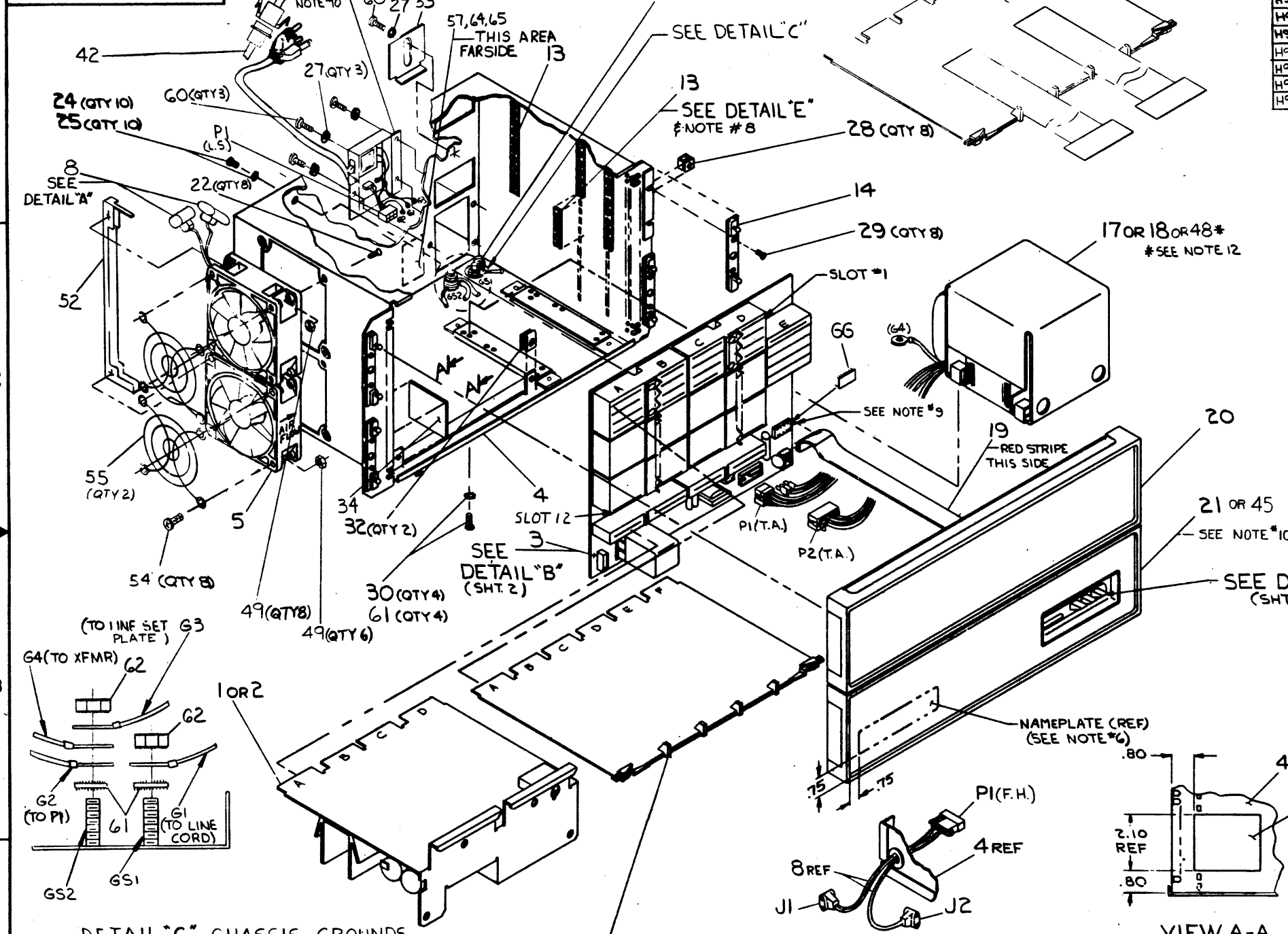
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NUMBER	VARIATION
H9300-AA	115V, 60HZ (MOS)
H9300-AB	230V, 50HZ (MOS)
H9300-BA	115V, 60HZ (CORE)
H9300-BB	230V, 50HZ (CORE)
H9300-AC	230V, 60HZ (MOS)
H9300-AD	115V, 50HZ (MOS)
H9300-BH	230V, 60HZ (CORE)
H9300-BJ	115V, 50HZ (CORE)

LEGEND
~~MOUNTING HARDWARE VARIATIONS FOR ITEM #6 (FAN) ARE AS FOLLOWS:~~
~~FAN VARIATIONS PART TYPE HARDWARE (ITEM #5) VARIATIONS (ITEM #6)~~

VENDOR	PAN	DESCRIPTION	DEC. PAN.
TYE	HO-01	CLIP	5006515
ROTRON	CS2A2	MUT. WEDGE	5006540
FORN	TASO-1	#6-3C	

~~IF ITEMS #6 (RETAINERS) AND #7 (FILTERS) ARE NOT AVAILABLE, USE TWO FAN SCREENS, DEC. PAN. CND. #40851-0-0~~
 NOTES CONT. ON PAGE 3 OF "UA".



DETAIL "C" CHASSIS GROUNDS
SCALE: 1:1 (SECUREMENT OF GRN/YEL WIRES)

HEX BOARD REF
SEE SHEET #3, NOTE #7 FOR PROPER MODULE SLOT ASSIGNMENTS

REV.	DESCRIPTION	DATE	BY	CHKD.
1	REVISED AND REDRAWN	12/76		
2	W. DASH			
3	W. X. KIRCHNER			
4	L. KIZINA			
5	C. NORRIS			
6	D. FRENIERE			

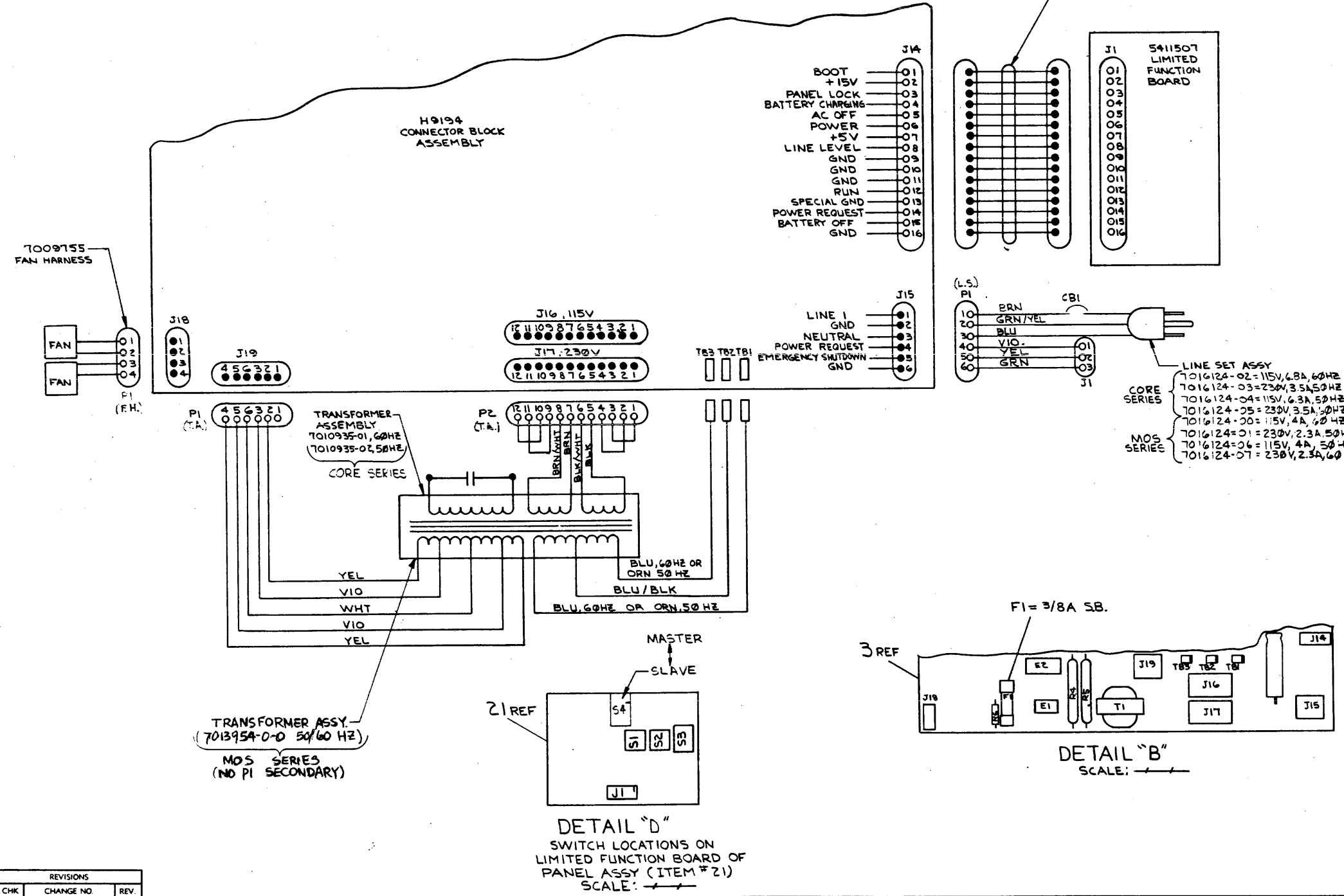
QUANTITY & VARIATION		DRN: [Signature]	FIRST USED ON
CHECKED: [Signature]		ENG: [Signature]	DATE: [Signature]
PROCESS: [Signature]		PROD: [Signature]	DATE: [Signature]
DO NOT SCALE DWG		NEXT HIGHER ASSY.	
MATERIAL SEE PARTS LIST		A-PL-BA400-0-0	SIZE CODE D UA
FINISH		SHEET 1 OF 3	DIST. H9300-0-0
TITLE CHASSIS ASSY, H9300			

DIGITAL H9300-0-0

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ASSEMBLY INSTRUCTIONS

- OPERATIONS TO BE PERFORMED PER HARDWARE STANDARDS SP-7005099-0 AND/OR DEC WORKMANSHIP STANDARDS.
- ATTACH FOAM TAPE (ITEM #34) TO CHASSIS (ITEM #4) AS SHOWN IN VIEW A-A.
- INSTALL FAN HARNESS (ITEM #8) INTO CHASSIS AS SHOWN IN DETAIL 'A'.
- ~~ATTACH FILTER RETAINERS (ITEM #9) TO THE TWO FANS (ITEM #8) WITH #8-32 X .75 FLAT HEAD SCREWS (ITEM #22) AND THE APPROPRIATE MOUNTING HARDWARE (SEE NOTE #3) FOUR PLACES EACH FAN.~~
- PLUG FAN HARNESS CONNECTORS J1 & J2 (SEE DETAIL 'A') ON TO THE FAN TERMINALS.
- ATTACH FANS TO CHASSIS WITH #8-32 X .75 FLAT HEAD SCREWS (ITEM #22) AND THE APPROPRIATE MOUNTING HARDWARE FOUR PLACES EACH FAN.
- REWORK FIVE CARD GUIDES (ITEM #13) AS SHOWN ON DETAIL 'E'.
- INSTALL FULL LENGTH CARD GUIDES (ITEM #13) AND REWORKED CARD GUIDES AS SHOWN (10 PLACES).
- INSTALL THE 1/4 TURN RECEPTACLES (ITEM #32) ON THE TWO TABS ON THE BOTTOM OF THE CHASSIS.
- ATTACH THE H9194 CONNECTOR BLOCK ASSEMBLY (ITEM #3) TO THE REAR OF THE CHASSIS WITH #8-32 X .25 PAN HEAD SCREWS (ITEM #24) AND #8 EXTERNAL TOOTH LOCK WASHERS (ITEM #25) TEN PLACES.
- PLUG P1 OF THE FAN HARNESS (4 PIN CONNECTOR) INTO J18 OF THE H9194 (SEE DETAIL 'B', SHEET 2).
- ATTACH THE LINE SET (ITEM #9) TO THE REAR OF THE CHASSIS WITH THREE #6-32 X .25 LG PAN HEAD SCREWS (ITEM #60) AND THREE #6 EXTERNAL TOOTH LOCK WASHERS (ITEM #27) AS SHOWN. SEE DETAIL 'C' (SHEET 1) FOR PROPER GROUNDING.
- PLUG P1 (6 PIN CONNECTOR) OF THE LINE SET INTO J15 OF THE H9194 (SEE DETAIL 'B').
- PLUG ONE END OF THE 16 CONDUCTOR CABLE (ITEM #19) INTO J14 OF THE H9194 AS SHOWN.
- ~~SET THE TRANSFORMER ASSEMBLY (ITEM #17 OR 18) IN THE CHASSIS AND FASTEN THE GREEN WIRE TO THE CHASSIS WITH ONE #4-40 X .75 SCREW (ITEM #28) TWO #4 EXTERNAL TOOTH LOCK WASHERS (ITEM #25) ONE FLAT WASHER (ITEM #23) AND ONE #4-40 NUT (ITEM #26) AS SHOWN IN DETAIL 'D'.~~
- PLACE THE TRANSFORMER ASSEMBLY IN POSITION (THE 16 CONDUCTOR CABLE SHOULD BE ROUTED UNDERNEATH THE TRANSFORMER) AND ATTACH TO THE CHASSIS WITH FOUR #10-32 X .50 PAN HEAD SCREWS (ITEM #30) AND #10 EXTERNAL TOOTH LOCK WASHERS (ITEM #25) AS SHOWN. SEE DETAIL 'C' (SHEET 1) FOR PROPER GROUNDING.
- PLUG P2 OF THE TRANSFORMER ASSEMBLY (12 PIN CONNECTOR) INTO EITHER J16 (115V) OR J17 (230V) OF THE H9194 (SEE DETAIL 'B').
- CONNECT THE THREE LARGE WIRES ON THE TRANSFORMER ASSEMBLY TO THE TABS TB1, TB2 AND TB3 (SEE DETAIL 'B') ON THE H9194. THE BLU/BLK WIRE IS ALWAYS CONNECTED TO THE CENTER TAB (TB2).
- PLUG P1 OF THE TRANSFORMER ASSEMBLY (6 PIN CONNECTOR) INTO J19 OF THE H9194 (SEE DETAIL 'B').
- PLUG THE 68010 REGULATOR BOARD (ITEM #2) INTO THE H9194 AS SHOWN, AND SECURE IN PLACE WITH THE TWO ATTACHED 1/4 TURN FASTENERS.
- ATTACH THE LATCH MOLDINGS (ITEM #14) TO THE CHASSIS WITH #10-32 X .75 FLAT HEAD SCREWS (ITEM #29) AND SPEED NUTS (ITEM #28).
- PLUG THE OTHER END OF THE 16 CONDUCTOR CABLE INTO J1 OF THE LIMITED FUNCTION PANEL (ITEM #21) SEE DETAIL 'D'.
- ATTACH THE LIMITED FUNCTION PANEL TO THE CHASSIS.
- ATTACH THE BLANK BEZEL ASSEMBLY (ITEM #20) TO THE CHASSIS.
- ~~SLIDE FILTERS (ITEM #7) INTO FILTER RETAINERS~~
- ITEMS 14, 16, 22, 23 & 24 THIS INSTRUCTION SHEET REFER TO NOTE #9 AND #10 FOR CORRECTIONS.



REVISIONS		
CHK	CHANGE NO	REV.

TITLE	CHASSIS ASSY, H9300	SIZE CODE	DUA	NUMBER	H9300-0-0	REV.	J
SCALE	SHEET 2 OF 3		DIST.	mk			

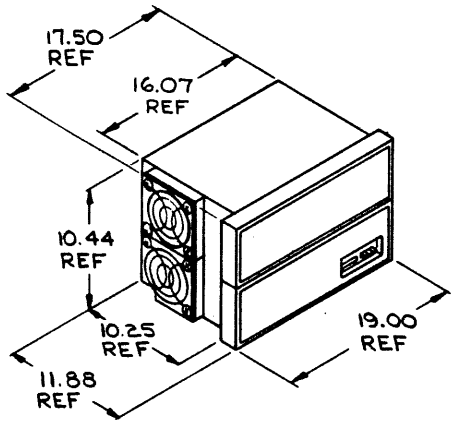
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MODULE ASSIGNMENTS AND POWER REQUIREMENTS (SEE NOTES #7 & 8)

OPTION	DESCRIPTION	BOARD SIZE	NO. SLOTS USED	ASSIGNED SLOT NO.	CURRENT		
					+5V	+15V	-15V
CMB-F	CARD RDR CONT.	QUAD	1	4 - 12	.55A	—	—
CRB-F	CARD RDR CONT.	↑	↑	4 - 12	.55A	—	—
DBB-EA	INTERPROC. BUFFER	↑	↑	2 - 12	.88A	—	.83A
DKB-EC	RTC, CRYSTAL	↑	↑	2 - 12	.34A	—	—
DKB-EP	RTC, PROG.	QUAD	2	2 - 12	1.43A	—	.87A
DKCB-A	OPTION #1	HEX	1	2 - 3	2.8A	.08A	.10A
DPB-EA,-EB	MODEM INTERFACE	QUAD	2	2 - 12	1.88A	.85A	.11A
DRB-EA	DIGITAL I/O	QUAD	1	2 - 12	2.25A	—	—
KAB-E	POSITIVE I/O	QUAD	1	4 - 12	1.48A	—	—
KCB-AA,-AB	PROG. CONSOLE	PNL. MT.	0	N.A.	2.5A	—	—
KDB-E	DATA BREAK	QUAD	1	4 - 12	1.2A	—	—
KGB-EA	REDUNDANCY CHECK	QUAD	1	4 - 12	.94A	—	—
KKB-A	C.P.U.	HEX	1	↑	5.8A	—	.84A
KLB-JA	ASYNC. DATA CONT.	QUAD	1	2 - 12	1.1A	.85A	.18A
KLB-M	MODEM CONTROL	QUAD	1	2 - 12	.48A	.84A	.84A
KMB-A	OPTION #2	HEX	1	2 - 3	2.8A	—	—
KMB-E	MEM. EXT. & T.S. CONT.	QUAD	1	4 - 12	1.8A	—	—
LEB-XX	LINE PRINTER CONT.	QUAD	1	2 - 12	.35A	—	—
LSB-F	LINE PRINTER CONT.	QUAD	1	2 - 12	.48A	—	—
MAB-AA	8K CORE, OPERATING	HEX	2	4 - 8	2.5A	—	—
MAB-AA	8K CORE, STANDBY	HEX	2	4 - 8	2.5A	—	—
MAB-AB	16K CORE, OPERATING	HEX	2	4 - 8	2.5A	—	—
MAB-AB	16K CORE, STANDBY	HEX	2	4 - 8	2.5A	—	—
MAB-AA	1K ROM	QUAD	1	2 - 12	2.8A	—	—
MAB-AB	2K ROM	↑	↑	2 - 12	3.0A	—	—
MAB-AC	3K ROM	↑	↑	2 - 12	4.0A	—	—
MAB-AD	4K ROM	↑	↑	2 - 12	5.8A	—	—
MAB-FB	1K PROM	↑	↑	2 - 12	3.8A	—	.35A
MSB-AA	1K RAM	↑	↑	4 - 12	1.4A	—	—
MSB-AB	2K RAM	↑	↑	4 - 12	2.1A	—	—
MSB-AC	3K RAM	↑	↑	4 - 12	2.8A	—	—
MSB-AD	4K RAM	↑	↑	4 - 12	3.5A	—	—
PCB-E, PRB-E	RDR/PUNCH CONTROL	↑	↑	4 - 12	.84A	—	.85A
RXB-E	RXB1 CONTROL	↑	↑	4 - 8	1.5A	—	—
RKB-EA	RKB5 CONTROL	↑	↑	4 - 12	3.10A	—	—
TAB-AA	TABB CONTROL	↑	↑	2 - 12	2.88A	—	—
TMB-EA,-FA	TUIB CONTROL	↑	↑	4 - 12	4.18A	—	—
VCB-E	DISPLAY CONTROL	↑	↑	2 - 12	.31A	—	—
YTB-E	DISPLAY CONTROL	↑	↑	4 - 12	3.78A	.89A	.13A
YTB-E	PLOTTER CONTROL	QUAD	1	4 - 12	.42A	.81A	.83A
KKB-E	M8300 MAJOR REG.	QUAD	1	12	1.7	—	—
	M8310 MAJOR REG. CONT.	QUAD	1	11	.6	—	—
	M8330 TIMING GEN.	QUAD	1	10	1.2	—	—
	M8320 BUS LOAD	QUAD	1	1	1.0	1.0	.53
MCB-CA	16K MOS RAM	HEX	1	4-8	3.3A	—	.7A
MCB-CP	32K MOS RAM	↑	↑	4-8	3.5A	—	.7A
KTR-A	MEM. MANAGEMENT	↑	↑	4-8	3.8A	—	—
RLE-A	RLOI CONTROL	HEX	1	4-12	2.5A	.2A	.1A

AVAILABLE CURRENT - H9300-AA,AB $\frac{+15V}{2.0A}$ $\frac{-15V}{2.5A}$ 1A SHARED $\frac{-15V}{2A}$

REVISIONS		
CHK	CHANGE NO.	REV



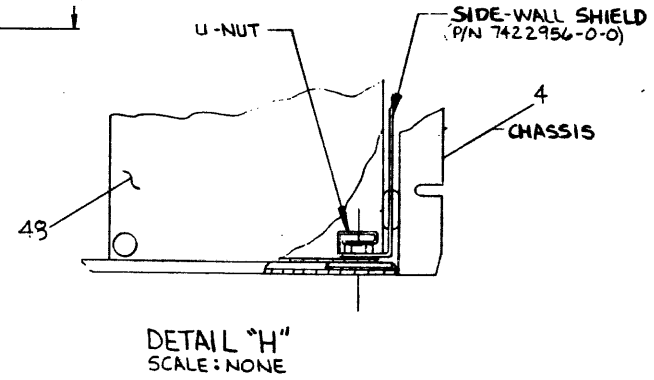
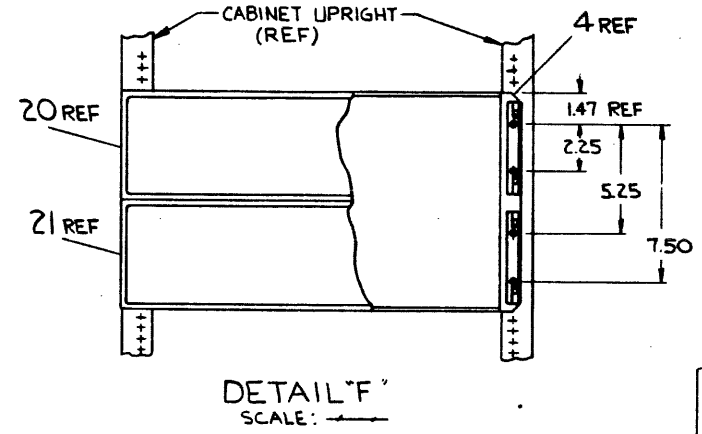
MAX. UNIT WEIGHT = 55 LB.

MOUNTING INSTRUCTIONS

- SEE DETAIL "F" FOR MTG DIM
- THE DIM FROM CENTER LINE OF RIGHT CAB UPRIGHT MOUNTING HOLE TO LEFT CAB UPRIGHT MOUNTING HOLE CENTER LINE IS 18.31.
- REMOVE THE BLANK BEZEL ASSY.
- REMOVE THE LIMITED FUNCTION PANEL AND DISCONNECT THE CABLE FROM THE LIMITED FUNCTION BO.
- REMOVE THE LATCH MOULDING (4 PLACES).
- REMOVE THE SPEED NUT, AND INSTALL ON CABINET POST. 8 PLACES PER MOUNTING DIMENSIONS.
- IT MAY BE NECESSARY TO REMOVE THE FILTER RETAINER AND THE FILTER IN ORDER TO MOUNT THE BOX IN A CABINET.
- WITH THE BOX IN PLACE, IN THE CABINET, REPLACE THE LATCH MOULDING AND SPACERS SO AS TO SECURE THE BOX TO THE CABINET.
- PLUG THE CABLE INTO THE LIMITED FUNCTION BO AND REPLACE LIMITED FUNCTION PANEL.
- REPLACE THE BLANK BEZEL ASSY; REINSTALL THE FILTER RETAINER AND THE FILTER.
- FOR MOUNTING INSTRUCTIONS #4 AND #9, SEE NOTES #9 AND #10.

NOTES:

- TO OBTAIN A 115V 50 HZ POWER VARIATION USE THE H9300-00 REPLACE THE LINE SET (ITEM #12) WITH A 115V 50HZ 0-8A LINE SET (800-0-0-0-0-0-0-0-0-0-0) AND PLUG #3 (12 PIN CONN) OF THE TRANSFORMER ASSEMBLY INTO THE 115V77 OF THE H9300.
- ALL H9300 POWER SUPPLY DC OUTPUTS ARE PROVIDED TO DRIVE LOGIC INTERNAL TO THE BASIC MACHINE ENCLOSURE. DIGITAL WILL NOT BE RESPONSIBLE FOR THE PERFORMANCE OF THE H9300 IF ANY DC POWER IS TAKEN OUTSIDE THE MACHINE.
- ENVIRONMENTAL CONDITIONS FOR H9300 ARE SPECIFIED IN DEC STD 102 CLASS "C" ENVIRONMENT.
- THIS ITEM (NAMEPLATE) IS SHOWN FOR REFERENCE ONLY. IT WILL BE ADDED ON A HIGHER LEVEL ASSEMBLY.
- INSTALL MODULES AS FOLLOWS: PLACEMENT OF HEX MODULES IS FROM SLOT #1(TOP OF BACKPLANE) DOWN ↓ PLACEMENT OF QUAD MODULES IS FROM SLOT #2(BOTTOM OF BACKPLANE) UP ↑
- CARD GUIDES (ITEM #13) ARE PROVIDED FOR SLOTS #1-10. WHEN A QUAD MODULE WITH AN H851 OR H8511 CONNECTOR BLOCK (M8B-A, MSB-A, ETC) ON THE "E" SET OF FINGERS IS INSTALLED, IT IS NECESSARY TO CLIP OFF THE FRONT-LEFT CARD GUIDE IN THOSE SLOTS SO THAT THE CONNECTOR BLOCK MAY BE PROPERLY SEATED (REF DETAIL "E" FOR AN EXAMPLE OF CARD GUIDE REWORK).
- WHEN USED AS AN EXPANDER BOX THE BC80C ITEM 40, OR THE BC80H CABLE ITEM 41 GOES INTO SLOT 1 OF THE H9300. ALSO THE 16 CONDUCTOR CABLE ITEM 19 IS REMOVED IN EXPANDER BOX AND THE REMOTE SLAVE CIRCUIT ITEM 44 IS INSTALLED IN J14 OF THE H9194 CONNECTOR BLOCK ASSEMBLY.
- ITEM 21 IS REPLACED BY ITEM 45 IN EXPANDER BOX AND ITEM 42 AND 43 ARE INCLUDED IN EXPANDER BOX VARIATIONS. ITEM 43 IS TO BE USED WITH ITEM 42 WHEN EXPANDING TO BE. IN ALL OTHER EXPANSION VARIATIONS ITEM 42 IS USED ALONE.
- REMOVE GROUND WIRE (GRN) OR GRN FROM LINE SET ASSY ITEM 510, 512, 46 OR 47 WELD STUD NOTE LEAD MAY ALREADY BE FREE ATTACH THIS GROUND WIRE TO CHASSIS (SEE DETAIL "E") USING ITEMS 27, 49, 50 AND 51 NOTE: HOLE IN RIGHT REAR SIDE MAY HAVE TO BE EXPANDED TO 1/8 IN. FOR #8 SCREW.
- FOR ITEM #46 WITH XFMR 16110 REV-B, A SPECIAL SIDE-WALL MAGNETIC SHIELD SHOULD BE INSTALLED DEC 7422956-0-0 (SEE DETAIL "H"). U-NUTS ARE REMOVED FROM RIGHT SIDE OF ASSY AND PLACED ON SHIELD; SHIELD IS THEN POSITIONED FLUSH ALONG SIDE OF ASSY WITH U-NUTS ENTERING OVER MOUNTING HOLES OF ASSY. (ITEM 49, DATE CODED 79 - -)



TITLE	SIZE CODE	NUMBER	REV.
CHASSIS ASSY, H9300	DUA	H9300-0-0	J
SCALE	SHEET 3 OF 3	DIST.	

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION													
				AA	AB	AC	AD	BA	BB	BC	BD	BE	BF	BH	BJ		
1	1	D-CS-G8016-0-1	G8016-00	REGULATOR FOR H763	1	1	1	1	-	-	-	-	-	-	-	-	-
2	2	D-CS-G8018-0-1	G8018-00	H774 REGULATOR	-	-	-	-	1	1	1	1	1	1	1	1	1
3	3	D-AD-H9194-0-0	H9194-00	BUS CONN 8/A 8+4 SLOTS	1	1	1	1	1	1	1	1	1	1	1	1	1
4	4	E-IA-7016715-0-0	7016715-00	CHASSIS WELDMENT	1	1	1	1	1	1	1	1	1	1	1	1	1
5	5		1209403-01	FAN,115CFM,SLEEVE BRNG	2	2	2	2	2	2	2	2	2	2	2	2	2
6	6	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
7	7	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
8	8	D-IA-7009755-0-0	7009755-00	HARNESS FAN (H763) PDP8A	1	1	1	1	1	1	1	1	1	1	1	1	1
9	9	D-AD-7016124-0-0	7016124-00	C.B. LINE SET ASSY	1	-	-	-	-	-	-	-	-	-	-	-	-
10	10	D-AD-7016124-0-0	7016124-01	LINESET ASSY	-	1	-	-	-	-	-	-	-	-	-	-	-
11	11	D-AD-7016124-0-0	7016124-02	LINESET ASSY	-	-	-	-	1	-	1	-	1	-	-	-	-
12	12	D-AD-7016124-0-0	7016124-03	LINESET ASSY	-	-	-	-	-	1	-	1	-	1	-	-	-
13	13		1211630-00	CARD GUIDE	10	10	10	10	10	10	10	10	10	10	10	10	10
14	14		1209224-00	LATCH, NORYL PLASTIC	4	4	4	4	4	4	4	4	4	4	4	4	4
15	15	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
16	16	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
17	17	D-IA-7010935-0-0	7010935-01	TRANSFORMER ASSY 60HZ	-	-	-	-	1	-	1	-	1	-	1	-	-
18	18	D-IA-7010935-0-0	7010935-02	TRANSFORMER ASSY 50HZ	-	-	-	-	-	1	-	1	-	1	-	1	-
19	19	C-IA-7010871-0-0	7010871-1F	CABLE KEY BOARD 8A	1	1	1	1	1	1	-	-	-	-	1	1	1
20	20	D-AD-7009978-0-0	7009978-00	BEZEL ASSY (H763)PDP8A	1	1	1	1	1	1	1	1	1	1	1	1	1
21	21	D-AD-7010039-0-0	7010039-04	PANEL LIMITED FUNCTION	1	1	1	1	1	1	-	-	-	-	1	1	1
22	22		9006026-02	SCREW,FLAT,PHIL, 6-32X 3/4	8	8	8	8	8	8	8	8	8	8	8	8	8
23	23	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
24	24		9006035-01	SCREW,PAN,PHIL 8-32X 1/4 SS	10	10	10	10	10	10	10	10	10	10	10	10	10
25	25		9008072-00	WASHER, LOCK, EXTERNAL TOOTH #8	10	10	10	10	10	10	10	10	10	10	10	10	10
26	26	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-	-
27	27		9007649-00	WASHER, LOCK, EXTERNAL TOOTH #6	4	4	4	4	4	4	4	4	4	4	4	4	4
28	28		9007786-01	RETAINER, U-NUT, 10-32	8	8	8	8	8	8	8	8	8	8	8	8	8
29	29		9006075-02	SCREW,FLAT,PHIL, 10-32X 3/4	8	8	8	8	8	8	8	8	8	8	8	8	8
30	30		9006037-01	SCREW,PAN,PHIL 8-32X 3/8 SS	4	4	4	4	4	4	4	4	4	4	4	4	4

REVISION HISTORY		BASIC PART NO: H9300		DRN: D.SULLIVAN	DATE: 21-FEB-75	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF B	<i>A. Gardner</i>	DATE: 09-MAY-75	TITLE	PARTS LIST						
DF	H9300-MK007	H	SECTION, VARIATION INDEX	CHK'D: P.GARDNER	DATE: 09-MAY-75	H9300 UNIT ASSEMBLY							
DF	H9300-MK008	J	[A] AA,AB,AC,AD,BA,BB, BC,BD,BE,BF,BH,BJ	DES.ENG.: P.GARDNER	DATE: 09-MAY-75	DOCUMENT NUMBER							
			[B] BK,BL,BM,BN	<i>Gary J Price</i>	DATE: 22-OCT-80	SIZE	CODE	NUMBER	REV				
			[C]	RESP.ENG.: D.FRENIERE									
			[D]	MFG.ENG.: D.DEHOME	DATE: 09-MAY-75	K	PL	H9300-0-0	J				
			[E]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #					
			[F]	ID-UA-H9300-0-0	#B-DD-H9300-0	MK0245.PLS		13					

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MK

PARTS LIST

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
					AA	AB	AC	AD	BA	BB	BC	BD	BE	BF	BH	BJ
31	31	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
32	32		9008196-00	RECP. CLIP ON F/1/4 TURN FASTNR	2	2	2	2	2	2	2	2	2	2	2	2
33	33	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
34	34		9009087-00	FOAM, TAPE, SINGLE SIDED 1/8 THK	1	1	1	1	1	1	1	1	1	1	1	1
35	35	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
36	36	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
37	37	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
38	38	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
39	39	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
40	40	D-UA-BC80C-0-0	BC80C-04	BC80C CABLE	-	-	-	-	-	-	-	-	-	-	-	-
41	41	D-UA-BC08H-0-0	BC08H-1F	CABLE	-	-	-	-	-	-	-	2	2	-	-	-
42	42	C-IA-7008288-0-0	7008288-3F	CABLE ASSY	-	-	-	-	-	-	1	1	1	1	-	-
43	43	C-IA-7013953-0-0	7013953-01	8E POWER CONTROL ADAPTER CABLE	-	-	-	-	-	-	-	-	1	1	-	-
44	44	D-UA-5413011-0-0	5413011-00	H9300 REMOTE SLAVE CIRCUIT	-	-	-	-	-	-	1	1	1	1	-	-
45	45	D-AD-7009978-0-0	7009978-01	BLANK BEZEL ASSY	-	-	-	-	-	-	1	1	1	1	-	-
46	46	D-AD-7016124-0-0	7016124-04	LINESET ASSY	-	-	-	-	-	-	-	-	-	-	-	1
47	47	D-AD-7016124-0-0	7016124-05	LINESET ASSY	-	-	-	-	-	-	-	-	-	-	-	1
48	48	D-IA-7013954-0-0	7013954-00	MOS TRANSFORMER ASSY	1	1	1	1	-	-	-	-	-	-	1	-
49	49		9008185-00	NUT,KEP 6-32X 1/4 AF	14	14	14	14	14	14	14	14	14	14	14	14
50	50	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
51	51	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
52	52	D-IA-7421088-0-0	7421088-00	COVER,FAN HARNESS	1	1	1	1	1	1	1	1	1	1	1	1
53	53	C-MD-7421087-0-0	7421087-00	ENCLOSURE PLATE	1	1	1	1	1	1	1	1	1	1	1	1
54	54		9006025-03	SCREW,TRUS,PHIL, 6-32X 5/8	8	8	8	8	8	8	8	8	8	8	8	8
55	55		1210263-00	GUARD,FINGER 4.125 X 4.125 MTG H	2	2	2	2	2	2	2	2	2	2	2	2
56	56	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
57	57		3613210-00	/REPLACED BY 36-17674-00	1	1	1	1	1	1	1	1	1	1	1	1
58	58	D-AD-7016124-0-0	7016124-06	LINESET ASSY	-	-	-	1	-	-	-	-	-	-	-	-
59	59	D-AD-7016124-0-0	7016124-07	LINESET ASSY	-	-	1	-	-	-	-	-	-	-	-	-
60	60		9006020-01	SCREW,PAN,PHIL 6-32X 1/4 SS	4	4	4	4	4	4	4	4	4	4	4	4
61	61		9007651-00	WASHER, LOCK, EXTERNAL TOOTH #10	6	6	6	6	6	6	6	6	6	6	6	6
62	62		9006565-00	NUT,KEP 10-32X 3/8 AF	2	2	2	2	2	2	2	2	2	2	2	2
63	63		3612680-01	DECAL, GROUND SIGN PER 3S6 *	1	1	1	1	1	1	1	1	1	1	1	1
64	64		3613211-00	DECAL,CLEAR PREPRINTED CSA 1-1/4	1	1	1	1	1	1	1	1	1	1	1	1
65	65	A-DC-7416197-0-0	7416197-02	DECAL-UL LISTED EDP	1	1	1	1	1	1	1	1	1	1	1	1
66	66	C-IA-7013952-0-0	7013952-00	REMOTE INTERLOCK JUMPER ASSY	-	-	-	-	-	-	1	1	1	1	-	-

D	I	G	I	T	A	L	TITLE	SECTION A OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							H9300 UNIT ASSEMBLY		K	PL	H9300-0-0	J

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION				
				BK	BL	BM	BN	
1	1	D-CS-G8016-0-1	G8016-00	*** THIS ITEM IS NOT USED ***	-	-	-	-
2	2	D-CS-G8018-0-1	G8018-00	H774 REGULATOR	1	1	1	1
3	3	D-AD-H9194-0-0	H9194-00	BUS CONN 8/A 8+4 SLOTS	1	1	1	1
4	4	E-IA-7016715-0-0	7016715-00	CHASSIS WELDMENT	1	1	1	1
5	5		1209403-01	FAN,115CFM,SLEEVE BRNG	2	2	2	2
6	6	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
7	7	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
8	8	D-IA-7009755-0-0	7009755-00	HARNESS FAN (H763) PDP8A	1	1	1	1
9	9	D-AD-7016124-0-0	7016124-00	*** THIS ITEM IS NOT USED ***	-	-	-	-
10	10	D-AD-7016124-0-0	7016124-01	*** THIS ITEM IS NOT USED ***	-	-	-	-
11	11	D-AD-7016124-0-0	7016124-02	*** THIS ITEM IS NOT USED ***	-	-	-	-
12	12	D-AD-7016124-0-0	7016124-03	*** THIS ITEM IS NOT USED ***	-	-	-	-
13	13		1211630-00	CARD GUIDE	10	10	10	10
14	14		1209224-00	LATCH, NORYL PLASTIC	4	4	4	4
15	15	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
16	16	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
17	17	D-IA-7010935-0-0	7010935-01	TRANSFORMER ASSY 60HZ	1	-	1	-
18	18	D-IA-7010935-0-0	7010935-02	TRANSFORMER ASSY 50HZ	-	1	-	1
19	19	C-IA-7010871-0-0	7010871-1F	*** THIS ITEM IS NOT USED ***	-	-	-	-
20	20	D-AD-7009978-0-0	7009978-00	BEZEL ASSY (H763)PDP8A	1	1	1	1
21	21	D-AD-7010039-0-0	7010039-04	*** THIS ITEM IS NOT USED ***	-	-	-	-
22	22		9006026-02	SCREW,FLAT,PHIL, 6-32X 3/4	8	8	8	8
23	23	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
24	24		9006035-01	SCREW,PAN,PHIL 8-32X 1/4 SS	10	10	10	10
25	25		9008072-00	WASHER, LOCK, EXTERNAL TOOTH #8	10	10	10	10
26	26	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
27	27		9007649-00	WASHER, LOCK, EXTERNAL TOOTH #6	4	4	4	4
28	28		9007786-01	RETAINER, U-NUT, 10-32	8	8	8	8
29	29		9006075-02	SCREW,FLAT,PHIL, 10-32X 3/4	8	8	8	8
30	30		9006037-01	SCREW,PAN,PHIL 8-32X 3/8 SS	4	4	4	4

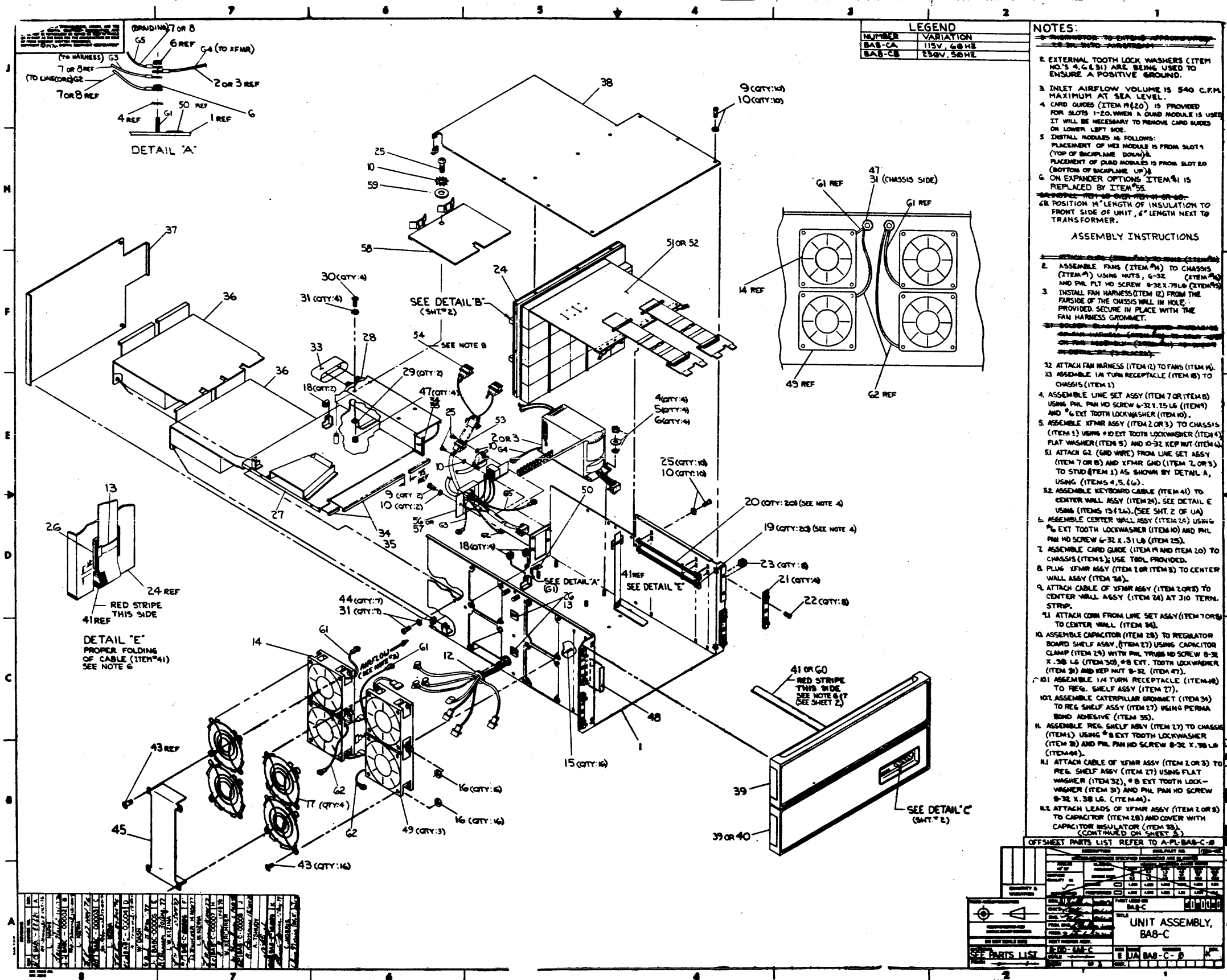
REVISION HISTORY			BASIC PART NO: H9300		DRN: D.SULLIVAN	DATE: 21-FEB-75	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF B	CHK'D: <i>A. Karsich</i>	P.GARDNER	DATE: 09-MAY-75	TITLE	PARTS LIST						
DF	H9300-MK007	H	SECTION, VARIATION INDEX			DATE: 09-MAY-75	H9300 UNIT ASSEMBLY							
DF	H9300-MK008	J	[A] AA,AB,AC,AD,BA,BB, BC,BD,BE,BF,BH,BJ			DATE: 09-MAY-75								
			[B] BK,BL,BM,BN	DES.ENG.: P.GARDNER		DATE: 09-MAY-75								
			[C]	RESP.ENG.: <i>D. P. Prie</i>	D. P. PRIERE	DATE: 22-OCT-80	DOCUMENT NUMBER							
			[D]				SIZE	CODE	NUMBER	REV				
			[E]	MFG.ENG.: D.DEHOME		DATE: 09-MAY-75	K	PL	H9300-0-0	J				
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #					
				D-UA-H9300-0-0		#B-DD-H9300-0	MK0245.PLS		13					

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MK

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
					BK	BL	BM	BN
31	31	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
32	32		9008196-00	RECP. CLIP ON F/1/4 TURN FASTNR	2	2	2	2
33	33	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
34	34		9009087-00	FOAM, TAPE, SINGLE SIDED 1/8 THK	1	1	1	1
35	35	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
36	36	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
37	37	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
38	38	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
39	39	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
40	40	D-UA-BC80C-0-0	BC80C-04	BC80C CABLE	-	-	1	1
41	41	D-UA-BC08H-0-0	BC08H-1F	CABLE	2	2	-	-
42	42	C-IA-7008288-0-0	7008288-3F	CABLE ASSY	1	1	1	1
43	43	C-IA-7013953-0-0	7013953-01	BE POWER CONTROL ADAPTER CABLE	-	-	1	1
44	44	D-UA-5413011-0-0	5413011-00	H9300 REMOTE SLAVE CIRCUIT	1	1	1	1
45	45	D-AD-7009978-0-0	7009978-01	BLANK BEZEL ASSY	1	1	1	1
46	46	D-AD-7016124-0-0	7016124-04	LINESET ASSY	-	1	-	1
47	47	D-AD-7016124-0-0	7016124-05	LINESET ASSY	1	-	1	-
48	48	D-IA-7013954-0-0	7013954-00	*** THIS ITEM IS NOT USED ***	-	-	-	-
49	49		9008185-00	NUT,KEP 6-32X 1/4 AF	14	14	14	14
50	50	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
51	51	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
52	52	D-IA-7421088-0-0	7421088-00	COVER,FAN HARNESS	1	1	1	1
53	53	C-MD-7421087-0-0	7421087-00	ENCLOSURE PLATE	1	1	1	1
54	54		9006025-03	SCREW,TRUS,PHIL, 6-32X 5/8	8	8	8	8
55	55		1210263-00	GUARD,FINGER 4.125 X 4.125 MTG H	2	2	2	2
56	56	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-
57	57		3613210-00	/REPLACED BY 36-17674-00	1	1	1	1
58	58	D-AD-7016124-0-0	7016124-06	*** THIS ITEM IS NOT USED ***	-	-	-	-
59	59	D-AD-7016124-0-0	7016124-07	*** THIS ITEM IS NOT USED ***	-	-	-	-
60	60		9006020-01	SCREW,PAN,PHIL 6-32X 1/4 SS	4	4	4	4
61	61		9007651-00	WASHER, LOCK, EXTERNAL TOOTH #10	6	6	6	6
62	62		9006565-00	NUT,KEP 10-32X 3/8 AF	2	2	2	2
63	63		3612680-01	DECAL, GROUND SIGN PER 3S6 *	1	1	1	1
64	64		3613211-00	DECAL,CLEAR PREPRINTED CSA 1-1/4	1	1	1	1
65	65	A-DC-7416197-0-0	7416197-02	DECAL-UL LISTED EDP	1	1	1	1
66	66	C-IA-7013952-0-0	7013952-00	REMOTE INTERLOCK JUMPER ASSY	1	1	1	1

DIGITAL										TITLE										SECTION B OF B										SIZE										CODE										DOCUMENT NUMBER										REV									
										H9300 UNIT ASSEMBLY																				K										PL										H9300-0-0										J									



LEGEND	
NUMBER	VARIATION
BAB-CA	115V, 60 HZ
BAB-CB	230V, 50 HZ

NOTES:

- EXTERNAL TOOTH LOCK WASHERS (ITEM NO. 3, 4, 6, 8, 9) ARE BEING USED TO ENSURE A POSITIVE GROUND.
- INLET AIRFLOW VOLUME IS 540 C.F.M. MAXIMUM AT SEA LEVEL.
- CARD GUIDES (ITEM 19, 20) IS PROVIDED FOR SLOTS 1-20. WHEN A QUAD MODULE IS USED IT WILL BE NECESSARY TO REMOVE CARD GUIDES ON LOWER LEFT SIDE.
- INSTALL MODULES AS FOLLOWS: PLACEMENT OF HEB MODULES IS FROM SLOT 1 (TOP OF BACKPLANE DOWN); PLACEMENT OF QUAD MODULES IS FROM SLOT 10 (BOTTOM OF BACKPLANE UP).
- ON EXPANDER OPTIONS ITEM 41 IS REPLACED BY ITEM 55.
- REPLACE ITEM 41 BY ITEM 55 OR 60.
- POSITION 1/4" LENGTH OF INSULATION TO FRONT SIDE OF UNIT, 6" LENGTH NEXT TO TRANSFORMER.

ASSEMBLY INSTRUCTIONS

- ASSEMBLE FANS (ITEM 43) TO CHASSIS (ITEM 38) USING NUTS, 6-32 (ITEM 4) AND PHL. FLT. HD. SCREW 8-32 X .75 L6 (ITEM 10) AND 1/4" EXT. TOOTH LOCKWASHER (ITEM 6).
- INSTALL FAN HARNESS (ITEM 7) FROM THE REAR SIDE OF THE CHASSIS WALL IN HOLE PROVIDED. SECURE IN PLACE WITH THE FAN HARNESS GROMMET.
- ATTACH FAN HARNESS (ITEM 7) TO FANS (ITEM 43).
- ASSEMBLE 1/4" TURN RECEPTACLE (ITEM 18) TO CHASSIS (ITEM 38).
- ASSEMBLE LINE SET ASSY (ITEM 7 OR ITEM 8) USING PHL. PAN. HD. SCREW 6-32 X .75 L6 (ITEM 4) AND 1/4" EXT. TOOTH LOCKWASHER (ITEM 6).
- ASSEMBLE XFMR ASSY (ITEM 2 OR 3) TO CHASSIS (ITEM 38) USING 1/4" EXT. TOOTH LOCKWASHER (ITEM 6) AND PHL. PAN. HD. SCREW 6-32 X .75 L6 (ITEM 4).
- ATTACH G2 (GRD WIRE) FROM LINE SET ASSY (ITEM 7 OR 8) AND XFMR. GND (ITEM 2 OR 3) TO STUD (ITEM 1) AS SHOWN BY DETAIL A, USING (ITEMS 4, 5, 6).
- ASSEMBLE KEYBOARD CABLE (ITEM 41) TO CENTER WALL ASSY (ITEM 24). SEE DETAIL E USING (ITEMS 13 & 14). (SEE SHT. 2 OF UJA)
- ASSEMBLE CENTER WALL ASSY (ITEM 24) USING 1/4" EXT. TOOTH LOCKWASHER (ITEM 6) AND PHL. PAN. HD. SCREW 6-32 X .75 L6 (ITEM 4).
- ASSEMBLE CARD GUIDE (ITEM 19 AND ITEM 20) TO CHASSIS (ITEM 38); USE TOOL PROVIDED.
- PLUG XFMR ASSY (ITEM 2 OR ITEM 3) TO CENTER WALL ASSY (ITEM 24).
- ATTACH CABLE OF XFMR ASSY (ITEM 2 OR 3) TO CENTER WALL ASSY (ITEM 24) AT J10 TERM. STRIP.
- ATTACH CORD FROM LINE SET ASSY (ITEM 7 OR 8) TO CENTER WALL (ITEM 24).
- ASSEMBLE CAPACITOR (ITEM 28) TO REGULATOR BOARD SHELF ASSY (ITEM 27) USING CAPACITOR CLAMP (ITEM 29) WITH PHL. TRUSS HD. SCREW 8-32 X .75 L6 (ITEM 10), 1/4" EXT. TOOTH LOCKWASHER (ITEM 6) AND REP. NUT 8-32 (ITEM 47).
- ASSEMBLE 1/4" TURN RECEPTACLE (ITEM 18) TO REG. SHELF ASSY (ITEM 27).
- ASSEMBLE CATERPILLAR GROMMET (ITEM 34) TO REG. SHELF ASSY (ITEM 27) USING PERMA BOND ADHESIVE (ITEM 35).
- ASSEMBLE REG. SHELF ASSY (ITEM 27) TO CHASSIS (ITEM 38) USING 1/4" EXT. TOOTH LOCKWASHER (ITEM 6) AND PHL. PAN. HD. SCREW 8-32 X .75 L6 (ITEM 4).
- ATTACH CABLE OF XFMR ASSY (ITEM 2 OR 3) TO REG. SHELF ASSY (ITEM 27) USING FLAT WASHER (ITEM 32), 1/4" EXT. TOOTH LOCKWASHER (ITEM 6) AND PHL. PAN. HD. SCREW 8-32 X .75 L6 (ITEM 4).
- ATTACH LEADS OF XFMR ASSY (ITEM 2 OR 3) TO CAPACITOR (ITEM 28) AND COVER WITH CAPACITOR INSULATOR (ITEM 33).

ITEM NO.	DESCRIPTION	QTY.	UNIT
1	STUD	1	
2	XFMR ASSY	1	
3	EXT. TOOTH LOCKWASHER	1	
4	NUT	1	
5	WASHER	1	
6	EXT. TOOTH LOCKWASHER	1	
7	FAN HARNESS	1	
8	LINE SET ASSY	1	
9	STUD	1	
10	PHL. PAN. HD. SCREW	1	
11	PHL. PAN. HD. SCREW	1	
12	WIRE	1	
13	WASHER	1	
14	WASHER	1	
15	PHL. PAN. HD. SCREW	1	
16	PHL. PAN. HD. SCREW	1	
17	PHL. PAN. HD. SCREW	1	
18	1/4" TURN RECEPTACLE	1	
19	CARD GUIDE	1	
20	CARD GUIDE	1	
21	PHL. PAN. HD. SCREW	1	
22	PHL. PAN. HD. SCREW	1	
23	PHL. PAN. HD. SCREW	1	
24	CENTER WALL ASSY	1	
25	PHL. PAN. HD. SCREW	1	
26	PHL. PAN. HD. SCREW	1	
27	REG. SHELF ASSY	1	
28	CAPACITOR	1	
29	CAPACITOR CLAMP	1	
30	PHL. TRUSS HD. SCREW	1	
31	PHL. PAN. HD. SCREW	1	
32	FLAT WASHER	1	
33	CAPACITOR INSULATOR	1	
34	CATERPILLAR GROMMET	1	
35	PERMA BOND ADHESIVE	1	
36	PHL. PAN. HD. SCREW	1	
37	PHL. PAN. HD. SCREW	1	
38	CHASSIS	1	
39	PHL. PAN. HD. SCREW	1	
40	PHL. PAN. HD. SCREW	1	
41	KEYBOARD CABLE	1	
42	PHL. PAN. HD. SCREW	1	
43	FAN	1	
44	PHL. PAN. HD. SCREW	1	
45	PHL. PAN. HD. SCREW	1	
46	PHL. PAN. HD. SCREW	1	
47	REP. NUT	1	
48	PHL. PAN. HD. SCREW	1	
49	PHL. PAN. HD. SCREW	1	
50	PHL. PAN. HD. SCREW	1	
51	PHL. PAN. HD. SCREW	1	
52	PHL. PAN. HD. SCREW	1	
53	PHL. PAN. HD. SCREW	1	
54	PHL. PAN. HD. SCREW	1	
55	PHL. PAN. HD. SCREW	1	
56	PHL. PAN. HD. SCREW	1	
57	PHL. PAN. HD. SCREW	1	
58	PHL. PAN. HD. SCREW	1	
59	PHL. PAN. HD. SCREW	1	

OFFSHEET PARTS LIST REFER TO A-PL-BAB-C-B

ITEM NO.	DESCRIPTION	QTY.	UNIT
60	PHL. PAN. HD. SCREW	1	
61	PHL. PAN. HD. SCREW	1	
62	PHL. PAN. HD. SCREW	1	
63	PHL. PAN. HD. SCREW	1	
64	PHL. PAN. HD. SCREW	1	
65	PHL. PAN. HD. SCREW	1	
66	PHL. PAN. HD. SCREW	1	
67	PHL. PAN. HD. SCREW	1	
68	PHL. PAN. HD. SCREW	1	
69	PHL. PAN. HD. SCREW	1	
70	PHL. PAN. HD. SCREW	1	

UNIT ASSEMBLY, BAB-C

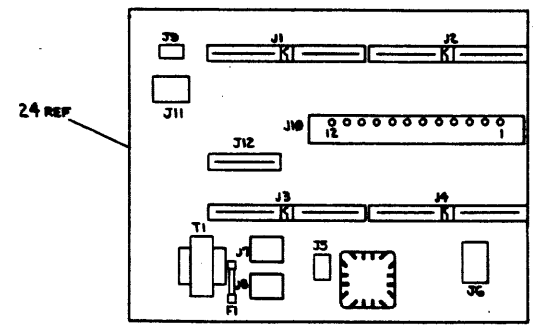
MODULE ASSIGNMENT AND POWER REQUIREMENTS

OPTION	DESCRIPTION	BOARD SIZE	NO. SLOTS USED	ASSIGNED SLOT NO.	CURRENT +5V	+15V	-15V
CMB-F	CARD RDR CONT	QUAD	1	4-20	50A	---	---
CMB-F	CARD RDR CONT	QUAD	1	4-20	50A	---	---
DBB-EA	INTERPROC BUFFER	---	1	2-20	80A	---	03A
DKB-EC	RTC CRYSTAL	---	1	2-20	34A	---	---
DWB-EP	RTC PROG	QUAD	2	2-20	143A	---	07A
DKCB-A	OPTION ¹	HEX	1	2-3	20A	06A	1A
DPB-EA-EB	MODEM INTERFACE	QUAD	2	2-20	180A	05A	11A
DWB-EA	DHBITAL I/O	QUAD	1	2-20	225A	---	---
KAB-E	POSITIVE I/O	QUAD	1	4-20	140A	---	---
KCB-AA-AB	PROG. CONSOLE	PHL. MT.	0	N.A.	25A	---	---
KDB-E	DATA BREAK	QUAD	1	4-20	12A	---	---
KGB-EA	REDUNDANCY CHECK	QUAD	1	4-20	94A	---	---
KKB-A	C.P.U.	HEX	1	1	50A	---	04A
KLB-BA	ASYNC DATA CONT.	QUAD	1	2-20	11A	03A	10A
KLB-M	MODEM CONTROL	QUAD	1	2-20	40A	04A	04A
KMB-AA OR-AB	OPTION ²	HEX	1	2-3	20A	---	---
KMB-E	MEM. EXT. # T. S. CONT.	QUAD	1	4-20	10A	---	---
LEB-XX	LPDS CONTROL	QUAD	1	2-20	35A	---	---
LSB-F	LSM CONTROL	QUAD	1	2-20	40A	---	---
MMB-AA	8K CORE, OPERATING	HEX	2	4-11	25A	---	---
MMB-AA	8K CORE, STANDBY	HEX	2	4-11	25A	---	---
MMB-AB	16K CORE, OPERATING	HEX	2	4-11	25A	---	---
MMB-AB	16K CORE, STANDBY	HEX	2	4-11	25A	---	---
MRB-AA	1K ROM	QUAD	1	2-20	20A	---	---
MRB-AB	2K ROM	---	1	2-20	30A	---	---
MRB-AC	3K ROM	---	1	2-20	40A	---	---
MRB-AD	4K ROM	---	1	2-20	50A	---	---
MRB-AB	1K PROM	---	1	2-20	3.8A	---	35A
MSB-AA	1K RAM	---	1	4-20	1.4A	---	---
MSB-AB	2K RAM	---	1	4-20	2.1A	---	---
MSB-AC	3K RAM	---	1	4-20	2.8A	---	---
MSB-AD	4K RAM	---	1	4-20	3.5A	---	---
PCB-E, PRB-E	PCD4 CONTROL	---	1	4-20	84A	---	82A
RXB-E	RXB CONTROL	---	1	4-20	15A	---	---
RKB-EA	RKB CONTROL	---	3	4-20	3.8A	---	---
TAB-AA	TUGS CONTROL	---	1	2-20	288A	---	---
TAB-EA, FA	TUIB CONTROL	---	4	4-20	418A	---	---
VCB-E	DISPLAY CONTROL	---	2	2-20	31A	---	---
VTB-E	DISPLAY CONTROL	---	3	4-20	378A	89A	13A
XVB-E	PLOTTER CONTROL	---	1	4-20	42A	91A	03A
KKB-E	M830B, MAJOR REG.	---	1	#18	17A	---	---
	M830B, MAJOR REG. CONT.	---	1	#19	60A	---	---
	M830B, TIMING GEN.	---	1	#20	12A	---	---
	M832B, BUS LOAD	---	1	10A	10A	53A	---
ADB-A	A/D CONV.	QUAD	1	4-20	325A	---	---
FPPB-A	FLOATING POINT	HEX	2	4-20	88A	---	---
KEB-E	M8340, EAE IR	---	1	#19	1.6A	---	---
	M8341, EAE REG.	---	1	#18	---	---	---
KLB-A	MSLU	HEX	1	4-20	25A	89A	425A
LAB-P	LAIRD CONT.	QUAD	1	4-20	10A	---	---
MIB-E	BOOT LOADER	QUAD	1	4-20	75A	---	05A
RKB-L	RKBS CONT.	QUAD	2	4-20	35A	---	---
TDB-E	TUS4 CONT.	QUAD	1	4-20	13A	---	---
VKB-A	VIDED DISPLAY CONT.	HEX	1	4-20	28A	---	---

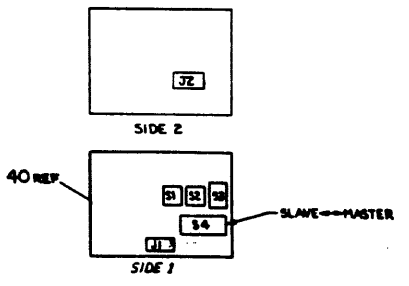
* WITH KEB-E OPTION, M830B & M831B MUST BE MOVED TO SLOTS 16 & 17 RESPECTIVELY. THE KEB-E OPTION PLUGS INTO SLOTS 18 & 19 (M8341 & M8340)

AVAILABLE CURRENT:

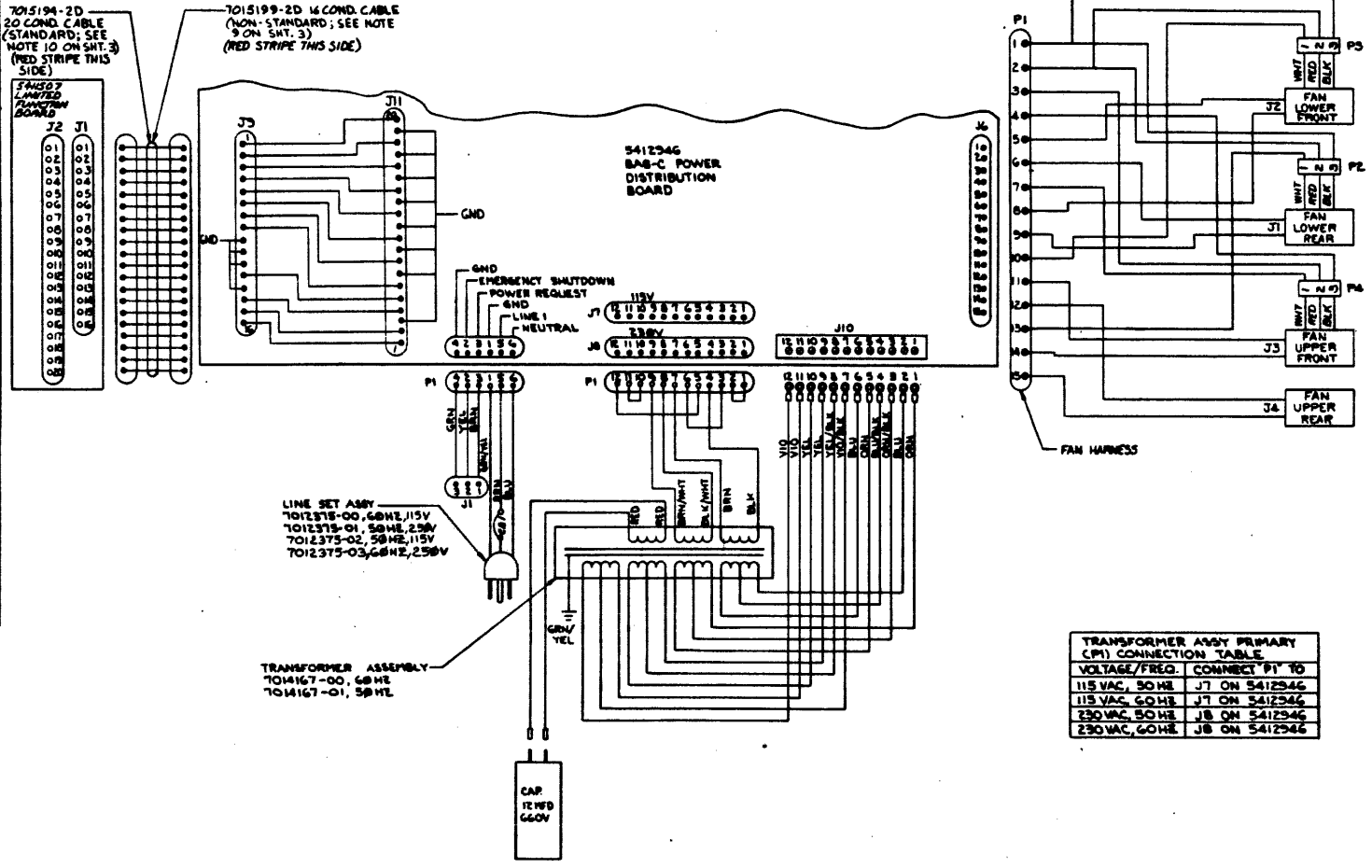
SLOTS #1 THRU 10	+5V	-15V	+15V
	25A (MAX)	2A (MAX)	2A (MAX)
SLOTS #11 THRU 20	+5V	-15V	+15V
	25A (MAX)	2A (MAX)	2A (MAX)



DETAIL 'B'
JACK LOCATIONS ON POWER DISTRIBUTION BOARD OF CENTER WALL ASSEMBLY (ITEM#24)



DETAIL 'C'
SWITCH LOCATIONS ON LIMITED FUNCTION BOARD OF PANEL ASSY (ITEM#40)



TRANSFORMER ASSY PRIMARY (P1) CONNECTION TABLE

VOLTAGE/FREQ.	CONNECT P1 TO
115 VAC, 60HZ	J7 ON 5412346
115 VAC, 60HZ	J7 ON 5412346
230 VAC, 60HZ	J8 ON 5412346
230 VAC, 60HZ	J8 ON 5412346

REVISIONS
 DATE CHANGE BY
 1
 2
 3

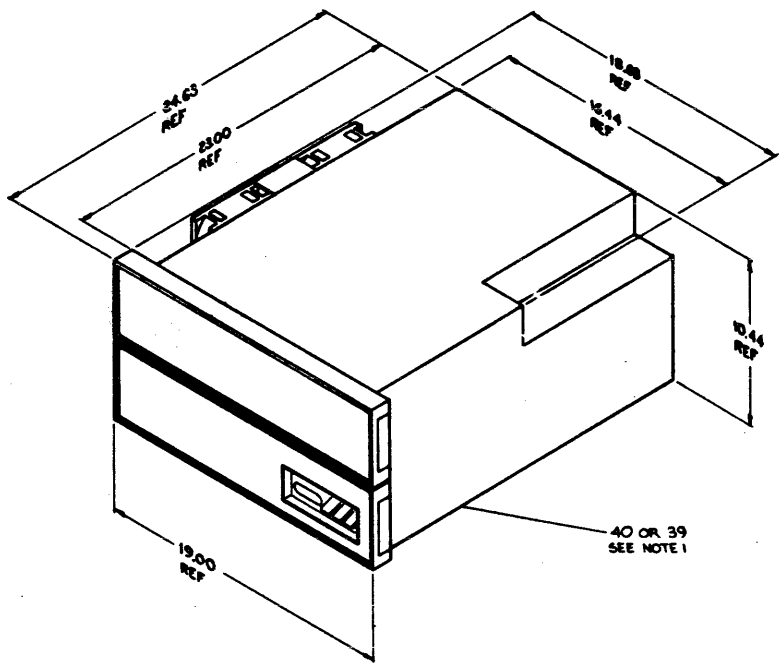
BAB-C TORQUE SPECIFICATIONS

ITEM	TORQUE ± 2 IN/LBS
16	12
6	25
9	13
25	13
30	15
2 OR 3 (TERMS)	13
44	15
22	12
43	12
15	12

- 11.5 INSERT GROUND (ITEM 54) TO CENTER WALL ASSY (ITEM 38) USING PHL PAN HD SCREW G-32 X .75 (ITEM 25) AND INT. TOOTH LOCKWASHER (ITEM 10) AND FLAT WASHR (ITEM 52).
12. INSERT LOWER GBOB (ITEM 34) TO CHASSIS (ITEM 1) AND CENTER WALL ASSY (ITEM 24).
- 12.1 INSERT UPPER GBOB (ITEM 34) TO REG SHELF ASSY (ITEM 27) AND CENTER WALL ASSY (ITEM 24).
13. ADD 1/4 TURN RECEIPTACLE (ITEM 46) TO TOP COVER (ITEM 38).
14. ASSEMBLE TOP COVER (ITEM 38) TO CHASSIS (ITEM 1) USING #6 EXT TOOTH LOCKWASHER (ITEM 10) AND PHL PAN HD SCREW G-32 X .75 LG (ITEM 9).
15. ASSEMBLE REAR COVER (ITEM 37) TO CHASSIS (ITEM 1).
16. ASSEMBLE LATCH MOLDING (ITEM 21) TO CHASSIS (ITEM 1) USING 10-32 SPEED NUT (ITEM 23) AND PHL FLAT HD SCREW 10-32 X .75 LG (ITEM 22).
17. ATTACH KEYBOARD CABLE (ITEM 41) TO LIMITED FUNCTION PANEL (ITEM 40). SEE NOTE 7.
18. ATTACH 5/16 BLANK BEZEL ASSY (ITEM 39) TO CHASSIS (ITEM 1). SEE NOTE 7.
19. ATTACH GND STRAP (ITEMS 54 + 56) TO FANS (ITEMS 14 + 45) WITH SCREW PROVIDED, THEN ATTACH GND STRAP TO CHASSIS STUDS (ITEM 1) WITH #8 REP-NUT (ITEM 47) & EXT TOOTH LOCKWASHER (ITEM 31).

- NOTES**
7. IN EXPANSION OPTIONS KEYBOARD CABLE IS REPLACED BY REMOTE INTERLOCK JUMPER (ITEM 55). THIS JUMPER IS INSERTED INTO J9 ON THE 5412946 POWER DISTRIBUTION BOARD. ALSO EXPANDER BOXES DO NOT INCLUDE LIMITED FUNCTION PANELS (ITEM 40). IN THIS CASE THERE WILL BE TWO 5/16 BLANK BEZELS. THESE ARE TO BE MOUNTED ONE ON TOP OF THE OTHER ON THE CHASSIS FRONT.
 8. ALL EXPANDER BOX VARIATIONS WILL CONTAIN ITEMS 53 AND 54. USE ITEMS 53 AND 54 WHEN EXPANDING TO BE BOX. IN ALL OTHER EXPANSION VARIATIONS USE ONLY ITEM 53 AS POWER CONTROL CABLE.
 9. THIS CABLE TO BE USED WITH LIMITED FUNCTION BOARD ETCH REV B OR EARLIER. (CS REV D OR EARLIER.)
 10. THIS CABLE TO BE USED WITH LIMITED FUNCTION BOARD ETCH REV C AND LATER. (CS REV E OR LATER.)

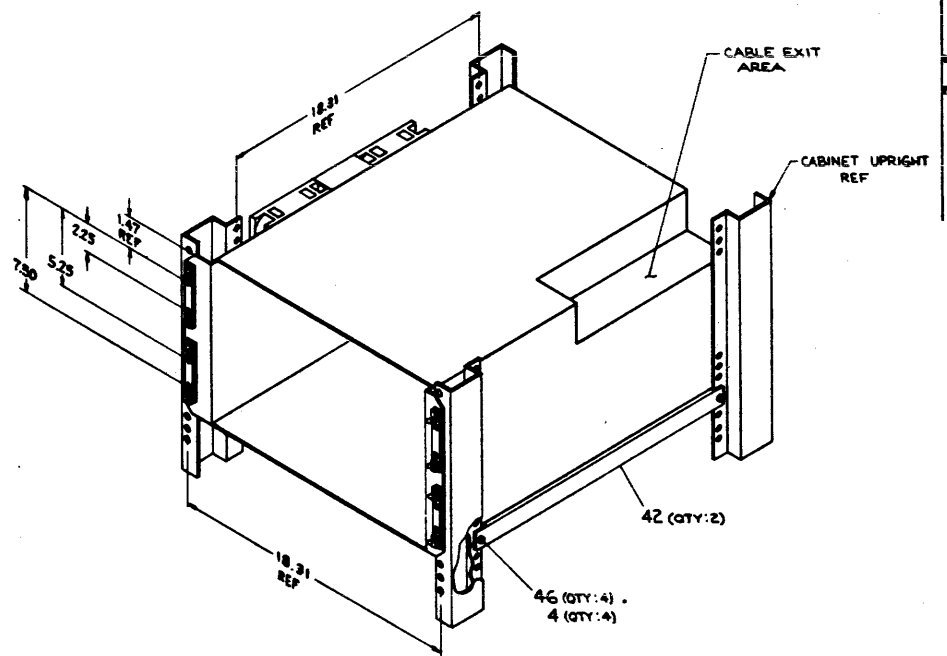
- ASSEMBLY INSTRUCTIONS**
- 10A. ATTACH WIRES TO CABLE TIE MOUNT (ITEM 26) WITH CABLE TIE (ITEM 15).
 - 10B. ASSEMBLE HARNESS COVER (ITEM 45) AND FINGER GUARD (ITEM 17) TO FANS (ITEM 14) USING KEPNUT G-32 (ITEM 16) AND PHL. PAN HD. SCREW G-32 X .75 LG. (ITEM 43).



MAX. UNIT WEIGHT = 117 LBS.

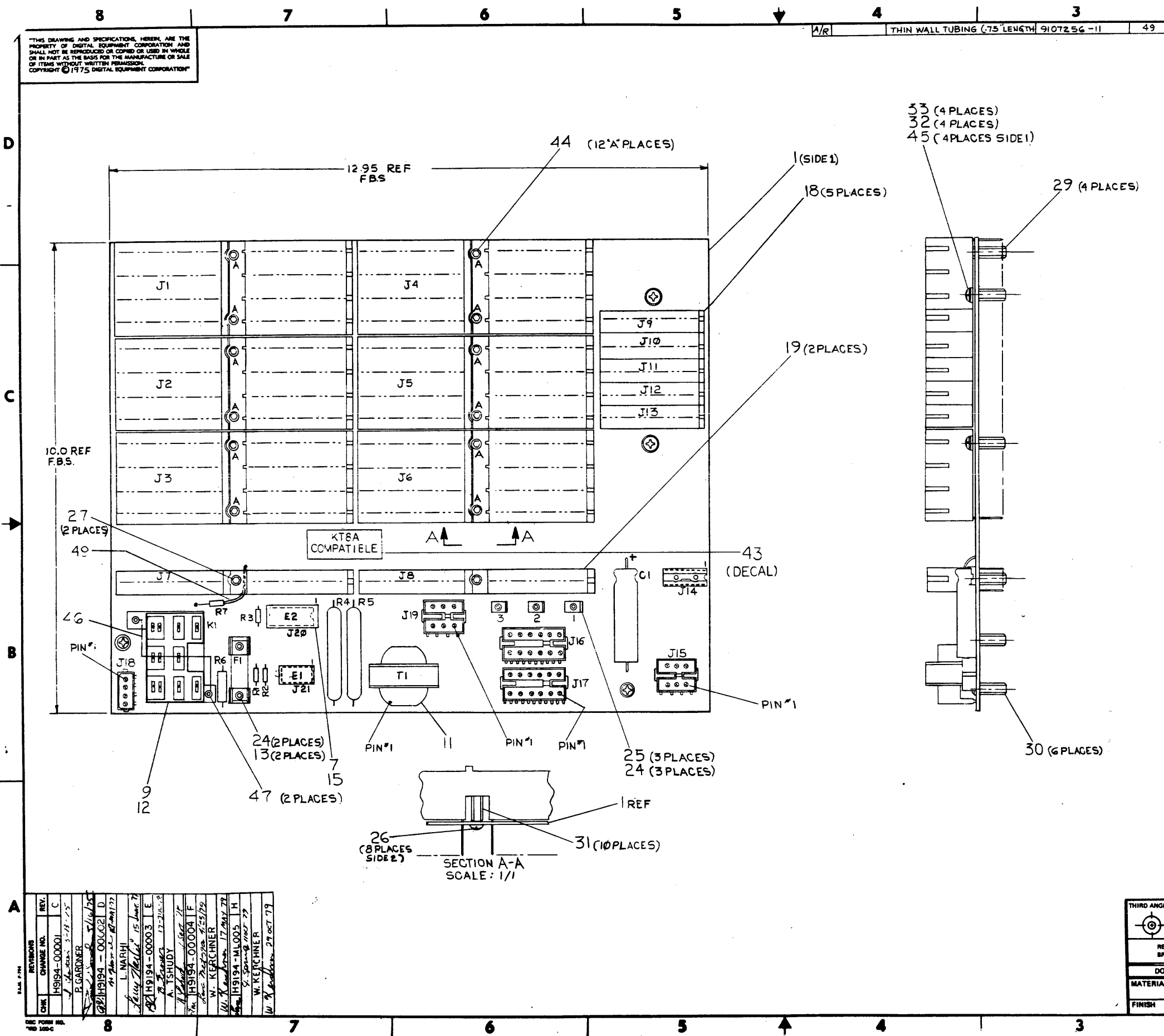
MOUNTING INSTRUCTIONS

1. SEE DETAIL 'D' FOR MOUNTING DIMENSIONS.
2. REMOVE THE BLANK BEZEL ASSEMBLY OR PROGRAMMER'S PANEL.
3. REMOVE THE LIMITED FUNCTION PANEL AND DISCONNECT THE CABLE FROM THE LIMITED FUNCTION BOARD.
4. REMOVE THE LATCH MOLDING (4 PLCS).
5. REMOVE THE SPEED NUT, AND INSTALL ON CAB UPRIGHT. EIGHT PLACES PER MOUNTING DIMENSIONS.
6. IT MAY BE NECESSARY TO REMOVE THE FINGER GUARDS (4) AND HARNESS COVER IN ORDER TO MOUNT BOX IN CAB.
7. ATTACH MOUNTING RAILS USING SCREWS AND LOCK WASHERS TO LEFT AND RIGHT SIDE OF CABINET AS PER DETAIL 'D'.
8. WITH THE BOX IN PLACE, IN THE CABINET, REPLACE THE LATCH MOLDING, SO AS TO SECURE THE BOX TO THE CABINET.
9. PLUG THE CABLE INTO THE LIMITED FUNCTION PANEL AND REPLACE THE PANEL.
10. REPLACE BLANK BEZEL OR PROGRAMMER'S PANEL.
11. RE-INSTALL FINGER GUARDS AND HARNESS COVER.



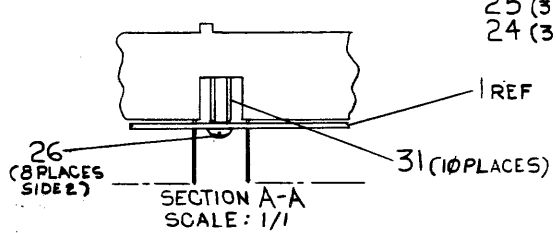
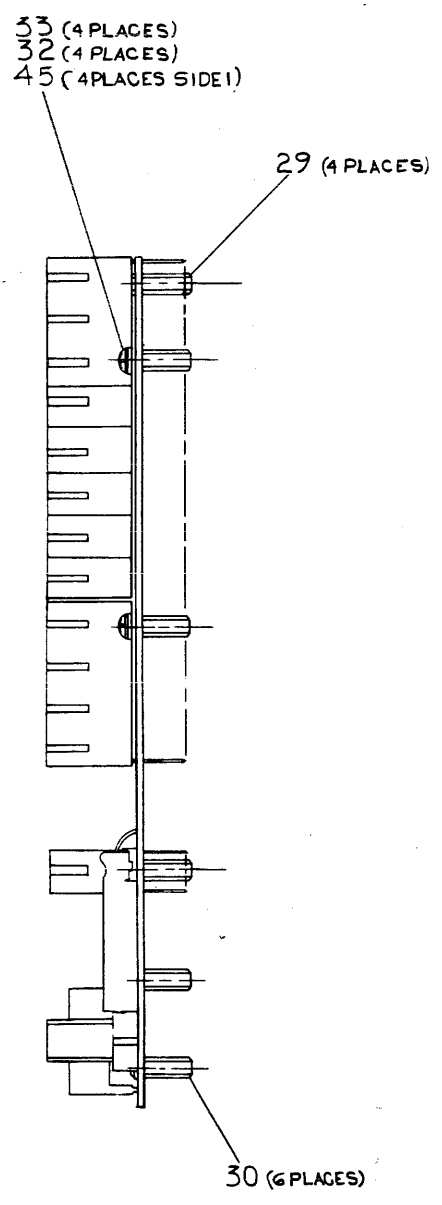
DETAIL 'D'
MOUNTING DIMENSIONS

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A/R THIN WALL TUBING (.75 LENGTH 9107256-11 49

QTY	REF	DESCRIPTION	DWG./PART NO.	ITEM NO.
1	R7	RES 1K 1/4W 5%	1300365-00	46
2		EYELET	9000024-07	47
1		SPRING RELAY HOLDDOWN	1216422-00	46
4		SCR. PHL PAN HD 8-32 x 5/16	9006036-01	45
12		SCR. SOCKET HD 8-32 x 3/8	9006339-08	44
1		DECAL "KT8A COMPATIBLE"	3615653-00	43
A/R		WIRE 30AWG SOLID GRN	9105740-55	42
A/R		WIRE 18 AWG STRANDED GRN	9107360-55	41
A/R		WIRE 22 AWG STRANDED GRN	9107350-55	40
REF		"OMNIBUS" SPEC	A-SP-OMNIB-US	39
REF		MODULE I/O HISTORY	B-MH-H9194-0-6	38
REF		ASSY/DRILLING HOLE LAYOUT	D-AH-H9194-0-5	37
REF		X-Y COORDINATE HOLE LOCATION	K-CO-H9194-0-4	36
REF		CIRCUIT SCHEMATIC	D-CS-H9194-0-1	35
A/R		WIRE 24AWG GREEN	9107683-55	34
4		WASHER, FLAT #8	9006660	33
4		WASHER, INTL TOOTH #8	9006634	32
10		SPACER, 8-32 x .25 AF x .56	9009602	31
6		SPACER, 8-32 x .25 AF x .62	9009629	30
4		SPACER, 8-32 x .25 AF x 1.25	9009603	29
12		SCR. SLEEPS 8-32 x .0	9009070	28
2		SCR. SOC. HD 8-32 x 1.25	9008471-08	27
3		SCR. PHL PAN HD 8-32 x .25	9006035-01	26
3		TERMINAL, SINGLE MALE TAB	9008219	25
5		EYELET	9009000	24
2		EYELET	9006746	23
1	J18	CONN, PC . . . 4 PIN	1211342-04	22
2	J15 J19	CONN, PC . . . 6 PIN	1211342-06	21
2	J16 J17	CONN, PC . . . 12 PIN	1211342-12	20
2	J7, J8	CONN BLK, 72 PIN SLTD	1211425-02	19
5	J9 -> J13	CONN BLK, 36 PIN SLTD	1211029	18
6	J1 -> J6	CONN BLK, 288 PIN SLTD	1210258-01	17
1	J21	SOCKET, IC, 14 PIN	1211813-01	16
2	J14, J20	SOCKET, IC, 16 PIN	1211813-02	15
6		CARD GUIDE, CENTER	1210698	14
2		CLIP, FUSE	9007203	13
1		SOCKET, RELAY	1210684	12
1	T1	TRANSFORMER	1611646	11
1	F1	FUSE, 3/8 A, S.B	9007207	10
1	K1	RELAY, 3 POLE, 6V, 10 AMP	1210683-01	9
1	E1	QUAD CORE DRIVER 40II	1511102	8
1	E2	DIODE ARRAY	C-1A-7010866-0-0	7
1	R6	RES, 10 Ω, 2W, 1%	1300172	6
2	R4, R5	RES, 20 Ω, 10W, 1%	1305416	5
3	R1, R2, R3	RES, 2.2K 1/4W, 5%	1300417	4
1	C2	CAP, .02 μF, 100V, DUAL DISC	1010767	3
1	C1	CAP 930 μF 30V	1010509-00	2
1		ETCHED CIRCUIT BOARD	5011505	1

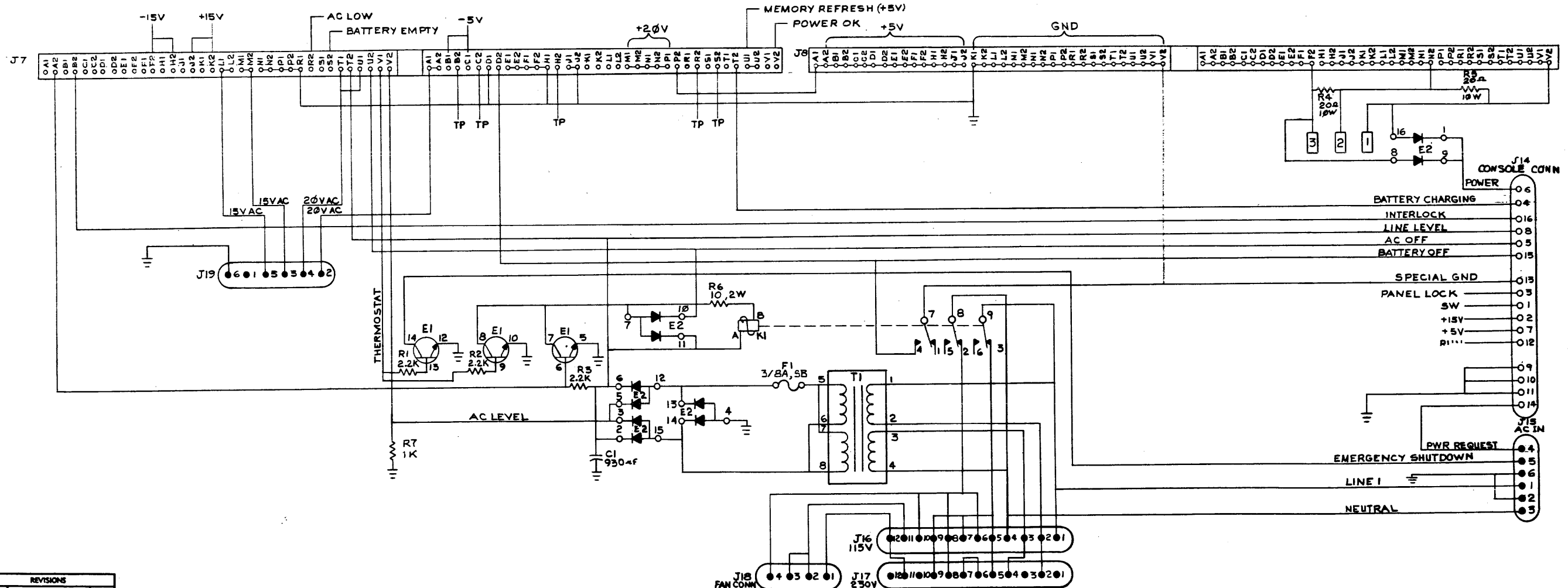


REV.	DATE	BY	CHKD	DESCRIPTION
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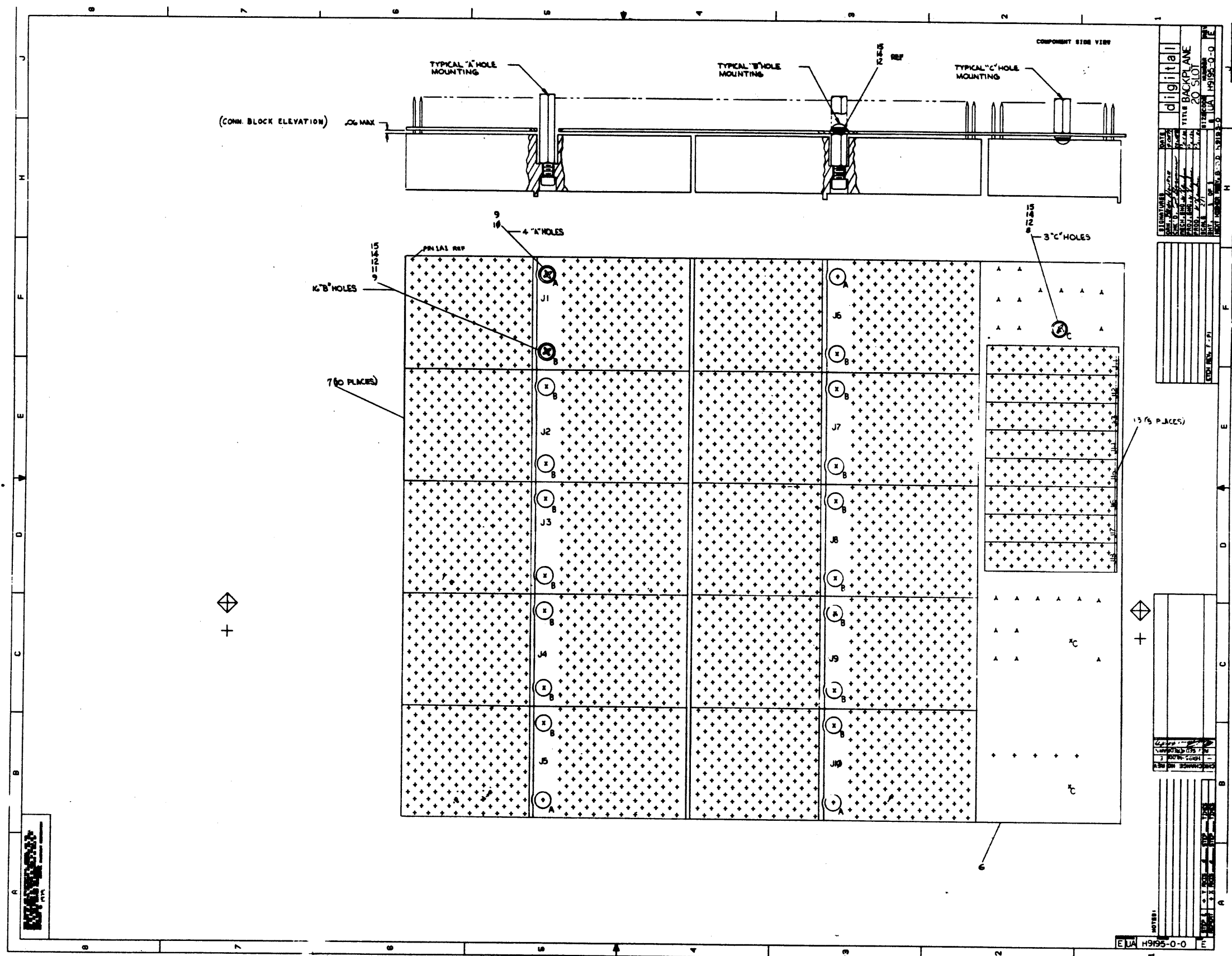
REF	DES	DESCRIPTION	DWG./PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES				
ANGLES		CLASS OF ACCURACY		
10° 30'		OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0
SURFACE QUALITY		MICRONS		
IN		±.004	±.008	±.012
MICRONS		±.012	±.018	±.025
QUANTITY & VARIATION		PREFERRED		
THIRD ANGLE PROJECTION				
DRN. 1/28/75		FIRST USED ON H9300		
CHK'D 2-8-75		TITLE		
ENG 2-29-75		CONNECTOR BLOCK ASS'Y		
PROJ 2-29-75		SIZE CODE DAD		
PROG 2-29-75		NUMBER H9194-0-0		
DO NOT SCALE DWG		SCALE 1/1		
NEXT HIGHER ASSY.		SHEET 1 OF 1		
MATERIAL		DIST.		
FINISH		REV. H		

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PIN	A1	A2	B1	B2	C1	C2	D1	D2	E1	E2
A	A01 = +5V ALL OTHERS = TP	+5V	B02#B03-BATTERY EMPTY, ALL OTHERS=TP	+5V	C01 = +5V, ALL OTHERS = TP	+5V	D02#D03-PANEL LOCK, ALL OTHERS=TP	+15V	TEST POINT	+20V
B	TEST POINT	-15V	B02#B03-AC LOW, ALL OTHERS=TP	-15V	TEST POINT	-15V	TEST POINT	-15V	TEST POINT	BANK SEL 0
C	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
D	MA0 L	EMA0 L	MA4 L	INT STROBE H	I/O PAUSE L	TP1 H	MA8 L	IR0 L	TEST POINT	BANK SEL 1
E	MA1 L	EMA1 L	MA5 L	BREAK IN PROG L	C0 L	TP2 H	MA9 L	IR1 L	TEST POINT	+20V
F	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
H	MA2 L	EMA2 L	MA6 L	MA, MS, LOAD CONT L	C1 L	TP3 H	MA10 L	IR2 L	TEST POINT	MEMORY REFRESH
J	MA3 L	MEM START L	MA7 L	OVERFLOW L	C2 L	TP4 H	MA11 L	F L	TEST POINT	MEMORY REFRESH
K	MD0 L	MDDIR L	MD4 L	BREAK DATA CONT L	BUS STROBE H	TS1 L	MD8 L	D L	TEST POINT	+20V
L	MD1 L	SOURCE H	MD5 L	BREAK CYCLE L	INTERNAL I/O L	TS2 L	MD9 L	E L	TEST POINT	BANK SEL 2
M	MD2 L	STROBE H	MD6 L	LOAD ADD ENABLE L	NOT LAST XFER L	TS3 L	MD10 L	USER MODE L	TEST POINT	-5V
N	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
P	MD3 L	INHIBIT H	MD7 L	INT IN PROG H	INT REQUEST L	TS4 L	MD11 L	F SET L	TEST POINT	+20V
R	DATA 0 L	RETURN H	DATA 4 L	NTS STALL L	INITIALIZE H	LINK DATA L	DATA 8 L	PULSE LA H	TEST POINT	BANK SEL 3
S	DATA 1 L	WRITE H	DATA 5 L	RES	SKIP L	LINK LOAD L	DATA 9 L	STOP L	UNUSED	UNUSED
T	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	JUMPER	GROUND
U	DATA 2 L	ROM ADDRESS L	DATA 6 L	RUN L	CPMA DISABLE L	IND 1 L	DATA 10 L	KEY CONTROL L	UNUSED	UNUSED
V	DATA 3 L	LINK L	DATA 7 L	POWER OK H	MS, IR DISABLE L	IND 2 L	DATA 11 L	SW	UNUSED	UNUSED



REVISIONS		
CHK	CHANGE NO.	REV.



DATE	
BY	
CHECKED	
APPROVED	
ESTIMATOR	
REVISION	
TITLE	BACKPLANE
FIG. NO.	20
PROJECT	100-10000-0-10

NO.	
DATE	
BY	
CHECKED	
APPROVED	
ESTIMATOR	
REVISION	
TITLE	
FIG. NO.	
PROJECT	

NO.	
DATE	
BY	
CHECKED	
APPROVED	
ESTIMATOR	
REVISION	
TITLE	
FIG. NO.	
PROJECT	

NO.	
DATE	
BY	
CHECKED	
APPROVED	
ESTIMATOR	
REVISION	
TITLE	
FIG. NO.	
PROJECT	

EUA H9195-0-0 E

8

7

6

5

4

3

3

1-0-9616H

2

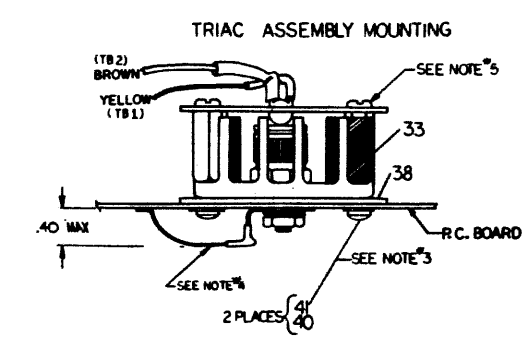
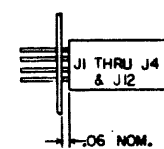
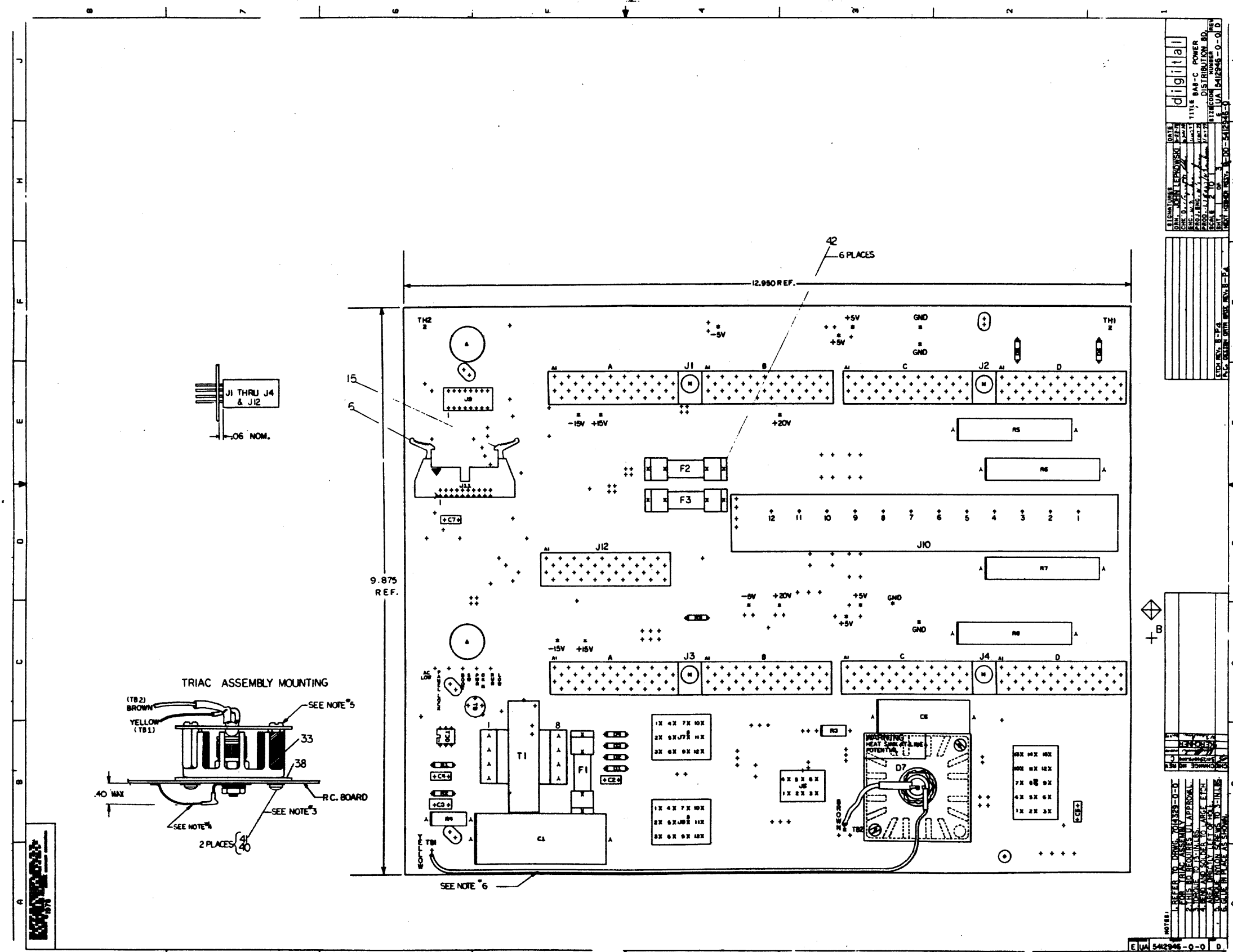
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PIN	A1	A2	B1	B2	C1	C2	D1	D2	E1	E2
A	A01 = +5V, ALL OTHERS = TP	+5V	B02 & B03 = BATTERY EMPTY, ALL OTHERS = TP	+5V	C01 = +5V, ALL OTHERS = TP	+5V	D02 & D03 = PANEL LOCK, ALL OTHERS = TP	+15V	TEST POINT	+20V
B	TEST POINT	-15V	B02 & B03 = AC LOW, ALL OTHERS = TP	-15V	TEST POINT	-15V	TEST POINT	-15V	TEST POINT	BANK SEL 0
C	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
D	MA0 L	EMA0 L	MA4 L	INT STROBE H	I/O PAUSE L	TP 1 H	MA8 L	IR0 L	TEST POINT	BANK SEL 1
E	MA1 L	EMA1 L	MA5 L	BREAK IN PROG L	C0 L	TP 2 H	MA9 L	IR1 L	TEST POINT	+20V
F	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
H	MA2 L	EMA2 L	MA6 L	MA, MS, LOAD CONT L	C1 L	TP 3 H	MA10 L	IR2 L	TEST POINT	MEMORY REFRESH
J	MA3 L	MEM START L	MA7 L	OVERFLOW L	C2 L	TP 4 H	MA11 L	F L	TEST POINT	MEMORY REFRESH
K	MD0 L	MD DIR L	MD4 L	BREAK DATA CONT L	BUS STROBE L	TS 1 L	MD8 L	D L	TEST POINT	+20V
L	MD1 L	SOURCE H	MD5 L	BREAK CYCLE L	INTERNAL I/O L	TS 2 L	MD9 L	E L	TEST POINT	BANK SEL 2
M	MD2 L	STROBE H	MD6 L	LOAD ADD ENABLE L	NOT LAST XFER L	TS 3 L	MD10 L	USER MODE L	TEST POINT	-5V
N	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
P	MD3 L	INHIBIT H	MD7 L	INT IN PROG H	INT REQUEST L	TS 4 L	MD11 L	F SET L	TEST POINT	+20V
R	DATA 7 L	RETURN H	DATA 4 L	MTS STALL L	INITIALIZE H	LINK DATA L	DATA 8 L	PULSE LA H	TEST POINT	BANK SEL 3
S	DATA 1 L	WRITE H	DATA 5 L	RES 2	SKIP L	LINK LOAD L	DATA 9 L	STOP L	UNUSED	UNUSED
T	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	JUMPER	GROUND
U	DATA 2 L	ROM ADDRESS L	DATA 6 L	RUN L	CPMA DISABLE L	IND 1 L	DATA 10 L	KEY CONTROL L	UNUSED	UNUSED
V	DATA 3 L	LINK L	DATA 7 L	POWER OK H	MS, IR DISABLE L	IND 2 L	DATA 11 L	SW	UNUSED	UNUSED

DESIGNED BY: M. L. G. / 11/10/76
 CHECKED BY: W. K. R. / 12/10/76
 W. K. R. / 12/10/76
 W. K. R. / 12/10/76

DRN: R. K. / 0-1676	FIRST USED ON: BA8-C	DIGITAL
CHK: D. B. / 11/10/76	TITLE: 20 SLOT BACK PLANE	
ENG: A. J. / 11/10/76	SIZE: DCS	NUMBER: H9195-0-1
PROJ. ENG: A. J. / 11/10/76	SCALE: 1:1	REV: E
PROD: /	SHEET: 2	OF: 1
NEXT HIGHER ASSY: D-UA-H9195-0-0	DIST.:	



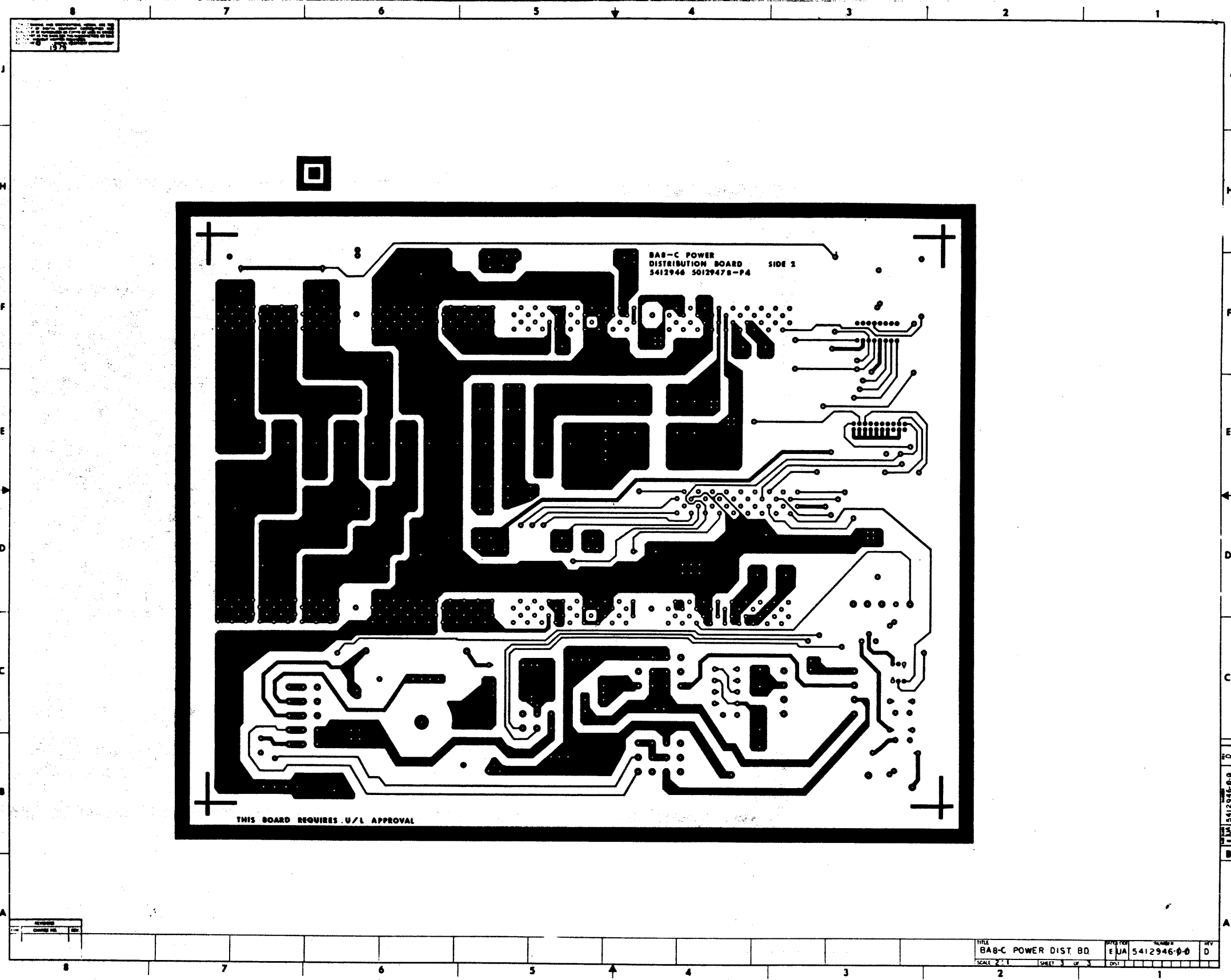
digital

111, BAB-C POWER DISTRIBUTION BOARD

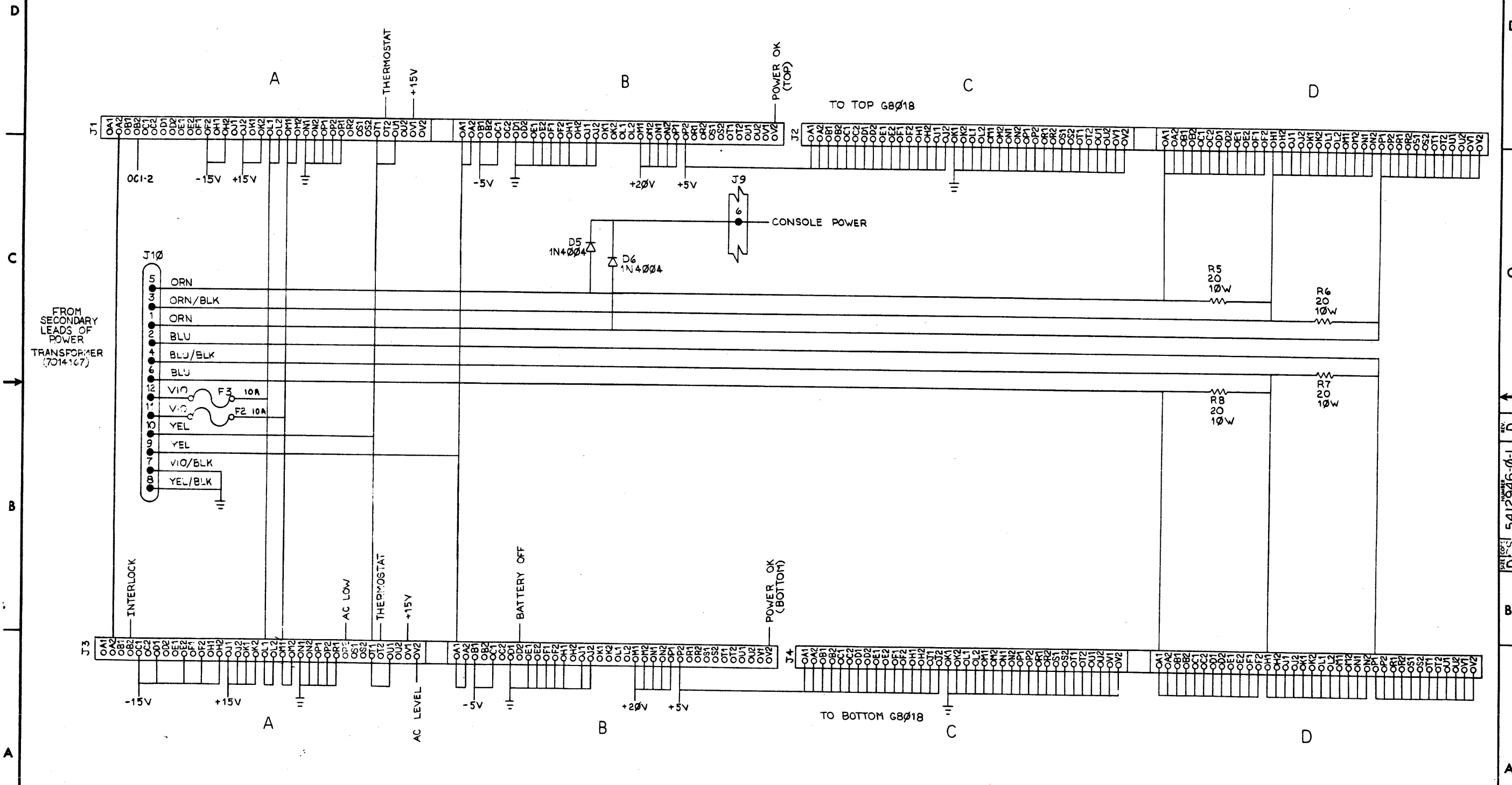
DATE: 7/27/74
 DESIGNED BY: J. LEPOWSKI
 CHECKED BY: J. LEPOWSKI
 PROJECT: 111, BAB-C POWER DISTRIBUTION BOARD
 PART NO: 111-5412946-0-0
 REV: 1

REVISED TO DRAWING 111-5412946-0-0
 TO INCLUDE APPROVAL
 AND TO INCLUDE APPROVAL
 AND TO INCLUDE APPROVAL
 AND TO INCLUDE APPROVAL
 AND TO INCLUDE APPROVAL

111-5412946-0-0



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REVISIONS		
CHK	CHANGE NO.	REV.

REV. D 5412946-0-1

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
1	1	E-MD-5012947-0-0	5012947-00	BABC POWER DISTRIBUTION BOARD	1	
2	2		1000043-00	1000.0 MMF 250V 20% YSF DISC	1	C3
3	3		1000033-00	.1 MFD 600V 10% MYLR	1	C6
4	4		1010509-00	.930 MFD 30V +75-10% AL EL	1	C1
5	5		1105796-00	1N 4004 PIV=400 I= 1A 0041 SP	6	D1-D6
6	6		1209941-04	HEADER RT ANGLE, RIGHT	1	
7	7		1211425-00	CONN, CARD 72PIN SLOTTED DOUBLE	4	J1-J4
8	8		1211029-00	CONN, CARD 36PIN SLOTTED	1	J12
9	9		1211813-02	SKT, IC 16PIN DIP GOLD PLATE	1	J9
10	10		1209941-06	HEADER, 100 20POS RT ANGLE	1	J11
11	11		1211905-01	TERM BLOCK 12POS 7/16 SPACING	1	J10
12	12		1212297-09	MATE-N-LOK 15PIN UNIV HEADER	1	J6
13	13		1212297-05	MATE-N-LOK 6PIN UNIV HEADER	1	J5
14	14		1212297-08	MATE-N-LOK 12PIN UNIV HEADER	2	J7, J8
15	15		1209941-03	HEADER RT ANGLE LEFT L	1	
16	16		1302199-00	47.0 1.0 W 5.0 % CC	1	R4
17	17		1300228-00	100.0 .50 W 5.0 % CC	1	R3
18	18		1304839-00	51.0 K .25 W 5.0 % CC	1	R1
19	19		1305416-00	20.0 10.0 W 1.0 % WW	4	R5-R8
20	20		1509338-00	DEC6531B NPN 310MW SI 40 90 P	1	Q1
21	21		1914194-00	OPTP-COUPLED ISOLATOR	1	OC1
22	22		9006707-00	*** THIS ITEM IS NOT USED ***	-	
23	23		1613282-00	XFMR P=AB S=14.53.12A	1	T1
24	24		9006023-01	*** THIS ITEM IS NOT USED ***	-	
25	25		9008185-00	*** THIS ITEM IS NOT USED ***	-	
26	26		9007203-00	*** THIS ITEM IS NOT USED ***	-	
27	27		9007208-00	FUSE, REG BLO 1/2 A, 250V GLASS	1	F1
28	28		9009000-00	*** THIS ITEM IS NOT USED ***	-	
29	29		9008838-00	FUSE, REG BLO 10 A, 32V GLASS	2	F2, F3
30	30		9107560-01	*** THIS ITEM IS NOT USED ***	-	

REVISION HISTORY			BASIC PART NO: 5412946			D I G I T A L		
ENG:	ECO NUMBER	REV	SECTION A OF A	DRN:	W.E.	DATE:	09-JUN-78	TITLE
ER	00003	C	SECTION VARIATION INDEX	CHK'D:	J.P. LEPKOWSKI	DATE:	09-JUN-78	PARTS LIST
WK	5412946-ML004	D	[A] 00	DES. ENG.:	AL DELUCA	DATE:	09-JUN-78	BAB-C POWER DISTRIBUTION BOARD
			[B]	RESP. ENG.:	AL DELUCA	DATE:	09-JUN-78	DOCUMENT NUMBER
			[C]	MFG. ENG.:	J.V. KANE	DATE:	09-JUN-78	SIZE: CODE: NUMBER: REV
			[D]	ASSEMBLY NUMBER:	E-UA-5412946-0-0	TOP DOCUMENT NUMBER:	BAB-C	K: PL: 5412946-0-DBP: D
			[E]			FILE NAME:	Z08050.PLS	EDIT #
			[F]					6
			[G]					
			[H]					
			[I]					
			[J]					
			[K]					
			[L]					
			[M]					
			[N]					

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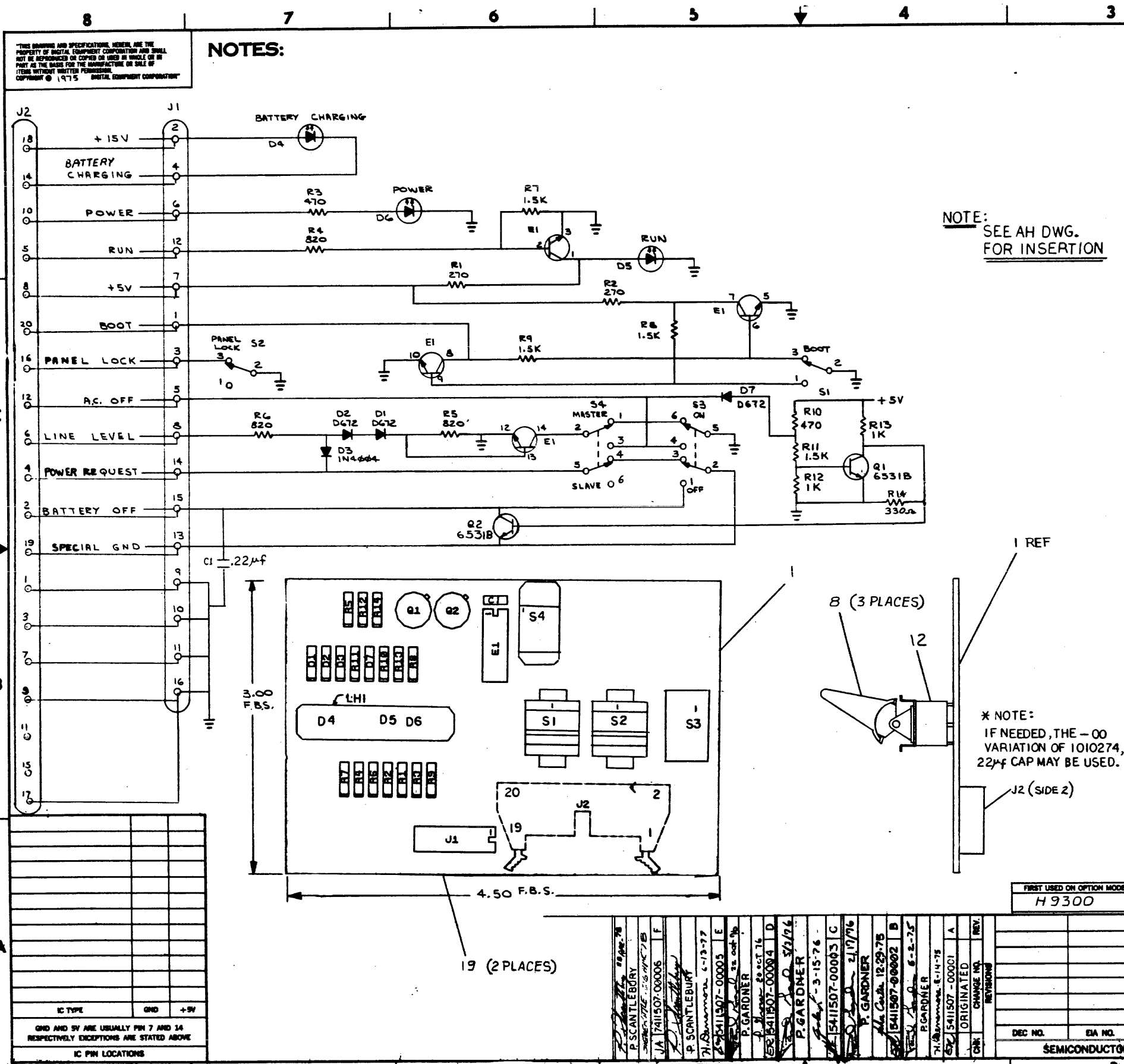
AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A2 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
31	31		1210929-01	*** THIS ITEM IS NOT USED ***	-		
32	32		1302396-00	150.0 K .25 W 5.0 % CC	1		R2
33	33		7014329-00	TRIAC ASSY	1		D7
34	34		1010274-00	.22 MFD 50V +80-20% Z5U CER	4		C2, C4, C5, C7
35	35		9105740-55	*** THIS ITEM IS NOT USED ***	-		
36	36		1300365-00	1.0 K .25 W 5.0 % CC	1		R9
37	37		9107256-11	*** THIS ITEM IS NOT USED ***	-		
38	38		7420187-00	PLATE LABEL	1		
39	39		9006431-0E	*** THIS ITEM IS NOT USED ***	-		
40	40		9007801-00	WASHER, LOCK, S.S. #6	2		
41	41		9006024-01	SCREW, PAN, PHIL 6-32X 1/2 SS	2		
42	42		9009513-03	CLIP, FUSE, WITH STOP, FOR PC BO	6		

D	I	G	I	T	A	L	TITLE	BAB-C POWER DISTRIBUTION BOARD	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	5412946-0-DBP	D



1-0-2091195 2

REF	X-Y COORDINATE HOLE LOCATION	K-CO-5411507-0-4	1
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-5411507-0-5	2
REF	MODULE ECO HISTORY	B-MM-5411507-0-6	3
1	ETCHED CIRCUIT BOARD	5011506	4
3	D1, D2, D7	DIODE, D672	110 5275
1	D3	DIODE, 1N4004	110 5796
3	D4, D5, D6	DIODE, LIGHT EMITTING	1110 324
2	S1, S2	KNOB, DARK GRAY (#47)	1210786-01
1	S4	SWITCH, TOGGLE, SPDT.	1210840
1	S3	SWITCH, SLIDE, DPST.	1210919
1	LH1	LED HOLDER, TRIPLE	1210940-02
1	S3	SWITCH, TOGGLE, D.P.D.T	1212010
2	R3, R10	RES. 470, 1/4W 5%	130 0316
4	R7, R8, R9, R11	RES. 1.5K, 1/4W 5%	1300 391
3	R4, R5, R6	RES. 820, 1/4W, 5%	1301 775
2	R1, R2	RES. 270, 1/4W, 5%	130 1972
1	E1	QUAD CORE DRIVER, 4011	1511102-00
1	J1	SOCKET, 16 PIN	1211813-02
2	R12, R13	SCR, SLOTTED PAN HD, SELF TMR	900 8230-01
2	Q1, Q2	RES. 1K, 1/4W 5%	13003 65
2	Q1, Q2	TRAN. DEC 6531B	1509338
1	R14	RES 330, 1/4W, 5%	13002 95
2	EYELET, FUNNEL FLANGE	2206796	23
2	WIRE, #22 GREEN SOLID	5105780-1	24
1	J2	RT. ANGLE HEADER, 20 PIN	1209941-06
1		RT. ANGLE HEADER, LEFT LATCH	1209941-03
1		RT. ANGLE HEADER, RT. LATCH	1209941-04
1	C1	* CAP., .22µf, 50V, 20%	1010274-01

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.

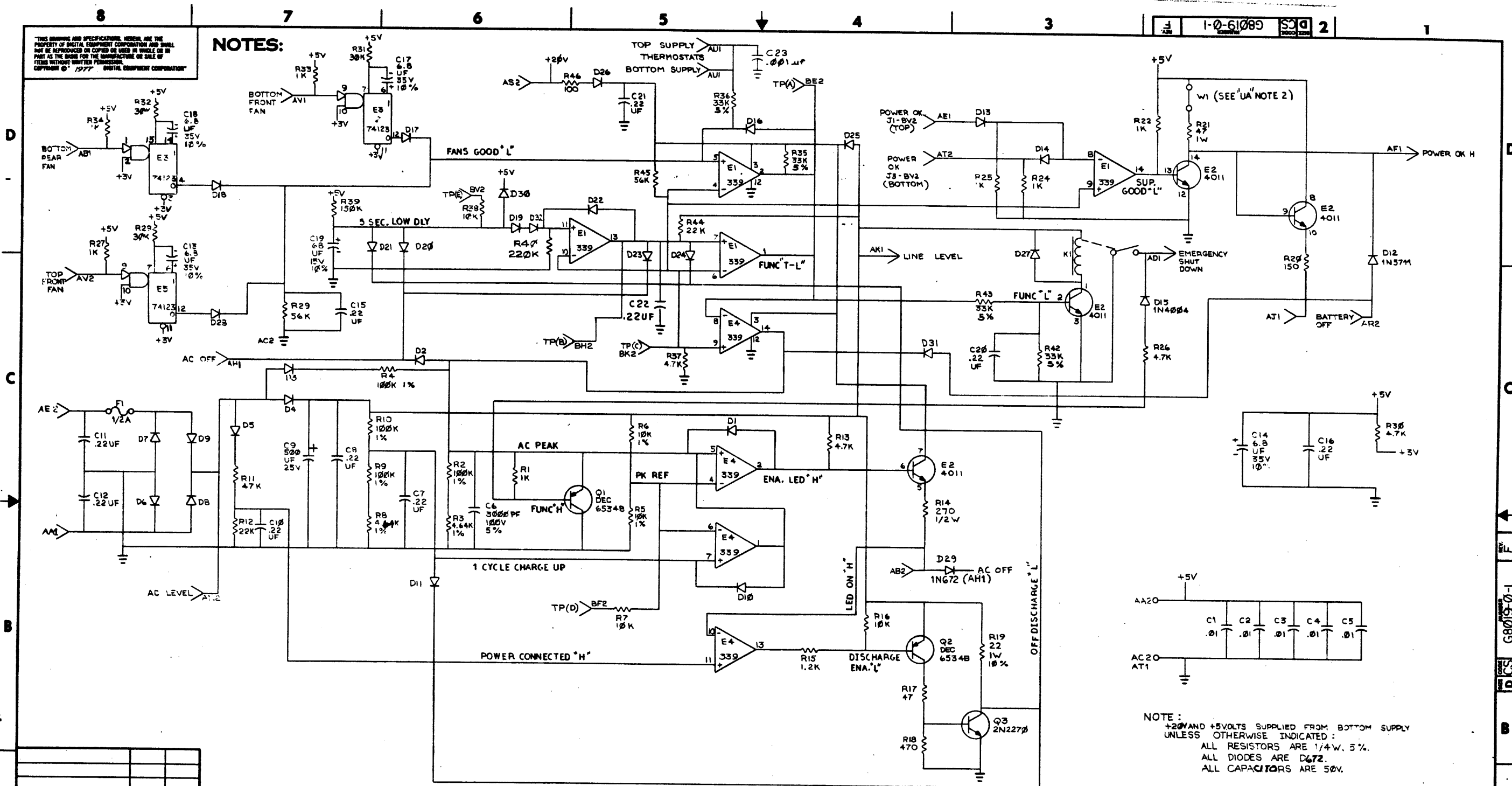
FIRST USED ON OPTION MODEL H9300		ETCH BOARD REV. C	
PARTS LIST			
DRAWN <i>K. G. G.</i> DATE 1-21-75	DATE 1-22-75	digital LIMITED FUNCTION BOARD D CS 5411507-0-1	
CHECKED <i>K. G. G.</i> DATE 2-27-75	DATE 2-27-75		
DESIGNED <i>K. G. G.</i> DATE 2-27-75	DATE 2-27-75		
APPROVED <i>K. G. G.</i> DATE 3-7-75	DATE 3-7-75		
PROJ. ENG. <i>K. G. G.</i> DATE 3-7-75	DATE 3-7-75	NEXT HIGHER ASST D-AD-7010033-p-p	
SCALE		SHEET 1 OF 1	
DEC. NO.		SQA. NO.	
SEMICONDUCTOR CONVERSION CHART			

REVISIONS

REV.	DATE	BY	DESCRIPTION
1	1-19-75	P. GARDNER	ORIGINATED
2	2-2-75	P. GARDNER	REVISED
3	2-27-75	P. GARDNER	REVISED
4	3-7-75	P. GARDNER	REVISED
5	3-15-76	P. GARDNER	REVISED
6	5-17-76	P. GARDNER	REVISED
7	6-2-75	P. GARDNER	REVISED
8	8-29-75	P. GARDNER	REVISED
9	12-29-75	P. GARDNER	REVISED
10	12-29-75	P. GARDNER	REVISED
11	12-29-75	P. GARDNER	REVISED
12	12-29-75	P. GARDNER	REVISED
13	12-29-75	P. GARDNER	REVISED
14	12-29-75	P. GARDNER	REVISED
15	12-29-75	P. GARDNER	REVISED

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NOTES:



NOTE:
+20V AND +5VOLTS SUPPLIED FROM BOTTOM SUPPLY
UNLESS OTHERWISE INDICATED:
ALL RESISTORS ARE 1/4W, 5%.
ALL DIODES ARE D672.
ALL CAPACITORS ARE 50V.

IC TYPE	QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
74123	2	E2, E3	MONOSTABLE MULTIVIBRATOR	74123	
339	4	E1, E4	CMOS INVERTER	339	
01 DEC 6534B	1	Q1	TRANSISTOR	01 DEC 6534B	
02 DEC 6534B	1	Q2	TRANSISTOR	02 DEC 6534B	
2N2270	1	Q3	TRANSISTOR	2N2270	

REV.	DATE	BY	CHKD.	DESCRIPTION
1	27-JUN-78	W. KERCHNER		INITIAL DESIGN
2	21-APR-79	W. KERCHNER		REVISED FOR MANUFACTURE
3	3-MAY-77	J. CARTER		REVISED FOR MANUFACTURE
4	5-DEC-77	A. DELUCA		REVISED FOR MANUFACTURE
5	25-AUG-77	A. DELUCA		REVISED FOR MANUFACTURE
6	21-APR-79	W. KERCHNER		REVISED FOR MANUFACTURE
7	21-APR-79	W. KERCHNER		REVISED FOR MANUFACTURE
8	21-APR-79	W. KERCHNER		REVISED FOR MANUFACTURE

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
1	AA20	5V BATTERY		
1	AC20	AC POWER		
1	AT1	AC POWER		
1	AA20	5V BATTERY		
1	AC20	AC POWER		
1	AT1	AC POWER		

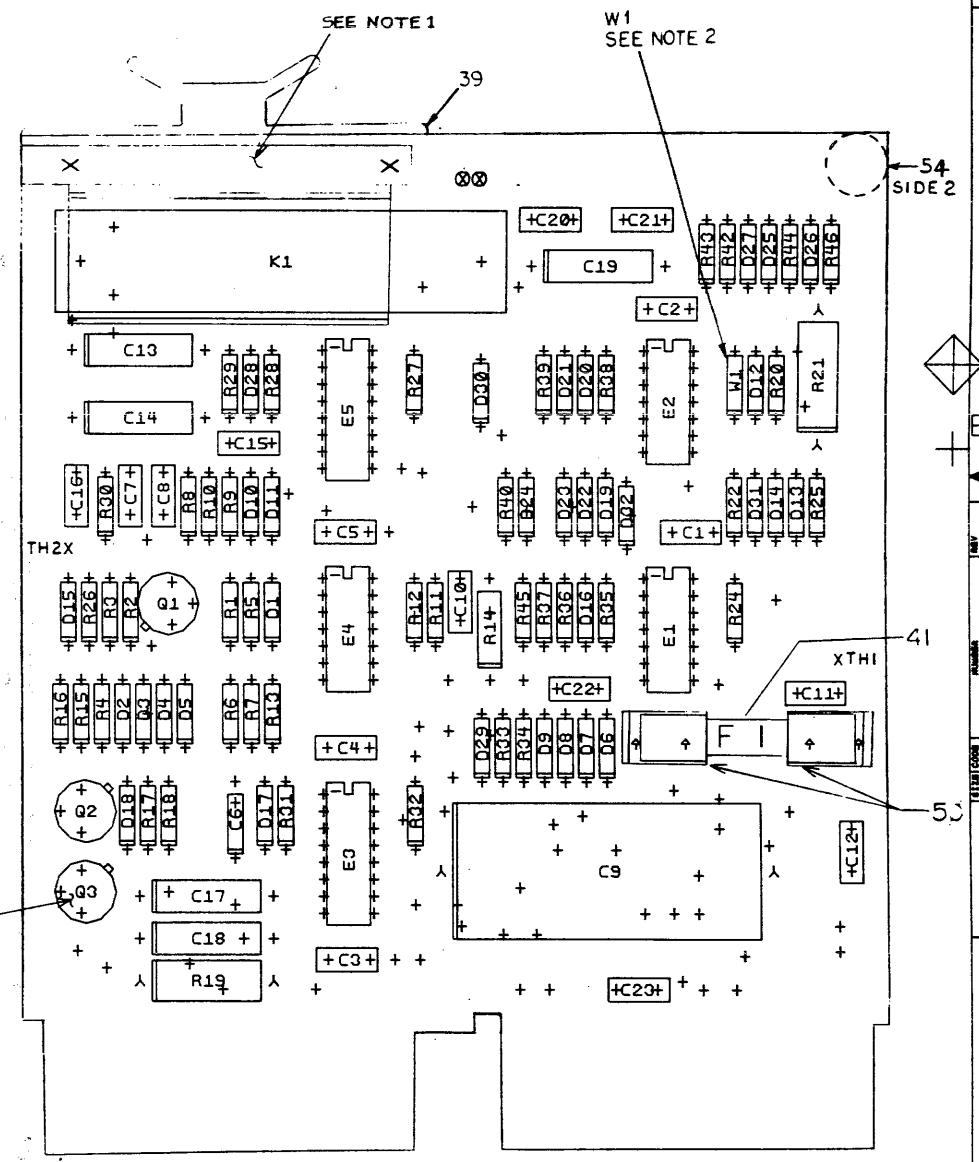
digital
POWER DISTRIBUTION BOARD CONTROL

SEMICONDUCTOR CONVERSION CHART

DEC. NO.	EIA NO.	DEC. NO.	EIA NO.

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COMPONENT SIDE VIEW



NOTES: 1. INSTALL A.K.C. BRACKET (7415122-02) USED TO SECURE RELAY K1 BY USING SCREWS (9026011-2-1), AND KEP NUTS (9006557-0-0), WASHERS (9006655-0-0). 2. POWER JACK W1 IS ONLY REMOVED WHEN 38213 IS AN EXPANDED UNIT.

CHANGE NO	REV	DATE	BY	CHK'D
ML025	E		J. CARTER	
			KEP-CHIER	
	F		M. MURPHY	
			D. FRENIERE	

ETCH REV. B-P1	
P.C. DESIGN DATA BASE REV. B	

SIGNATURES	DATE	REV
DRN. D. DING...	5-15-78	
CHK'D. L. G...		
ENG. ...		
PROJ. ENG. ...		
PROD. ...		
SHT. 10F3		
NEXT HIGHER ASSY. B-DD-G8019-0		

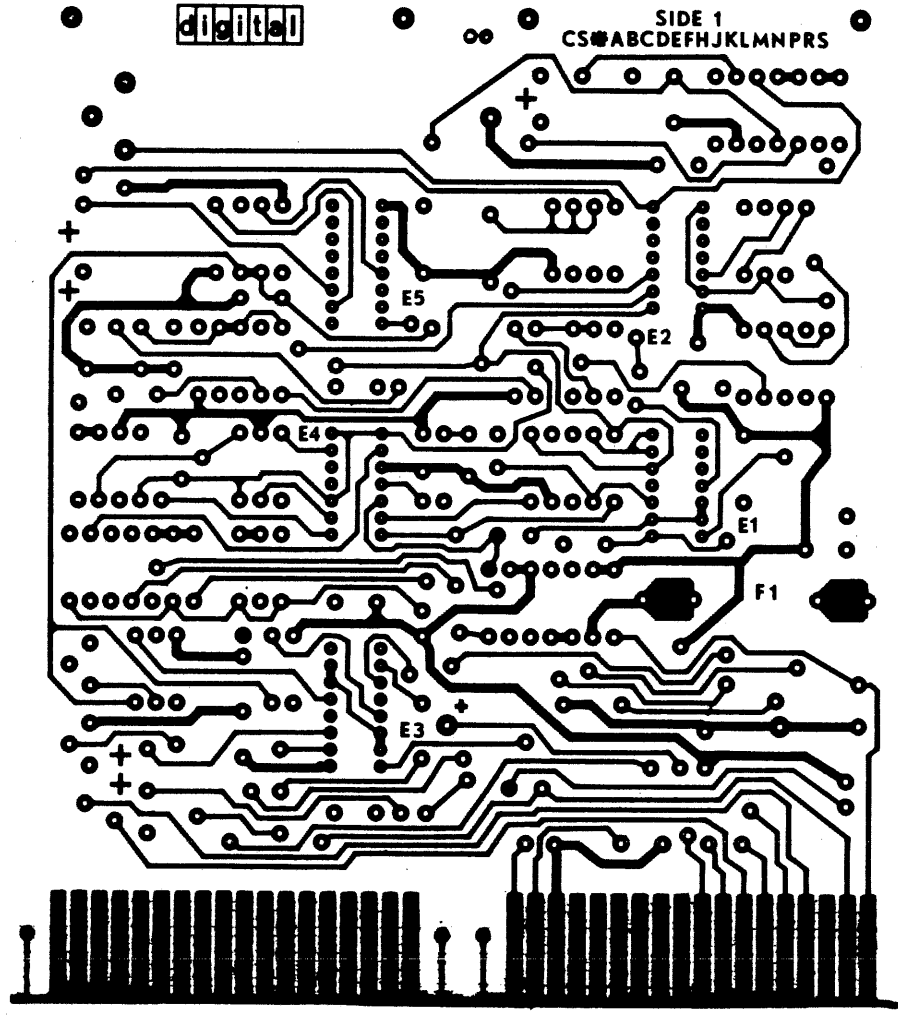
TITLE B18-0 POWER DISTRIBUTION BOARD CONTROL

SIZE CODE NUMBER REV

D VA G8019-0-0 F

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G8019 5012948B-P1



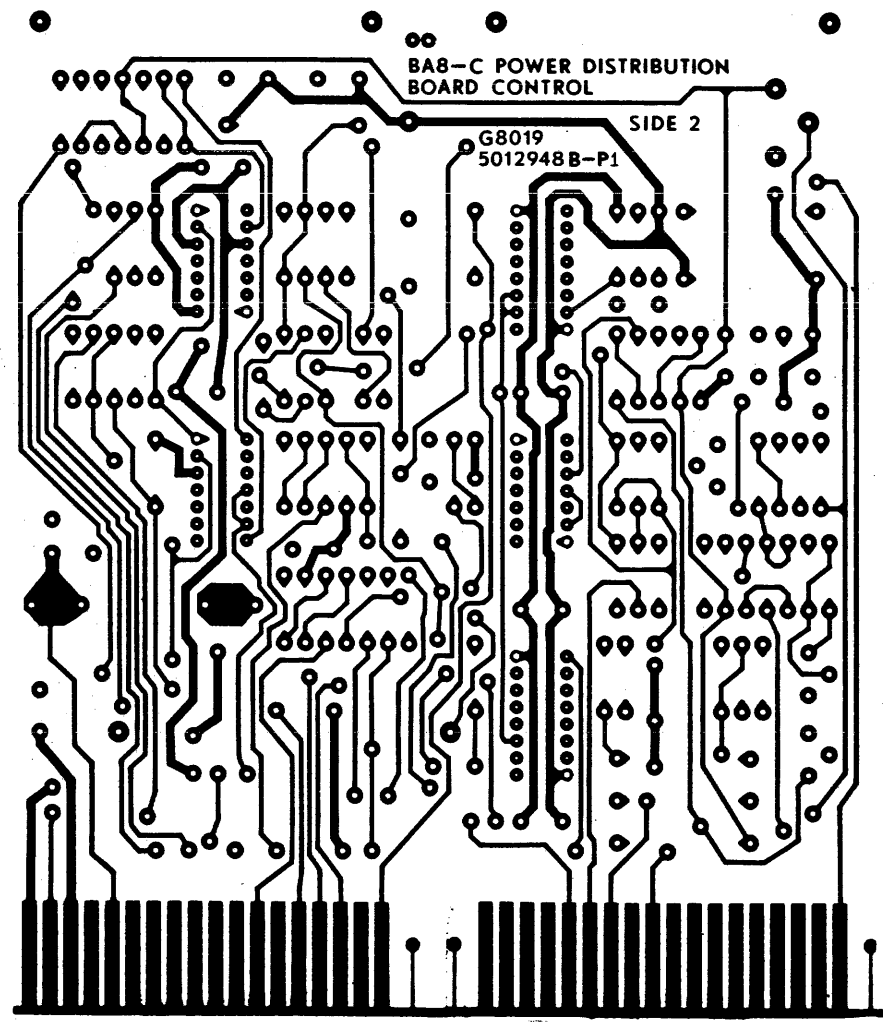
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	BA8-C POWER DISTRIBUTION BOARD CONTROL	SIZE CODE	D UA G8019-0-0	NUMBER		REV.	F
SCALE	2+1	SHEET	2	OF	3	DIST.	

D UA G8019 0-0

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REV. 1
 DUA G8019-0-0
 NUMBER 2



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	BA8-C POWER DISTRIBUTION BOARD CONTROL	SIZE/CODE	DUA G8019-0-0	NUMBER	2	REV.	F
SCALE	2+1	SHEET	3	OF	3	DIST.	

8 7 6 5 4 3 2 1

D

C

B

A

↑

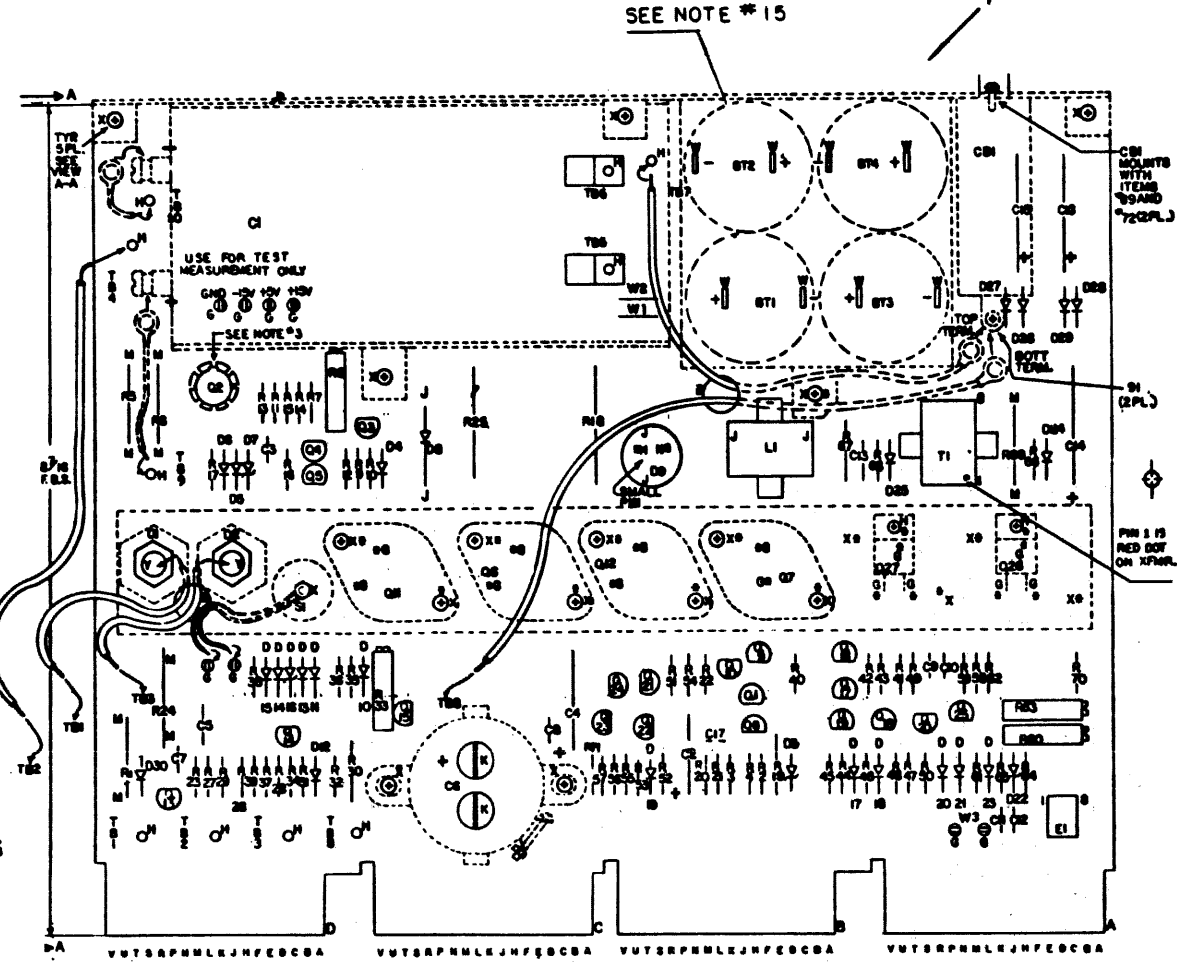
DUA G8019-0-0

8 7 6 5 4 3 2 1

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NOTES:

1. TRANSISTOR DEC 2N5362, 1510196 MAY BE USED AS A SECOND SOURCE FOR ITEM #62 TRANSISTOR DEC D44H8 1511654.
2. ALL COMPONENTS SHOWN BY A BROKEN LINE MOUNT ON SIDE 2.
3. TRANSISTOR PAD MOUNTS UNDER Q2, AND HEAT SINK MOUNTS ON Q2. REFER TO PARTS LIST, ITEM #25 AND #77.
4. ITEM #24 BRKT IS MOUNTED ON BOARD WITH ITEM #68, 73 AND #81 (2 PL).
5. Q6, Q7, Q11 AND Q12 ARE MOUNTED WITH ITEM #73, 78, 81, 92 AND 101.
6. S1 MOUNTS WITH ITEM #73, 81 AND 92 (1 PLACE).
7. Q26 AND Q27 ARE MOUNTED WITH ITEM #67, #71, 74, 92 AND 103.
9. PHYSICAL IS MADE IN REVERSE, SIDE 1 IS LIGHT, SIDE 2 IS DARK.
10. INSERT JUMPER W3 FOR 50MHz. OPERATION.
11. JUMPERS W1, W2 SHOULD BE INSTALLED AFTER TEST.
12. D1, D2, D3, D4, D5, D6, D7, D8, D9, D10, D11, D12, D13, D14, D15, D16, D17, D18, D19, D20, D21, D22, D23, D24, D25, D26, D27, D28, D29, D30, D31, D32, D33, D34, D35, D36, D37, D38, D39, D40, D41, D42, D43, D44, D45, D46, D47, D48, D49, D50, D51, D52, D53, D54, D55, D56, D57, D58, D59, D60, D61, D62, D63, D64, D65, D66, D67, D68, D69, D70, D71, D72, D73, D74, D75, D76, D77, D78, D79, D80, D81, D82, D83, D84, D85, D86, D87, D88, D89, D90, D91, D92, D93, D94, D95, D96, D97, D98, D99, D100.
13. S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S12, S13, S14, S15, S16, S17, S18, S19, S20, S21, S22, S23, S24, S25, S26, S27, S28, S29, S30, S31, S32, S33, S34, S35, S36, S37, S38, S39, S40, S41, S42, S43, S44, S45, S46, S47, S48, S49, S50, S51, S52, S53, S54, S55, S56, S57, S58, S59, S60, S61, S62, S63, S64, S65, S66, S67, S68, S69, S70, S71, S72, S73, S74, S75, S76, S77, S78, S79, S80, S81, S82, S83, S84, S85, S86, S87, S88, S89, S90, S91, S92, S93, S94, S95, S96, S97, S98, S99, S100.
14. FOR HANDLING AND SOLDERING REQUIREMENTS OF 15.1V10 LEAD ACID BATTERIES (ITEM #23) SEE A-5P-7645252-D-0



REF	X-Y COORDINATE HOLE LOCATION	K-CB-6016-B-4
REF	ASSY/DRILLING HOLE LAYOUT	D-AN-6016-B-5
REF	MODULE ECO HISTORY	D-IN-6016-B-6
1	ETCHED CIRCUIT BOARD	D-1A-500964-C-0
2	C5, C7, C9	CAP .1 UF 100V 20% DISC 1000030
3	C4	CAP 100 UF 6V 20% STANT 1000000
4	C8, C9, C10, C11, C12	CAP .01 UF 100V 20% DISC 1001010-01
5	C14, C16, C18	CAP 100 UF 20V -10% + 75% ELECT 1002771
6	C2	CAP 22 UF 5V 10% STANT 1000030
7	C7, C8	CAP .22 UF 50V -20% + 60% CER 100274-00
8	C9	CAP 6000 UF 10V -10% + 75% ELECT 1010704
9	C1	CAP .1UF 10V 1011070
10	D6, D6, D11, D12, D13, D14, D15	DIODE 6002 1100113
11	D1, D2	DIODE 1N1103 1111000
12	D10, D17, D19, D20, D21, D22, D24, D25, D30	DIODE 9072 1100275
13	D4, D26, D27, D28, D29	DIODE 1N4004 1100700
14	D8	DIODE 1N752 1110015
15	D7, D22	DIODE 1N751A ZENER 1110004
16	D9, D10	DIODE SCREENED 6.7V 2% ZENER 1111205
17	S1	THERMOSTAT 1211602
18		HEAT SINK 1211014
19	C81	CIRCUIT BREAKER 25A 1211073
20	D71, D72, D73, D74	BATTERIES D-CELL 1211070
21		BRACKET 1210420
22		HEAT SINK T-06 TRANSISTOR MOUNTED 1211540
23	R5, R6, R90	RES. 10 1% 50 1300171
24	R20, R42	RES. 47 1/4% 50 1300202
25	R1	RES. 100 1% 50 1300232
26	R71, R4, R22, R26, R28, R50, R57,	RES. 100 1/4% 50 1300220
27	R12, R47	RES. 100 1/4% 50 1300247
28	R27, R43	RES. 450 1/4% 50 1300250
29	R10, R17, R28, R54	RES. 220 1/4% 50 1300271
30	R3, R18, R27, R54, R56	RES. 330 1/4% 50 1300295
31	R11, R13, R20, R21, R29, R40, R41, R46, R52, R55, R58, R59, R70	RES. 1K 1/4% 50 1300300
32	R14, R50	RES. 1.5K 1/4% 50 1300391
33	R23, R44, R46, R48, R51, R53	RES. 3.9K 1/4% 50 1300430
34	R30	RES. 10 1/4% 50 1301317
35	R40	RES. 100 1/4% 50 1301322
36	R2	RES. 62 1/4% 50 1301477
37	R10	RES. 56 5% 50 1301502
38	R10, R82	RES. 511 1/4% 1% 50 1302411
39	R24	RES. 90 2% 50 1302630
40	R34	RES. 100 1/4% 1% 50 1302950
41	R7	RES. 1.21K 1/4% 1% 50 1302071
42	R31	RES. 100 1/4% 1% 50 1302250
43	R53	RES. 464 1/4% 1% 50 1303047
44	R7, R25	RES. 1.50 10% 50 1303360
45	R32	RES. 1.00K 1/4% 1% 50 1304633
46	R8, R61	RES. 303 1/4% 1% 50 1305125
47	R53	RES. 27K 1/4% 50 1305340
48	R8, R33, R60	RES. 100 3/4% 10% POT 70 PR 1309143-04
49	R63	RES. 5K 3/4% 10% POT 70 PR 1309143-00

16. APPLY THERMAL COMPOUND TO SIDES OF ALL THERMAL INSULATORS AND TO BOTTOM OF THERMOSTAT.

17. BELOW IS LISTED TORQUE VALUES TO BE USED IN ASSEMBLY:

SIZE OF SCREW	SECURING PC BOARD TO C6	INCH/LB
10-32 SCREWS	SECURING THE CRIMPS TO C1	14
10-32 SCREWS	SECURING THE CRIMPS TO C1	14
6-32 SCREWS	HOLDING PC BOARD TO THE CHASSIS	14
6-32 SCREWS	HOLDING PC BOARD TO THE CHASSIS	14
4-40 SCREWS	ON THE TRANSISTORS Q6, Q7, Q11, Q12	10
4-40 SCREWS	ON THE TRANSISTORS Q26, Q27	10
NUTS	HOLDING THE DIODES, D1 & D2	20
NUT	HOLDING THERMOSTAT, S1	10
6-32 SCREWS	HOLDING THE CIRCUIT BREAKER TO CHASSIS	14
6-32 SCREWS	HOLDING BATTERY SHIELD TO CHASSIS	14
6-32 SCREWS	HOLDING CRIMPS TO CIRCUIT BREAKER	14

NOTE:
TO ACHIEVE TRUE TORQUE READINGS, ALWAYS USE A NUT DRIVER ON ONE SIDE AND A TORQUE DRIVER ON THE OTHER WHERE A SCREW AND A NUT ASSEMBLY IS INVOLVED.
G.C. TOLERANCE: INSPECT TO MINUS (-) 2 INCH/LB OF ASSEMBLED TORQUE VALUES.

CAUTION:
OFF SHEET PA DATA BASE PER 68016-MK006

72741	4	7
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTS ARE STATED ABOVE		
IC PIN LOCATIONS		

DESIGNED BY: D. VAENIERE	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
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DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68
DESIGNED BY: J. A. DELUCA	DATE: 8-DEC-68

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	FILM NO.
PARTS LIST				
ETCH BOARD REV E				
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	

FIRST USED ON OPTION MODEL PDP8A	
ETCH BOARD REV E	
SEMICONDUCTOR CONVERSION CHART	
DEC NO.	EIA NO.
DEC NO.	EIA NO.
DEC NO.	EIA NO.
DEC NO.	EIA NO.
DEC NO.	EIA NO.
DEC NO.	EIA NO.

SCALE: SHEET 1 OF 3

digital EQUIPMENT CORPORATION

TITLE: H763 REGULATOR BOARD

NUMBER: DCSG8016-0-1

REV: H

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WIRE TABLE				
PART NO.	FROM	TO	TERMINATION	WIRE LENGTH
91-07380-22	TB1	D1	SOLDER AT D1 END. SOLDER AT TB1 END	5.00
91-07380-22	TB3	D2	SOLDER AT D2 END. TAB AT TB3 END	5.00
91-07380-00	TB2	TB4	SOLDER AT TB2 END SOLDER AT TB4 END	7.00
91-07380-00	+ TERMINAL OF C1	TB9 HOLE	SOLDER WIRE AT HOLE END TB9. SOLDERLESS CONNECTOR AT C1 END 90-07926	2.50
91-07380-00	- TERMINAL OF C1	TB10 HOLE	SOLDER WIRE AT HOLE END TB10. SOLDERLESS CONNECTOR AT C1 END 90-07926	1.25
91-07380-00	TB7	TOP TERMINAL OF CBI	SOLDER AT TB7 END. SOLDERLESS CONNECTOR AT CBI END 90-07926	7.00
91-07380-00	TB8	BOTTOM TERMINAL OF CBI	SOLDER AT TB8 END. SOLDERLESS CONNECTOR AT CBI END 90-07926	11.25
91-07350-22	S1	SPLIT LUG	SOLDER AT SPLIT LUG END	4.00
	S1	SPLIT LUG	SOLDER AT S1 END	4.00

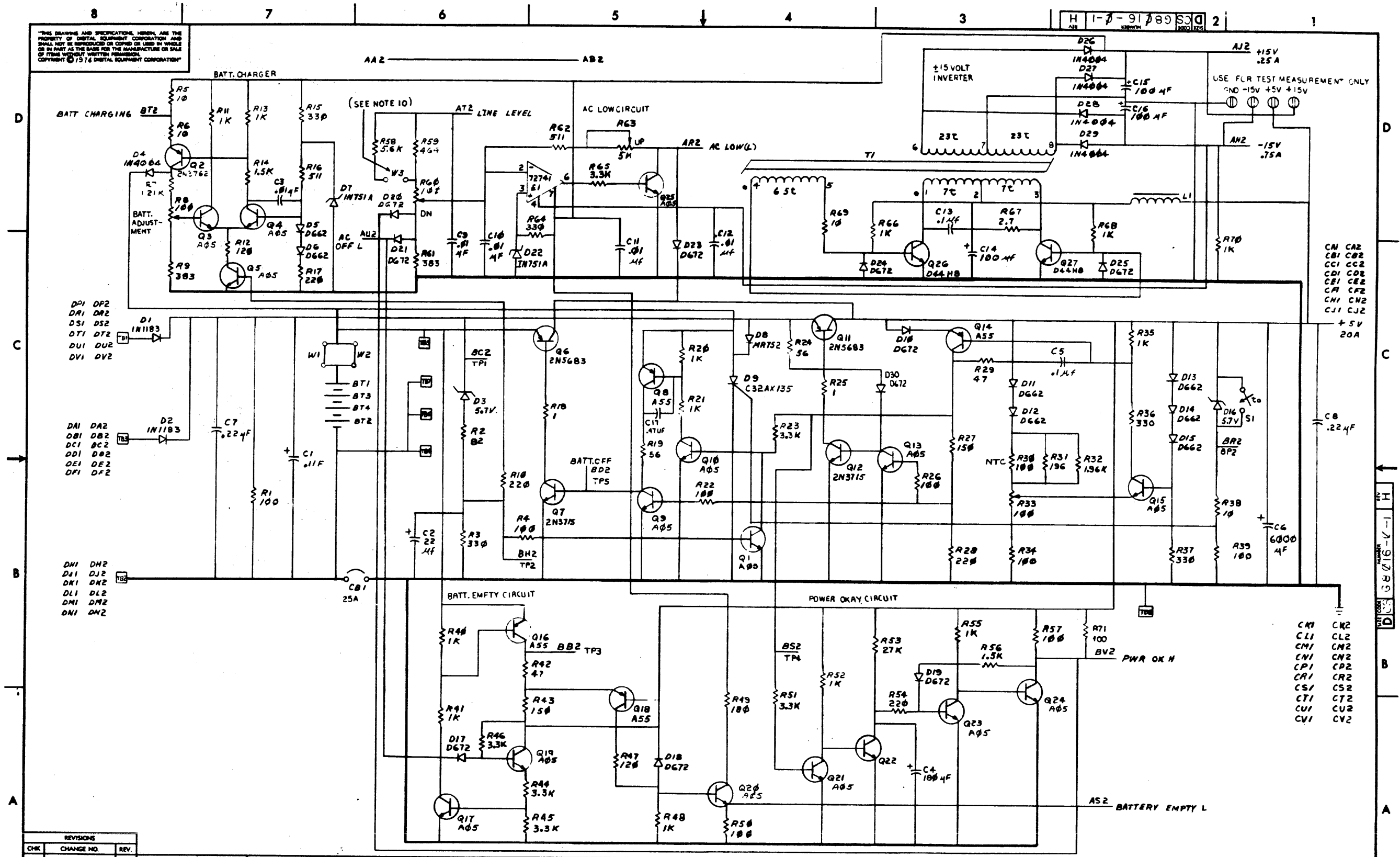
QTY	REF	DESIGNATION	DESCRIPTION	PART NO.	ITEM
1	R30		RES. 100 1/2W MTC	1311760	57
1	R67		RES. 2.7 1/2W 105	1300444	54
2	Q7, Q12		TRANS. DEC 2M3715	1503088	55
1	Q2		TRANS. 2M3762	1500648-01	56
18	Q1, Q3, Q4, Q5, Q8, Q10, Q13, Q15, Q17, Q18, Q20, Q21, Q22, Q23, Q24, Q25		TRANS. DEC A95	1510705	57
1	R39		RES. 5.6K, 1/4W, 5%	1301874	58
4	Q8, Q14, Q16, Q18		TRANS. DEC A55	1510705	59
1	D8		DIODE SCR C32A135	1510920	60
2	Q8, Q11		TRANS. 2M5803	1511647	61
2	Q26, Q27		TRANS. D44M8	1510707-01	62
1	T1		TRANSFORMER	1811758	63
1	L1		CHORE	1811759	64
1	E1		I.C. DEC 72741	1810290	65
1			BRACKET REG. B.D.	7411470	66
2			SCREW 4-48 x .50 PH	9008013-1	67
4			SCREW 8-32 x .25 PH	9008020-1	68
2			SCREW 8-32 x .31 PH	9008021-1	69
3			SCREW 8-32 x .75 PH	9008026-1	70
2			KEPHIT 4-48	9008557	71
7			WASHER #8 INTERNAL	9008633	72
8			WASHER #8 FLAT	9008656	73
2			WASHER #10 FLAT	9008772	74
8			SPLIT LUGS	9008735	75
2	TB5, TB6		TAB FAST-ON (OFF SET)	9007112	76
1			TRANSI PAD #10134	9007200	77
8			SCREW 6-32 x .56 PH	9007793-1	78
REF			G8016 REG. BOARD SPEC	G8016-0-E	73
4			SOLDERLESS CONNECTOR	9007920-01	80
11			KEPHIT 8-32	9008185	81
A/R			WIRE #12 AWG	9107380-08	82
A/R			WIRE #12 AWG	9107380-22	83
A/R	W3		BUS WIRE #22 AWG (SEE NOTE #10)	9107560-01	84
A/R	W1, W2		REEL JUMPER (SEE NOTE #11)	9107560	85
1			SHIELD BATTERY	7411693-0-0	86
3			SPACER #6 .38 LG.	9006601	87
2	TB5, TB6		EYELET	9009000	88
8	TB1, TB2, TB3, TB4, TB7, TB8, TB9, TB10		EYELET GS4-3	9007836	89
A/R			WIRE #22 AWG	9107350-22	90
2			WASHER #8 INTERNAL	9006634	91
A/R			THERMO COMPOUND	9008268	92
2			SCREW 10-32 x .31	9008070-01	93
2			WASHER #10 INTERNAL	9006635	94
1	C17		CRP .47UF 25V 20% CER.	1010275	95
1			DECAL	A-DC-7413109-01	96
REF			FINAL INSP. PROC. FOR G8016	A-SP-G8016-0-9	98
REF			POWER SUPPLY TESTER	B-DD-G8016-TA	99
1			PACKAGING INSTRUCTION	A-SP-3700175-0-0	100

REVISIONS		
CHK	CHANGE NO	REV

TITLE H763 REGULATOR BOARD
 SCALE 1:1 SHEET 2 OF 3
 SIZE CODE DCS G8016-0-1
 NUMBER H
 MK 1

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H 1-0-91789 SCD 2



- CN1 CN2
- CB1 CB2
- CC1 CC2
- CD1 CD2
- CE1 CE2
- CF1 CF2
- CH1 CH2
- CJ1 CJ2

+5V
20A

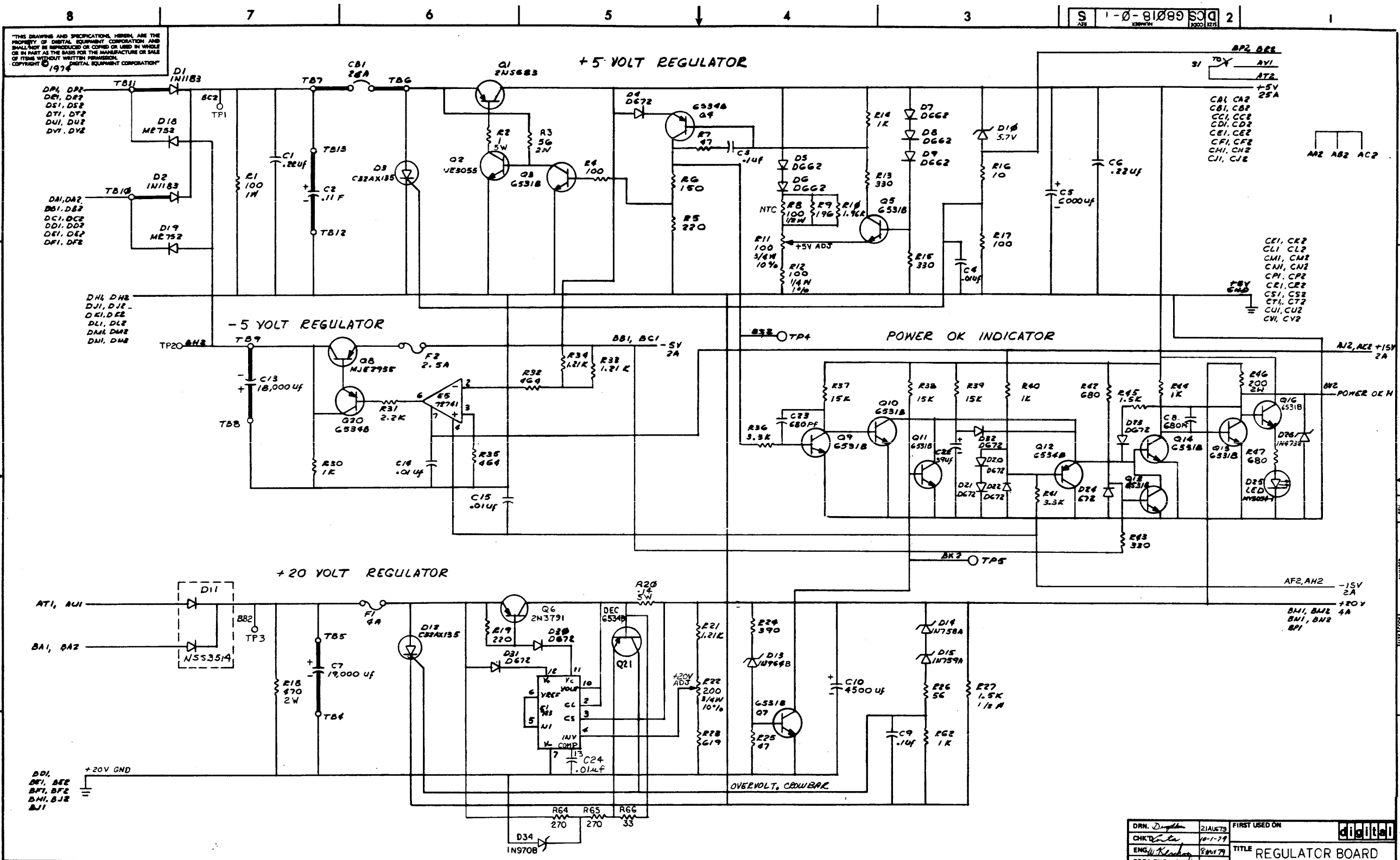
C8
.224F

C6
6000
4F

- CK1 CK2
- CL1 CL2
- CM1 CM2
- CN1 CN2
- CP1 CP2
- CR1 CR2
- CS1 CS2
- CT1 CT2
- CU1 CU2
- CV1 CV2

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE H763
REGULATOR BOARD
SCALE 1:1 SHEET 3 OF 3
SIZE CODE DCS NUMBER G8016-0-1 REV. H
MK MK



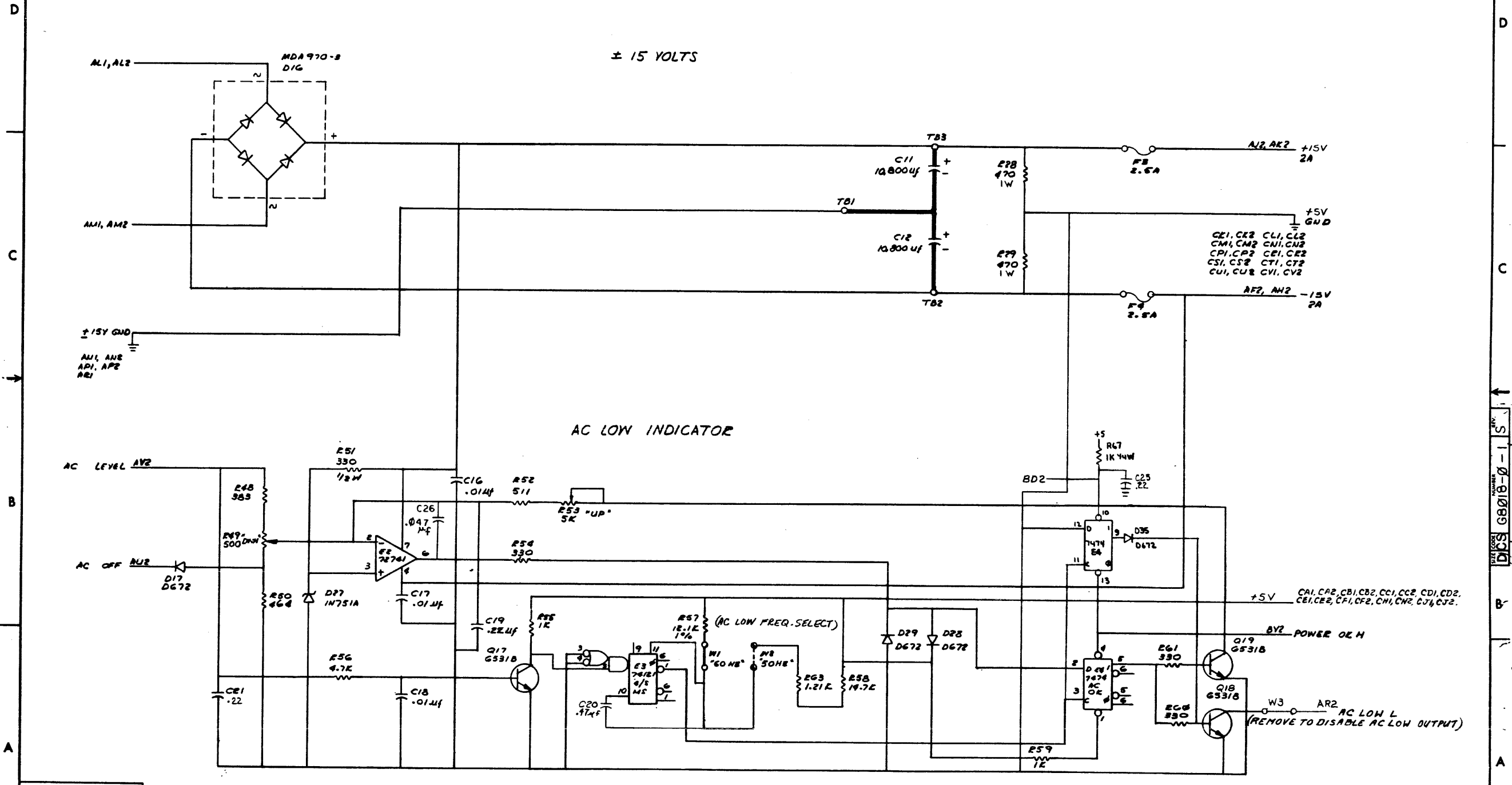
REVISIONS		CHK	CHANGE NO.	REV.
-	REVISED & REDRAWN MLO10	N		

DRN	CHK'D	ENG'G	PROL ENGR	PROD	DATE	BY
D. Freniere	W. Kerchner	D. Freniere			13 JUL 81	

DRN. <i>D. Freniere</i>	21AUG79	FIRST USED ON	
CHK'D. <i>W. Kerchner</i>	10-1-79	TITLE	
ENG'G. <i>D. Freniere</i>	8 MAY 79	REGULATOR BOARD	
PROL ENGR. <i>W. Kerchner</i>	8 MAY 79	8A CORE	
NEXT HIGHER ASSY.			SIZE CODE
SCALE <i>1/2"</i>			NUMBER
SHEET 1 OF 2			DIST.

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DCS 1-0-81089 2

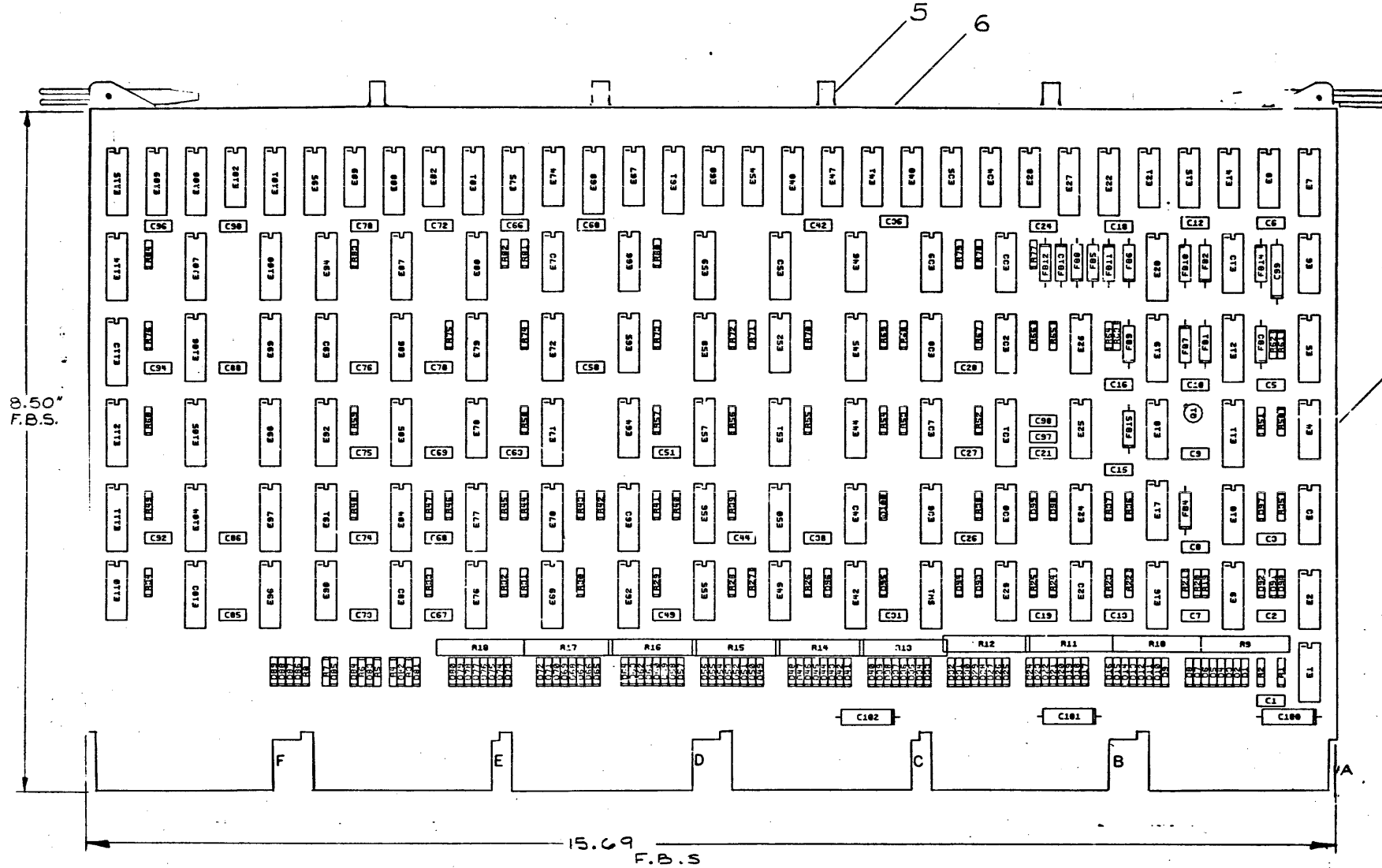


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE REGULATOR BOARD
BA CORE
SCALE 1:1
SHEET 2 OF 2
DISTRIBUTION
NUMBER DCS 68018-0-1
REV. S11

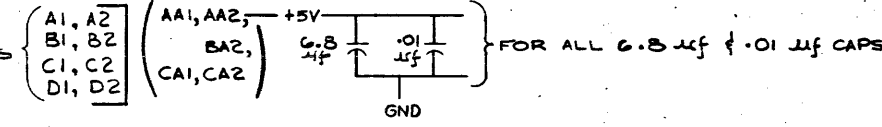
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NOTES:
 1. ALL UNLABELED DIODES ARE DEC TYPE D664
 2. +5V, +3VA AND +3VB GENERATION ON SHEET # 8
 3. FOR ETCH CUTS REFER TO D-AH-M8315-0-3
 4. ETCH CUTS



REF	REF	X-Y COORDINATE HOLE LOCATION	K-CO-M8315-0-4	1
		ASSY/DRILLING HOLE LAYOUT	D-AH-M8315-0-5	2
		MODULE ECO HISTORY	B-MH-8315-0-6	3
1		ETCHED CIRCUIT BOARD	5010932	4
1		HEX BOARD HANDLE ASSY	1210711-2	5
12		EYELETS	9006732	6
3	C100 THRU C102	CAP 0.01uf 35V 10%	1005306	7
2	C97, C98	CAP .047uf 16V DISC	1005678	8
1	C99	CAP 15uf 20V 10%	1004A12	9
48	C1 THRU C3, C5 THRU C10, C12, C13, C15, C16, C18, C19, C21, C24, C26, C27, C28, C31, C36, C38, C42, C44, C49, C51, C56, C70, C83, C86 THRU C70, C72 THRU C78, C79, C85, C86, C88, C90, C92, C94, C96	CAP .01uf 100V DISC	1001610-01	10
8	D86 THRU D92, D97	DIODE D662	1100113	11
92	D1 THRU D85, D93 THRU D98, D98 THRU D100	DIODE D664	1100114	12
1	SW1	DIP SWITCH PACKAGE	1211164-04	13
11	R2, R5, R6, R8, R19, R36, R82 THRU R66	RES 390 1/4W 5%	1300309	14
12	R1, R3, R4, R7, R20, R21, R23 THRU R26, R35, R37	RES 470 1/4W 5%	1300316	15
45	R22, R27, R29 THRU R34, R39 THRU R49, R52, R53, R55 THRU R60, R67 THRU R84	RES 1K 1/4W 5%	1300365	16
1	R28	RES 3.3K 1/4W 5%	1300439	17
1	R61	RES 22K 1/4W 5%	1301808	18
2	R50, R51	RES 27 1/4W 5%	1301522	19
5	R9, R10, R13, R14, R15	RES PACK 390 OHM	1312114-00	20
5	R11, R12, R15, R16, R17	RES PACK 470 OHM	1312114-01	21
2	R38, R54	RES 150 1/4W 5%	1300250	22
1	Q1	TRANSISTOR DEC 3009B	1503100	23
15	F81 THRU F815	FERRITE BEAD CHOKE	1611257-01	24
1	E2	20 MHZ X'TAL OSC	1811660-00	25
6	E1, E10, E17, E26, E29, E46	IC DEC 74500	1910532	26
1	E33	IC DEC 7402	1909004	27
8	E3, E23, E25, E40, E56, E70, E82, E89	IC DEC 74504	1910534	28
3	E47, E65, E74	IC DEC 7408	1910155	29
3	E24, E64, E90	IC DEC 74510	1910536	30
2	E13, E28	IC DEC 74511	1910537	31
1	E60	IC DEC 7412	1909955	32
3	E44, E49, E52	IC DEC 7417	1909929	33
2	E110, E102	IC DEC 74H2i	1909058	34
1	E54	IC DEC 7430	1905578	35
3	E41, E43, E66	IC DEC 7432	1911521	36
1	E14	IC DEC 7437	1910091	37
2	E18, E32	IC DEC 74540	1910541	38
1	E71	IC DEC 7442	1910046	39
2	E4, E6	IC DEC 74551	1911712	40
4	E8, E15, E21, E34	IC DEC 74574	1910544	41
3	E85, E101, E113	IC DEC 7483	1909932	42
3	E19, E22, E27	IC DEC 74120	1911314	43
1	E7	IC DEC 74123	1910436	44
2	E20, E58	IC DEC 74S139	1911676	45
2	E48, E59	IC DEC 74151	1909936	46
1	E45	IC DEC 74153	1902937	47

IC	QTY	LOC	IC TYPE	QTY	LOC
IC 74157	8	16			
74S159	8	16			
74163	8	16			
74S175	8	16			
74S194	8	16	1024 BIT ROM	8	16
380	1	8	7442	8	16
8097	8	16	74B3	12	5
8235	8	16	74120	8	16
8234	8	16	74123	8	16
8271	8	16	74S157	8	16
74173-1	8	16	74151	8	16
256 BIT ROM	8	16	74153	8	16
IC TYPE	GND	+5V	IC TYPE	GND	+5V



SEMICONDUCTOR CONVERSION CHART

DEC NO. EIA NO. DEC NO. EIA NO.

REVISIONS

DATE

DRG. M.B.

DATE

CHK. M.B.

DATE

ENGR. M.B.

DATE

PROD. M.B.

DATE

NEXT HIGHER ASSY

A-PL-KK8-A-0

SCALE

SHEET 1 OF 10

REV. F

SIZE CODE DCS

NUMBER M8315-0-1

digital

TITLE HEX OMNIBUS CPU

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1	E51	IC DEC 74157	1910096	40
7	E00, E70, E00, E06, E107, E100, E115	IC DEC 74S150	1910040	40
3	E75, E93, E94	IC DEC 74103	1911713	50
2	E5, E42	IC DEC 74S175	1910067	51
0	E0, E11, E12, E10, E01, E00, E100, E114	IC DEC 74S104	1910992	52
1	E30	IC DEC 300	1909485	53
2	E07, E73	IC DEC 0003	1910037	54
4	E77, E04, E104, E105	IC DEC 0007	1911527	55
3	E00, E00, E90	IC DEC 0234	1911315	56
3	E01, E07, E92	IC DEC 0235	1909035	57
1	E31	IC DEC 0271	1909015	58
5	E30, E35, E37, E30, E95	IC DEC 0001	1900705	59
11	E50, E02, E03, E01, E00, E07, E100, E100, E100, E111, E112	IC DEC 74173-1	1911330-01	60
1	E60	256 BIT ROM (A)	23078A1	61
1	E57	256 BIT ROM (B)	23077A1	62
1	E70	256 BIT ROM (C)	23078A1	63
1	E72	256 BIT ROM (D)	23075A1	64
1	E70	256 BIT ROM (E)	23074A1	65
1	E63	256 BIT ROM (H)	23073A1	66
1	E30	256 BIT ROM (J)	23070A1	67
1	E53	1024 BIT ROM (F)	23000A2	68
WIRE		30-NO-ORGN	24-00-00-00	00

SWITCH SELECTION CHART
(FOR AUTO RESTART LOCATION)

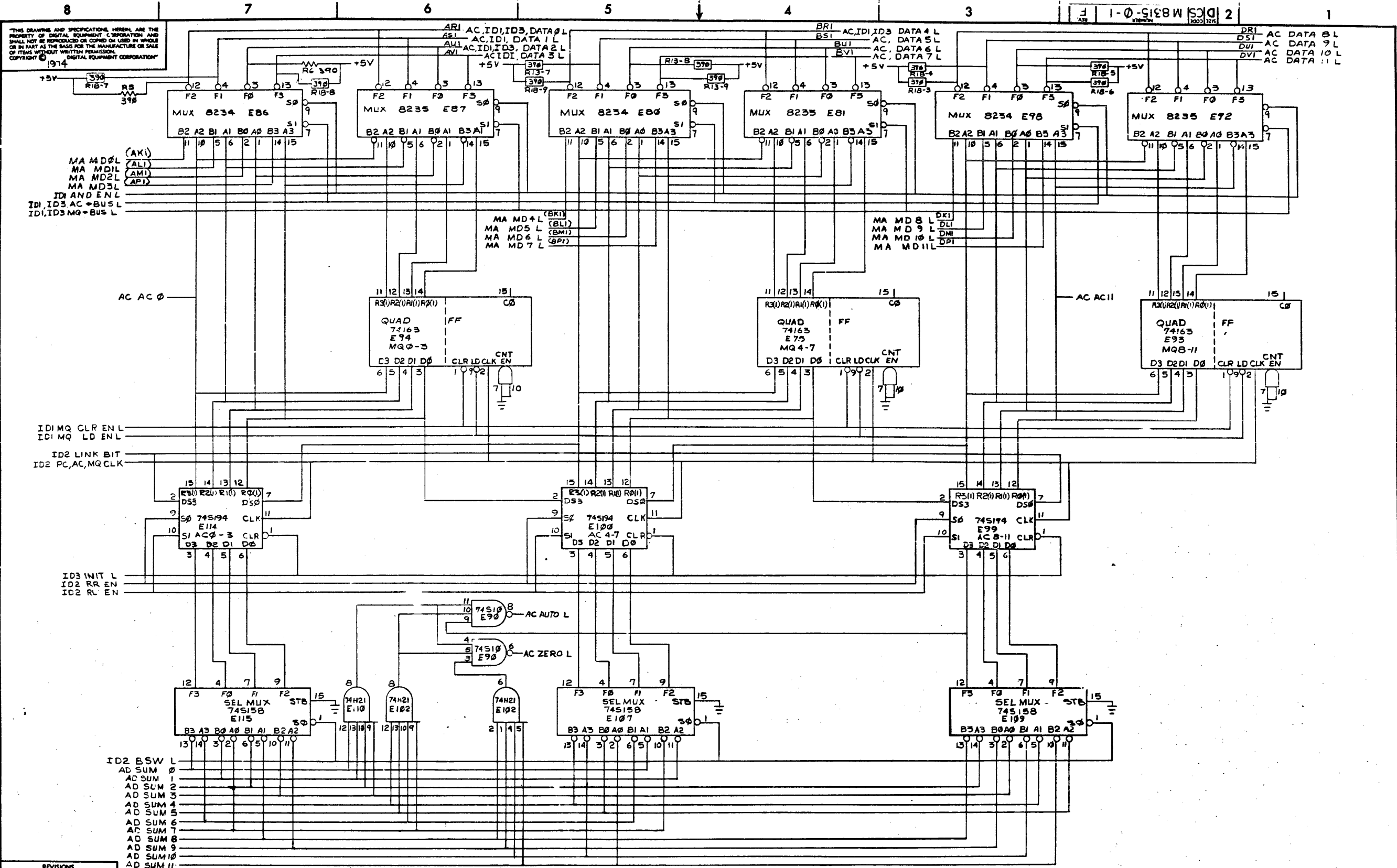
SW1-	1	FIELD 7
	2	4000
	3	2000
	4	1000
	5	400
	6	200
	7	OFF (DISABLES AUTO RESTART)
	8	OFF FOR NORMAL OPERATION

ONLY ONE SWITCH MAY BE CLOSED AT A TIME.

COMPONENT SUBSTITUTION CHART

PART CALLED FOR			SUBSTITUTE PART		
QTY	PART NO	DESC	QTY	PART NO	DESC
1	9901485	IC 380	1	710332	5580
			1	701771	5780
			1	710330	7380
			1	141142	8240

REVISIONS		
CHK	CHANGE NO.	REV.

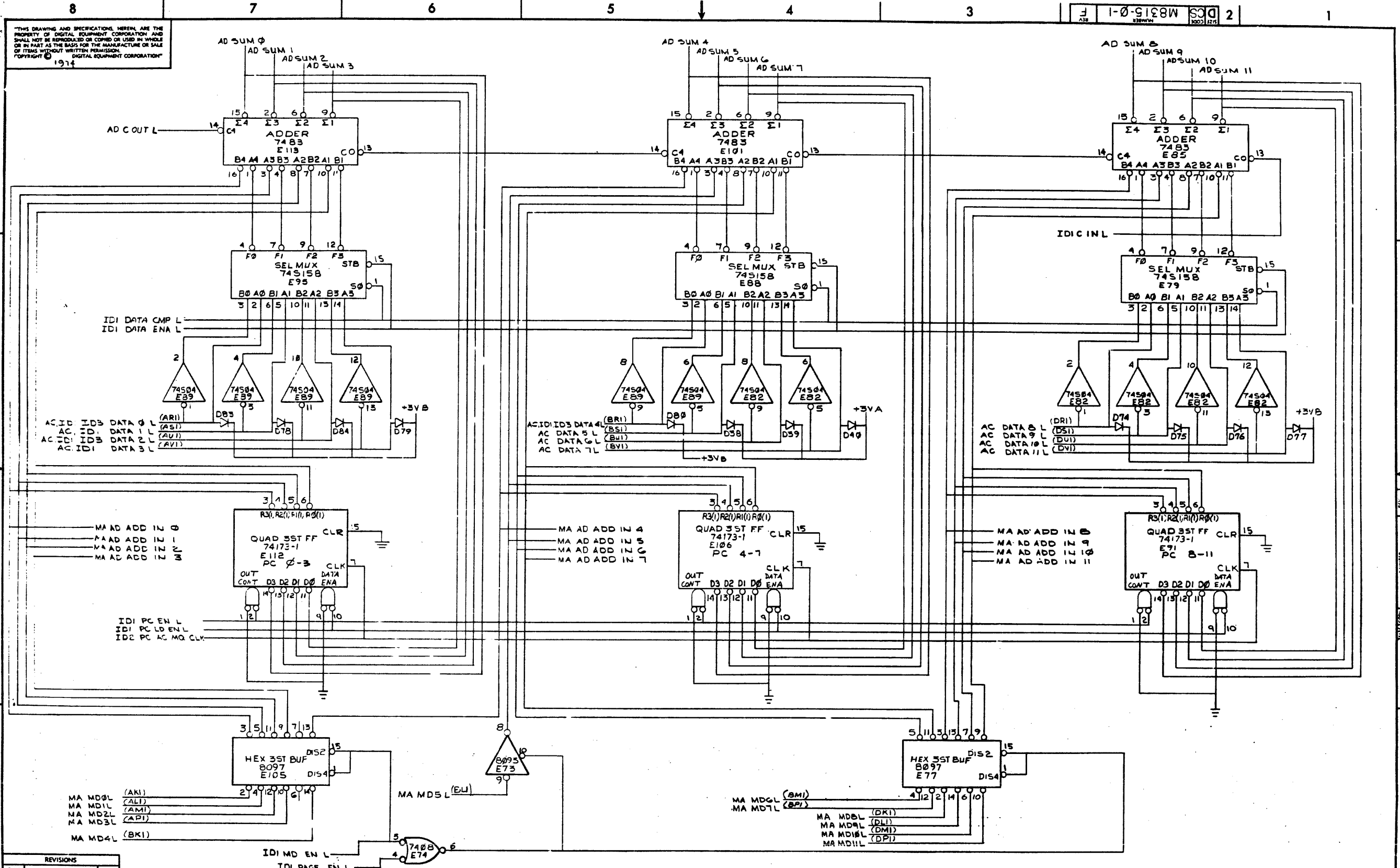


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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8A CPU	SIZE CODE	(AC)	NUMBER	D CS M8315-0-1	REV.	F
SCALE	→	SHEET	3 OF 10	DIST.			

D CS M8315-0-1



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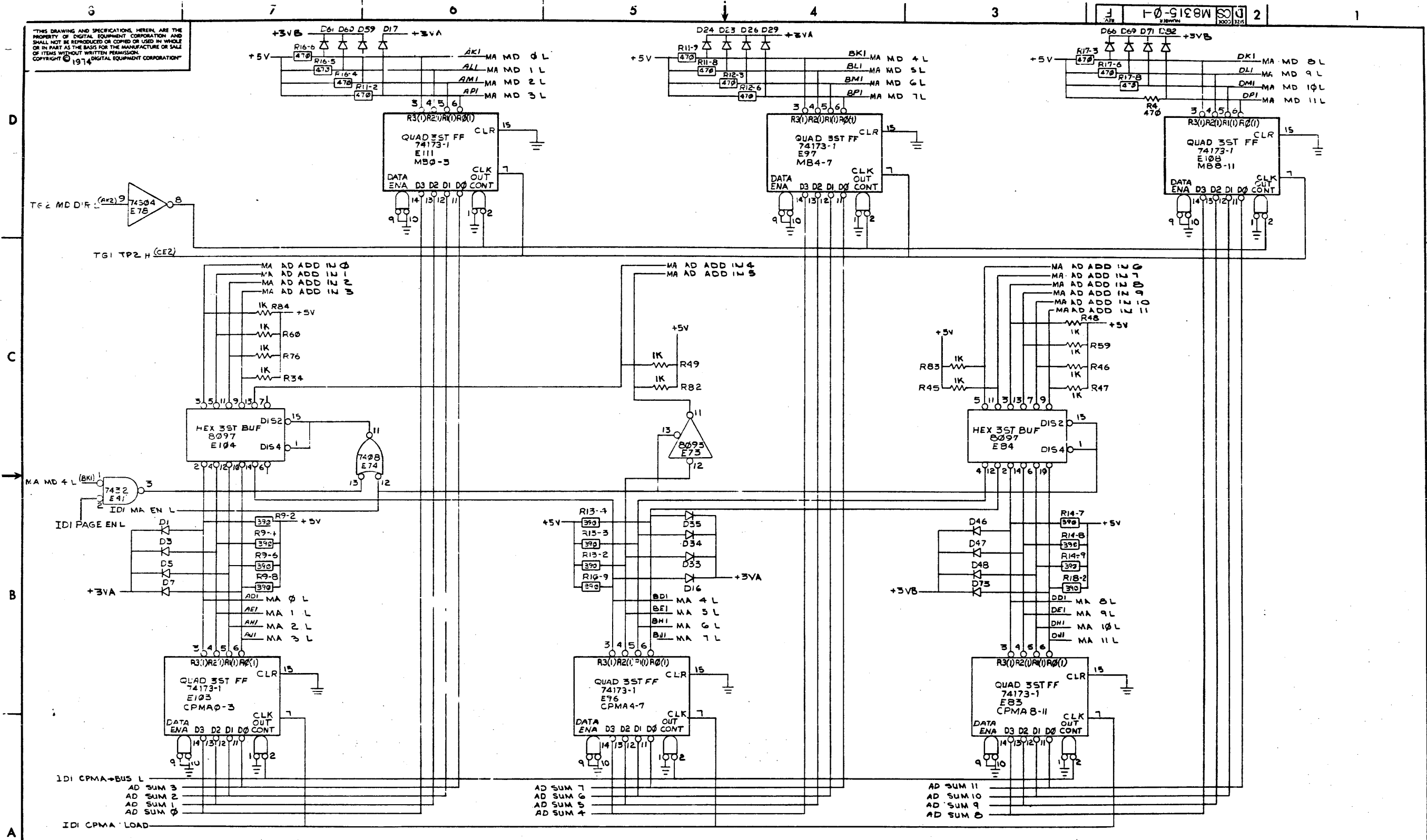
1-0-1 M8315-0-1 DCS 2

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8A CPU	SIZE CODE	(AD) DCS	NUMBER	M8315-0-1	REV.	F
SCALE		SHEET	4	OF 10			

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DCS M8315-0-1

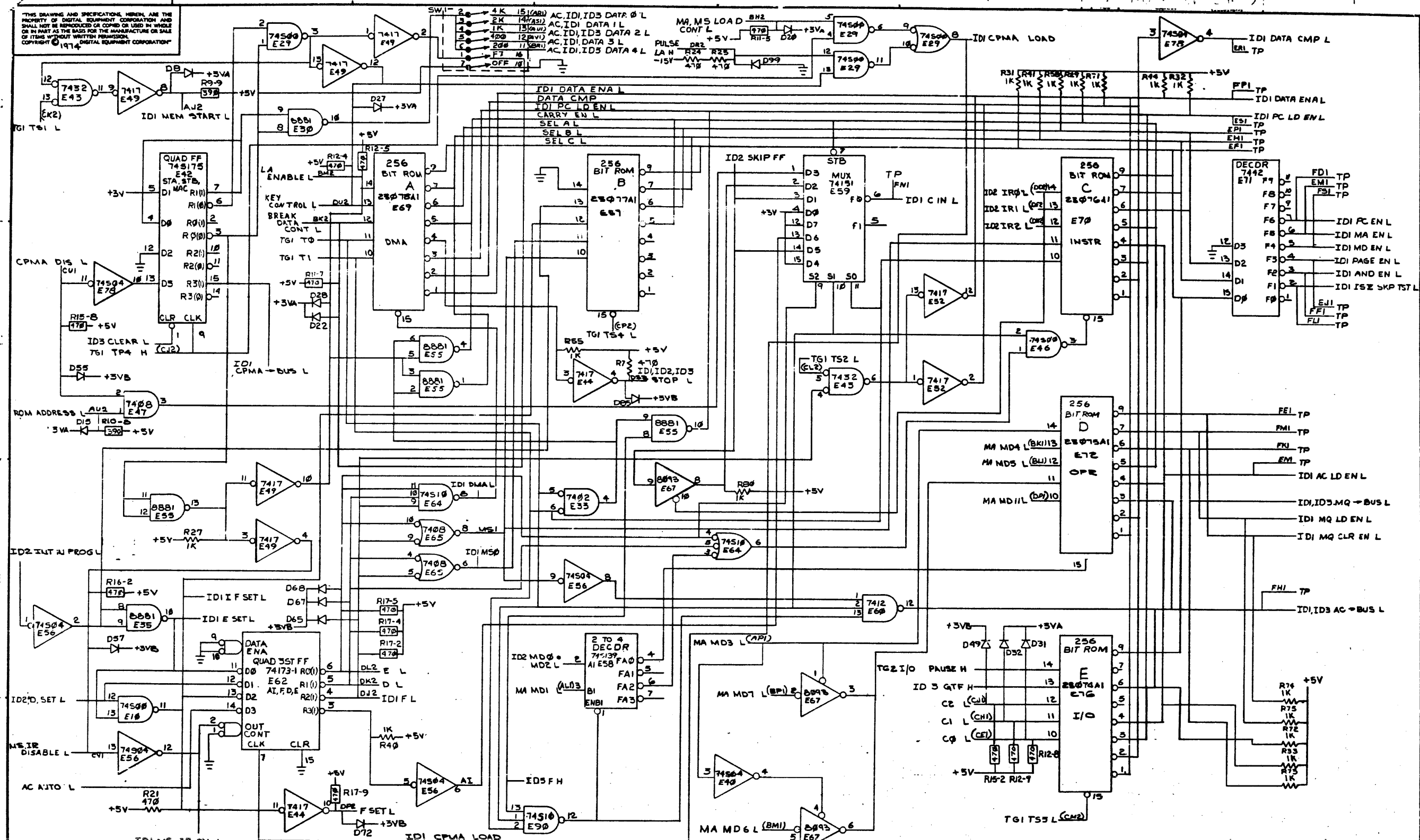


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE PDP8A CPU (MA) SIZE CODE DCS NUMBER M8315-0-1 REV. F
SCALE / / SHEET 5 OF 10 DIST.

DCS M8315-0-1

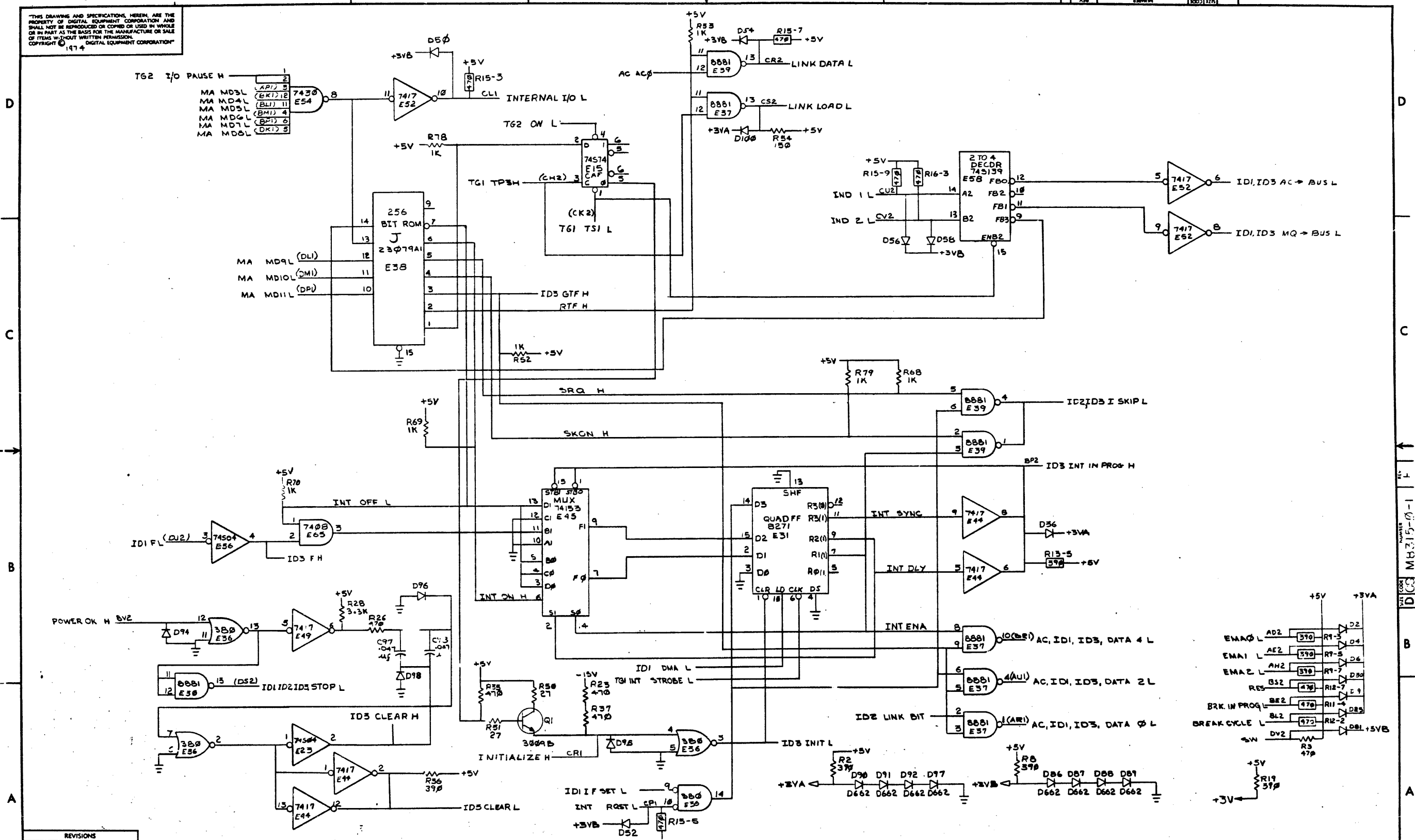
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REV. NO.	CHANGE NO.	REV.
1		

TITLE	PDP8A CPU (DD)	SIZE CODE	D/CS	NUMBER	M8315-0-1	REV.	F
SCALE	SHEET 6 OF 10		DIST.				

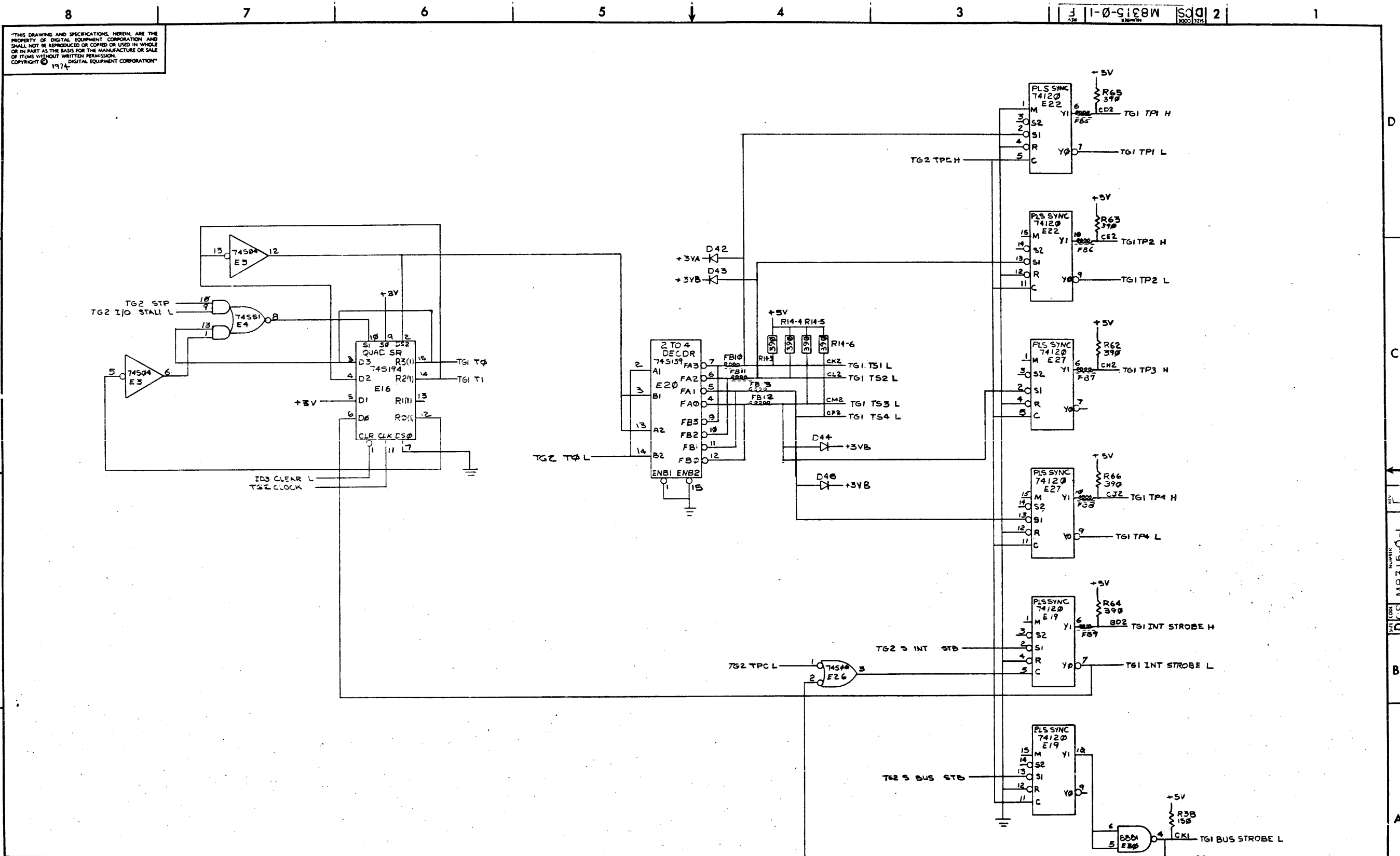
THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. DIGITAL EQUIPMENT CORPORATION. COPYRIGHT © 1974



REVISIONS table with columns: CHK, CHANGE NO., REV.

Metadata table with columns: TITLE (PDP8A-CPU), SIZE CODE (D3), NUMBER (M8315-0-1), REV. (F), SHEET 8 OF 10.

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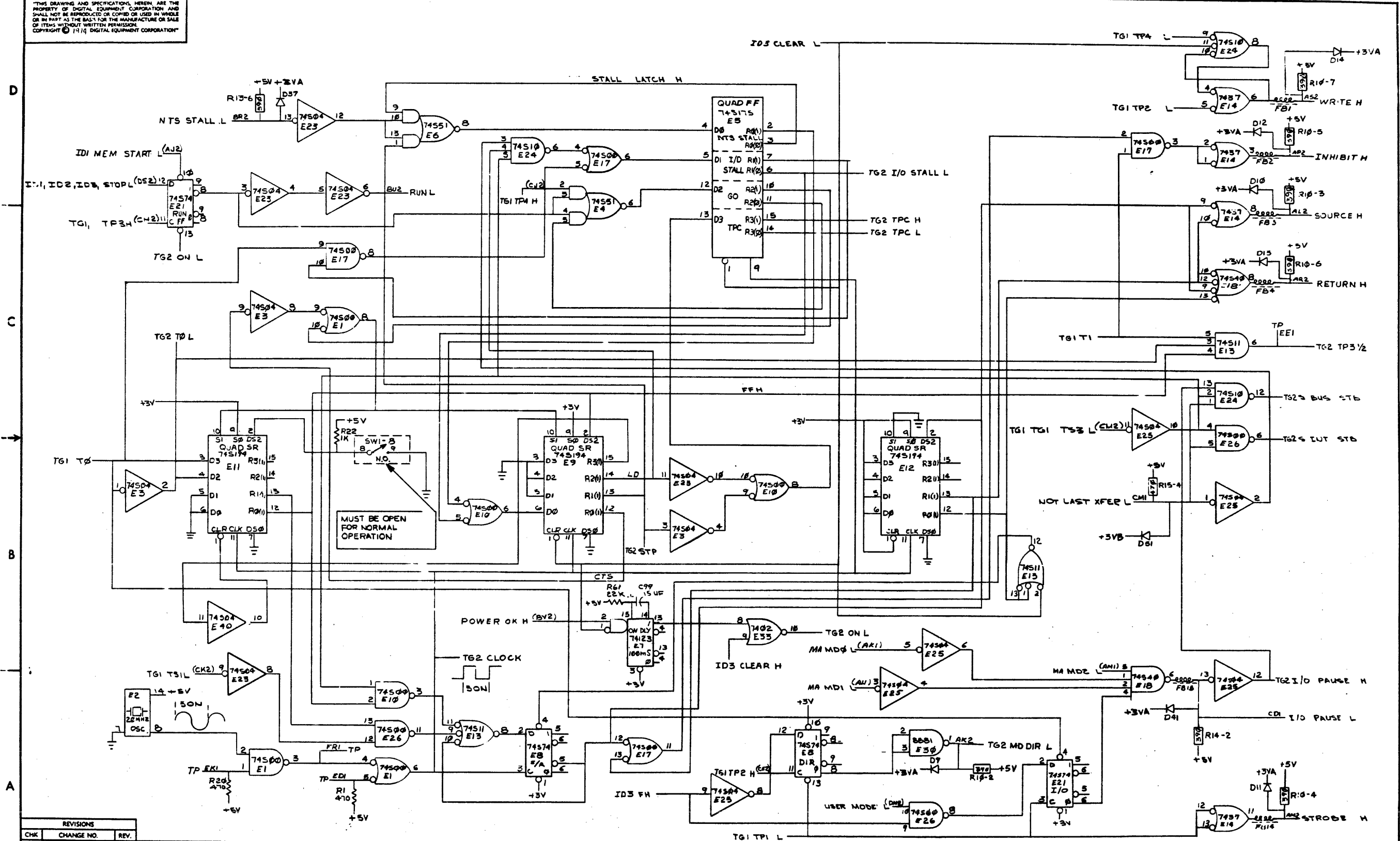


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8A CPU (TGD)	SIZE CODE	DCS	NUMBER	M8315-0-1	REV.	F
SCALE	1:1	SHEET	9	OF 10	DIST.		

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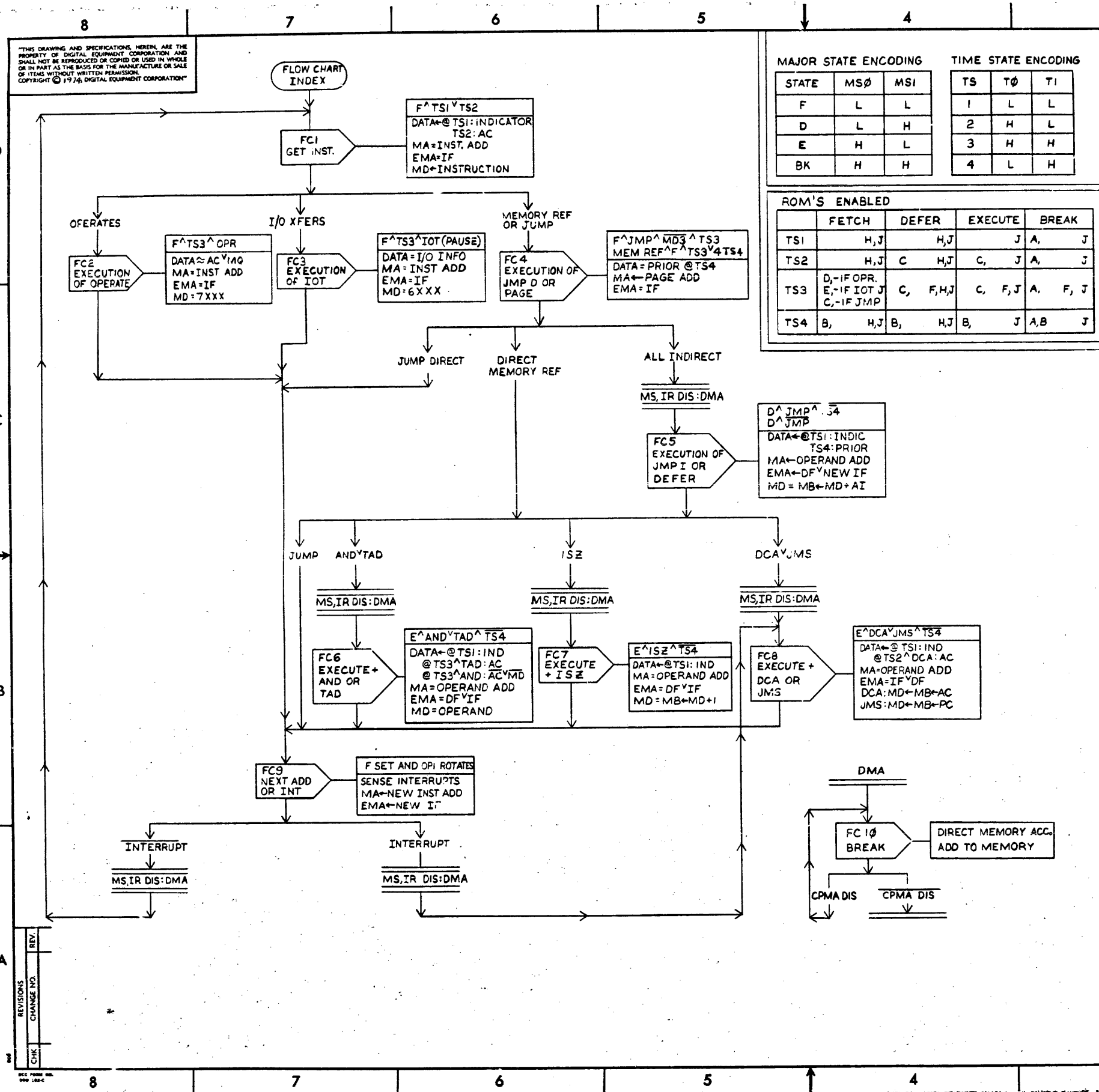
DCS M8315-0-1
REV. 11/74



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8A CPU (TG2)	SIZE CODE	DCS	NUMBER	M8315-0-1	REV.	F
SCALE	1:1	SHEET	10	OF	10	DIST.	

DCS M8315-0-1



NOTES:
 THIS IS AN INDEX TO THE 8A FLOW CHARTS.
 THE FLOW CHART NUMBER THAT APPEARS WITHIN THE SYMBOL [FCX] REFERS TO ANOTHER FLOW WHICH DETAILS THE ACTION WHICH IS BRIEFLY DESCRIBED IN THE SYMBOL []
 OPTION FLOW CHARTS WILL USE THE SAME FCX TIME REFERENCE TO SHOW ITS RELATION TO THE CPU
 FLOWS WILL BE NUMBERED AS FOLLOWS
 M8315-FCX CPU FLOW FOR TIME X
 MABCD-FCX OPTION FLOW FOR CPU TIME X

THE FOLLOWING IS A LIST OF MAJOR OMNIBUS SIGNALS AND THE FLOW CHARTS MOST PERTINENT TO THEM

BUS SIGNAL	FLOW CHARTS	MOST IMPORTANT LOGIC PRINTS
IR \emptyset -2	FC1	ID2
F, D, E	(FC1, FC4), FC5, FC8	ID1
USER MODE	FC2, FC3	ID2, T62
F SET	FC8	ID1
PULSE LA	FC1 \emptyset	ID1
STOP	FC2, FC1 \emptyset	ID1, ID3, T62
KEY CONTROL	FC1 \emptyset	ID1
SW	SEE M8317 TIMING & FLOW CHARTS	
I/O PAUSE	FC3	T62
C \emptyset -2	FC3	ID1
BUS STB	FC3	ID2, T61
NOT LAST XFER	FC3	T62
INT RQST	FC3	ID3
SKIP	FC7, FC8, FC9	ID2
INITIALIZE	FC3	ID3
CPMA DIS	FC4, FC5, FC9	ID1
MSIR DIS	FC1 \emptyset	ID1
LK LD & DATA	FC3	ID2, ID3
INDI-2	FC1	ID3
MAMS LD CTRL	FC4, FC9, FC1 \emptyset	ID1
OVERFLOW	FC7	ID2
BK DATA CTRL	FC1 \emptyset	ID1
LA ENABLE	FC1, FC1 \emptyset	ID1
INT IN PROG	FC9	ID2, ID3
RUN	FC2, FC1 \emptyset	T62
PWR OK		ID3
MEM START	FC1 \emptyset	ID1, T62

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A				
DIMENSIONAL TOLERANCE				
DIMENSIONS IN INCHES		DRN	DATE	
UNLESS OTHERWISE SPECIFIED		CHK	DATE	
MILLIMETERS	INCHES	ENG	DATE	
X.XX ±0.10	.XX ±0.008	PROJ. ENG.	DATE	
X.X ±0.05	.X ±0.02	PROD.	DATE	
X ±0.2	.X ±0.1		DATE	
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.		
MATERIAL	FINISH	SCALE	SIZE CODE	NUMBER
		B-DD-KK8A- \emptyset	DFD	M8315- \emptyset -16
		SHEET 1 OF 1	DIST.	

digital
 TITLE
 FLOW DIAGRAM
 M8315 INDEX

REV. CHANGE NO.
 CHK

DATE CODE NUMBER
 DFD M8315- \emptyset -16

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DATE CODE DFD M8315-0-17 2

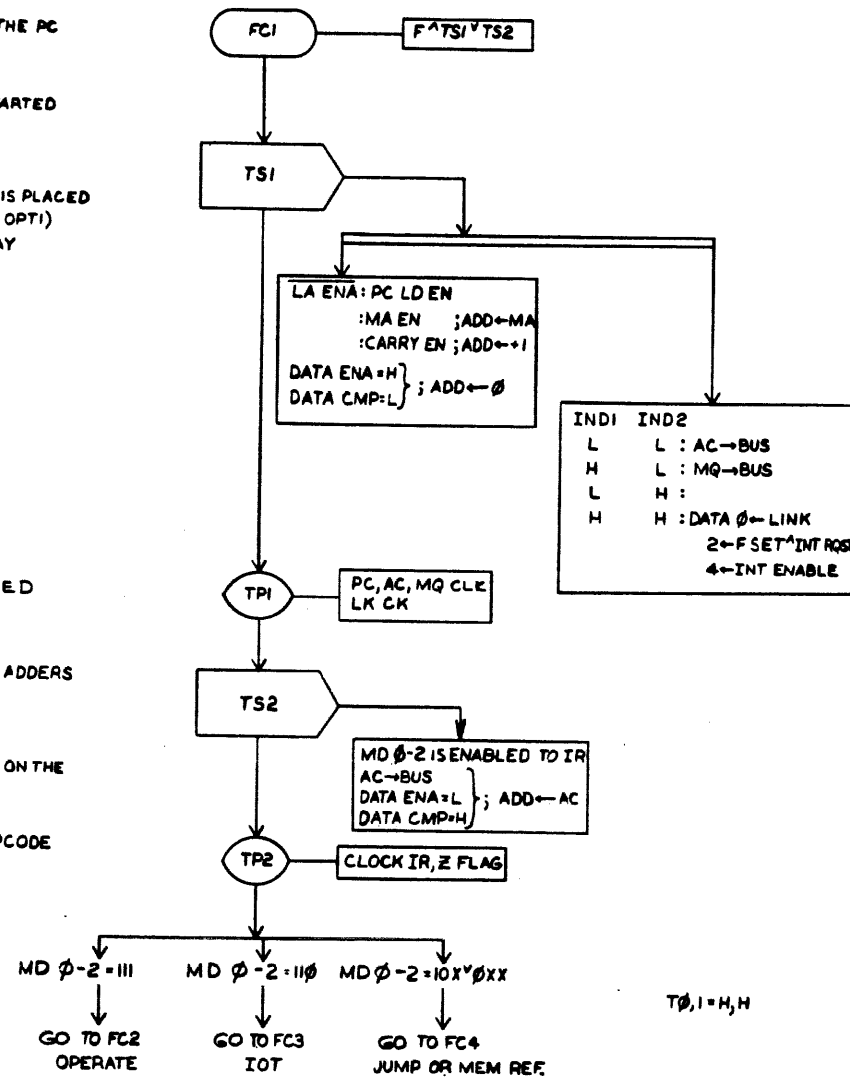
MA+1 IS ENABLED TO THE PC
 A MEMORY READ IS STARTED
 (REFER TO TIMING)
 INDICATOR INFORMATION IS PLACED
 ON THE DATA BUS (REFER TO OPT1)
 FOR THE PANEL TO DISPLAY

THE PC IS LOADED

THE AC IS GATED THROUGH THE ADDERS
 TO SEE IF IT EQUALS 0

THE INSTRUCTION WILL APPEAR ON THE
 MD LINES FROM MEMORY

THE IR GETS LOADED WITH THE OPCODE
 AND THE Z FLAG IS ADJUSTED



THE INSTRUCTION IS DECODED AT THIS POINT AS FOLLOWS:

MD 0 1 2 3 4 5 6 7 8 9 10 11
 AND 0 0 0
 TAD 0 0 1
 ISE 0 1 0
 DCA 0 1 1
 JMS 1 0 0
 JMP 1 0 1
 IOT 1 1 0
 OPR 1 1 1

BITS 3-11
 ARE NOT
 IMPORTANT AT
 THIS TIME

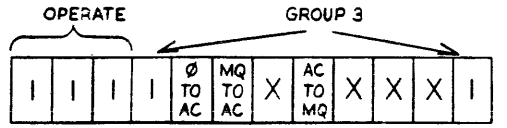
FIRST USED ON OPTION/MODEL				QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A							
DIMENSIONAL TOLERANCE				PARTS LIST			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED				DRN <i>R. Vowler</i>	DATE 11-13-74		
MILLIMETERS				CHK'D <i>[Signature]</i>	DATE 12/3/74		
THIRD ANGLE PROJECTION				ENG <i>[Signature]</i>	DATE 1-24-75		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				PRG. ENG. <i>[Signature]</i>	DATE 1-24-75		
MATERIAL				NEXT HIGHER ASSY.		TITLE	
FINISH				B-DD-KK8A-0		FLOW DIAGRAM M8315 FC1	
SCALE				SIZE CODE		NUMBER	
SHEET 1 OF 1				DFD M8315-0-17		REV.	

REV. NO. 1
 CHG. NO. 1
 REVISIONS

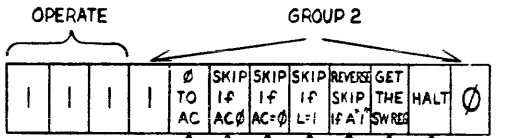
DATE CODE DFD M8315-0-17

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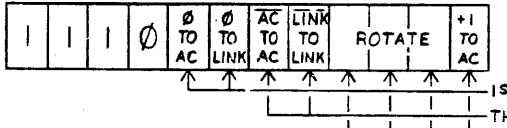
THE INSTRUCTION IS DECODED AT THIS POINT AS FOLLOWS
THE SEQUENCES OF OPERATION ARE LOGICAL NOT CHRONOLOGICAL
ALL OP2 & OP3 OCCUR AT TP3
ALL OP1 EXCEPT ROTATE LEFT OR RIGHT OCCUR AT TP3
A SINGLE LEFT OR RIGHT ROTATE OCCURS AT TP4
A DOUBLE LEFT OR RIGHT ROTATE OCCURS AT TP3 1/2 AND 4 } SEE FLOW CHART 9 F SET



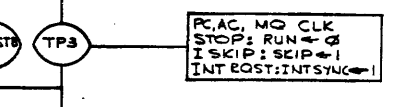
- NO OPERATION
- AC GOES TO THE MQ AND THE AC IS CLEARED
- MQ "ORED" WITH THE AC GOES TO THE AC
- AC & MQ SWAPS WITH MQ & AC
- THE AC IS CLEARED
- BOTH THE AC AND MQ ARE CLEARED
- THE MQ GOES TO THE AC
- THE MQ GOES TO THE AC AND THE MQ IS CLEARED



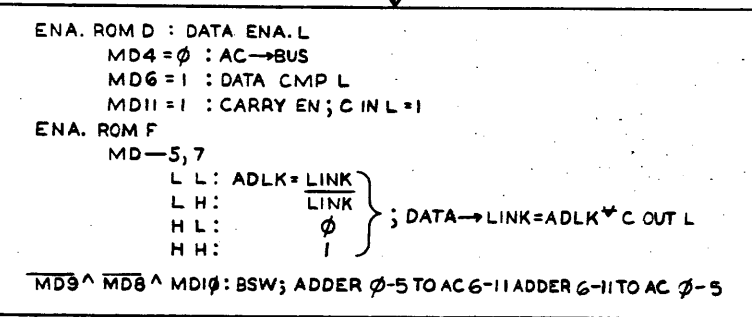
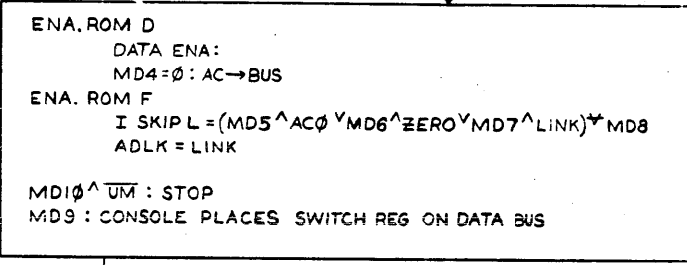
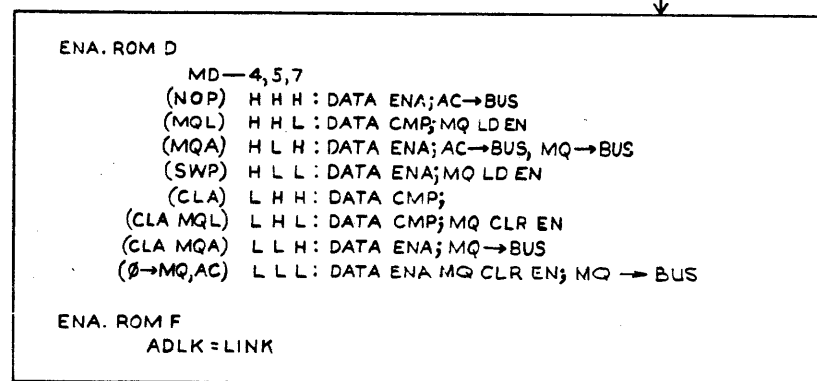
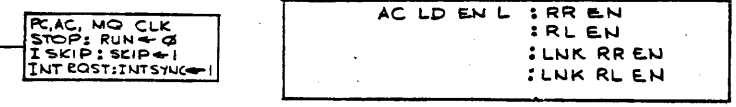
- 1ST MAKE A SKIP DECISION
- THEN REVERSE THEN DECISION IF BIT 8 = 1
- THEN CLEAR THE AC IF BIT 4 = 1
- THEN "OR" THE AC WITH THE SWITCHES IF BIT 9 = 1
- THEN STOP IF BIT 10 = 1



- 1ST CLEAR THE AC & LINK IF BITS = 1
 - THEN COMPLEMENT IF BITS = 1
 - THEN INCREMENT THE L, AC IF BIT 11 = 1
 - THEN ROTATE DEPENDENT UPON 8, 9, 10 AS FOLLOWS
- MD 8 9 10
- H H H NO ROTATE
 - H H L SWAP AC & SW WITH AC & L
 - H L H ROTATE LEFT ONCE
 - H L L ROTATE LEFT TWICE
 - L H H ROTATE RIGHT ONCE
 - L H L ROTATE RIGHT TWICE
 - L L X ILLEGAL
- THESE OCCUR AT F SET TIME



GO TO FC9



EXECUTION OF AN OPERATE

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A				
DIMENSIONAL TOLERANCE				
DIMENSIONS ARE UNLESS OTHERWISE SPECIFIED		MILLIMETERS	INCHES	ANGLES
X, XX ±0.10	J, XX ±0.206			40° 30'
XX ±0.5	J, X ±0.02			
X ±2	X ±2.1			
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			
	MATERIAL			
	FINISH			

digital

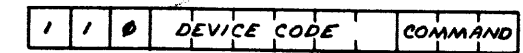
TITLE: FLOW DIAGRAM M8315 FC2

SIZE CODE: D FD NUMBER: M8315-Q-18 REV: 1

NUMBER DFCM 8315-Q-18

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THE INSTRUCTION AT THIS POINT IS DECODED AS FOLLOWS: ONLY IF USER MODE IS NEGATED THUS ALLOWING PAUSE TO BE ASSERTED.



FOR DEVICE CODE 000 THE CPU TAKES CONTROL DEPENDING UPON THE COMMAND AS FOLLOWS:

MD-9	10	11	
SKON	0 0 0		SKIP IF INT ON, TURN IT OFF
ION	0 0 1		TURN INT SYS ON
IOF	0 1 0		TURN INT SYS OFF
SRQ	0 1 1		SKIP IF INT RQST
*GTF	1 0 0		LINK, INT ON, INT RQST TO AC0, 2, 4
*RTF	1 0 1		AC0 TO LINK, TURN INT SYS ON
NOP	1 1 0		NO OPERATION
CAF	1 1 1		GENERATE INITIALIZE.

* ALSO SEE OPT 2

OTHER DEVICES SEND OR RECEIVE DATA DEPENDING UPON THE C LINES AS FOLLOWS:

CO	C1	C2	
AC-DEV	H H H		THE DEVICE RECEIVES THE AC AT TP3
RELATIVE JUMP	H H L		THE DATA LINES+THE PC GO TO PC AT BUS STB
INPUT OR TO AC	H L H		THE AC'ORED' WITH DATA LINES GOES TO THE AC BUS STB
ABSOLUTE JUMP	H L L		THE DATA LINES GO TO THE PC AT BUS STB
AC-DEV 0-AC	L H H		THE DEVICE RECEIVES THE AC AT TP3 AND THE AC IS CLEARED
INPUT JAM TO AC	L L H		THE DATA LINES GO TO THE AC BUS STB

NOTE ALL I/O XFEARS TAKE PLACE OVER THE DATA LINES.

IN REALITY ALL XFEARS TAKE PLACE ON THE LEADING EDGE OF BUS STB IN ACCORDANCE WITH THE C LINES AT THAT TIME. ASSERTING NOT LAST XFER CAUSES THE CPU TO WAIT FOR A BUS STROBE TO DO THE NEXT XFER. THE CPU WILL NOT ADVANCE TO TS4 UNTIL IT SEES A BUS STROBE WITH NOT LAST XFER NEGATED - THIS IN TURN CAUSES INTERRUPT STROBE.

LINK LOAD SHOULD BE GIVEN IN SYNC WITH BUS STROBE AND CAUSES LINK DATA TO GO TO THE LINK.

FC3 F^TS3^MD0-2=110

MS0,1=L,L

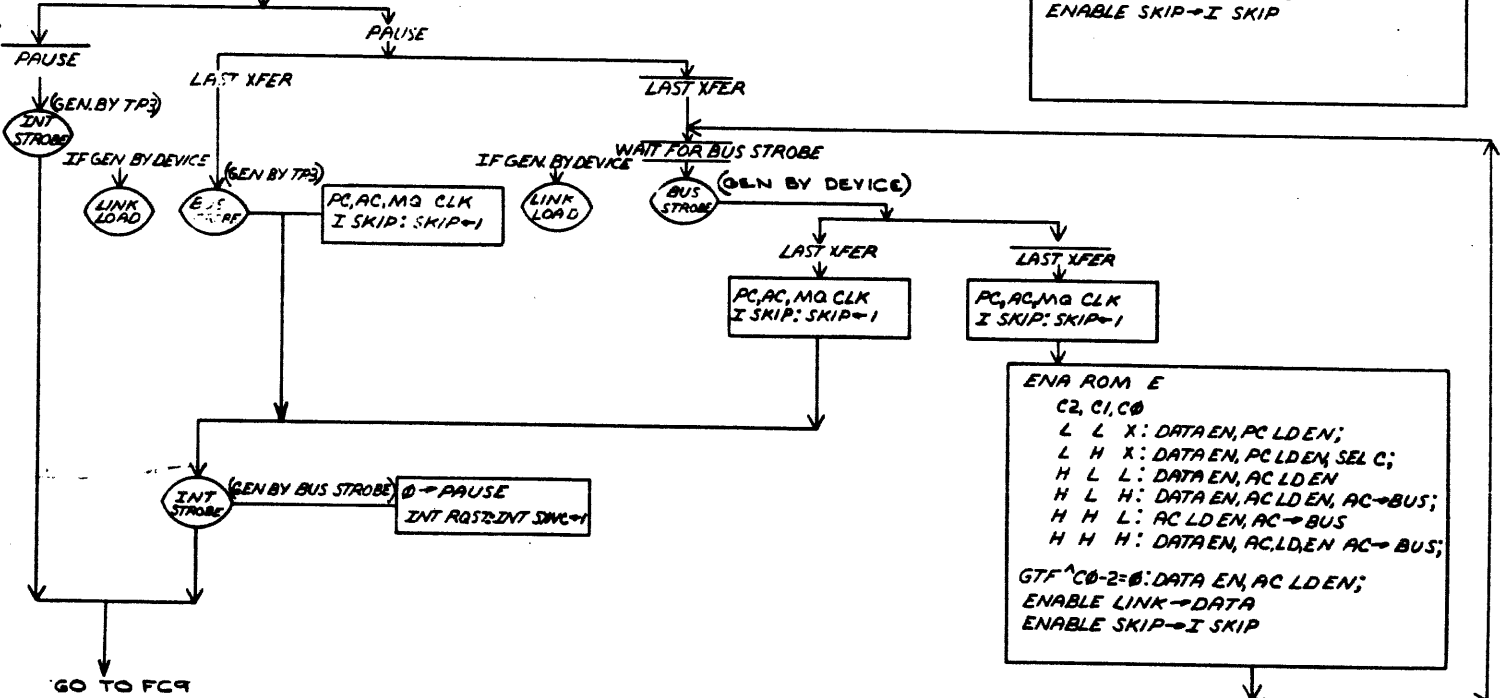
TS3

PAUSE
DO NOTHING

T0,1=H,H

ENA ROM J
MD-9,10,11
L L L: CAF=1; CAF
L H L: LINK DATA-AC0, EN LNK LD, INT ENA=1; RTF
L H H: DATA 4-INT ENA, DATA 2-INT RQST, DATA 0-LINK; GTF
H L L: I SKIP-INT RQST; SRQ
H L H: INT DLY=0; IOF
H H L: INT ENA=1; ION
H H H: I SKIP-INT ENA, INT DLY=0; SKON

ENA ROM E
C2, C1, C0
L L X: DATA EN, PC LDEN;
L H X: DATA EN, PC LDEN, SEL C;
H L L: DATA EN, AC LDEN;
H L H: DATA EN, AC LDEN, AC-BUS;
H H L: AC LDEN, AC-BUS;
H H H: DATA EN AC LDEN, AC-BUS;
GTF^C0-2=0: DATA EN, AC LDEN;
ENABLE LINK DATA- LINK
ENABLE SKIP-I SKIP



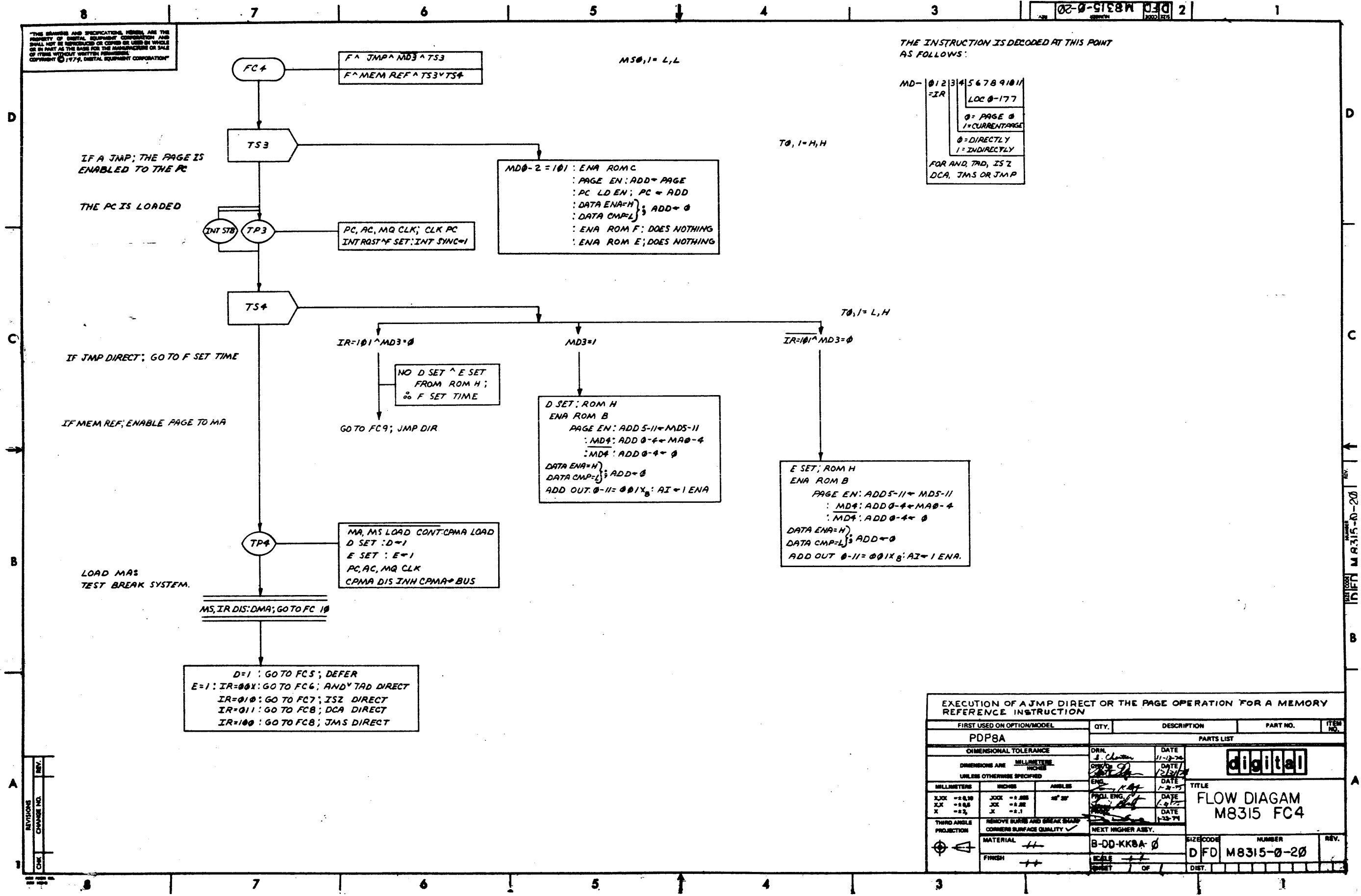
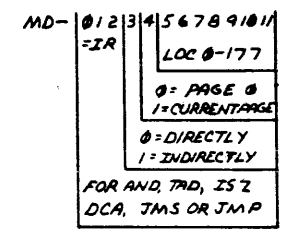
REV.	CHG.	NO.	DATE

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A					
DIMENSIONAL TOLERANCE		PARTS LIST			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		digital			
MILLIMETERS	INCHES	ANGLES	TITLE		
XJX ±0.10	JXX ±.008	± 30'	FLOW DIAGRAM		
XJ ±0.05	JX ±.005	X ±.1	M8315 FC3		
X ±.02			THIRD ANGLE PROJECTION		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		NEXT HIGHER ASSY.			
MATERIAL		B-DD-KK8A-0	SIZE CODE	NUMBER	REV.
FINISH		SCALE	DFD	M8315-0-19	
		SHEET 1 OF 1	DIST.		

DFD M8315-0-19

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THE INSTRUCTION IS DECODED AT THIS POINT AS FOLLOWS:



EXECUTION OF A JMP DIRECT OR THE PAGE OPERATION FOR A MEMORY REFERENCE INSTRUCTION			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDP8A			
DIMENSIONAL TOLERANCE			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		DATE 11-12-76	digital
MILLIMETERS INCHES ANGLES		DATE 12/17/74	
TITLE			
FLOW DIAGRAM M8315 FC4			
MATERIAL		SIZE/COOR	NUMBER
FINISH		B-DD-KK0A 0	D FD M8315-0-20
SCALE		1 OF 1	
REV.			

REVISIONS
 CHANGE NO. REV.
 1
 2

REV. M8315-0-20

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A MEMORY READ IS STARTED
INDICATOR INFORMATION IS PLACED ON DATA BUS

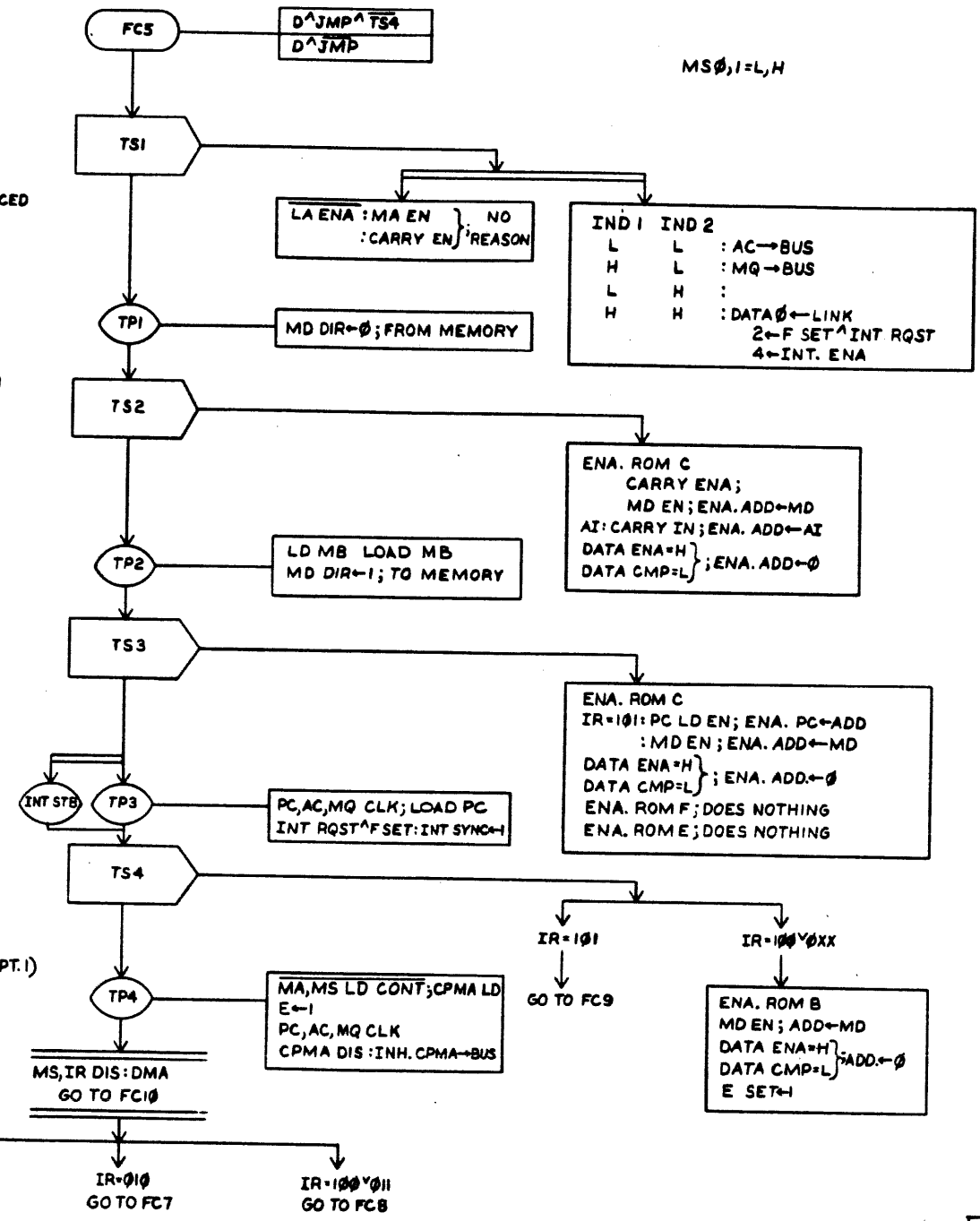
THE ADDRESS WILL APPEAR ON THE MD LINES
MEMORY DATA+AI GOES TO MEMORY BUFFER (AI=ADDRESS 0010-0017)

MEMORY BUFFER IS LOADED AND PLACED ON MD LINES

A MEMORY WRITE IS STARTED
IF JMP; ENABLE MD TO THE PC

IF JMP; LOAD THE PC

IF JMP; GO TO F SET TIME
IF JMP; GO TO E SET
MA=MD (PLACES THE OPERAND ADDRESS IN THE MA) (THE EMA LINES MAY HAVE CHANGED-SEE OPT.1)



T0,1=L,L

T0,1=H,L

T0,1=H,H

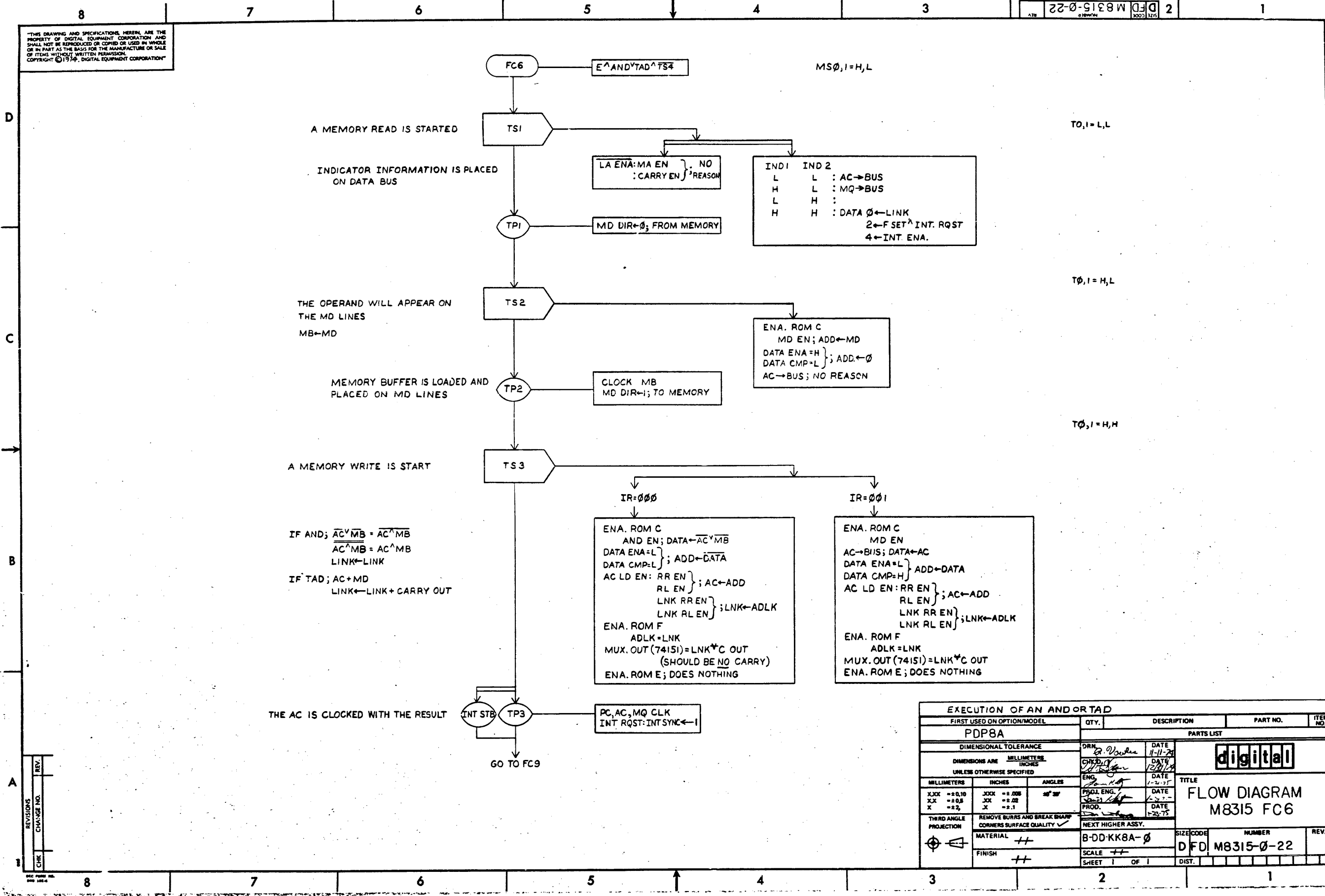
T0,1=L,H

MS0,1=L,H

FIRST USED ON OPTION/MODEL				QTY.	DESCRIPTION	PART NO.	REV. NO.
PDP8A							
DIMENSIONAL TOLERANCE				PARTS LIST			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED				digital			
MILLIMETERS	INCHES	ANGLES	DATE	TITLE			
±0.10	±0.005	±0.10	12-18-63	FLOW DIAGRAM			
±0.25	±0.010	±0.10	12-18-63	M8315 FC5			
±0.50	±0.020	±0.10	12-18-63	MATERIAL			
±1.00	±0.040	±0.10	12-18-63	FINISH			
THIRD ANGLE PROJECTION				NEXT HIGHER ASSY.			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				MATERIAL			
				B-DD-KK8A-0			
				SCALE			
				SHEET			
				OF			
				DST.			
				REV.			
				NUMBER			
				D F D M8315-0-21			
				SIZE CODE			
				REV.			

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22-0-22



EXECUTION OF AN AND OR TAD			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDP8A			
DIMENSIONAL TOLERANCE			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		DRN	DATE
		CHKD	DATE
		ENG	DATE
		PROL ENG.	DATE
		PROD.	DATE
THIRD ANGLE PROJECTION			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓			
NEXT HIGHER ASSY.			
MATERIAL		SIZE CODE	NUMBER
FINISH		SCALE	REV.
		SHEET	DIST.
		OF	

digital

TITLE: FLOW DIAGRAM M8315 FC6

SIZE CODE: D FD

NUMBER: M8315-0-22

REV. NUMBER M8315-0-22

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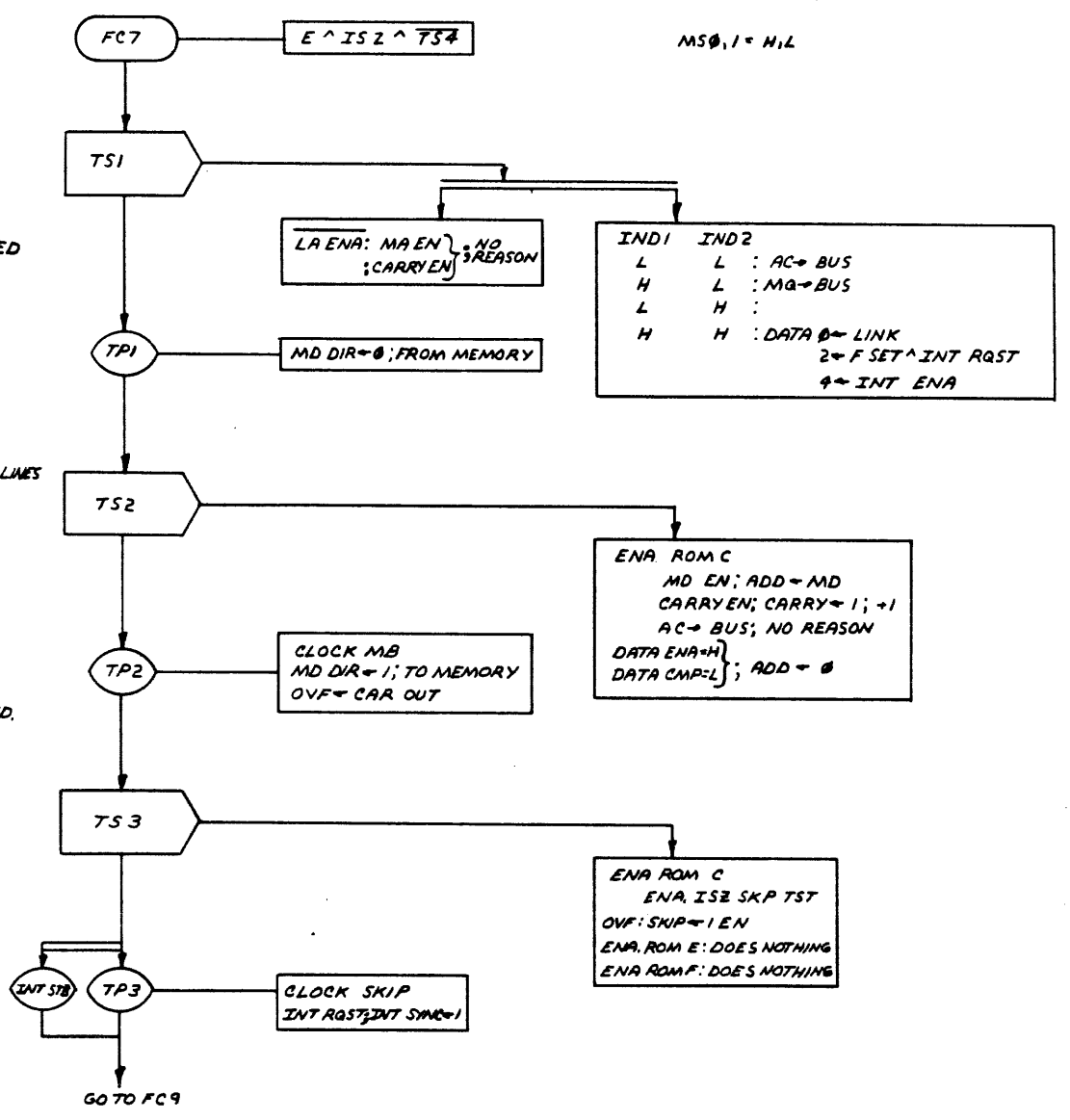
A MEMORY READ IS STARTED

INDICATOR INFORMATION IS PLACED ON DATA BUS

THE OPERAND WILL APPEAR ON THE MD LINES MB ← MD + 1

THE INCREMENTED MD IS SAVED IN THE MB AND PLACED ON THE MD LINES; THE CARRY IS SAVED.

SET SKIP = OVER FLOW



MS0,1 = H,L

T0,1 = L,L

T0,1 = H,L

T0,1 = H,H

LA ENA: MA EN } NO
; CARRY EN } REASON

IND1 IND2
L L : AC → BUS
H L : MA → BUS
L H :
H H : DATA ← LINK
2 ← F SET ^ INT RQST
4 ← INT ENA

ENABLER ROM C
MD EN: ADD ← MD
CARRY EN; CARRY ← 1; +1
AC → BUS; NO REASON
DATA ENA = H }
DATA CMP = L } ; ADD ← 0

ENABLER ROM C
ENA, ISZ SKP TST
OVF: SKIP ← 1 EN
ENA, ROM E: DOES NOTHING
ENA, ROM F: DOES NOTHING

CLOCK MB
MD DIR ← 1; TO MEMORY
OVF ← CAR OUT

CLOCK SKIP
INT RQST; INT SYNC ← 1

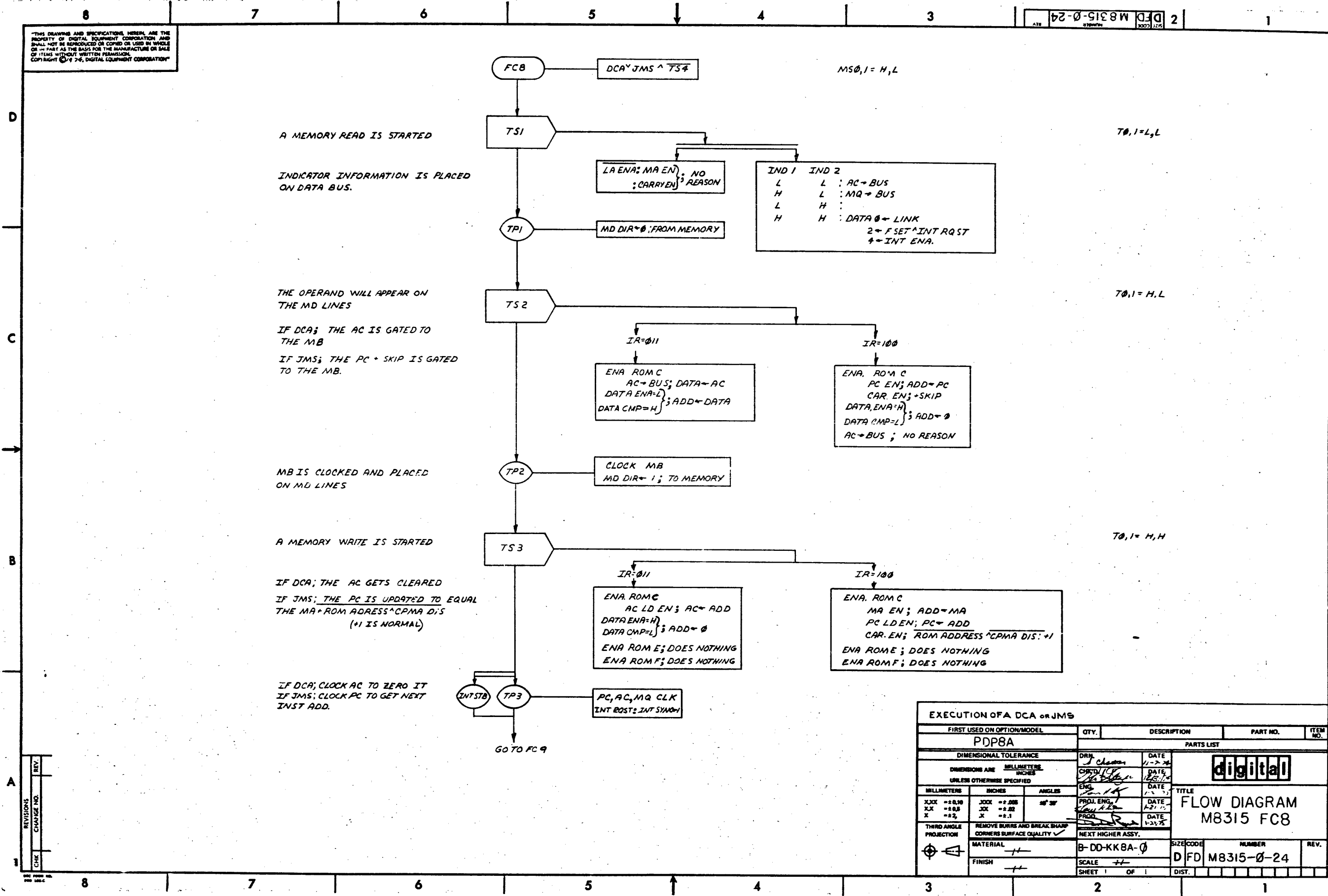
EXECUTION OF AN ISZ				QTY.	DESCRIPTION	PART NO.	ITEM NO.
FIRST USED ON OPTION/MODEL							
POP8A							
DIMENSIONAL TOLERANCE				DRN.	DATE	PARTS LIST	
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED				CHKD.	DATE	digital	
MILLIMETERS				ENG.	DATE	TITLE	
XXX ±0.10	XXX ±0.08	XXX ±0.05	PROL. ENG.	DATE	FLOW DIAGRAM		
XX ±0.5	XX ±0.25	XX ±0.1	PROD.	DATE	M8315 FC7		
X ±2	X ±1	X ±0.5	PROD.	DATE	SIZE CODE		
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.	MATERIAL	B-00-KK8A-0	NUMBER	REV.	
			FINISH	SCALE	DFD	M8315-0-23	
				SHEET	OF	DIST.	

REVISIONS
NO. CHANGE NO. REV.

REV. 2 DFD M8315-0-23

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D FD M8315-0-24



A MEMORY READ IS STARTED
INDICATOR INFORMATION IS PLACED ON DATA BUS.

THE OPERAND WILL APPEAR ON THE MD LINES
IF DCA; THE AC IS GATED TO THE MB
IF JMS; THE PC + SKIP IS GATED TO THE MB.

MB IS CLOCKED AND PLACED ON MD LINES

A MEMORY WRITE IS STARTED
IF DCA; THE AC GETS CLEARED
IF JMS; THE PC IS UPDATED TO EQUAL THE MA + ROM ADDRESS ^ CPMA DIS (+1 IS NORMAL)

IF DCA; CLOCK AC TO ZERO IT
IF JMS; CLOCK PC TO GET NEXT INST ADD.

EXECUTION OF A DCA OR JMS				
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A				
PARTS LIST				
DIMENSIONAL TOLERANCE		DRN	DATE	
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		CHD	DATE	
		ENG	DATE	
MILLIMETERS	INCHES	ANGLES	PROJ. ENG.	DATE
XX ±0.10	XX ±0.008	30° 30'	PROJ.	DATE
XX ±0.05	XX ±0.02			DATE
X ±0.2	X ±0.1			DATE
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.		
MATERIAL	FINISH	B-DD-KK8A-0	SCALE	SIZE CODE
				D FD M8315-0-24
			SHEET 1 OF 1	NUMBER
				REV.

D FD M8315-0-24

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IF NO INTERRUPT, PREPARE TO GET NEXT INSTRUCTION
PC + SKIP → MA
IF INTERRUPT, EXECUTE A JMS TO LOCATION 0

CLOCK THE MA

FC9 F SET TIME MS0,1=X,X

TS4 T0,1=L,H

ENA. ROM B
INT. IN PROG ^F^D^E : PC EN; ADD←PC
 : CAR.EN: SKIP; CAR←1; ADD←SKIP
 : F SET←1
INT. IN PROG : NO ENABLES; ADD←0
 : JMS→IR ENA.
 : E SET←1

IF OPI: ENABLE 74S158 ROTATE MUX;

MD	8	9	
	0	0	NOP
	0	1	RL EN ROTATE LEFT
	1	0	RR EN ROTATE RIGHT
	1	1	RR EN, RL EN LOAD; SHOULD NOT BE USED

TP3 1/2 OPI ^MDI0: PC, AC, MQ CLK

MA, MS LOAD CONT: CPMA LOAD
F SET: F←1
E SET: E←1
 : IR←JMS; IR0-2=100
PC, AC, MQ CLK
CPMA DIS: INH CPMA→BUS

TP4

MS, IR DIS: DMA
GO TO FC10

F GO TO FC1
E GO TO FC8
AN INTERRUPT HAS BEEN ALLOWED

REV. NO.	REV.
1	
2	
3	
4	
5	
6	
7	
8	

GETTING ADDRESS OF NEXT INSTRUCTION, OR ANSWERING INTERRUPT			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDP8A			
DIMENSIONAL TOLERANCE		PARTS LIST	
DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED		DATE 11-6-74	
MILLIMETERS	INCHES	ANGLES	DATE 1/20/75
±.01	±.0005	±.01	DATE 2-2-75
±.02	±.001	±.02	DATE 2-2-75
±.05	±.002	±.05	DATE 2-2-75
THIRD ANGLE PROJECTION	NEEDED DIMENSIONS QUALITY	NEXT HIGHER ASSY.	
MATERIAL	FINISH	SCALE	
		SCALE 1:1	
		SHEET 1 OF 1	
TITLE		REV. CODE	NUMBER
FLOW DIAGRM M8315 FC9		D	M8315-0-25
		SHEET	OF

M8315-0-25

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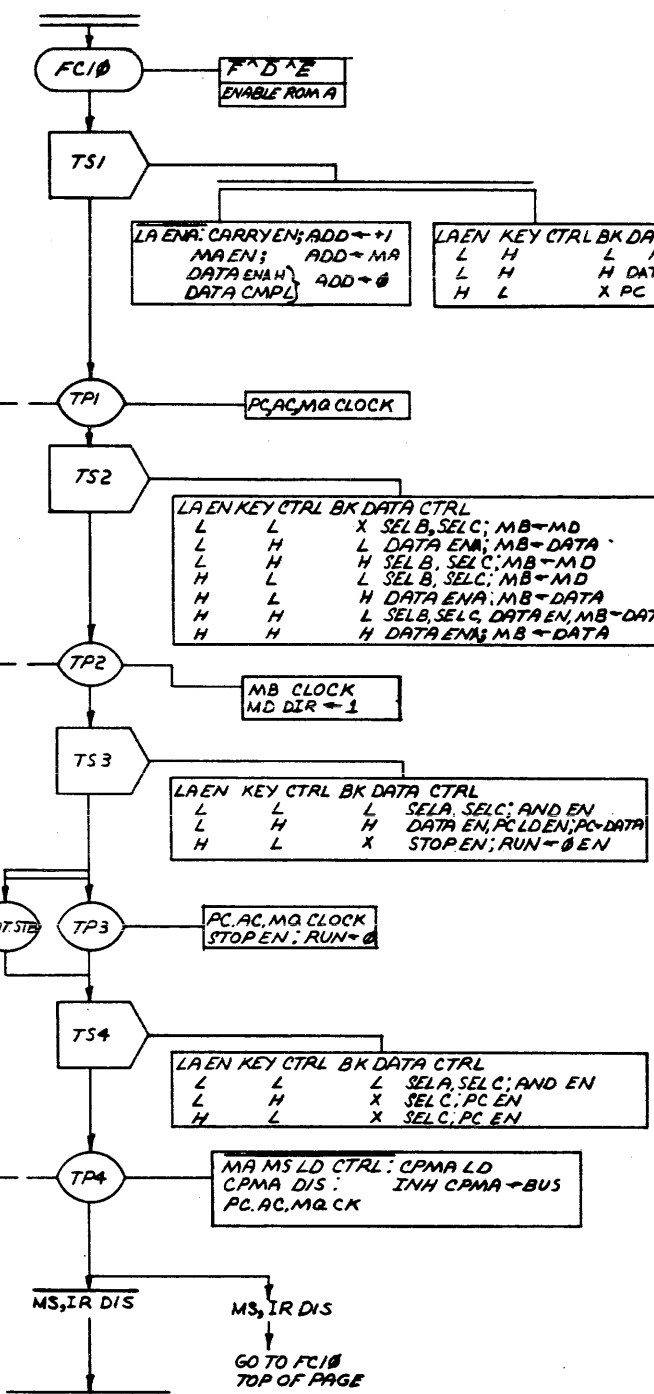
THE BREAK CONTROL WORD (LA, EN KEY CTRL BK DATA CTRL) IS NORMALLY SET UP AT THE BEGINNING OF THE DMA CYCLE AND REMAINS STABLE FOR THE ENTIRE CYCLE. IT DEFINES OPERATIONS AS FOLLOWS.

LA, EN KEY CTRL BK DATA CTRL	H H H	H H L	H L L	L H H	L H L	L L H	L L L
DATA TO MEM	ADD TO MEM	CON DEPOSIT	CON EXAMINE	LOAD ADD	BOOT DEPOSIT	LOAD FIELD 0	LOAD FIELD 7
THE CONTENTS OF THE DATA LINES GO TO THE MB	THE CONTENTS OF THE MD LINES PLUS THE CONTENTS OF THE DATA LINES GO TO THE MB	THE CONTENTS OF THE DATA LINES GO TO THE MB	THE CONTENTS OF THE MD LINES GO TO THE MB	THE CONTENTS OF THE MD LINES GO TO THE MB	THE CONTENTS OF THE MD LINES GO TO THE MB	THE CONTENTS OF THE MD LINES GO TO THE MB	THE CONTENTS OF THE MD LINES GO TO THE MB
THE MB IS WRITTEN INTO MEMORY	THE MB, THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY	THE MB IS WRITTEN INTO MEMORY
	THE PC GOES TO THE MA	THE PC GOES TO THE MA	THE PC GOES TO THE MA	THE PC GOES TO THE MA	THE PC GOES TO THE MA	AND IF AT TP4	AND IF AT TP4

TO DO A TRANSFER OF DATA TO A DEVICE DO AN ADD TO MEM WITH THE DATA LINES EQUAL TO 0 AND TAKE DATA FROM THE MD LINES AT TP3.

SWITCH SELECTED 1ST CYCLE OR 1ST CICLE

THESE ARE THE 1ST TWO CYCLES DURING A CPU AUTO RESTART



MS, IR DIS

TP, I = L, L

TP, I = H, L

TP, I = H, H

TP, I = L, H

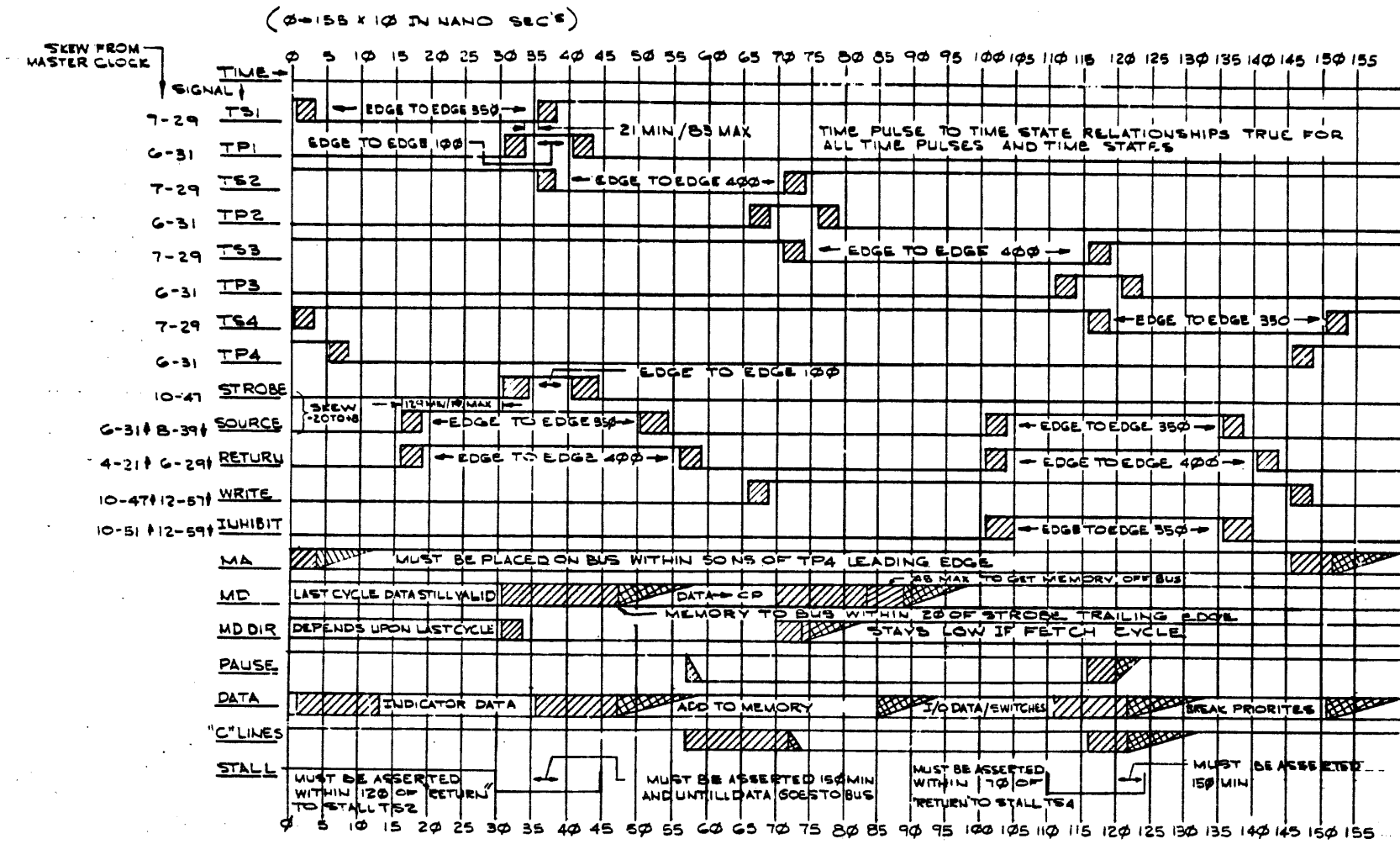
DATA BREAK/CONSOLE OPERATIONS/AUTO RESTART				
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A				
DIMENSIONAL TOLERANCE				
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED				
MILLIMETERS	INCHES	ANGLES		
XX.XX ±0.10	XX.XX ±0.005	XX° 00'		
XX.X ±0.05	XX.X ±0.002	XX° 00'		
X ±0.2	X ±0.1			
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.		
MATERIAL	FINISH	B DD KK8A-0	SIZE CODE	NUMBER
			D FD	M8315-0-26
SCALE		SHEET 1 OF 1		
DIST.				

REV.	CHANGE NO.

REV. NO. 1 DFD M8315-0-26

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FORM 8315-0-27 2



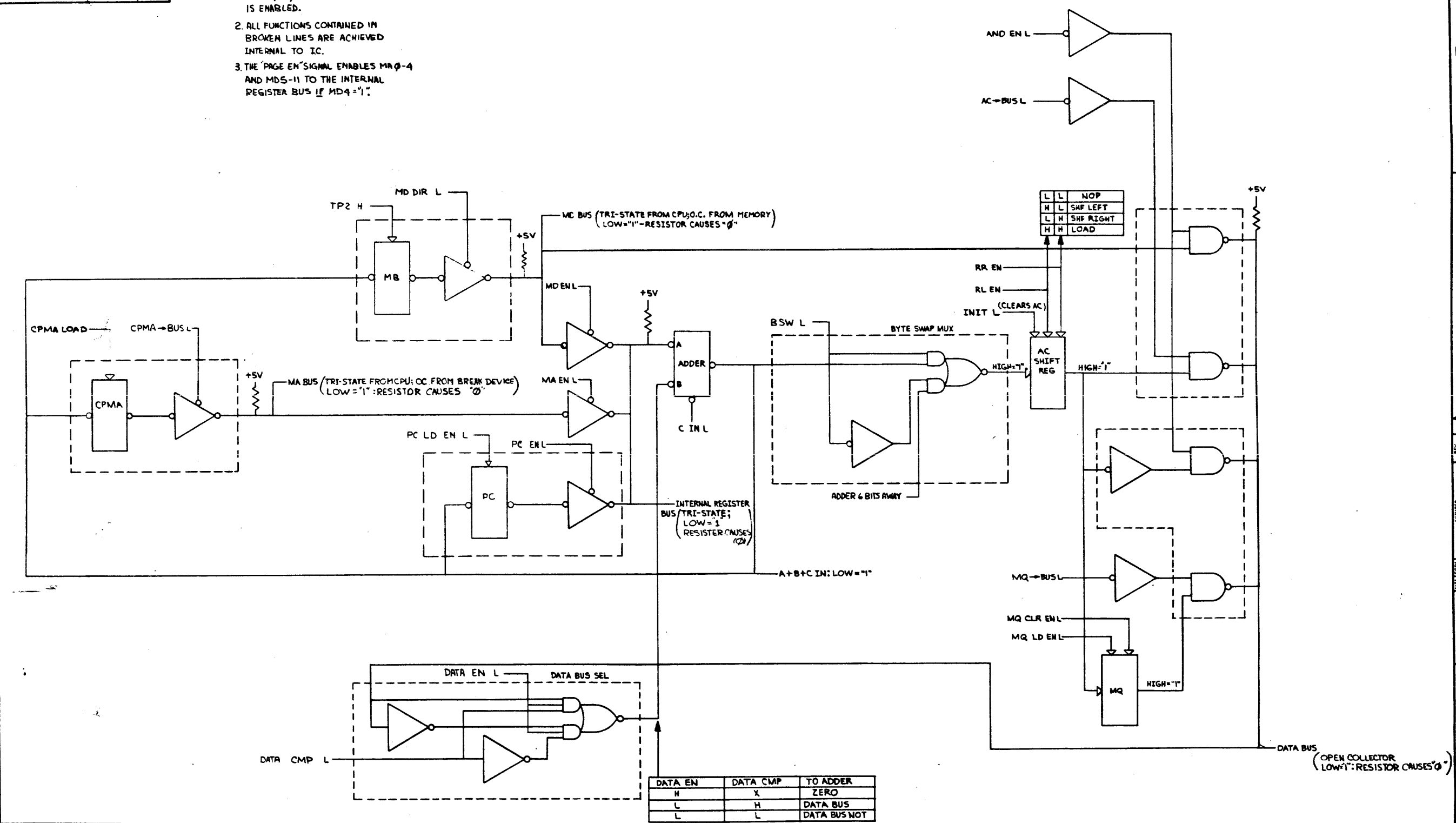
8A DATA PATH FUNCTION TIMING			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO. ITEM NO.
PDP8A			
PARTS LIST			
DIMENSIONAL TOLERANCE		DATE	digital
DIMENSIONS ARE MILLIMETERS INCHES UNLESS OTHERWISE SPECIFIED		DATE	
MILLIMETERS	INCHES	ANGLE	TITLE
K .XX = ±0.10 X .X = ±0.05 X = ±0.2	J .XX = ±0.008 J .X = ±0.004 J = ±0.1	30° 30°	FLOW DIAGRAM M8315 BUS TIMING
THIRD ANGLE PROJECTION	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.	DATE
MATERIAL	FINISH	SCALE	DATE
		SHEET OF 2	DATE
			REV.

REV.	
CHG	
NO.	

REV. 127

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- NOTES:
1. THE PC, AC AND MQ ARE LOADED BY PC, AC, MQ CLK IF THE LOAD IS ENABLED.
 2. ALL FUNCTIONS CONTAINED IN BROKEN LINES ARE ACHIEVED INTERNAL TO IC.
 3. THE "PAGE EN" SIGNAL ENABLES MR 0-4 AND MDS-11 TO THE INTERNAL REGISTER BUS IF MD4 = "1".

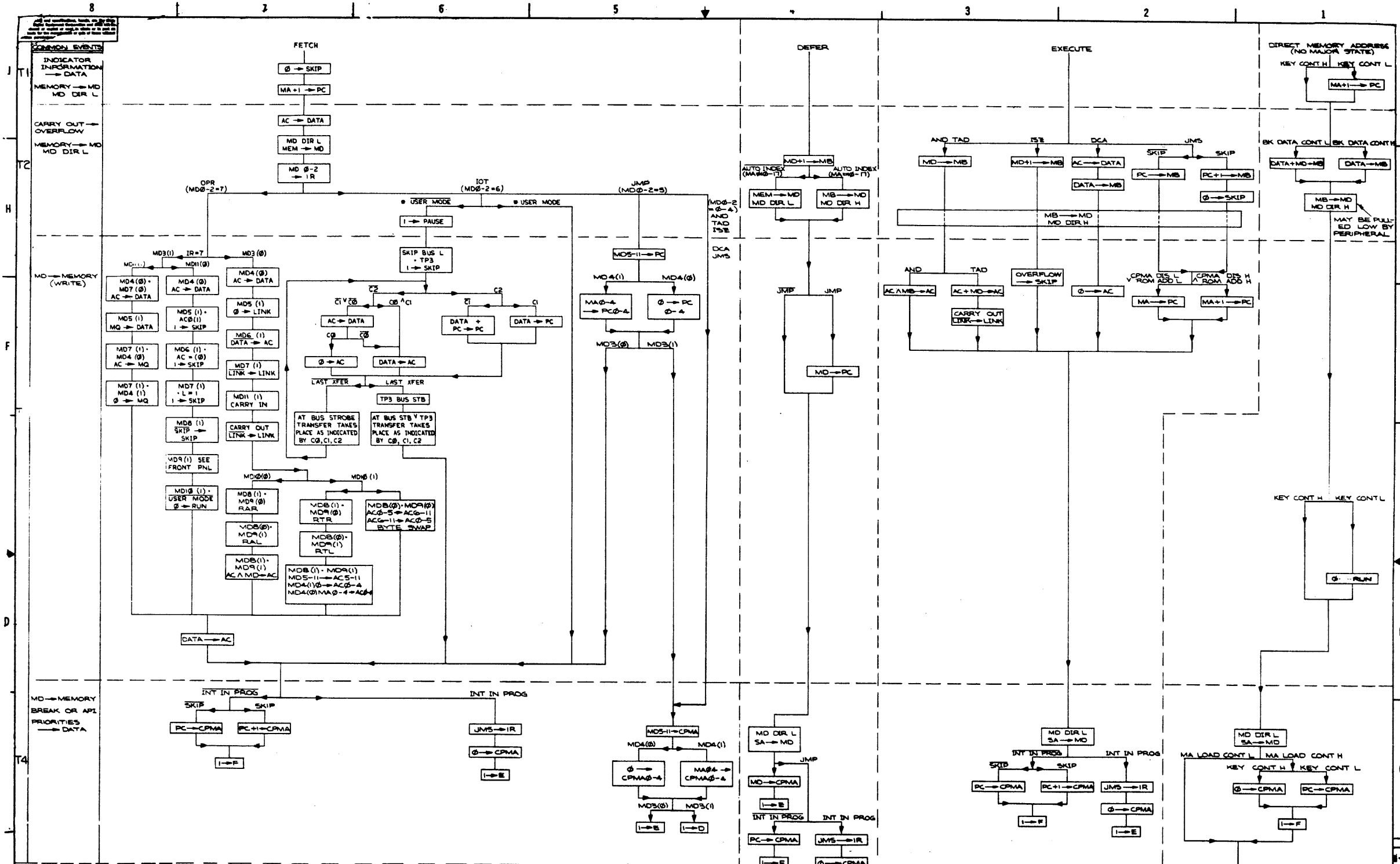


DATA EN	DATA CMP	TO ADDER
H	X	ZERO
L	H	DATA BUS
L	L	DATA BUS NOT

DATA PATH FUNCTIONS			
TITLE	SIZE CODE	NUMBER	REV.
FLOW DIAGRAM M8315 BUS TIMING	DFD	M8315-0-27	
SCALE	SHEET 2 OF 2	DIST.	

REVISIONS		
CHK	CHANGE NO.	REV.

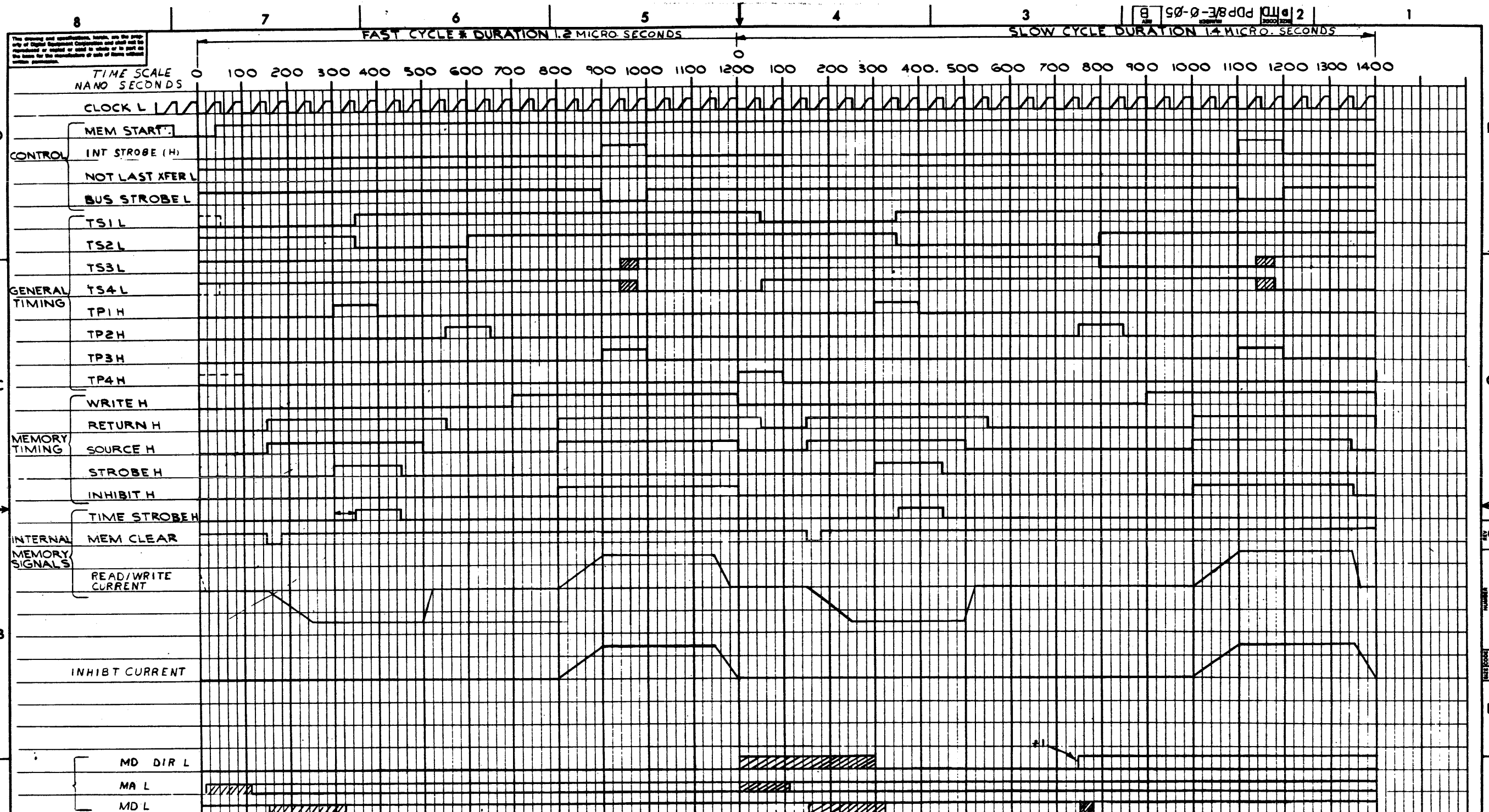
DFD M8315-0-27



NOTES:
 * USER MODE IS USED BY THE TIME SHARING OPTION ONLY TO INHIBIT HALT, OBR, LAS, & PAUSE

REV A
 L-00019
 11-11-71
 L.K. RIDE

PART USED ON OTHER MODELS		QTY.	DESCRIPTION	PART NO.	TIME
PDP8/E					
UNLESS OTHERWISE SPECIFIED TOLERANCES ARE AS SHOWN					
EQUIPMENT CORPORATION					
TITLE: PROCESSOR FLOW CHART					
DRAWING NO: EFD PDP8/E-0-06 A					
SHEET 1 OF 1					

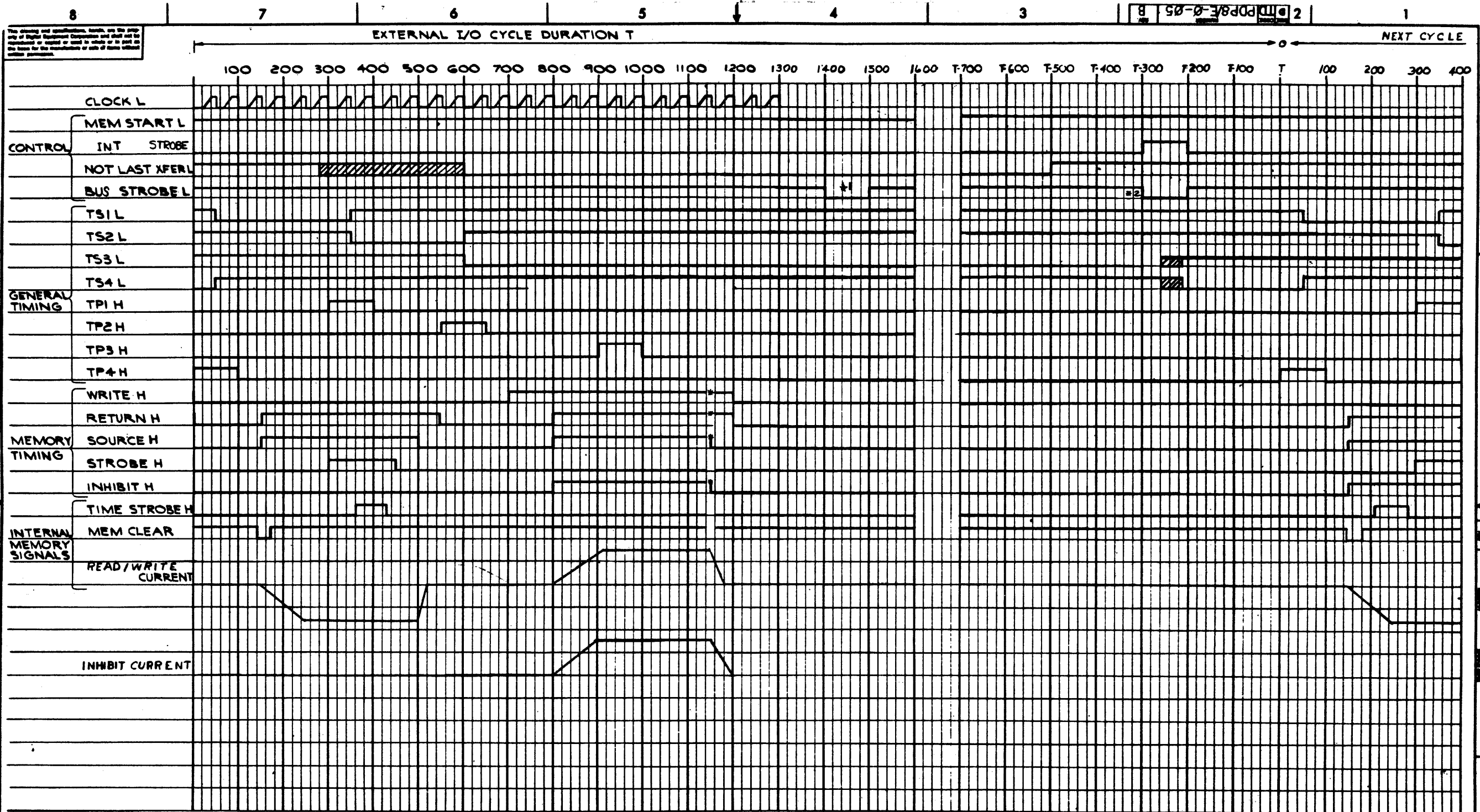


*THIS PLOT SHOWS AN INITIAL FAST CYCLE
 THE DOTTED LINES INDICATE A REGULAR CYCLE
 #1: MD DIR GOES LOW ONLY IF F+ [D-AUTO INDEX]

CIRCUIT DELAYS ARE NEGLECTED IN THIS TIMING DIAGRAM

REV. NO.	REV.
CHANGE NO.	A
DATE	11-10-71
BY	L. KLOTZ
CHK	BE-00049
DATE	11-10-71
BY	L. KLOTZ

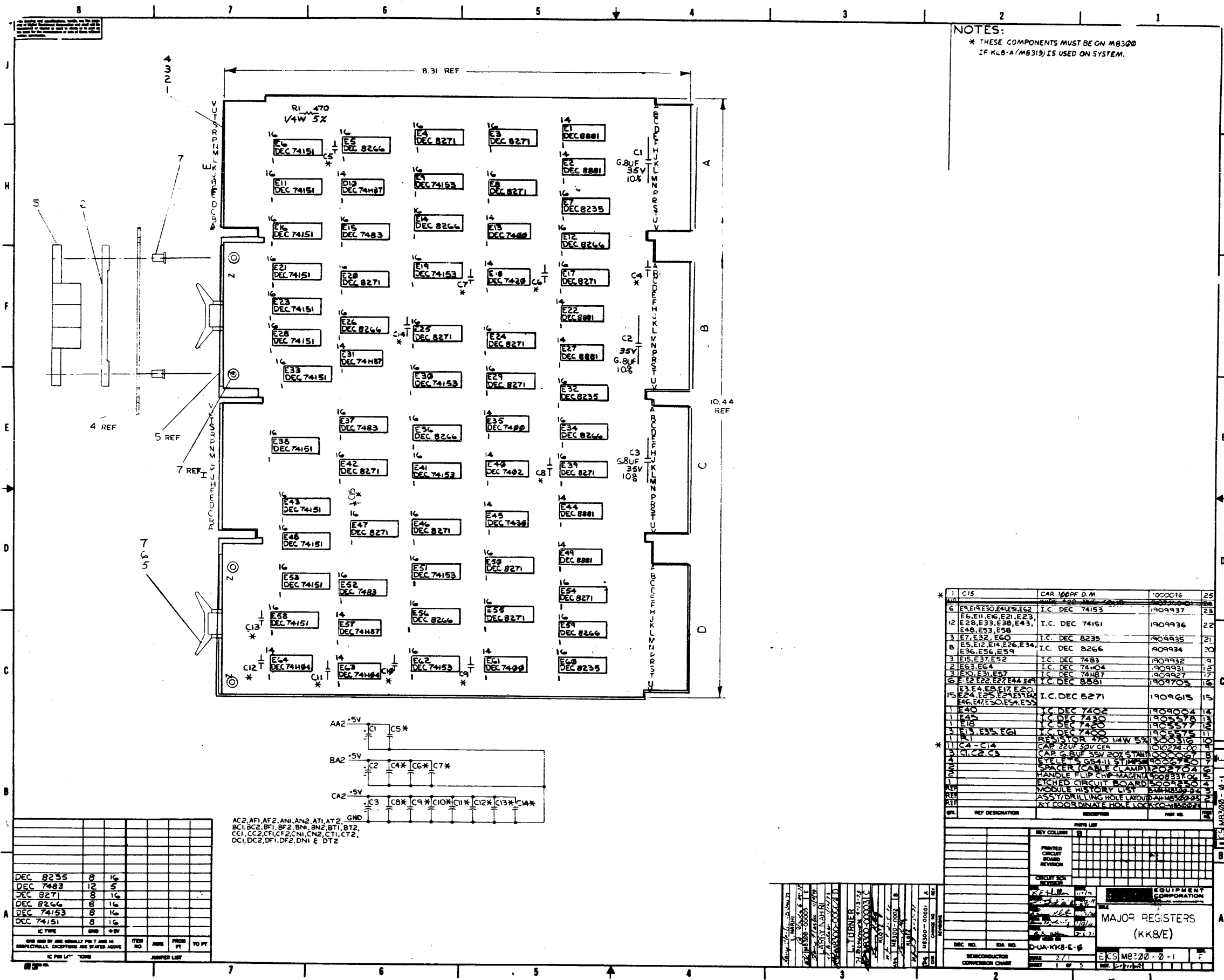
FIRST USED ON OPT/MOD	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8/E				
UNLESS OTHERWISE SPECIFIED	PARTS LIST			
DIMENSIONS IN INCHES	digital EQUIPMENT CORPORATION MAYLAND, MASSACHUSETTS			
TOLERANCES	TITLE			
DECIMALS FRACTIONS ANGLES	TIMING (PDP8/E)			
± .001 ± .005 ± .010	SIZE CODE			
± .001 ± .005 ± .010	NUMBER			
± .001 ± .005 ± .010	DITD PDP8/E-0-05			
± .001 ± .005 ± .010	REV.			
± .001 ± .005 ± .010	B			
MATERIAL	NEXT HIGHER ASSY			
FINISH	A-ML-PDP8/E-0			
	SCALE NONE			
	SHEET 1 OF 2			



NOTE: * MEMORY SIGNALS TIME OUT, AS IN A FAST CYCLE
 * 1 GENERATED BY PERIPHERAL TO STROBE DATA
 * 2 GENERATED BY PERIPHERAL TO TERMINATE EXT. I/O CYCLE AND RESUME NORMAL OPERATION.

FIRST USED ON OPT/MOD PDP8/E	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES FRACTIONS ANGLES ± .010 ± .005 ± .002 FINAL SURFACE QUALITY 7 REMOVE BURRS AND BREAK SHARP EDGES	DATE 1-7-71	DATE 1/16/71	DATE 1/16/71	DATE 1/16/71
MATERIAL ALUMINUM	PARTS LIST EQUIPMENT CORPORATION WATUWAT, MASSACHUSETTS	TITLE TIMING (PDP8/E)		
FINISH ANODIZED	A-ML-PDP8/E-0	SCALE NONE	DTD PDP8/E-0-05	REV. B
SHEET 2 OF 2	2	1	1	1

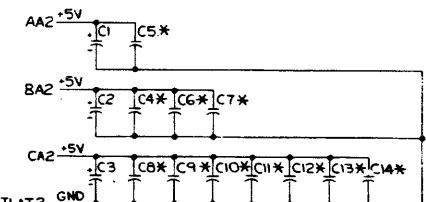
REV. B
 CHANGE NO. 1
 DATE 1/16/71



NOTES:
 * THESE COMPONENTS MUST BE ON M8300
 IF KLB-A/M8313 IS USED ON SYSTEM.

IC TYPE	QTY	POS	FROM	TO
DEC 8235	8	16		
DEC 7483	12	5		
DEC 8271	8	16		
DEC 8266	8	16		
DEC 74153	8	16		
DEC 74151	8	16		

AC2, AF1, AF2, AN1, AN2, AT1, AT2,
 BC1, BC2, BF1, BF2, BN1, BN2, BT1, BT2,
 CC1, CC2, CF1, CF2, CN1, CN2, CT1, CT2,
 DC1, DC2, DF1, DF2, DN1 & DT2



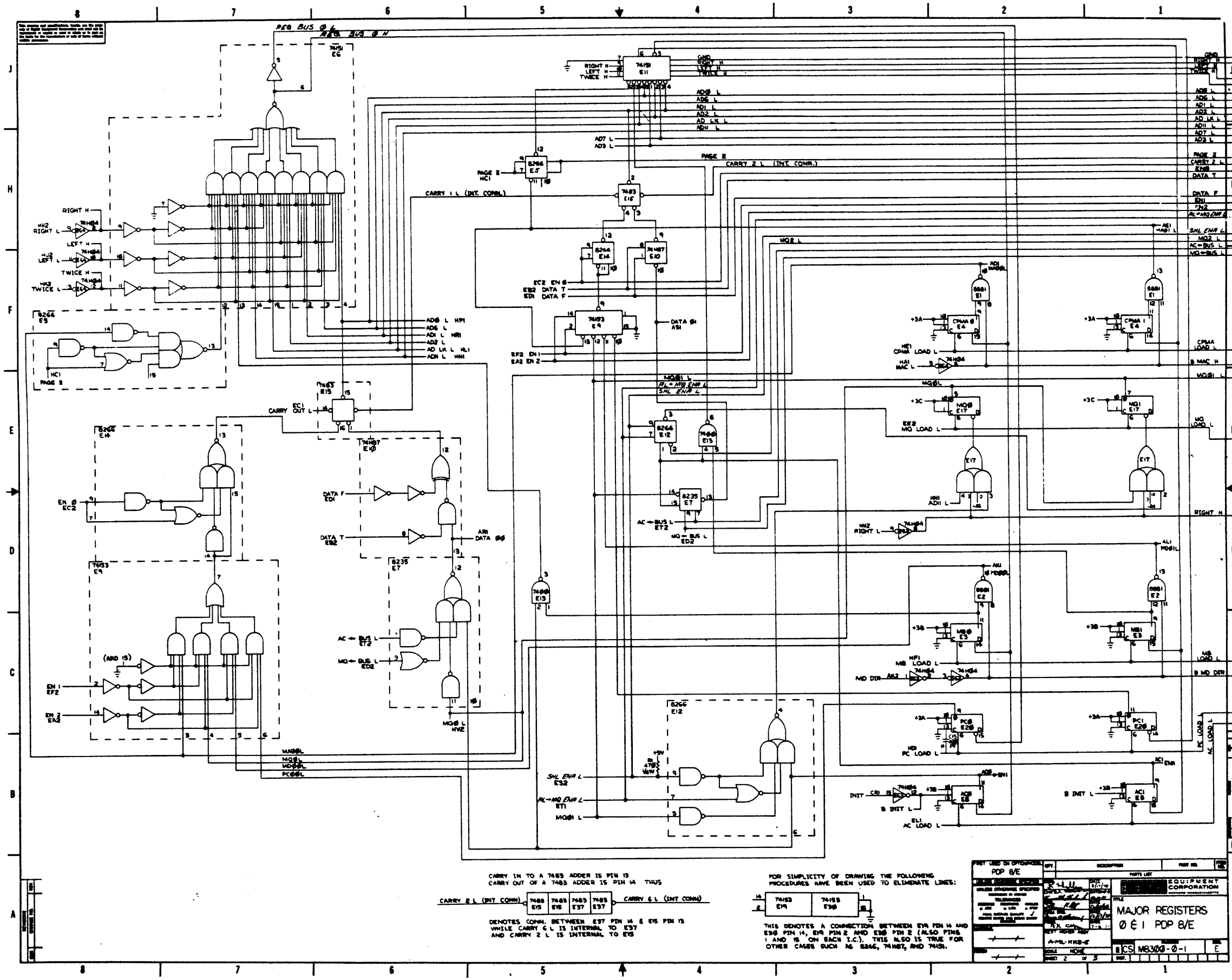
REF	REF DESIGNATION	DESCRIPTION	QTY	REV. NO.
*	C15	CAP 100PF D.M.	1000016	25
6	E9, E10, E30, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60	I.C. DEC 74153	1909937	23
12	E20, E33, E38, E43, E48, E53, E58	I.C. DEC 74151	1909936	22
3	E7, E32, E60	I.C. DEC 8235	1909935	21
B	E5, E12, E14, E26, E34, E36, E56, E59	I.C. DEC 8266	1909934	20
3	E15, E37, E52	I.C. DEC 7483	1909932	9
2	E63, E64	I.C. DEC 74104	1909931	18
3	E10, E31, E57	I.C. DEC 74107	1909927	17
6	E12, E22, E27, E44, E47, E54, E55, E56, E57, E58, E59, E60	I.C. DEC 8881	1909705	16
15	E24, E25, E27, E32, E34, E35, E36, E37, E38, E39, E40, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60	I.C. DEC 8271	1909615	15
1	E40	I.C. DEC 7402	1909604	14
1	E45	I.C. DEC 7420	1909579	13
1	E18	I.C. DEC 7420	1909579	13
3	E11, E35, E61	I.C. DEC 7400	1909575	11
1	R1	RESISTOR 470 1/4W 5%	1009310	10
11	CA - C14	CAP 220F 50V CER	1009274	9
3	C1, C2, C3	CAP 0.8UF 35V 10% TAN	1009267	8
4		SPACER CABLE CLAMP	1009204	6
1		HANDLE FLIP CHIP MAGENTA	1008337	5
1		ETCHED CIRCUIT BOARD	1008250	4
1		MODULE HISTORY LIST	1008222	3
1		ASSY/DRILLING HOLE LOCATOR	1008222	3
1		XY COORDINATE HOLE LOCATOR	1008222	3

REV.	DATE	BY	CHKD	APPD	DESCRIPTION
1	11/11/73	J. L. TURNER			ISSUED FOR FAB
2	11/11/73	J. L. TURNER			ISSUED FOR FAB
3	11/11/73	J. L. TURNER			ISSUED FOR FAB
4	11/11/73	J. L. TURNER			ISSUED FOR FAB
5	11/11/73	J. L. TURNER			ISSUED FOR FAB

REV.	DATE	BY	CHKD	APPD	DESCRIPTION
1	11/11/73	J. L. TURNER			ISSUED FOR FAB
2	11/11/73	J. L. TURNER			ISSUED FOR FAB
3	11/11/73	J. L. TURNER			ISSUED FOR FAB
4	11/11/73	J. L. TURNER			ISSUED FOR FAB
5	11/11/73	J. L. TURNER			ISSUED FOR FAB

MAJOR REGISTERS (KKB/E)

SEMICONDUCTOR CONVERTER CHART



CARRY IN TO A 7483 ADDER IS PIN 13
 CARRY OUT OF A 7483 ADDER IS PIN 14 THUS

CARRY 1 L (INT. CONN.) 7483 7483 7483 7483 CARRY 2 L (INT. CONN.)
 E15 E15 E15 E15

DENOTES CONN. BETWEEN E17 PIN 14 & E15 PIN 13
 WHILE CARRY 1 L IS INTERNAL TO E17
 AND CARRY 2 L IS INTERNAL TO E15

FOR SIMPLICITY OF DRAWING THE FOLLOWING
 PROCEDURES HAVE BEEN USED TO ELIMINATE LINES:

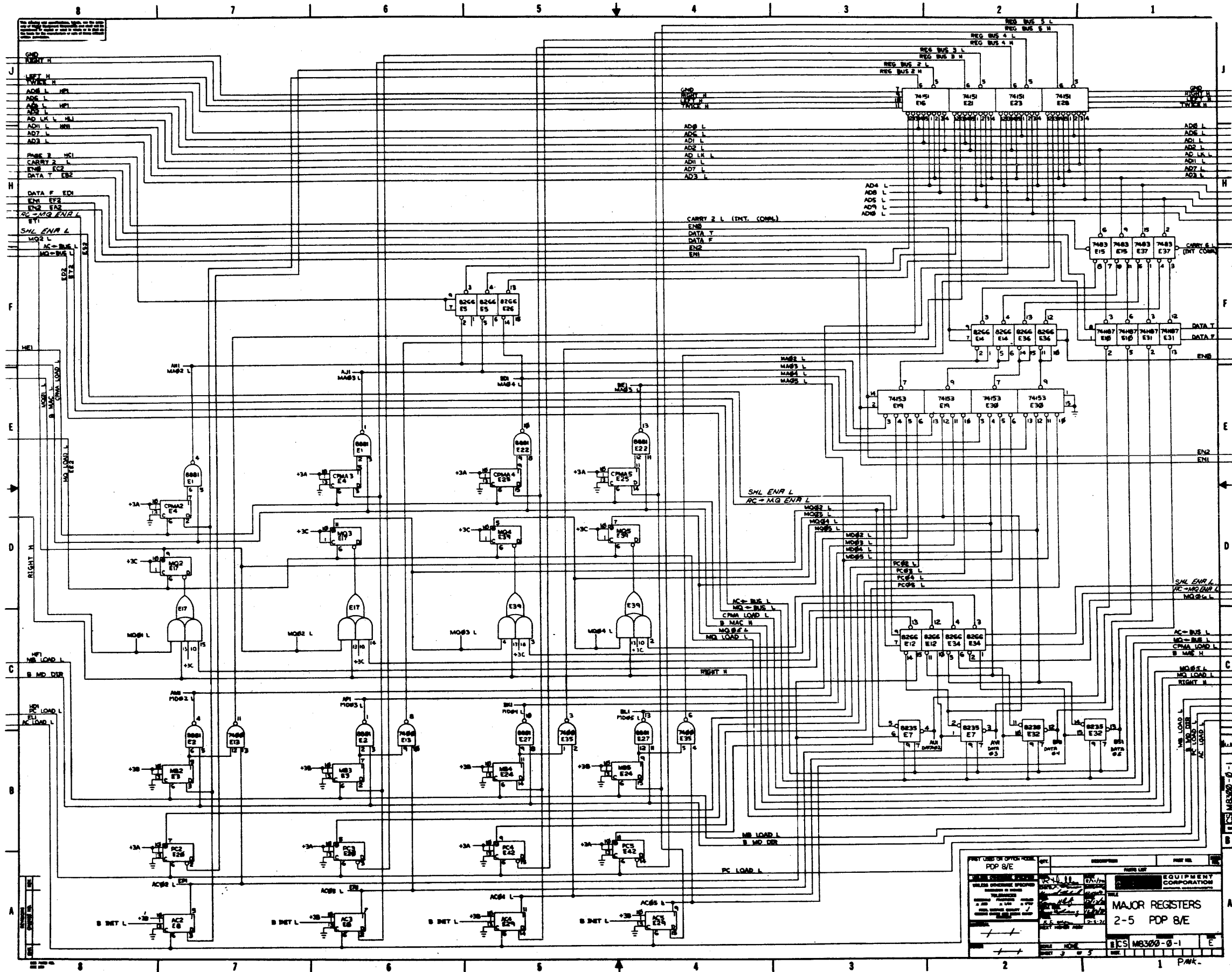
74153 74155
 E14 E14

THIS DENOTES A CONNECTION BETWEEN E14 PIN 14 AND
 E15 PIN 14, E16 PIN 2 AND E15 PIN 2 (ALSO PINS
 1 AND 15 ON EACH I.C.). THIS ALSO IS TRUE FOR
 OTHER CASES SUCH AS E246, 74157, AND 74158.

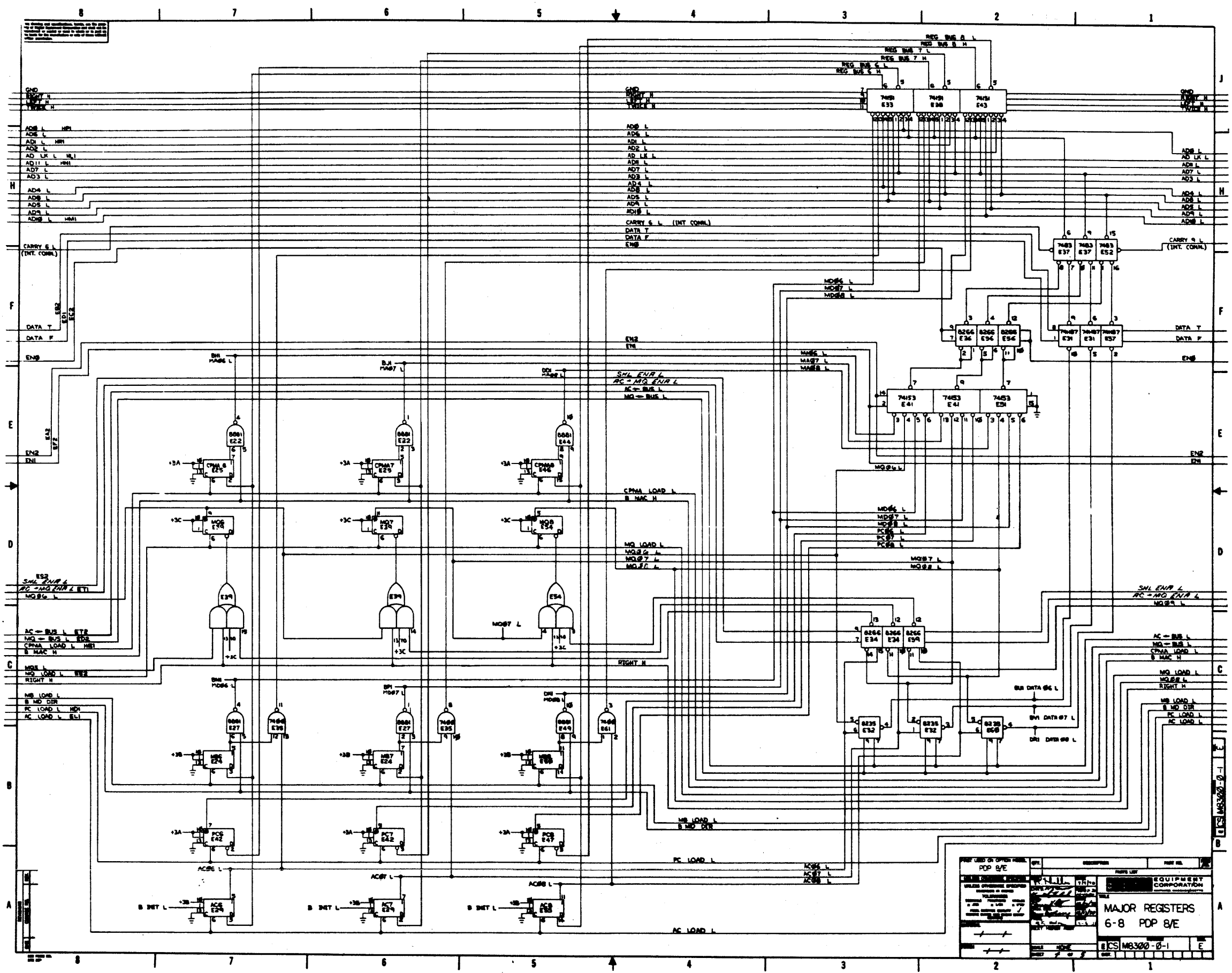
REV. LIST IN DISCREPANCY	REV.	DESCRIPTION	DATE	BY
POP 8/E	1	MAJOR REGISTERS	1/11/70	
	2		1/11/70	
	3		1/11/70	

MAJOR REGISTERS
 0 E 1 POP 8/E

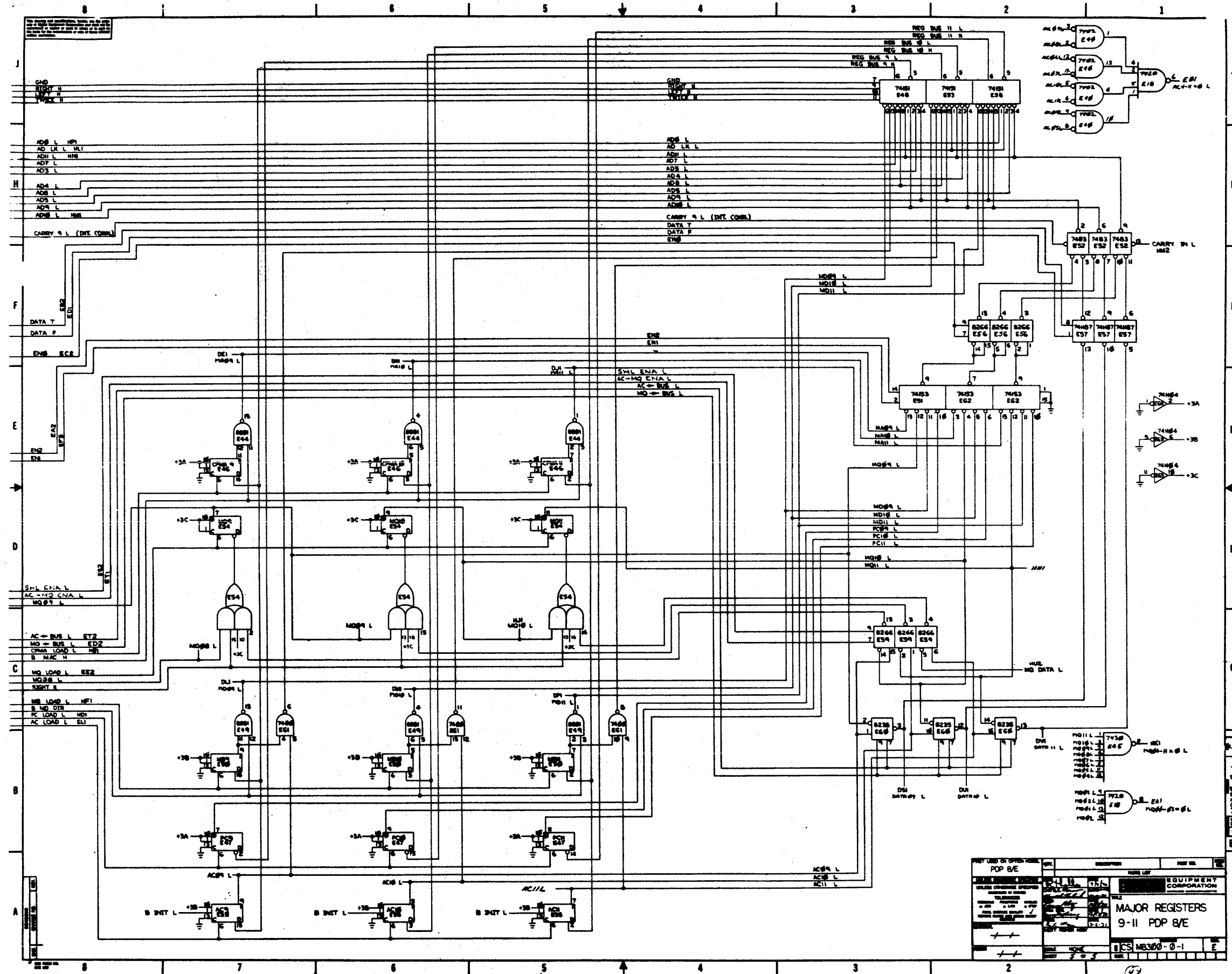
ICSI M8300-0-1



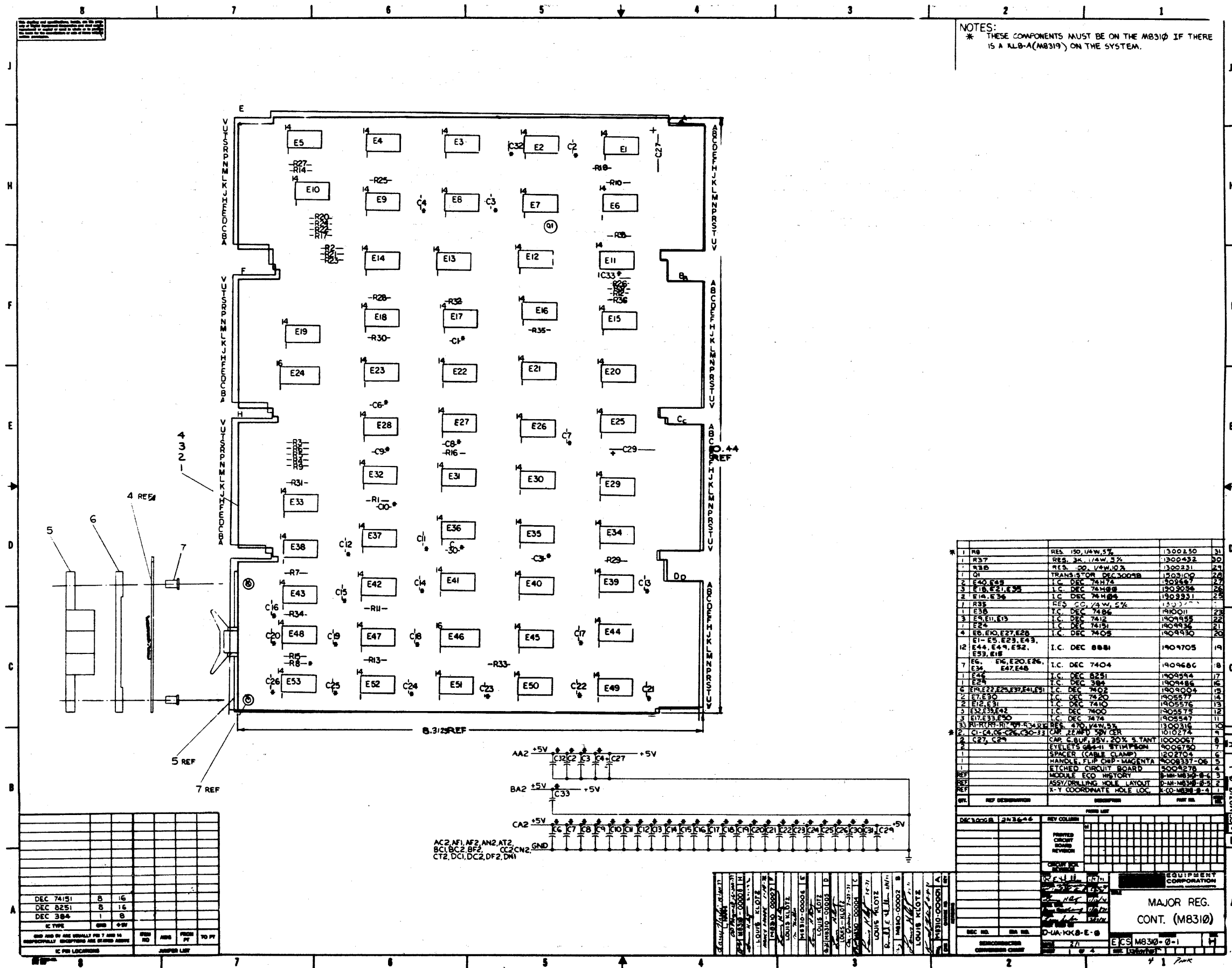
PART LIST OF OPTION PACK PDP 8/E UNLESS OTHERWISE SPECIFIED STANDARD P/N FROM MANUFACTURER SEE DRAWING FOR PART NUMBER AND QUANTITY SEE DRAWING FOR PART NUMBER AND QUANTITY	PARTS LIST EQUIPMENT CORPORATION MAJOR REGISTERS 2-5 PDP 8/E ESI M8360-0-1 100% 100% 100%
--	---



THIS UNIT IS OPERATIONAL PDP-8/E SERIAL OPERATIONS SPECIFICATION MODEL NUMBER PART NUMBER DATE OPERATING ROOM OPERATOR TESTER APPROVED BY TESTER TEST DATE	EQUIPMENT CORPORATION MAJOR REGISTERS 6-8 PDP 8/E ICS M8300-0-1 E
---	--



PDP 8/E		EQUIPMENT CORPORATION	
MAJOR REGISTERS		9-11 PDP 8/E	
ICS M8300-0-1		E	



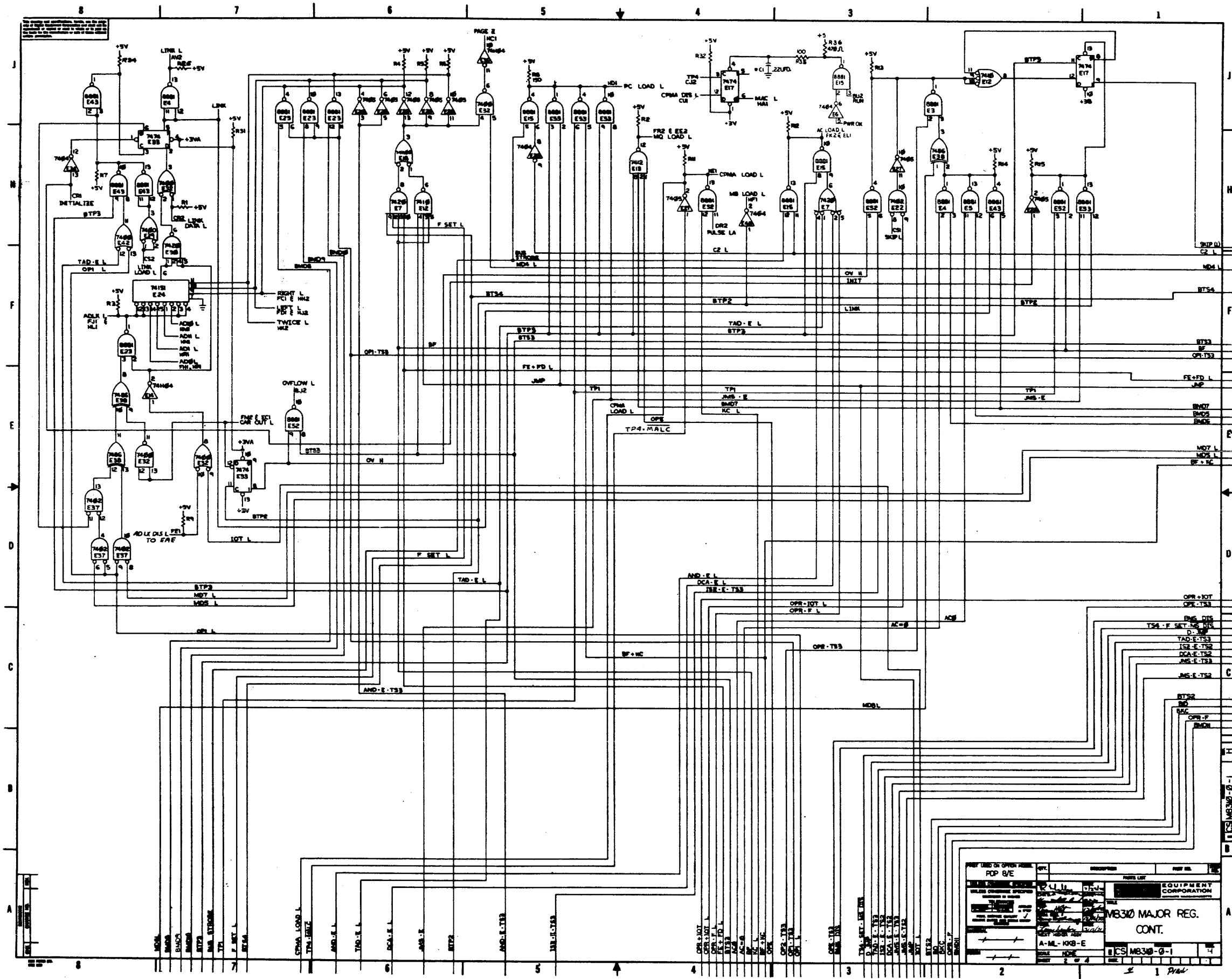
NOTES:
* THESE COMPONENTS MUST BE ON THE M8310 IF THERE IS A ALB-A(M8319) ON THE SYSTEM.

REV	REF DESCRIPTION	DESCRIPTION	REV NO.	DATE
1	R8	RES. 1/4W, 5%	1300150	31
1	R37	RES. 3/4W, 5%	1300432	30
1	R38	RES. 20V, 1/4W, 10%	1300231	29
1	Q1	TRANSISTOR DEC 3005B	1503100	28
2	E40, E49	I.C. DEC 74174	1503557	27
3	E18, E21, E34	I.C. DEC 74108	1503035	26
2	E14, E36	I.C. DEC 74105	1503531	25
1	R35	RES. 1/4W, 5%	1300150	31
1	E35	I.C. DEC 7484	190011	23
3	E9, E11, E19	I.C. DEC 7412	1904555	22
1	E24	I.C. DEC 74151	1904936	21
4	E8, E10, E27, E28	I.C. DEC 7405	1904930	20
12	E44, E49, E52, E53, E18	I.C. DEC 8081	1904705	19
7	E6, E7, E20, E26, E47, E48	I.C. DEC 7404	1904660	18
1	E42	I.C. DEC 8251	1905584	17
1	E24	I.C. DEC 384	1904486	16
6	E1, E22, E25, E37, E41, E51	I.C. DEC 7402	1909004	15
2	E7, E30	I.C. DEC 7420	1905577	14
2	E12, E31	I.C. DEC 7410	1905576	13
3	E32, E35, E42	I.C. DEC 7400	1905575	12
3	E17, E33, E50	I.C. DEC 7474	1905547	11
33	AL1, R1, R11, R17, R18, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100	RES. 1/4W, 5%	1300316	10
2	C1, C4, C5, C6, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29, C30, C31, C32, C33	CAP. 220PF, 50V, 50%	1502774	9
2	C27, C28	CAP. 5.1UF, 35V, 20% S TANT	1000057	8
1	D1, D2, D3, D4	EYELETS 24-11 BYTIMPSON	1900750	7
1		SPACER (CABLE CLAMP)	1202704	6
1		HANDLE FLIP CHIP - MAGENTA	1900337-06	5
1		ETCHED CIRCUIT BOARD	1900478	4
REF		MODULE ECO HISTORY	D-M-M8308-8-4	3
REF		ASSY/DRILLING HOLE LAYOUT	D-M-M8308-8-2	2
REF		X-Y COORDINATE HOLE LOC.	D-M-M8308-8-1	1

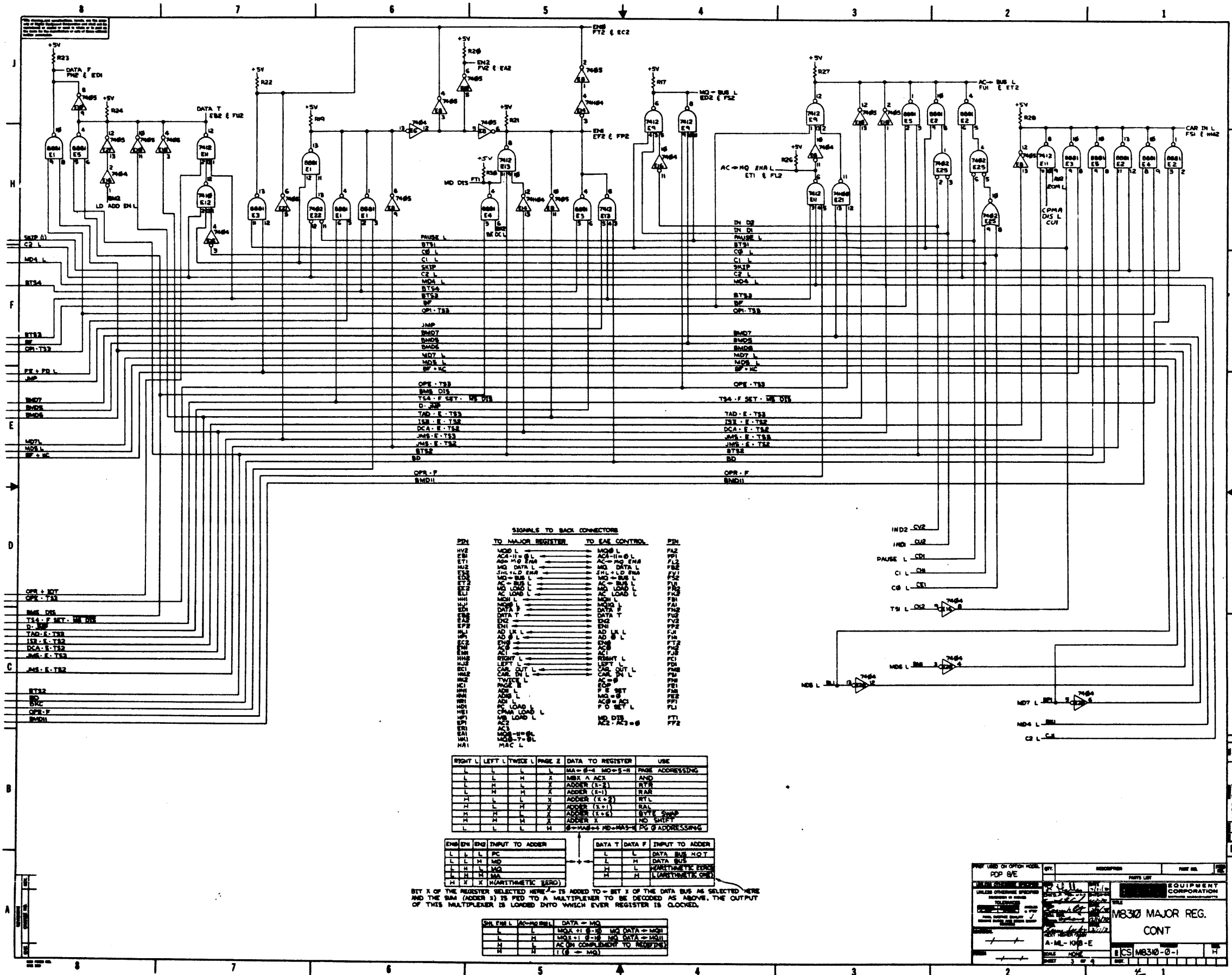
REV	REV NO.	DATE	DESCRIPTION
1	1	DEC 74/51	8 16
1	1	DEC 82/51	8 16
1	1	DEC 3/84	1 8

MAJOR REG. CONT. (M8310)

ECS M8310-0-1



POP 8/E	POP 8/E	POP 8/E	POP 8/E
EQUIPMENT CORPORATION			
MB310 MAJOR REG.			
CONT.			
A-M-108-E			
ECS MB310-0-1			



PART USED ON OTHER MODELS

POP 0/E

DESCRIPTION

PART NO.

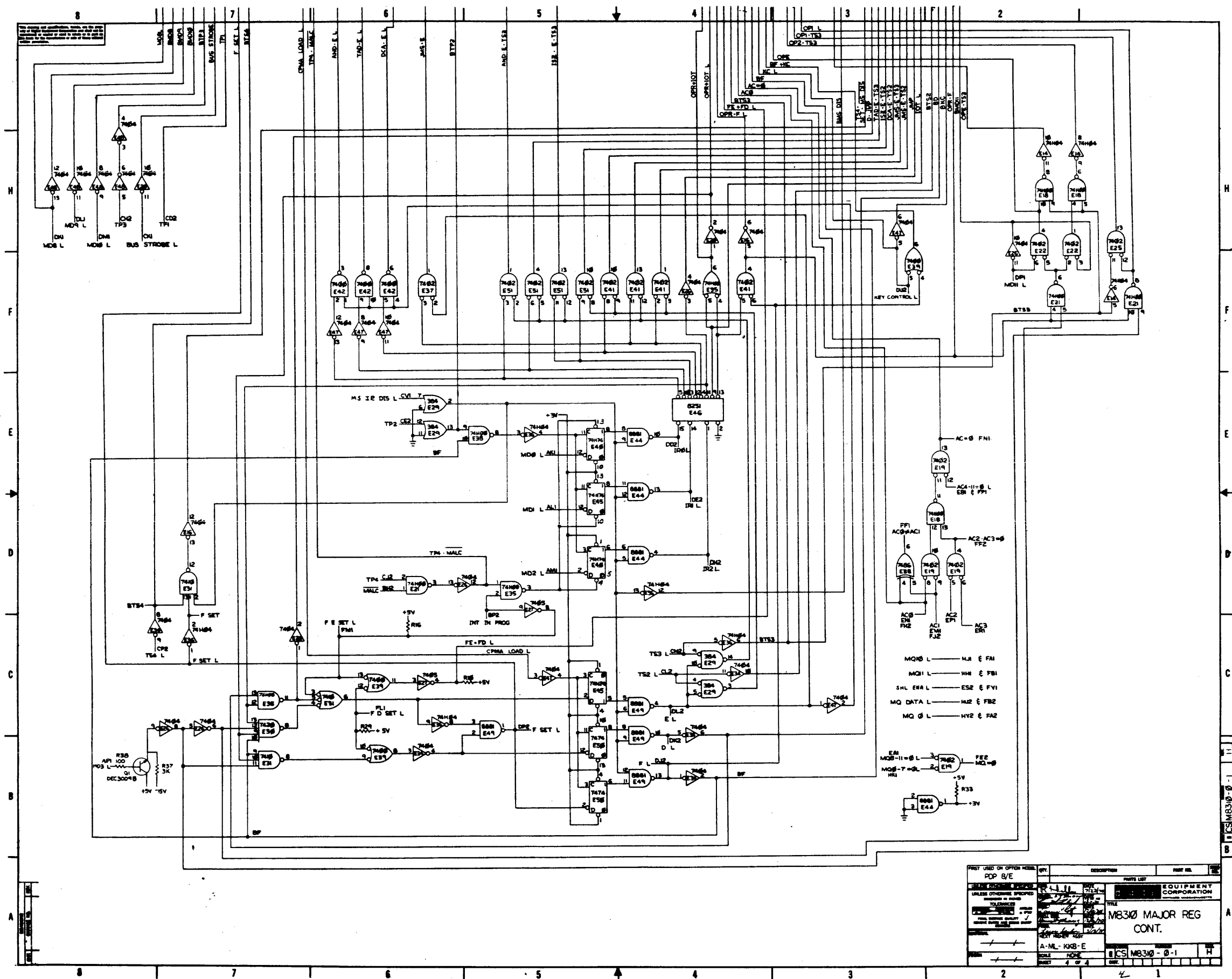
EQUIPMENT CORPORATION

M830 MAJOR REG.

CONT

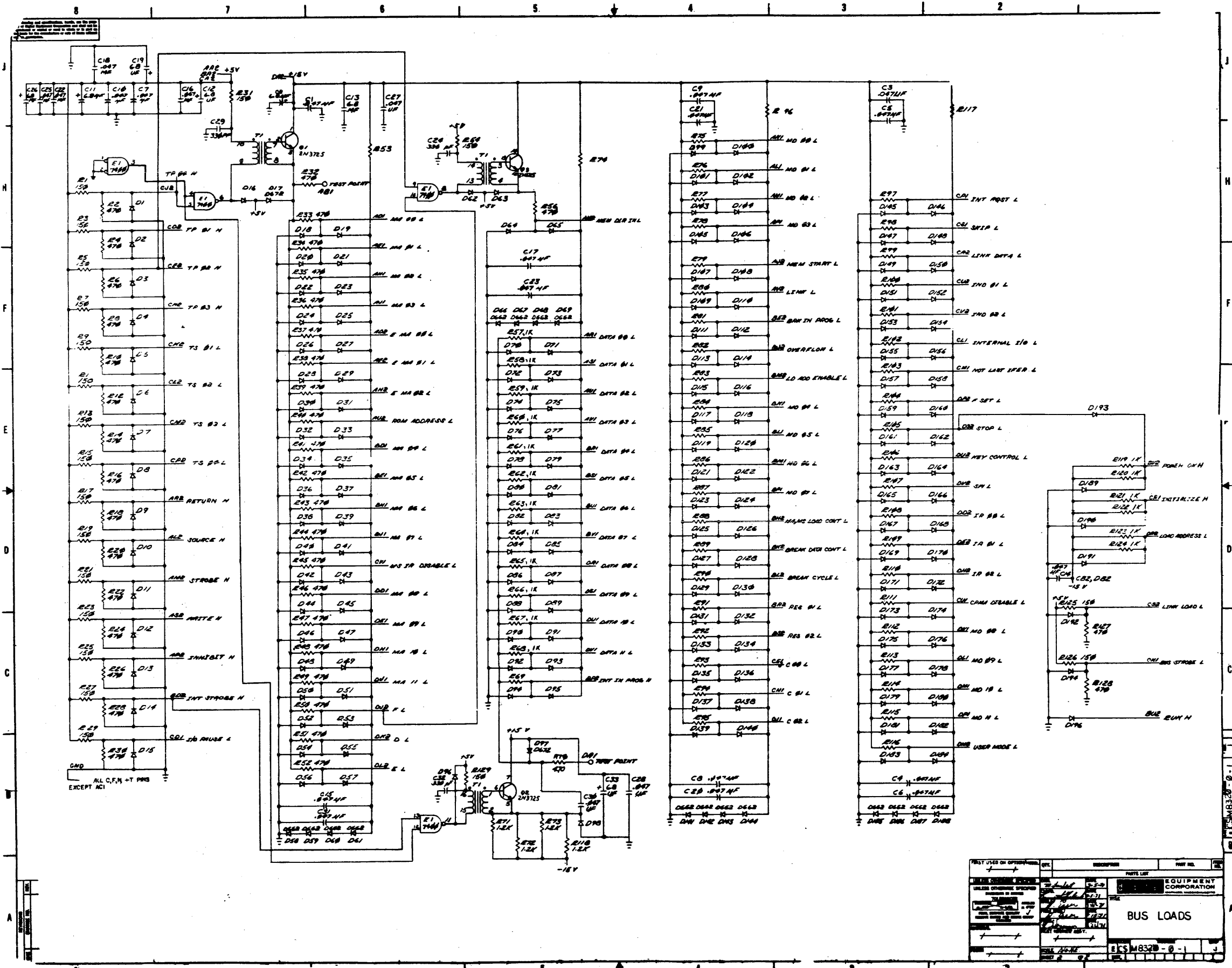
A-ML-100-E

CS M830-0-1



REV.	DESCRIPTION	DATE	BY
1	POP B/E		
2			
3			
4			
5			
6			
7			
8			
9			
10			

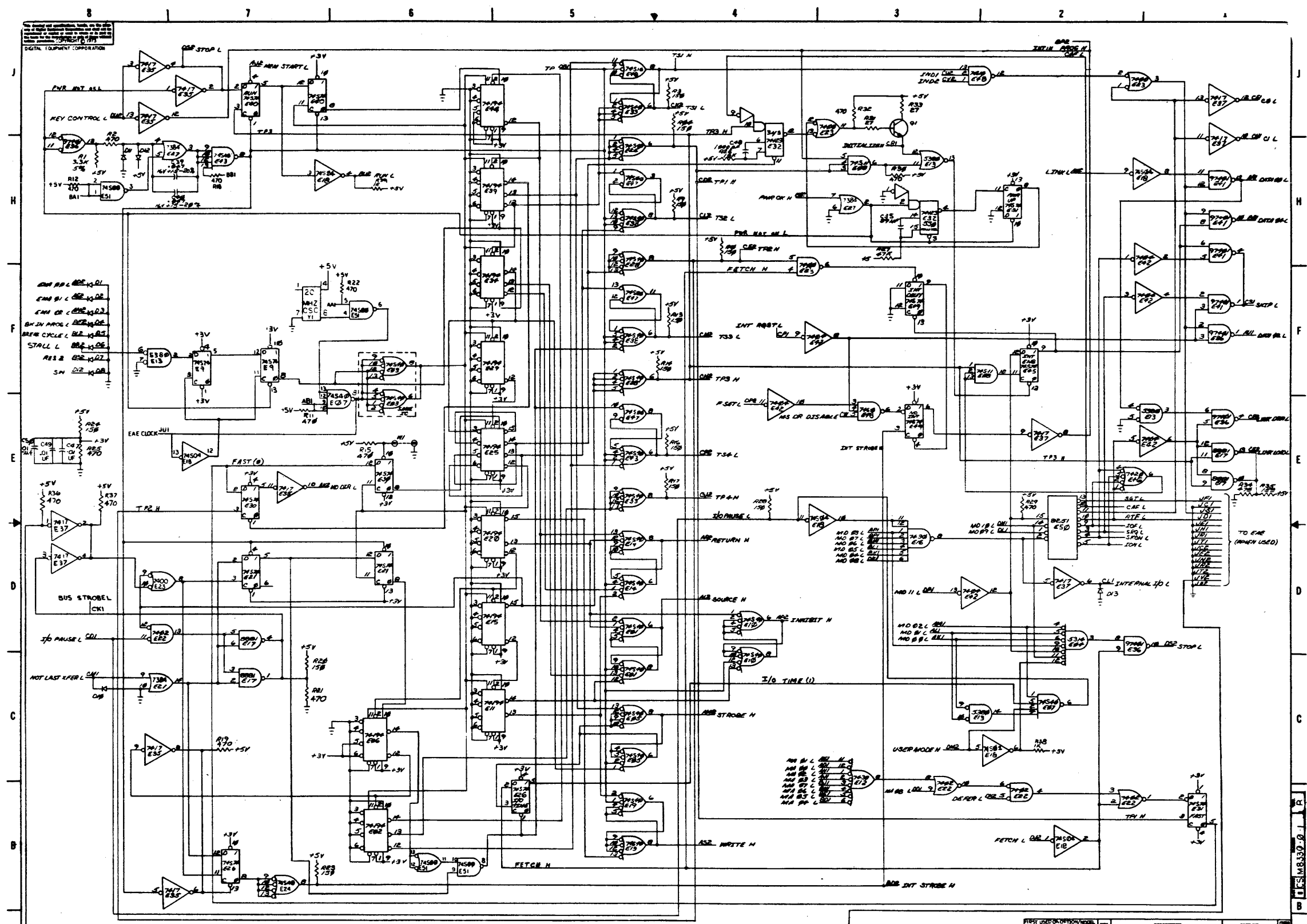
PARTS LIST EQUIPMENT CORPORATION TITLE M8340 MAJOR REG CONT.	A-M-L-KKB-E SCALE NONE SHEET 4 OF 4
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REV.	DESCRIPTION	DATE	BY
1	ISSUED FOR FABRICATION	11-1-68	W. J. ...
2	REVISION	11-1-68	W. J. ...
3	REVISION	11-1-68	W. J. ...
4	REVISION	11-1-68	W. J. ...
5	REVISION	11-1-68	W. J. ...
6	REVISION	11-1-68	W. J. ...
7	REVISION	11-1-68	W. J. ...
8	REVISION	11-1-68	W. J. ...
9	REVISION	11-1-68	W. J. ...
10	REVISION	11-1-68	W. J. ...

ECS M832B-0-1

BUS LOADS



UNLESS OTHERWISE SPECIFIED
EQUIPMENT CORPORATION

REV. USE OR OPTION NO.	OPT.	DESCRIPTION	PART NO.
MR8-F			
EQUIPMENT CORPORATION			
TIMING GENERATOR			
EICS MB330-01			
REV. 1			

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 11/19/74

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>Larry Nash</i> 12/24/74	APPD <i>Carl Chize</i>	SIZE A	CODE SP	NUMBER KM8-A-1	REV
--------------------------------	------------------------	---------------	---------	----------------	-----

DEC 16-(392)-1079-N971
DRA 107

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

I GENERAL

This procedure defines the performance standards required of the KM8A*, option board #2. This procedure refers to both system and add-on acceptance.

NOTE: If KM8A was shipped as part of a PDP-8A system, then proceed to installation procedure.

- * Memory Extension & Time Share
- Bootstrap Loaders
- Power Fail/Auto Restart

II INSPECTION

After removing the KM8A from the packing material, inspect the module for the following:

1. Inventory hardware against shipping list.
2. Inventory software against software list, if ordered.
3. Inventory prints against shipping list, if ordered.
4. Check module for loose or broken components.

III INSTALLATION PROCEDURE

Install the equipment using the following procedure:

1. Set the switches as indicated by the diagnostic write up.

NOTE: Refer to Operator's Handbook for switch setting descriptions.

2. Insure that the PDP-8A power is removed from the Omnibus™.
3. Insert the KM8A into the second or third slot of the Omnibus™.
4. Turn the power back "ON".

IV ACCEPTANCE PROCEDURE

Perform the acceptance procedure defined in Table A. If abnormal indications are encountered, refer to the diagnostic listing for the type of error. Reference the diagnostic write ups and Operator's Manual for instructions for loading diagnostics.

SIZE A	CODE SP	NUMBER KM8-A-1	REV
---------------	---------	----------------	-----

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

IV ACCEPTANCE PROCEDURE (continued)

Equipment required:

1. PDP-8A with 1K min. R/W Memory
2. Paper Tape Input Device
3. Diagnostic and Listings
4. Programmer's Console (KC8-A & DKC8-A)
5. W987 Quad Extender

NOTE: If the programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.

TABLE A

Acceptance of KM8A with 4K of R/W Memory

<u>Program Name</u>	<u>Maindec #</u>	<u>Accept Time</u>	<u>Restrictions</u>
KM8A Option Test #2	08-DJKMA-PB	30 min	4K R/W Memory Min

Acceptance of KM8A with Less than 4K R/W Memory

KM8A Option Test #2 Segment #1 (RIM)	08-DJKMA -PM1	10 min	1K R/W memory min
KM8A Option Test #2 Segment #2 (RIM)	08-DJKMA -PM2	10 min	1K R/W Memory Min
KM8A Option Test #2 Segment #4 (RIM)	08-DJKMA -PM4	10 min	1K R/W Memory Min

SIZE	CODE	NUMBER	REV
A	SP	KM8-A-1	

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BOOTSTRAP/AUTO RESTART FUNCTIONAL SWITCH SETTINGS							
DESIRED FUNCTION	ACTIVATING SIGNAL	SI-4	SI-5	SI-6	SI-7	SI-8	S2-1
BOOTSTRAP ENABLED	"BOOT" SW	*	SPARE	OFF	OFF	ON	N/A
AUTO-RESTART DISABLED	N/A	*		ON	ON	ON	N/A
BOOTSTRAP ENABLED	"BOOT" SW	*		ON	ON	OFF	N/A
AUTO-RESTART DISABLED	"AC LOW"	*		ON	OFF	OFF	N/A
BOOTSTRAP ENABLED	"AC LOW"	*		ON	OFF	OFF	N/A
AUTO-RESTART DISABLED	N/A	*	SPARE	ON	OFF	ON	N/A
TIME SHARE DISABLED	N/A	N/A	N/A	N/A	N/A	N/A	ON
TIME SHARE DISABLED	N/A	N/A	N/A	N/A	N/A	N/A	OFF

BOOTSTRAP SELECT SWITCH SETTINGS FOR 465A2/469A2 ROMS									
PROGRAM	S2-5	S2-6	S2-7	S2-8	SI-1	SI-2	SI-3	ROM ST ADD	MEM ST ADD
HI-LO PTR	ON	ON	ON	OFF	ON	ON	ON	20	7734
RK8E	ON	OFF	ON	OFF	ON	OFF	ON	124	24
RX8E	ON	OFF	OFF	ON	OFF	ON	ON	150	33
RL8A	OFF	ON	OFF	OFF	OFF	ON	OFF	272	1

* RX8E BOOT FOR BOTH RX01 AND RX02

AUTO-RESTART SELECT SWITCH SETTINGS			
RESTART ADDRESS	S2-2	S2-3	S2-4
0	OFF	OFF	OFF
200	OFF	ON	OFF
2000	ON	OFF	OFF
4000	ON	ON	OFF

NOTES: * SI-4 "OFF"-BOOTSTRAP CAN BE ACTIVATED BY "BOOT" SW EITHER IN THE RUN OR "RUN" STATE. SI-4 "ON"-BOOTSTRAP CAN BE ACTIVATED BY "BOOT" SW IN THE RUN STATE.

1. "AC LOW" WILL CAUSE AUTO-RESTART OR BOOTSTRAP DEPENDING ON SWITCH SETTINGS TO OCCUR ONLY IN THE "RUN" OR STOPPED STATE SI-6,7,8 "OFF"-BOOTSTRAP AND AUTO-RESTART DISABLED.

2. E76 AND E61 ARE NOT ON THE YC VARIATION KMB-AD. ALL OTHER PARTS REMAIN THE SAME.

3. IF AUTO-RESTART IS ENABLED, THE AUTO-START FEATURE OF THE CPU (M8315) MUST BE DISABLED.

4. AUTO RESTART SELECT SWITCHES ARE DEFINED AS FOLLOWS:

A. ROM ADDRESS RANG; 0-16.

B. ON=LOGIC 1 OR LOW; OFF=LOGIC 0 OR HIGH.

C. ORDER OF SIGNIFICANCE

$S_{22} = 2^3 = 10$
 $S_{23} = 2^2 = 4$
 $S_{24} = 2^1 = 2$

5. TO CONFIGURE MODULE FOR USE WITH KT8-A OPTION, INSTALL JUMPERS AS SHOWN BELOW.

	W1	W2	W3	W4
NORMAL	IN	OUT	CUT	CUT
WITH K7EA	OUT	IN	IN	IN

BOOTSTRAP SELECT SWITCH SETTINGS FOR 158A2/159A2 ROMS									
PROGRAM	S2-5	S2-6	S2-7	S2-8	SI-1	SI-2	SI-3	ROM ST ADD	MEM ADD START
HI-LO PTR RDR	ON	ON	ON	OFF	ON	ON	ON	20	7734
RK8E	ON	OFF	ON	OFF	ON	OFF	ON	124	24
RX8E	ON	OFF	OFF	ON	OFF	ON	ON	150	33
RFO8/DF32D	OFF	ON	OFF	ON	OFF	ON	ON	252	7750
TABE	OFF	ON	OFF	OFF	OFF	ON	OFF	272	4000

3. BOOTSTRAP SELECT SWITCHES ARE DEFINED AS FOLLOWS:

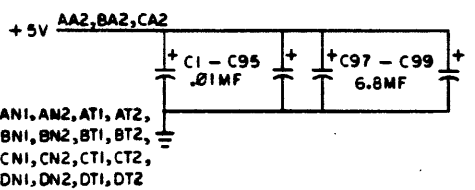
A. ROM ADDRESS RANGE: 0-377

B. ON=LOGIC 0 OR LOW; OFF=LOGIC 1 OR HIGH

C. ORDER OF SIGNIFICANCE

$S_{25} = 2^7 = 200$
 $S_{26} = 2^6 = 100$
 $S_{27} = 2^5 = 40$
 $S_{28} = 2^4 = 20$
 $S_{11} = 2^3 = 10$
 $S_{12} = 2^2 = 4$
 $S_{13} = 2^1 = 2$

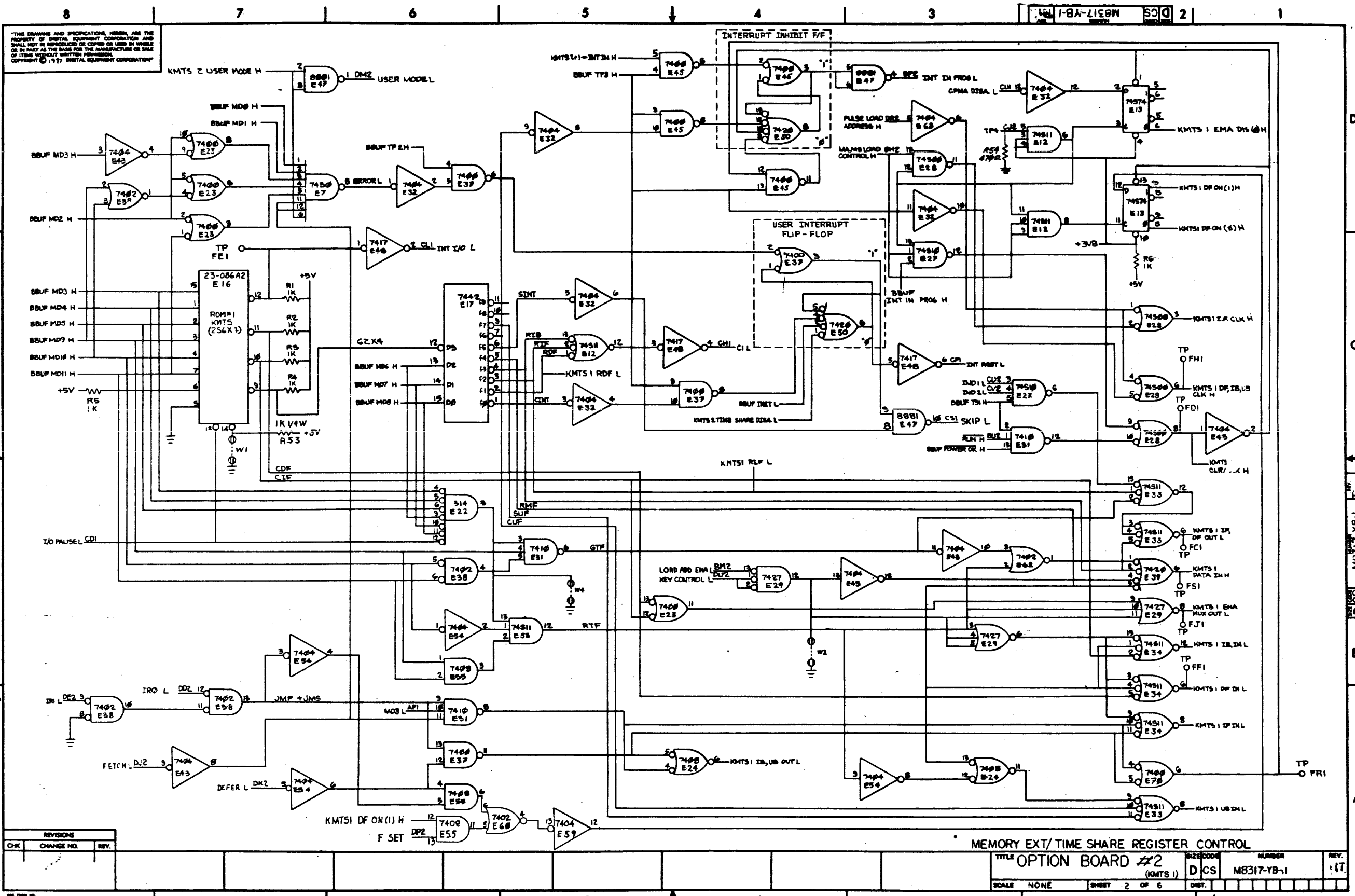
THE USE OF ADDRESS IS CONTROLLED BY THE BOOTSTRAP/AUTO-RESTART LOGIC



PART CALLED FCR			SUBSTITUTE PART		
QTY	PART NO	DESCRIPTION	QTY	PART NO	DESCRIPTION
96	1001610-01	.01UF DISC	96	1001610-01	.01UF GLASS
3	1503100	DEC 3009B	3	1503238	DEC 6531
6	1911330	74173	6	1911711	811C
1	1909704	314	1	1910391	7314
			1	19C9972	6314
			1	1910389	7314
6	19C9705	6661	6	19C9973	97401
1	23158A2	RCM1 (E76)	1	23465A2	RCM1 (E76)
1	23159A2	RCM2 (E81)	1	23409A2	RCM2 (E81)

REV.	DATE	DESCRIPTION
1		

DATE	BY	CHKD	APP'D	TITLE
				OPTICN BOARD #2
DOCUMENT NUMBER				
D	CS	M8317-YB-1	T	
B-DD-M8317-YB				



REVISIONS		
CHK	CHANGE NO.	REV.

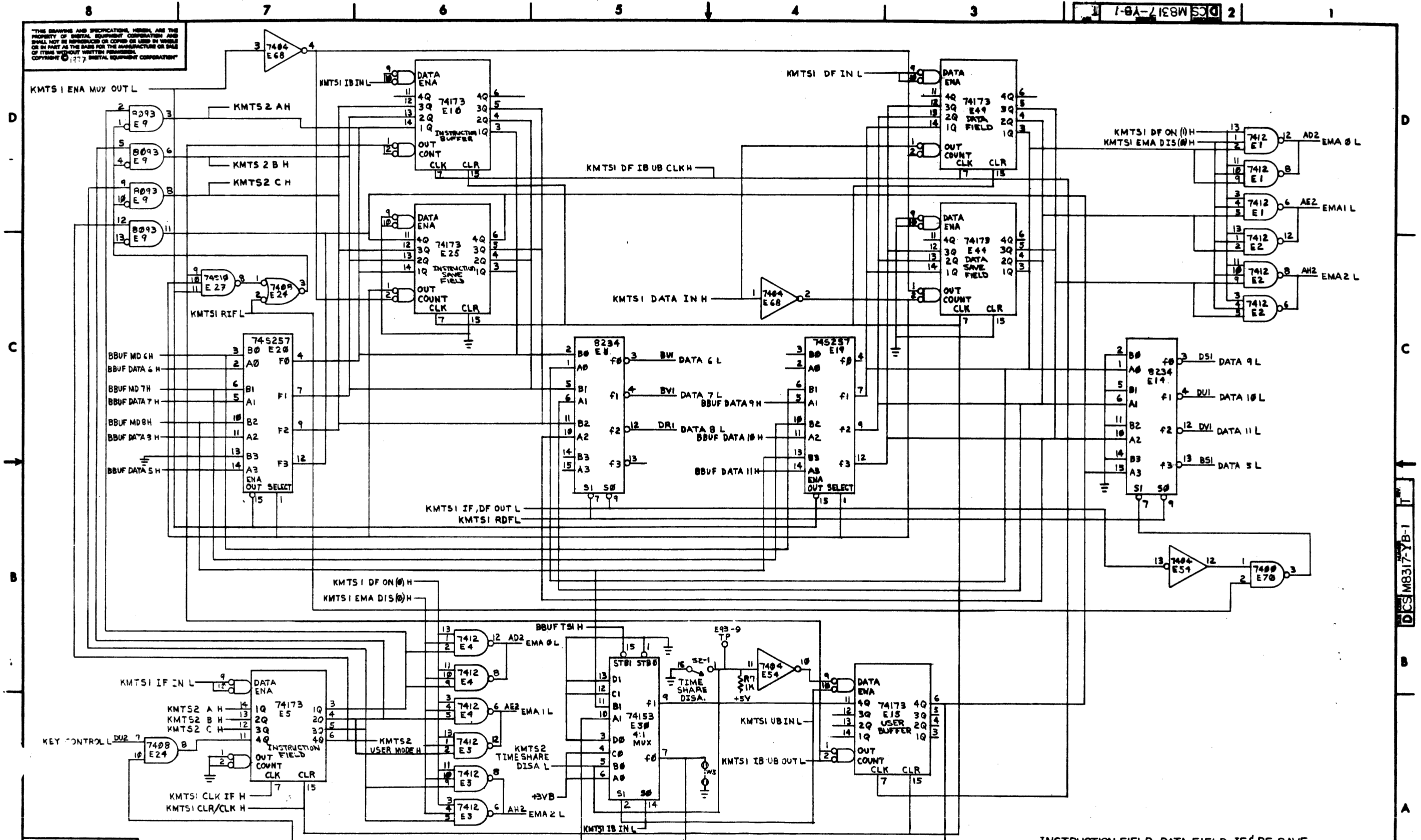
MEMORY EXT/TIME SHARE REGISTER CONTROL			
TITLE	OPTION BOARD #2	SIZE/COOR	NUMBER
	(KMTS I)	D CS	M8317-YB-1
SCALE	NONE	SHEET	2 OF 6
		DIST.	

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1-8A-2100W 2

REV. 11/77 M8317-YB-1

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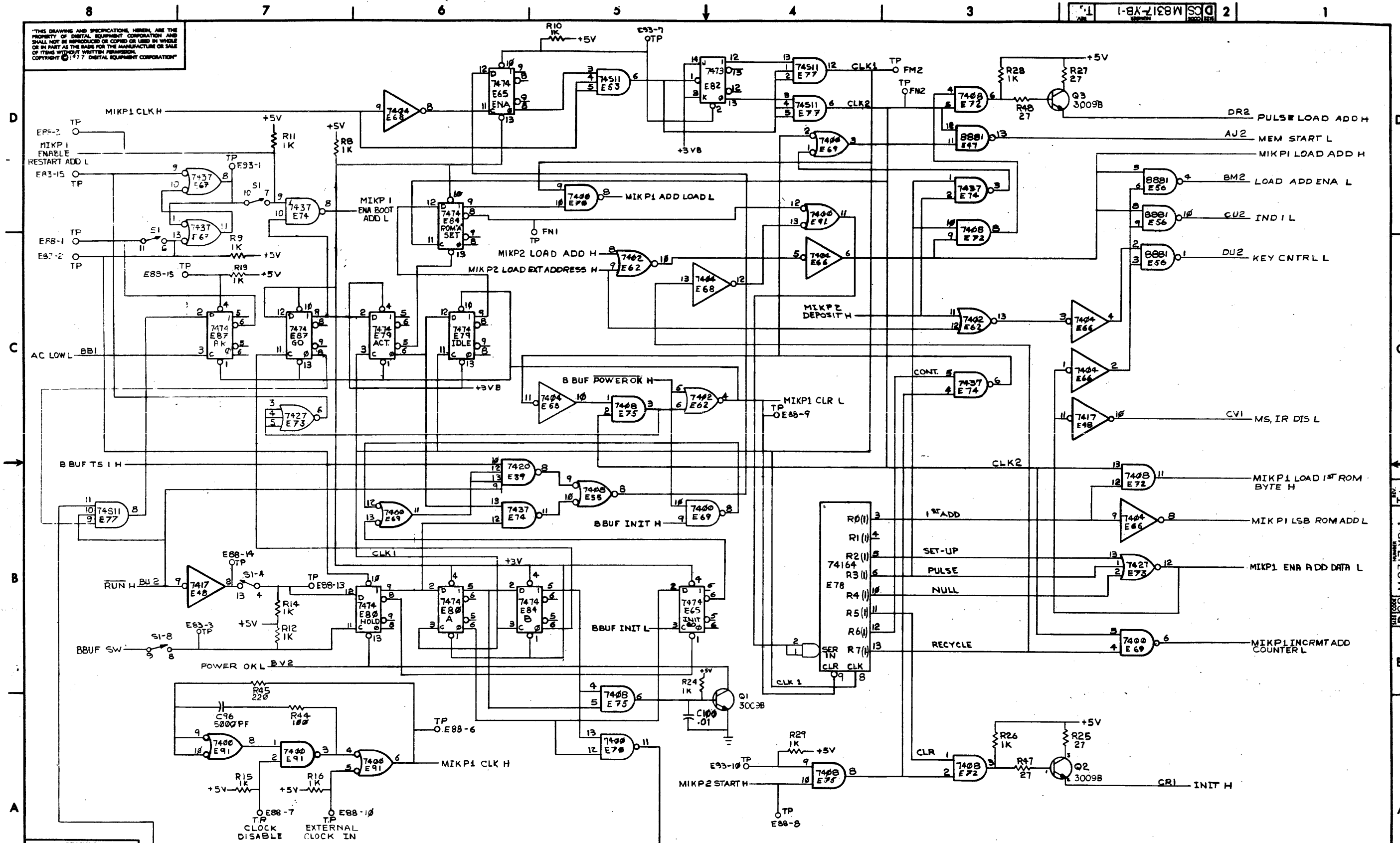
REVISIONS		
CHK	CHANGE NO.	REV.

INSTRUCTION FIELD, DATA FIELD, IF & DF SAVE FIELD, INSTRUCTION BUFFER, USER BUFFER

TITLE	OPTION BOARD #2	SIZE CODE	NUMBER	REV.
	(KMTS2)	D CS	M8317-YB-1	T
SCALE	NONE	SHEET	3 OF 6	DIST.

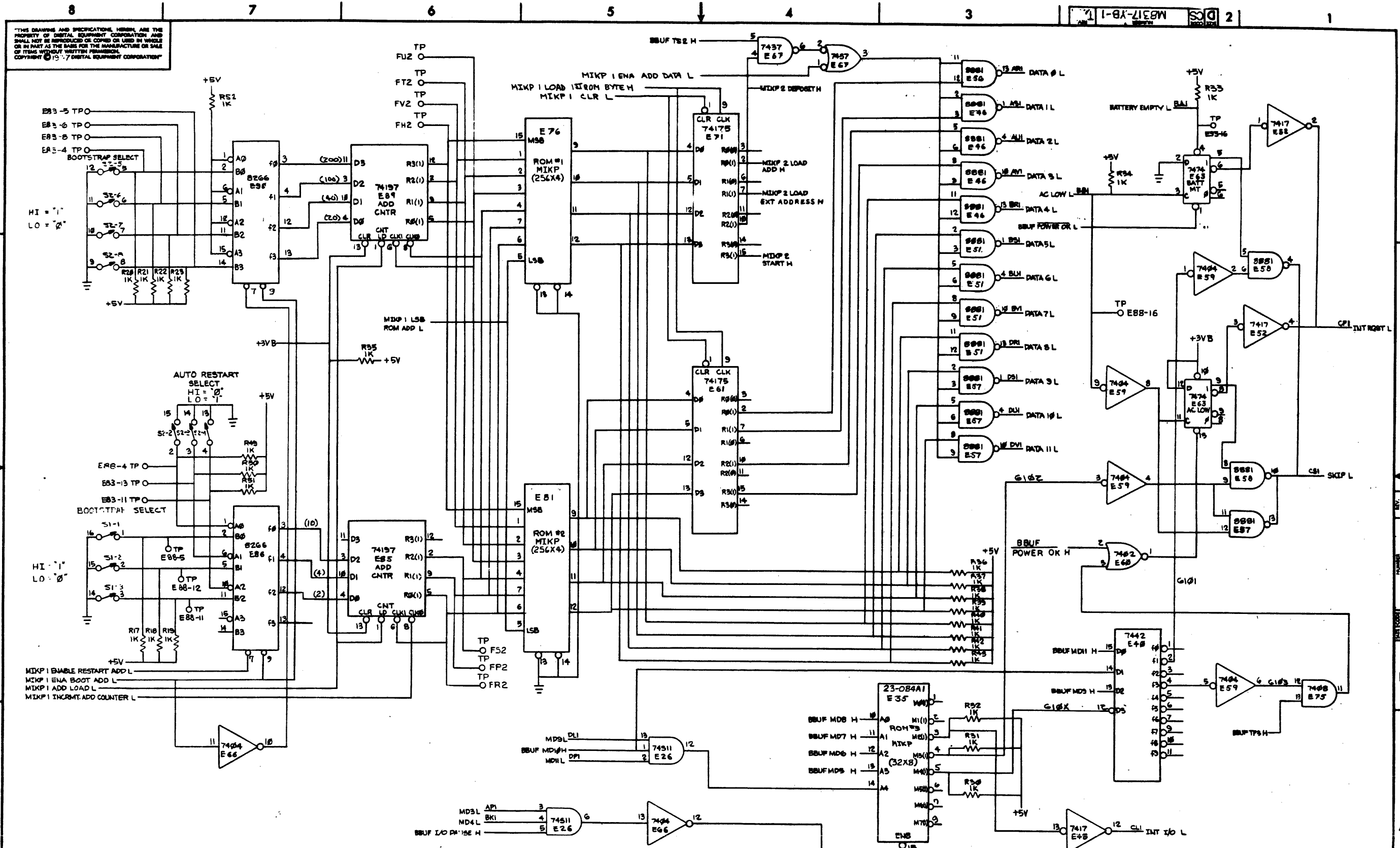
D CS M8317-YB-1

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		OPTION BOARD # 2 (MIKP1)		SIZE CODE	D CS	NUMBER	M8317-YB-1	REV.	
SCALE	+	SHEET	4	OF	6	DIST.			



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1-8A-M8317-YB-1

REVISIONS		
CHK	CHANGE NO.	REV.

BOOTSTRAP/AUTO-RESTART ROMS AC LOW AND BATTERY EMPTY FLAGS

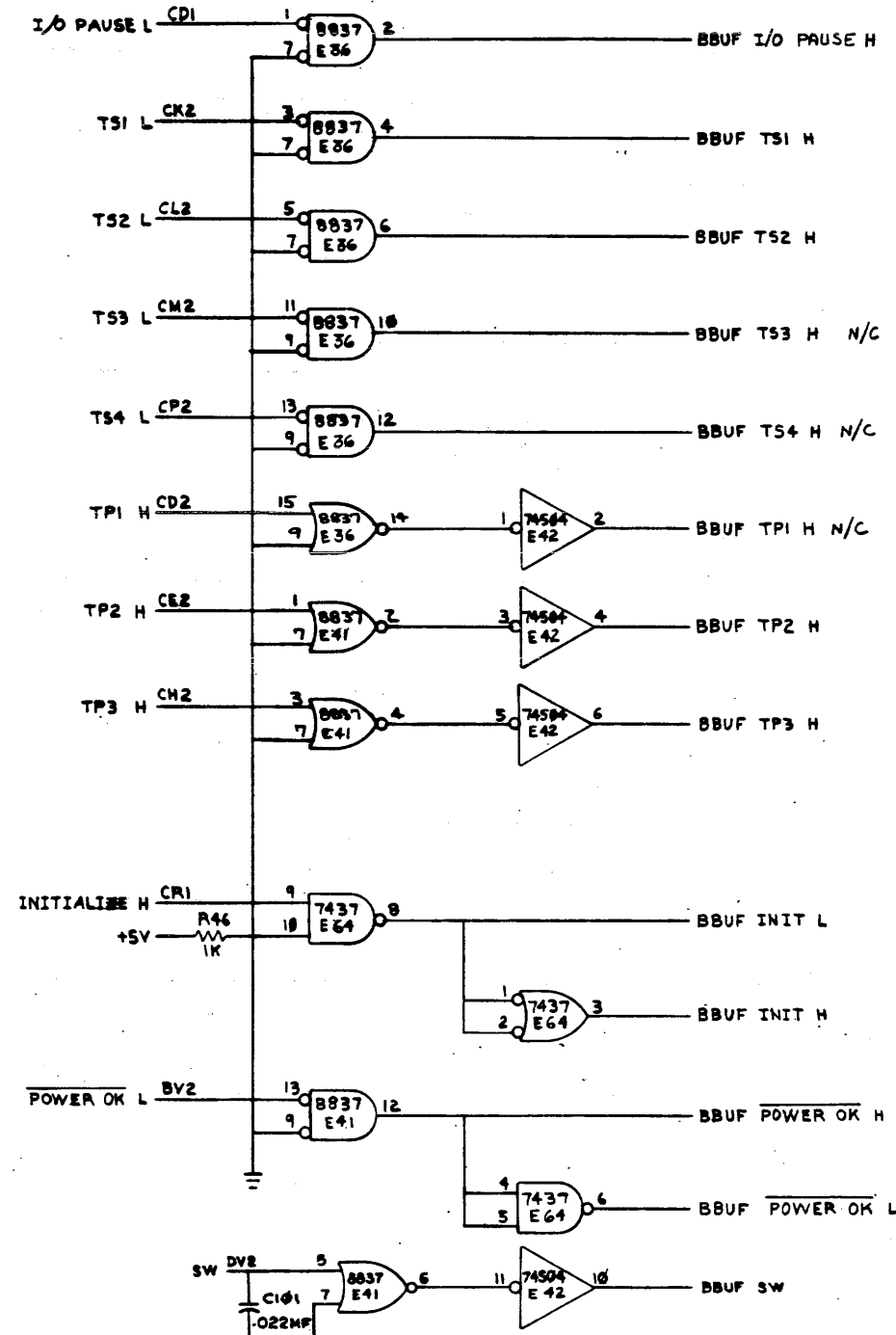
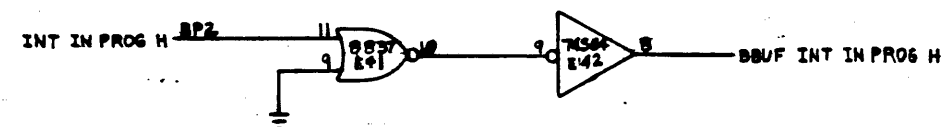
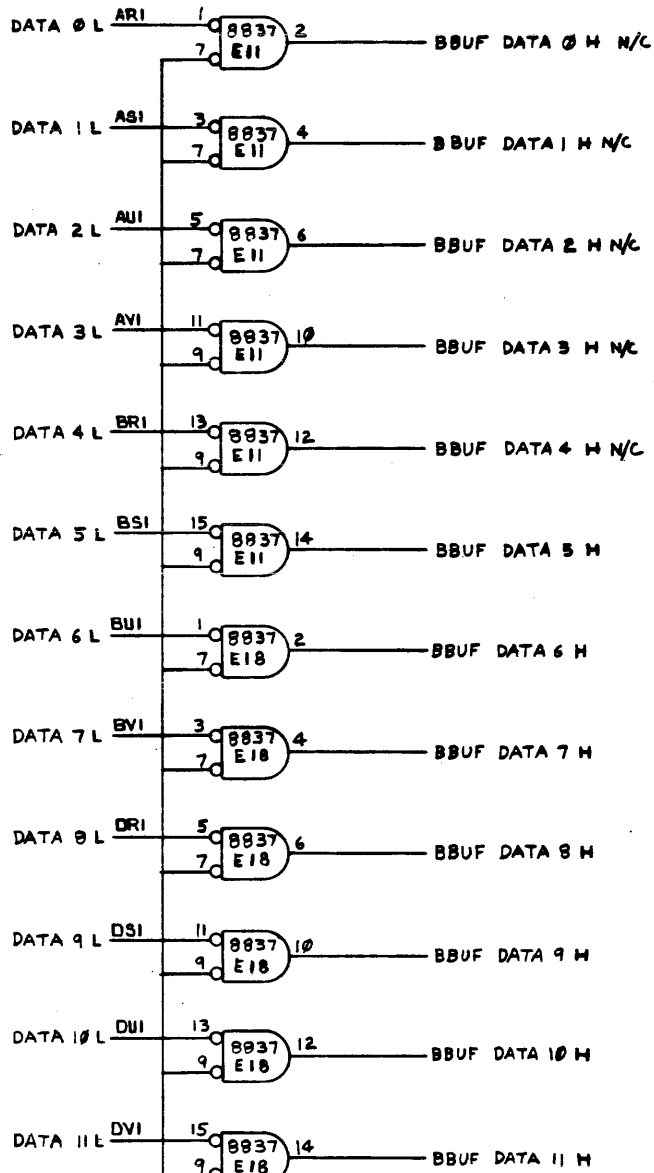
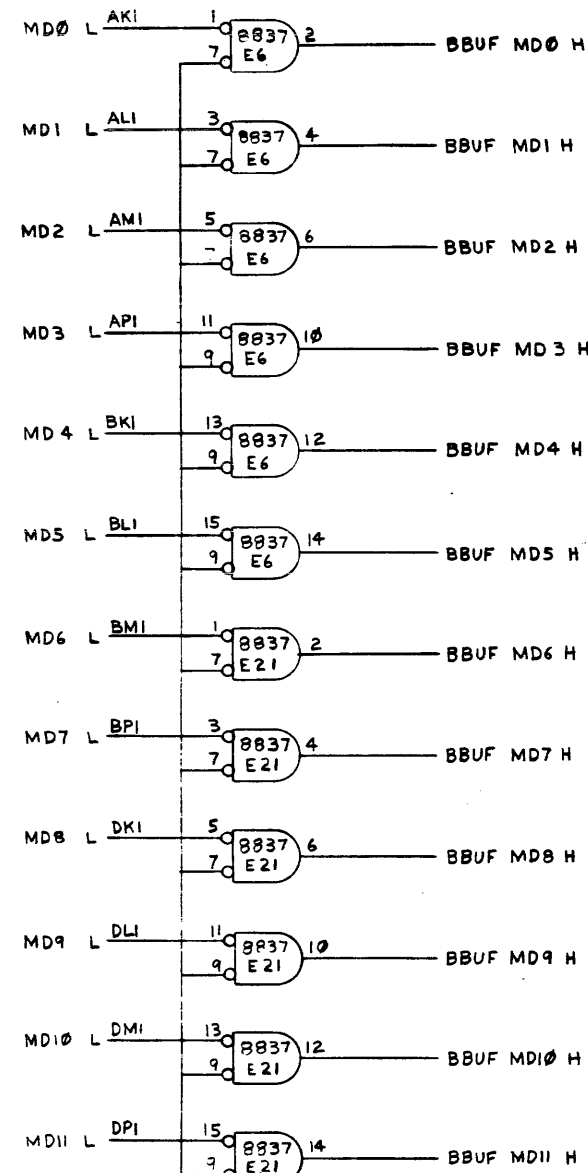
TITLE	OPTION BOARD #2 (MIKP 2)	SIZE CODE	D CS	NUMBER	M8317-YB-1	REV.	1T
SCALE	NONE	SHEET	5 OF 6	DST.			

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NOTE: SIGNALS WITH N/C HAVE NO CONNECTION

DCS M8317-YB-1 2

D
C
B
A



BUS BUFFERS

REVISIONS		
CHK	CHANGE NO.	REV.

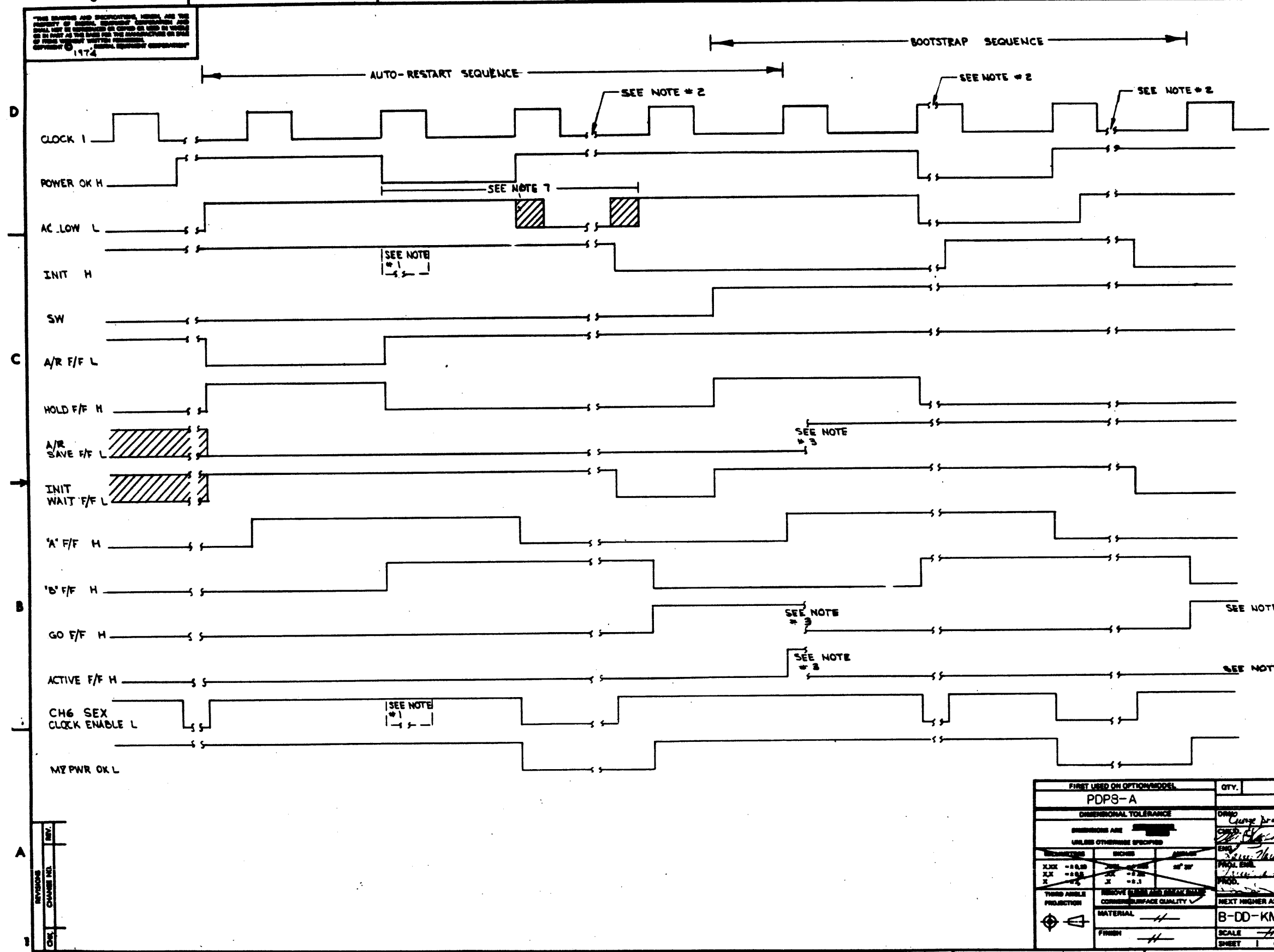
TITLE	8/A INTERNAL OPTION #2 (BBUF)	SIZE CODE	DCS	NUMBER	M8317-YB-1	REV.	1
SCALE	NONE	SHEET	6	OF	6	DATE	

DCS M8317-YB-1

AL 1

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 1974

- NOTES:**
1. CLOCK ENABLE MAY BE FALSE AT THIS TIME, IF INIT. H IS FALSE. CLOCK ENABLE THEN RETURNS HIGH (ASSERTED) WHEN INTN BECOMES TRUE
 2. THE LOGIC WAITS FOR INITIALIZED TO START OR END BEFORE CONTINUING
 3. THESE FLIP-FLOPS ARE CLEARED AT THE CONCLUSION OF THE AUTO RESTART CYCLE
 4. ACTIVE SETS ON THE NEXT CLOCK PULSE THEN CLEARS AT THE END OF THE CYCLE
 5. GO F/F CLEARS AT THE END OF CYCLE
 6. SEE D-FD-KMB-A FOR DETAILED TIMING DATA AFTER ACTIVE IS SET
 7. THE SWITCHING TIMES FOR AC LOW ARE A FUNCTION OF THE SLEW RATE OF THE CIRCUITS IN THE POWER SUPPLY



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
POPS-A				
PARTS LIST				
DIMENSIONS ARE UNLESS OTHERWISE SPECIFIED		DATE	DATE	
UNLESS OTHERWISE SPECIFIED		DATE	DATE	
THIRD ANGLE PROJECTION		DATE	DATE	
REMOVE ALL SURFACE MARKS		DATE	DATE	
CORNER SURFACE QUALITY		DATE	DATE	
MATERIAL		DATE	DATE	
FINISH		DATE	DATE	
NEXT HIGHER ASSEMBLY		TITLE		
B-DD-KMB-A		AUTO RESTART/ BOOTSTRAP START-UP SEQUENCE		
SCALE		NUMBER		
SHEET 1 OF 1		D TD KMB-A-4		
		REV.		

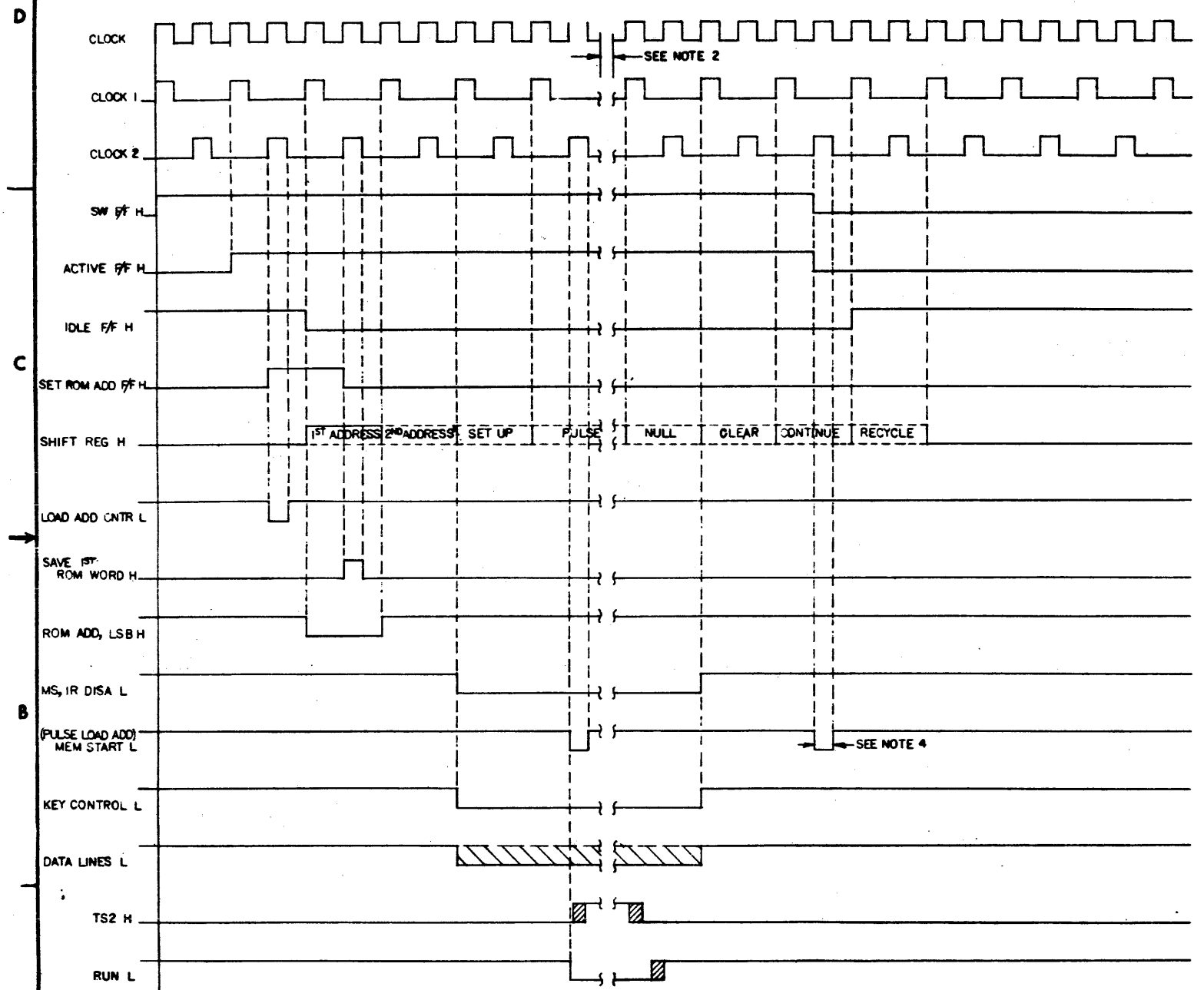
REV.	CHG.	DATE
1		

D TD KMB-A-4

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9-V-8MK DTD 2

- NOTES:
- ONE 'DEPOSIT' CYCLE IS SHOWN IN DIAGRAM.
 - WHEN 'RUN' IS TRUE (LOW) ALL TIMING IS HELD OFF UNTIL THE NEXT CLOCK PULSE AFTER 'RUN' GOES FALSE (HIGH).
 - FOR THE 'LOAD ADD' CYCLE SIGNALS REMAIN THE SAME AS SHOWN EXCEPT THAT 'PULSE LOAD ADD' REPLACES 'MEM START' AND 'KEY CONTROL' IS NEGATED. FOR 'EXT. LOAD ADD' KEY CONTROL IS TRUE.
 - MEM START APPEARS HERE ONLY FOR THE 'START' FUNCTION. THE EARLIER MEM START IS FOR 'DEPOSITS' ONLY.

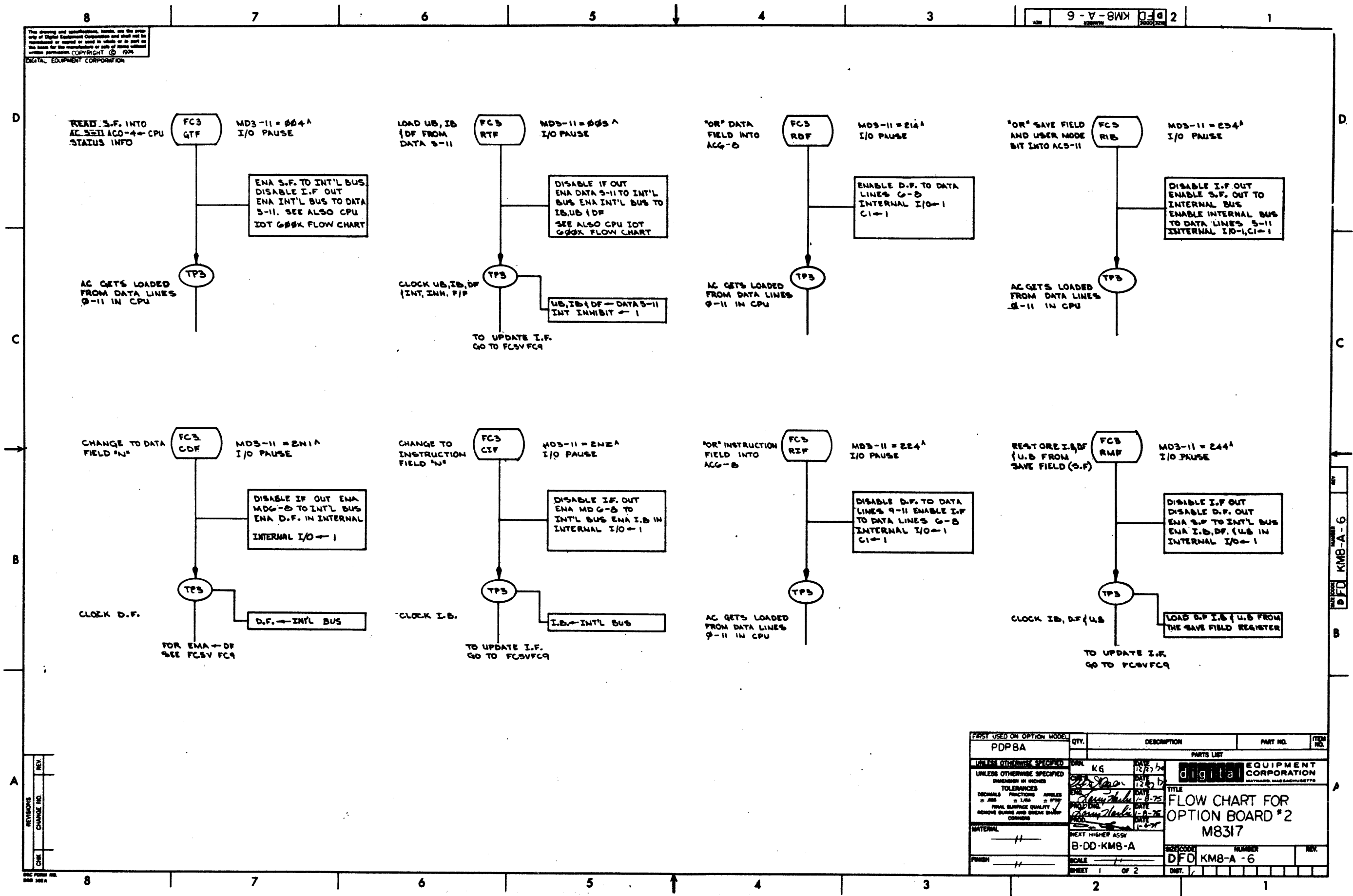


REV.	
CHG	
REV.	
CHG	

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8-A					
DIMENSIONAL TOLERANCE		PARTS LIST			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		DRN	DATE	TITLE	
		<i>M. W. White</i>	7-3-74	digital	
		<i>John G. ...</i>	DATE	BOOTSTRAP TIMING DIAGRAM	
		<i>Clayton ...</i>	DATE		
		<i>...</i>	DATE		
		<i>...</i>	DATE		
THIRD ANGLE PROJECTION		REMOVE BURNS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		NEXT HIGHER ASBY.	
MATERIAL		B-DD-KMB-A		SIZE CODE	NUMBER
FINISH		NONE		D TD	KMB-A-5
		SCALE		REV.	
		SHEET		DST.	

REV. 1
D TD
KMB-A-5

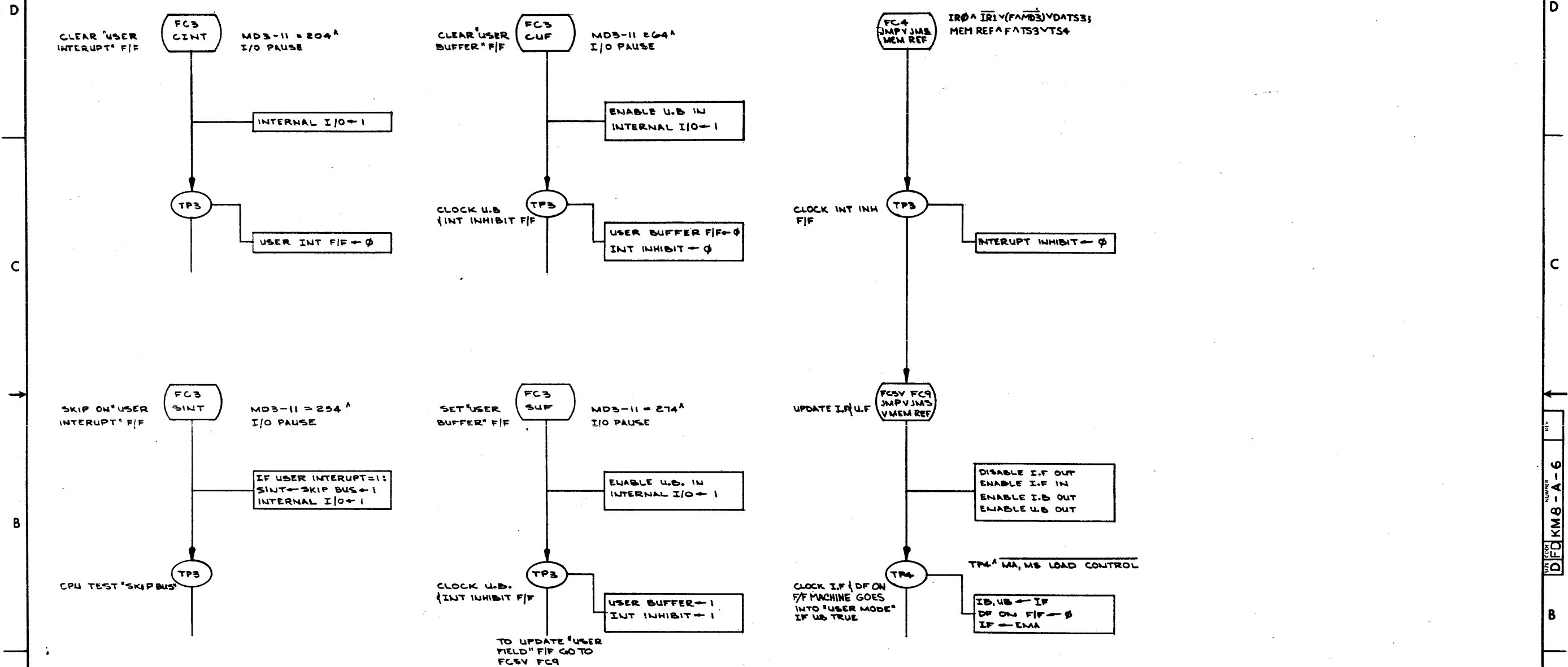
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REV	
CHANGE NO.	
CHK	

FIRST USED ON OPTION MODEL: PDP8A	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES TOLERANCES DECIMALS FRACTIONS HOLE DIA. ±.0015 FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	DATE 12/25/74	DATE 12/25/74	digital EQUIPMENT CORPORATION MAYFIELD, MASSACHUSETTS	
MATERIAL H	NEXT HIGHER ASSY B-00-KMB-A	DATE 1-8-75	TITLE FLOW CHART FOR OPTION BOARD #2 M8317	
FINISH H	SCALE 1:1	DATE 1-8-75	NUMBER DFD KMB-A-6	
SHEET 1 OF 2		REV.		

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REVISIONS		
CHK	CHANGE NO.	REV.

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**DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS**

ENGINEERING SPECIFICATION

DATE 5/8/74

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	E.C.O. CHANGE	00001	L.NARHI	14 MAY 76	<i>L. Narhi</i>	21-MAY
B	E.C.O. CHANGE	00002	L.NARHI	12-14-77	<i>L. Narhi</i>	5-JAN-78

ENG Larry Narhi	APPD <i>Larry Narhi</i>	SIZE A	CODE SP	NUMBER KM8-A-7	REV B
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DEC FORM NO.
DRA 107

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

1. Introduction

This document describes the organization of the two 256 x 4 ROMs, hereafter called ROM #1 and ROM #2, that control and supply data for the Auto-Restart and Bootstrap portions of Option Board #2.

This information is made available to help users program their own ROMs for their specific Auto-Restart and/or Bootstrap program(s).

2. Organization

The two ROMs are connected as follows: the address lines are connected in parallel; i.e., two corresponding address lines of each ROM are connected together, the outputs are arranged in serial fashion forming an 8 bit word, 4 outputs from each ROM. Because 12 bits are required for data/address information, two sequential addresses must be accessed from the ROMs to form a 16 bit word. Where the first 8 bits are temporarily stored in a register, then the next 8 bits are accessed from the ROMs. At this point the control then decides what to do with 12 of the 16 bits. There are four possible actions that can take place at this time:

- a) Load Address
- b) Load Extended Address, IF AND DF
- c) Deposit
- d) Start

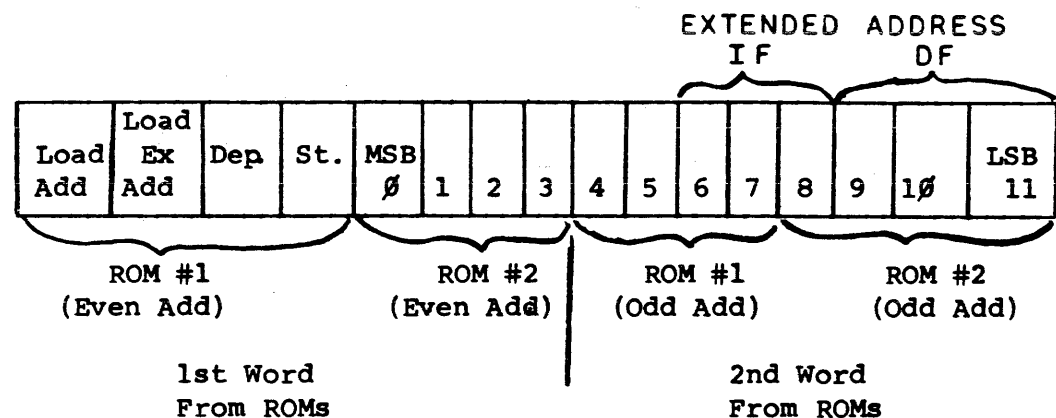
The remaining 4 bits of the 16 actually tell the control which of the four actions are to take place. So the 16 bit word would look like the word in Figure 1.

SIZE A	CODE SP	NUMBER KM8-A-7	REV B
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DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

Figure 1



The use of ROMs that have 256 addressable locations allows up to 128 words of ROM storage. These 128 locations may be used for Bootstrap and/or Auto-restart programs. Any Auto-restart or Bootstrap program may be located anywhere in the ROMs so long as the program starts in an even address in the ROM. If it is required that both Bootstrap and Auto-restart programs be accessible at the same time, activated by different signals; of course the Auto-restart program(s) must be located in addresses 0 through 15 in the ROMs. This is due to the addressing limits of the Auto-restart select switches.

3. Auto-Restart/Bootstrap Sequence

The following events should take place when an auto-restart is initiated:

- a) Load a 12 bit address
- b) LOAD THE IF AND DF AND START.

The following events should take place when the Bootstrap is initiated:

- a) Load a 12 bit initial address.
- b) Load the IF AND DF
- c) Deposit 12 bit data words repeating as required by length of program to be deposited.
- d) Load a 12 bit starting address and start.

SIZE	CODE	NUMBER	REV
A	SP	KM8-A-7	B

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

The decision to do a Bootstrap or an auto-restart is directed by a set of switches on the module. The Bootstrap may be actuated by the transition of the signal AC Low from a logic low to a logic high or by a similar transition of the SW line on the OMNIBUS.

AN AUTO-RESTART MAY ONLY BE INITIATED BY THE AC LOW SIGNAL. IT SHOULD BE OBVIOUS THAT BOTH THE BOOTSTRAP OR AUTO-RESTART SHOULD NOT BE ACTIVATED BY THE SAME INITIALIZING SIGNAL.

4. ROM Programming Examples

Auto-restart example:

- a) Load address 0200
- b) Load field 0, start

Starting at ROM address 004

Bootstrap example:

- a) Load address 0023
- b) Load field 7 (BOTH IF AND DF)
- c) Deposit 2000
- d) Deposit 6745
- e) Deposit 0023
- f) Deposit 7650
- h) Deposit 5024
- j) Deposit 6733
- k) Deposit 5031
- l) Load address 0024 and start

Starting at ROM address 124.

SIZE	CODE	NUMBER	REV
A	SP	KM8-A-7	B

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

Auto-Restart example:

Bit Add	ROM #1				ROM #2			
	4	3	2	1	4	3	2	1
4	1	0	0	0	0	0	0	0
5	1	0	0	0	0	0	0	0
6	0	1	0	1	0	0	0	0
7	0	0	0	0	0	0	0	0

Load Address
0200
Load Ext. Add 0
and Start

NOTE: Logic one (1) = +3V

Bootstrap example:

Bit Add	ROM #1				ROM #2			
	4	3	2	1	4	3	2	1
124	1	0	0	0	0	0	0	0
125	0	0	0	1	0	0	1	1
126	0	1	0	0	0	0	0	0
127	0	0	1	1	1	1	1	1
130	0	0	1	0	0	1	0	0
131	0	0	0	0	0	0	0	0
132	0	0	1	0	1	1	0	1
133	1	1	1	0	0	1	0	1
134	0	0	1	0	0	0	0	0
135	0	0	0	1	0	0	1	1
136	0	0	1	0	1	1	1	1
137	1	0	1	0	1	0	0	0
140	0	0	1	0	1	0	1	0
141	0	0	0	1	0	1	0	0
142	0	0	1	0	1	1	0	1
143	1	1	0	1	1	0	1	1
144	0	0	1	0	1	0	1	0
145	0	0	0	1	1	0	0	1
146	1	0	0	1	0	0	0	0
147	0	0	0	1	0	1	0	0

Load Add 0023
Load Ext Add 7
Dep 2000
Dep 6745
Dep 0023
Dep 7650
Dep 5024
Dep 6733
Dep 5031
Load Add 24 & Start

SIZE CODE NUMBER REV
A SP KM8-A-7 B

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

5. ROMs

Unprogrammed ROMs should be purchased by the user from Digital Equipment Corporation. The part number for an unprogrammed 256 x 4 ROM is 23-000A2.

SIZE CODE NUMBER REV
A SP KM8-A-7 B

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DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS						
ENGINEERING SPECIFICATION					DATE 11/19/74	
TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A						
REVISIONS						
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>J. Harlin</i> 12/20/74	APPD <i>Carl Christie</i>	SIZE A	CODE SP	NUMBER DKC8-A-1	REV
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ENGINEERING SPECIFICATION	CONTINUATION SHEET																		
TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A																			
<p>I GENERAL</p> <p>This procedure defines the performance standard required of the DKC8*, option board #1. This procedure refers to both system acceptance and add-on acceptance.</p> <p>NOTE: If DKC8 was shipped as part of a PDP-8A system, proceed to installation procedure.</p> <p>* Serial Line Unit Real Time Clock Parallel I/O Programmer's Console Logic</p> <p>II INSPECTION</p> <p>After removing the DKC8 from the packing material, inspect the module for the following:</p> <ol style="list-style-type: none"> 1. Inventory hardware against shipping list. 2. Inventory software against software list, if ordered. 3. Inventory prints against shipping list, if ordered. 4. Check hardware for loose or broken components. <p>III INSTALLATION PROCEDURE</p> <p>Install the equipment using the following procedure:</p> <ol style="list-style-type: none"> 1. Set up switches as indicated by the diagnostic write up. <table style="margin-left: 20px; border: none;"> <tr> <td style="padding-right: 20px;">S1-1 thru S1-3</td> <td style="padding-right: 20px;">"ON"</td> <td>9600 baud</td> </tr> <tr> <td>S1-4</td> <td>"ON"</td> <td>Normally "ON"</td> </tr> <tr> <td>S1-5</td> <td>"ON"</td> <td>Real Time Clock Enable</td> </tr> <tr> <td>S1-6</td> <td>"ON"</td> <td>Normally "ON"</td> </tr> <tr> <td>S1-7</td> <td>"ON"</td> <td>One Stop Bit</td> </tr> <tr> <td>S1-8</td> <td>"OFF"</td> <td>Disable TTY 20 MA Filter</td> </tr> </table> <p>NOTE: Reference Operator's Handbook for switch setting descriptions.</p> <ol style="list-style-type: none"> 2. Insert TTY loop back cable (DEC Part #7008517) on DKC8. 3. Insert parallel I/O cable loop back cable (DEC Part # BC08R-1) on DKC8. 		S1-1 thru S1-3	"ON"	9600 baud	S1-4	"ON"	Normally "ON"	S1-5	"ON"	Real Time Clock Enable	S1-6	"ON"	Normally "ON"	S1-7	"ON"	One Stop Bit	S1-8	"OFF"	Disable TTY 20 MA Filter
S1-1 thru S1-3	"ON"	9600 baud																	
S1-4	"ON"	Normally "ON"																	
S1-5	"ON"	Real Time Clock Enable																	
S1-6	"ON"	Normally "ON"																	
S1-7	"ON"	One Stop Bit																	
S1-8	"OFF"	Disable TTY 20 MA Filter																	
	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">SIZE A</td> <td style="width: 15%;">CODE SP</td> <td style="width: 25%;">NUMBER DKC8-A-1</td> <td style="width: 45%;">REV</td> </tr> </table>	SIZE A	CODE SP	NUMBER DKC8-A-1	REV														
SIZE A	CODE SP	NUMBER DKC8-A-1	REV																

TITLE FIELD INSTALLATION AND ACCEPTANCE PROCEDURE FOR DKC8-A

III INSTALLATION PROCEDURE (continued)

4. Insert two programmer's console cables on DKC8.
5. Insure that the 8A Power is removed from the Omnibus™.
6. Insert DKC8 into the second or third slot of the Omnibus™.
7. Turn the power back "ON".
8. Check the operation of the programmer's console.

IV ACCEPTANCE PROCEDURE

Perform the acceptance procedure defined in Table A. If abnormal indications are encountered, refer to the diagnostic listing for type of error. Reference the diagnostic write ups and operator's manual for instructions on loading diagnostics.

Equipment Required:

1. PDP-8A with 1K or more R/W Memory
2. Paper Tape Input Device
3. Programmer's Console (KC8-A)
4. Diagnostic and Listings
5. TTY loop Back Cable
6. Parallel I/O Loop Back Cable
7. W987 Quad Extender

NOTE: If the programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.

SIZE	CODE	NUMBER	REV
A	SP	DKC8-A-1	

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A

TABLE A

Acceptance of DKC8 with 4K or More R/W Memory

Program Name	Maindec #	Accept Time	Restriction
DKC8-AA Option Test #1	08-DJDKA-PB	30 min	4K R/W Memory

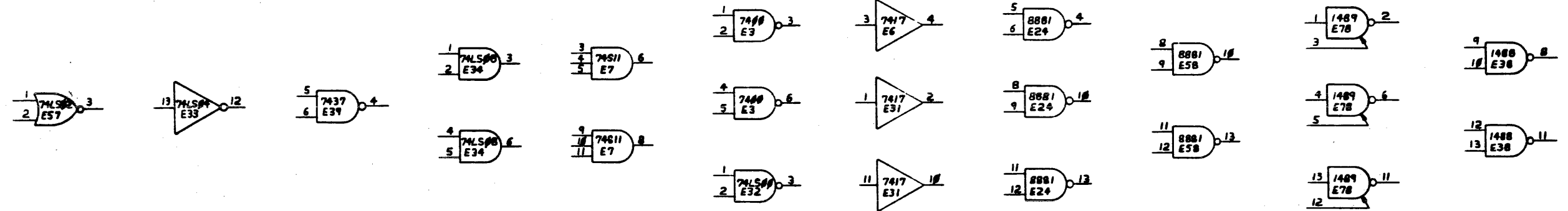
Acceptance of DKC8 with less than 1K of R/W Memory

DKC8-AA Opteion Test #1 Segment #1 (RIM)	08-DJDKA -PM1	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #2 (RIM)	08-DJDKA -PM2	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #3 (RIM)	08-DJDKA -PM3	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #4 (RIM)	08-DJDKA -PM4	10 min	1K R/W Memory

SIZE	CODE	NUMBER	REV
A	SP	DKC8-A-1	

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DIGITAL EQUIPMENT CORPORATION
M8316-0-1-1



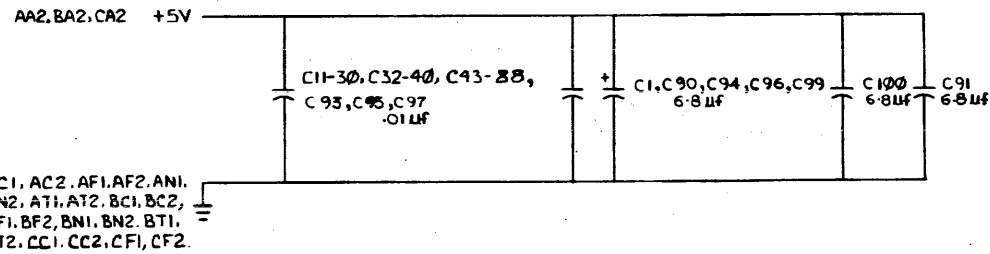
M8316 SWITCH SETTINGS

- SI-1 } SERIAL LINE BAUD RATE (SEE CHART)
- SI-2 }
- SI-3 }
- SI-4 }
- SI-5 ON= REAL TIME CLOCK ENABLED
OFF= REAL TIME CLOCK DISABLED
- SI-6 ON= TEST SWITCH (ALWAYS ON)
- SI-7 ON= 1 STOP BIT IN SLU CHARACTER
OFF= 2 STOP BITS IN SLU CHARACTER
- SI-8 ON= ASR/KSR 33 DR35 FILTER IN (ACROSS SLU 20 MA REC'V LEADS)
OFF= FILTER OUT
- SI-9 ON= TS1 CLEARS "DATA AVAIL" F/F IN PARALLEL I/O SECTION
OFF= "DATA AVAIL" NOT CLEARED BY TS1

SLU BAUD RATE SELECT CHART

SI-4	SI-3	SI-2	SI-1	BAUD RATE
ON	ON	ON	ON	50 BAUD
ON	ON	ON	OFF	75 BAUD
ON	ON	OFF	ON	110 BAUD
ON	ON	OFF	OFF	134.5 BAUD
ON	OFF	ON	ON	150 BAUD
ON	OFF	ON	OFF	300 BAUD
ON	OFF	OFF	ON	600 BAUD
ON	OFF	OFF	OFF	1200 BAUD
OFF	ON	ON	ON	1800 BAUD
OFF	ON	ON	OFF	2000 BAUD
OFF	ON	OFF	ON	2400 BAUD
OFF	ON	OFF	OFF	3600 BAUD
OFF	OFF	ON	ON	4800 BAUD
OFF	OFF	ON	OFF	7200 BAUD
OFF	OFF	OFF	ON	9600 BAUD
OFF	OFF	OFF	OFF	19.2 K BAUD

* SERIAL LINE WILL NOT RUN AT THIS BAUD RATE. THIS SETTING IS NOT TO BE USED



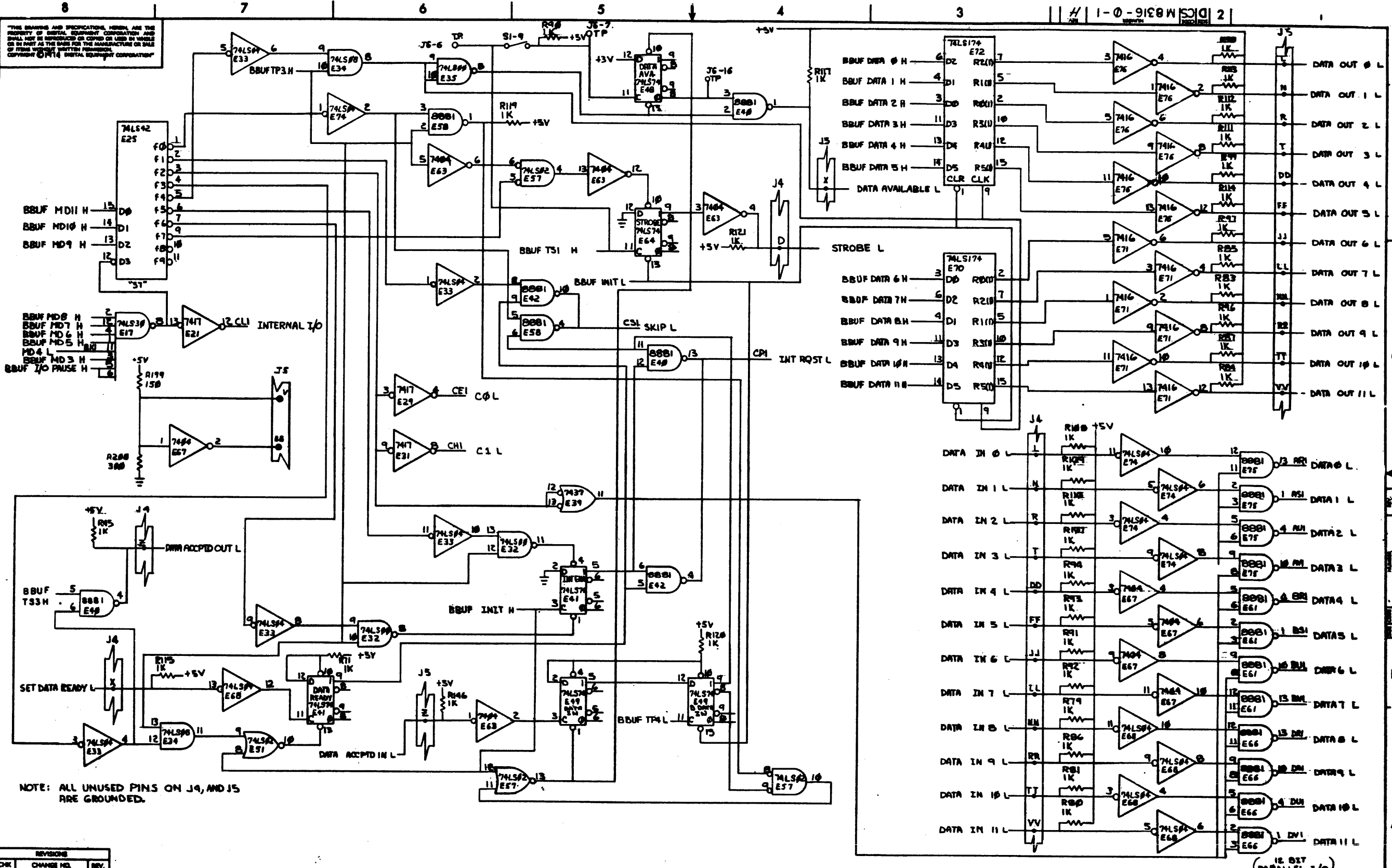
COMPONENT SUBSTITUTION CHART

PART CALLED P/N	DESCRIPTION	SUBSTITUTE PART P/N	DESCRIPTION
1001610-01	0.01 μF DISC.	1001610-00	0.01 μF GLASS
1909705	8881	1909973	97401
		1910392	5380
1911469	8640	1909971	6380
		1910390	7380
		1911113	11380
1912824	74LS74	1905547	7474
1912799	74LS00	1905575	7400
1912807	74LS10	1905576	7410
1912815	74LS30	1905578	7430
1912801	74LS02	1909004	7402
1912803	74LS04	1909686	7404
1912819	74LS42	1910046	7442
1912805	74LS08	1910155	7408
1912853	74LS175	1910651	74175
1912697	74LS174	1910652	74174

REVISED	DATE	BY	REASON
1	08-11-76	P. SCANTLEBURY	INITIAL DESIGN
2	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
3	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
4	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
5	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
6	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
7	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
8	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0

CHK'D	DATE	BY	REASON
1	09-01-76	P. SCANTLEBURY	INITIAL DESIGN
2	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
3	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
4	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
5	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
6	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
7	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0
8	09-01-76	P. SCANTLEBURY	REVISED TO MATCH M8316-0-0

DIGITAL EQUIPMENT CORPORATION
M8316-0-1-1



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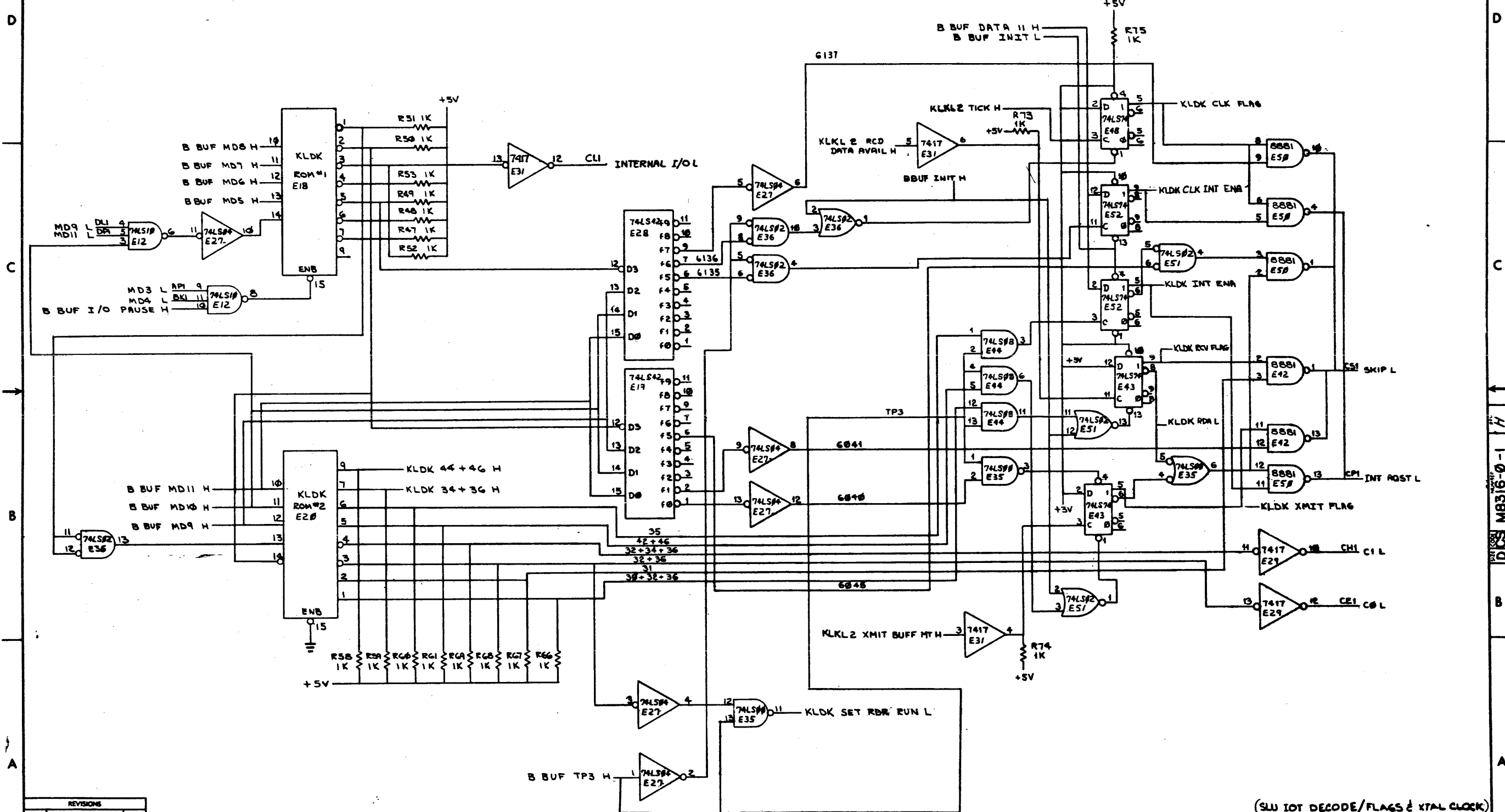
NOTE: ALL UNUSED PINS ON J4, AND J5 ARE GROUNDED.

(16 BIT PARALLEL I/O)

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	OPTION BD #1	SIZE CODE	NUMBER	REV.
SCALE	1:1	SHEET	2 OF 8	D
D CS M8316-0-1		H		

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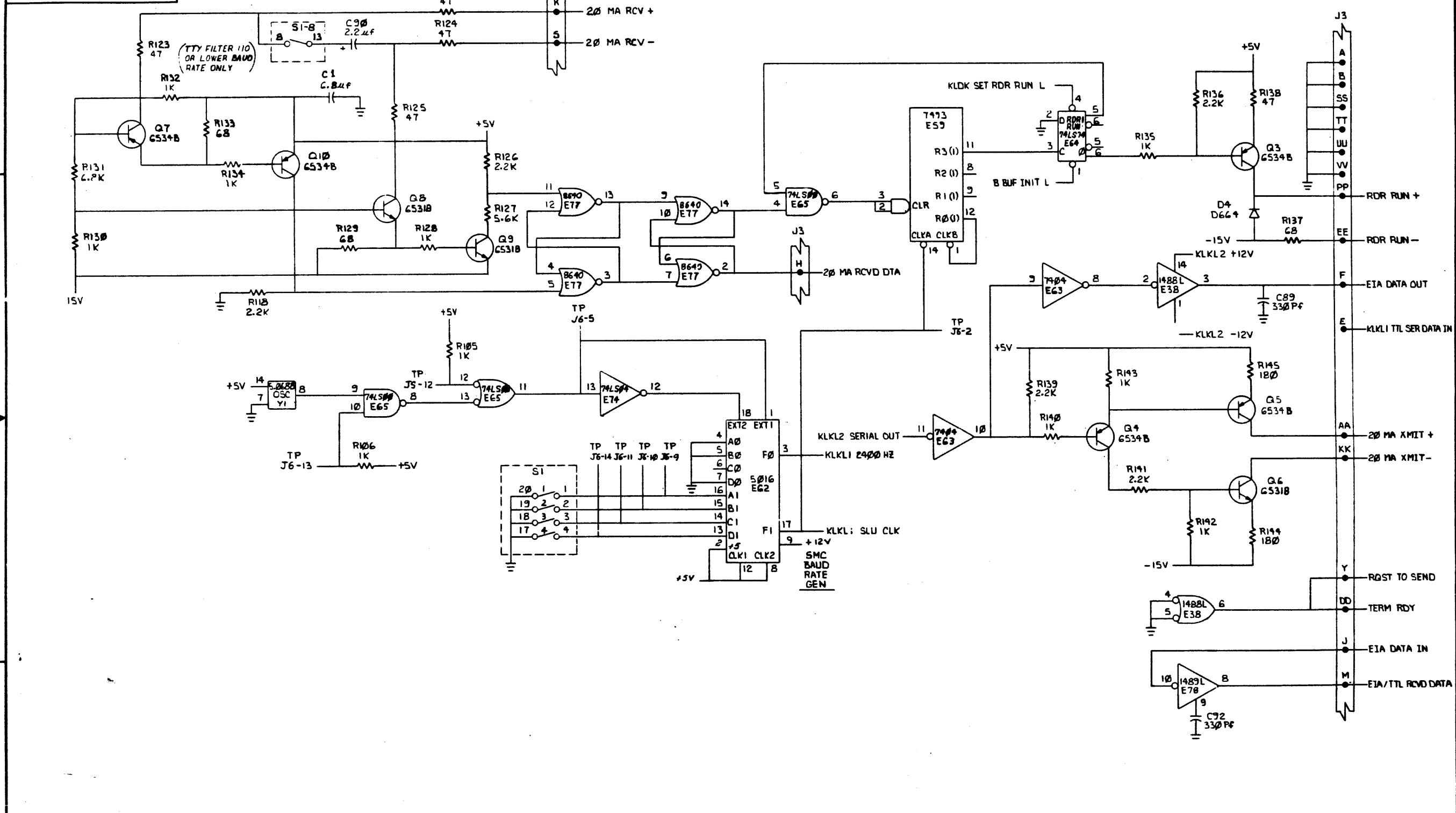


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		OPTION BD #1 (KLDK)		SIZE CODE	NUMBER	REV.
SCALE		SHEET 3 OF 8		DIST.	DCS M8316-0-1	H

(SLU IOT DECODE/FLAGS & XTAL CLOCK)

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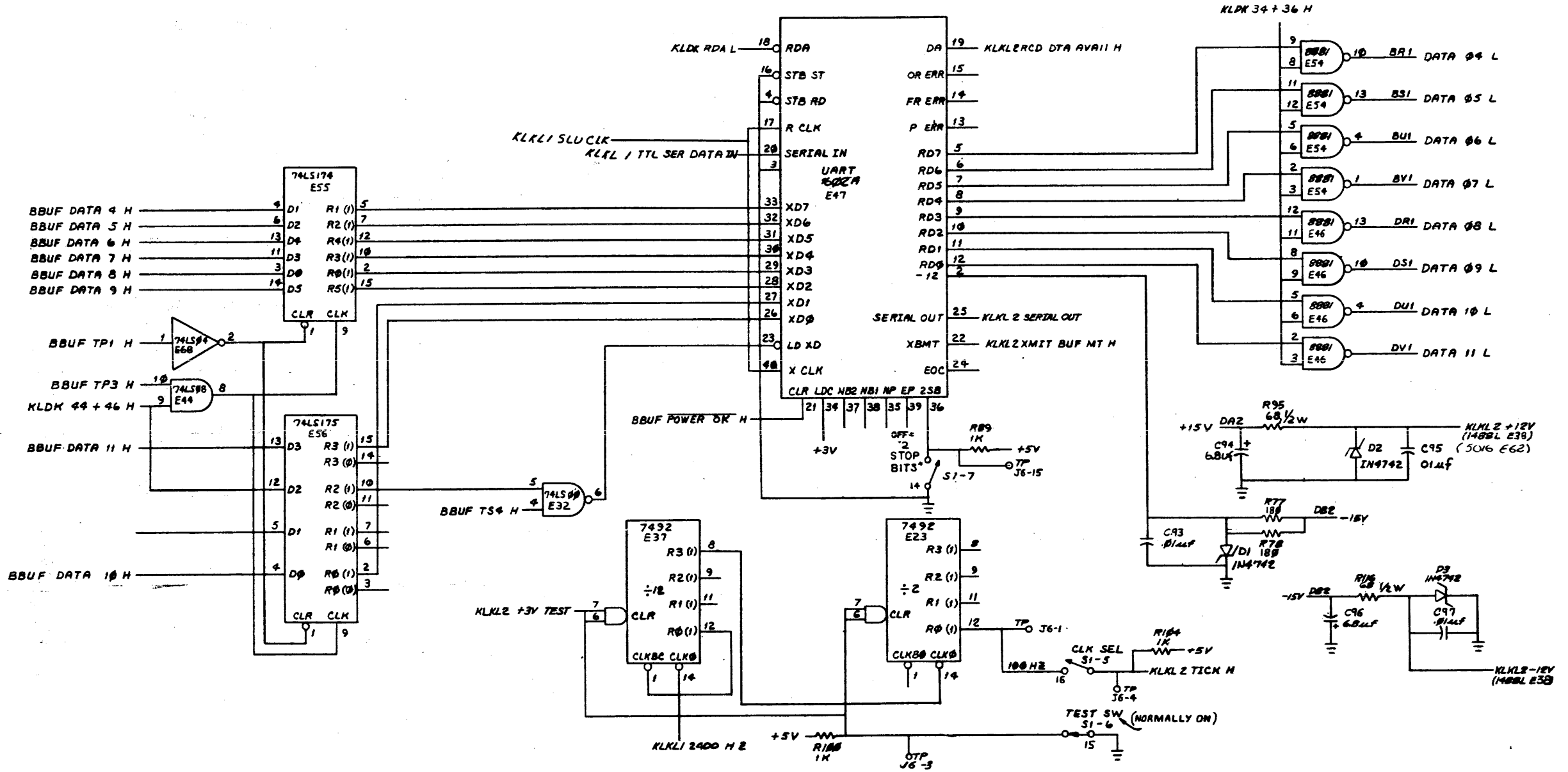
REVISIONS		
CHK	CHANGE	REV.

(BAUD RATE GEN & 20 MA, EIA DRIVERS / RECEIVERS)

TITLE	OPTION BD #1	SIZE CODE	D CS	NUMBER	M8316-0-1	REV.	H
SCALE	1:1	SHEET	4	OF	8	DIST.	

DCS M8316-0-1 H

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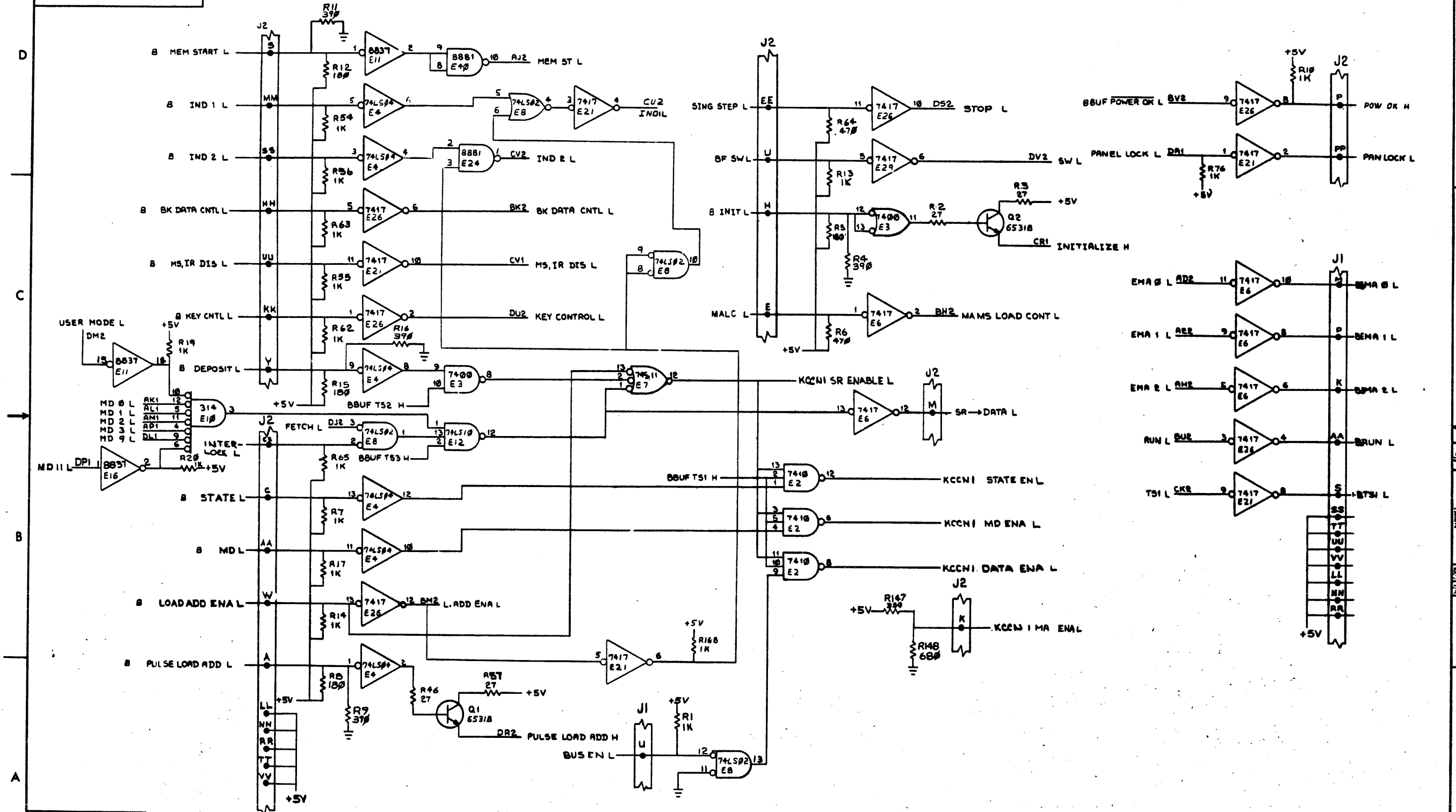
REVISIONS		
CHE	CHANGE NO.	REV.

(UART & XTAL CLK FREQ SOURCE)

TITLE	OPTION BD #1 (KLKL 2)	SIZE/CODE	DCS	NUMBER	M8316-0-1	REV.	H
SCALE	/ /	SHEET	5	OF	6	DIST.	

REV. 10/74 DCS M8316-0-1 H

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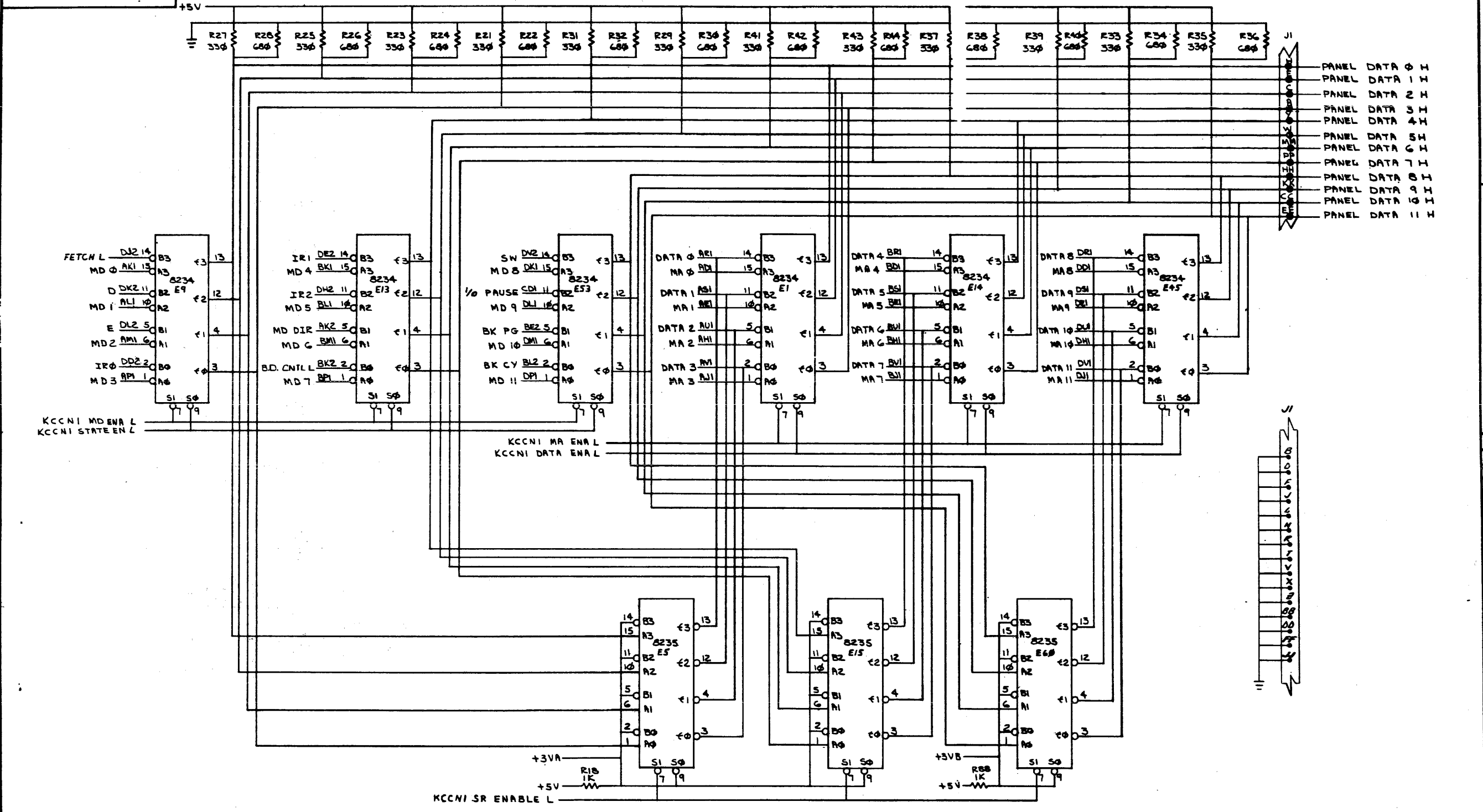
(PROGRAMMER'S PANEL CONTROL)

REVISIONS		
CHK	CHANGE NO	REV

TITLE	OPTION BD #1 (KCCN.1)	SIZE CODE	D CS	NUMBER	M8316-0-1	REV.	H
SCALE	1-1	SHEET	6 OF 8	DIST.			

D CS M8316-0-1 H

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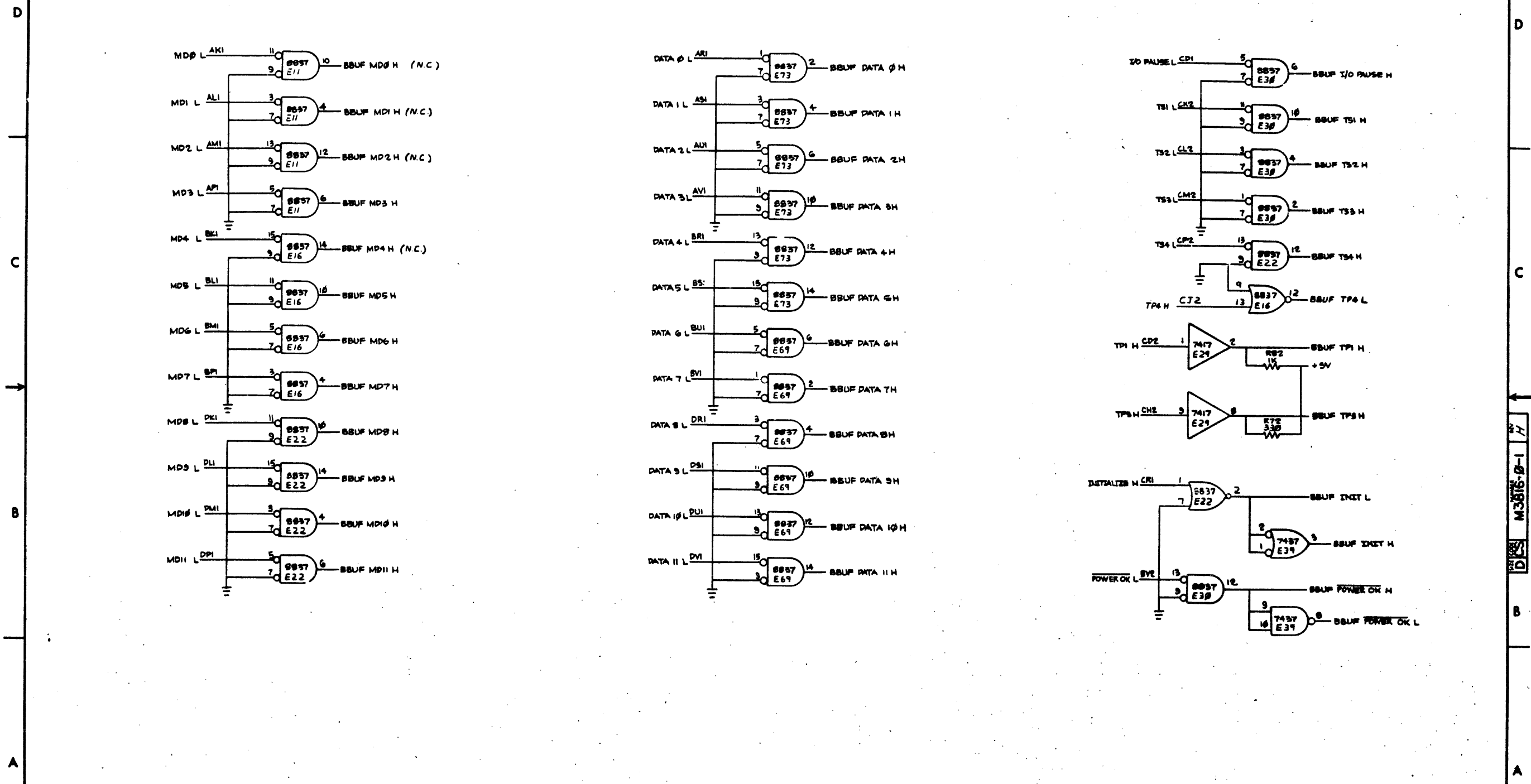


REVISIONS		
CHK	CHANGE NO.	REV.

(PROGRAMMERS PANEL MULTIPLEXERS)

TITLE	OPTION BOARD #1	SIZE CODE	NUMBER	REV.
	(KCCNI)	DCSM8316-0-1	H	
SCALE	7-7	SHEET	7 OF 8	DIST.

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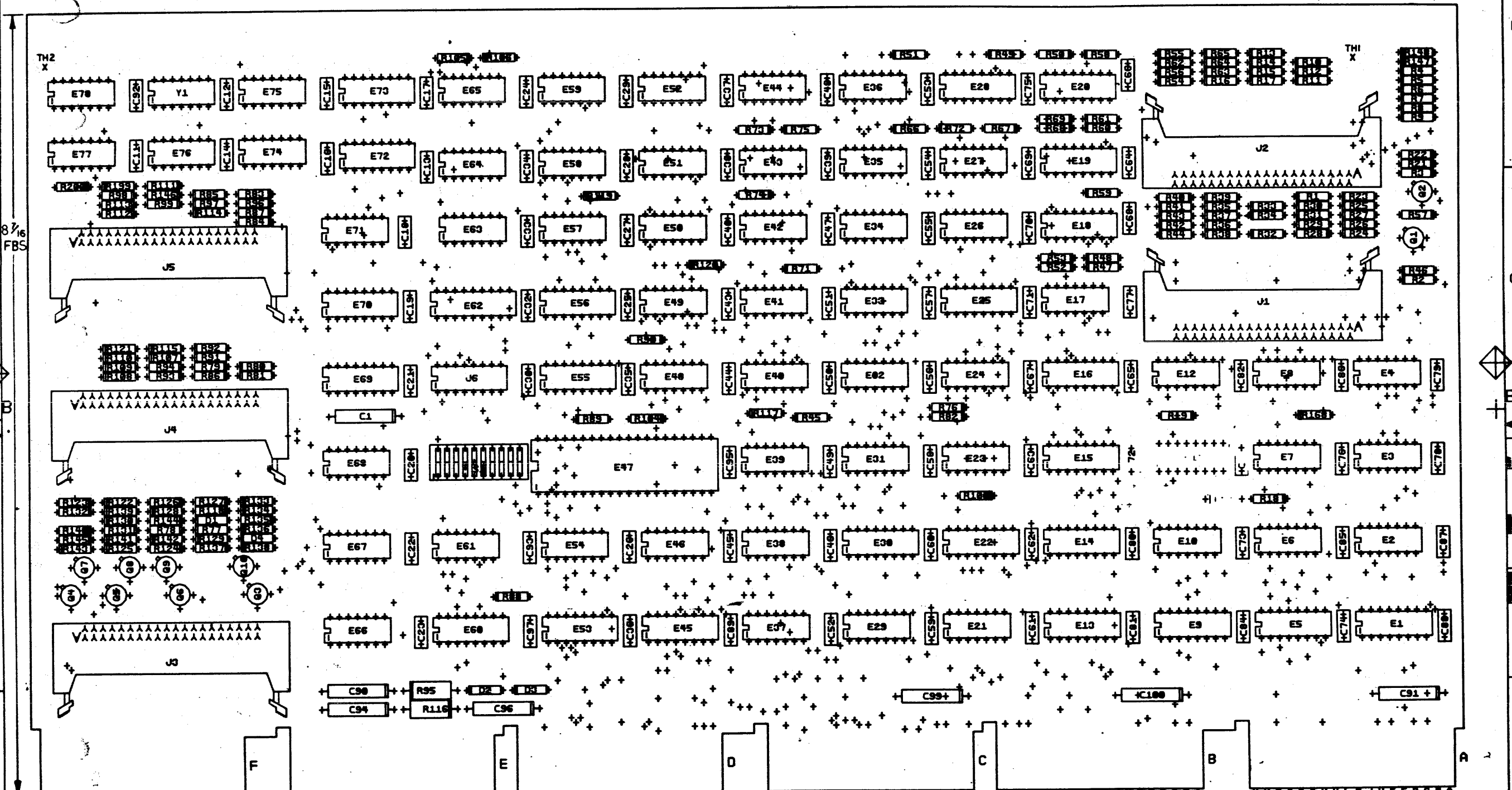


(BUS BUFFERS)

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	8/A INTERNAL OPTIONS (BUFF)	SIZE CODE	D CS	NUMBER	M8316-D-1	REV.	H
SCALE	1:1	SHEET	8	OF	8	DIST.	

THIS BOARD IS IDENTIFIED BY THE
 SERIAL NUMBER AND DATE OF MANUFACTURE
 WHICH ARE PRINTED ON THE BOARD



NOTES:

CHANGE NO	REV
ORIGINATED	
DATE	
BY	

ETCH REV.	
P.C. DESIGN DATA REVISION	

SIGNATURES	DATE	digital
DRM. <i>V. J. ...</i>	6-27	
CHK'D. <i>P. ...</i>	6-27	
ENG. <i>R. ...</i>	7-27	
PROD. <i>S. ...</i>	7-27	
TITLE		OPTION BOARD #1
SCALE 2/1	SIZE CODE	NUMBER
SHT. 1 OF 3	0 UA	M8316-0-0
NEXT HIGHER ASSY. B-00-M8316-0		REV *

8

7

6

5

4

3

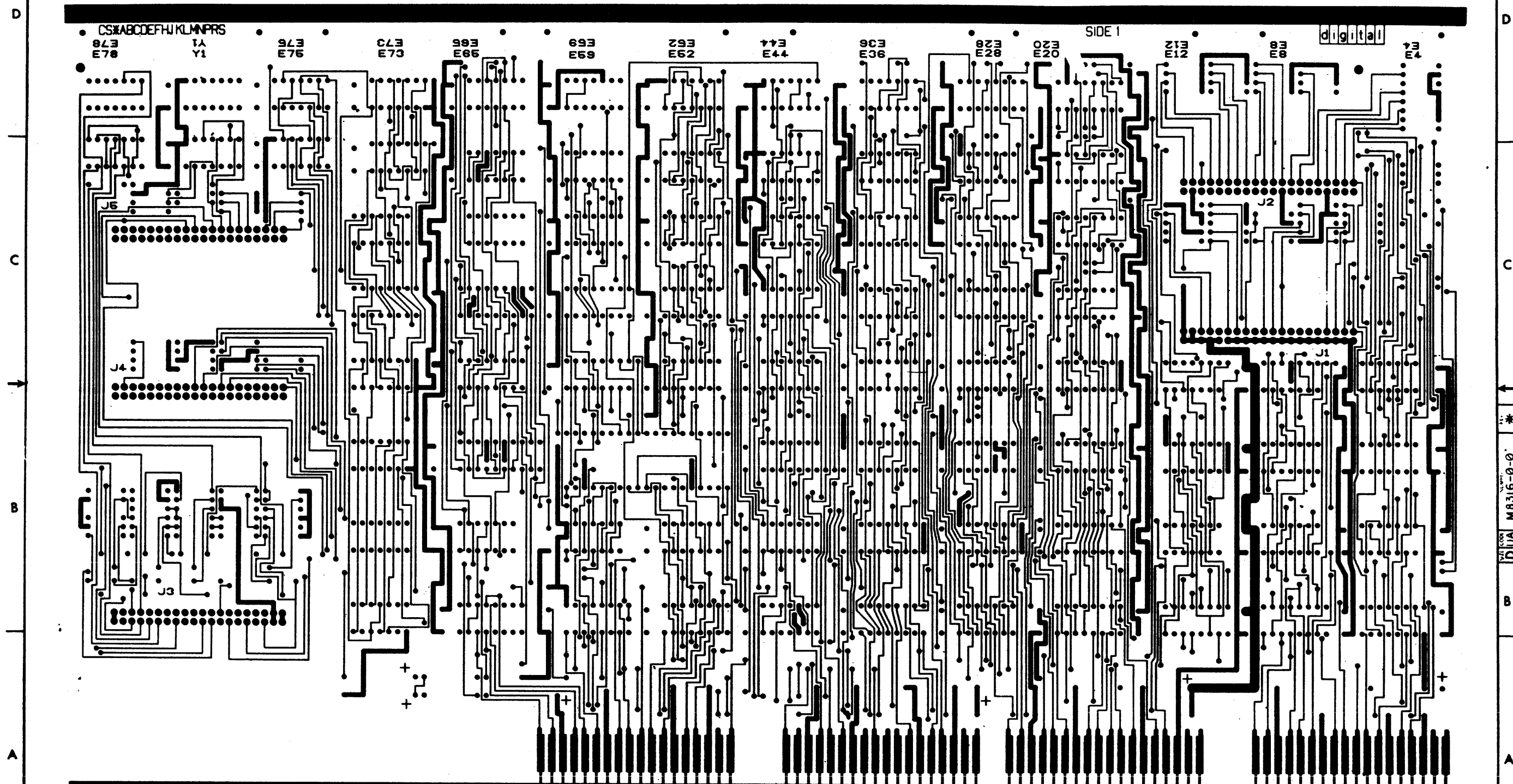
* 0-0-91E8W DUA M8316-0-0 2

1

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L1

MS40566 M8316 5010900D P4



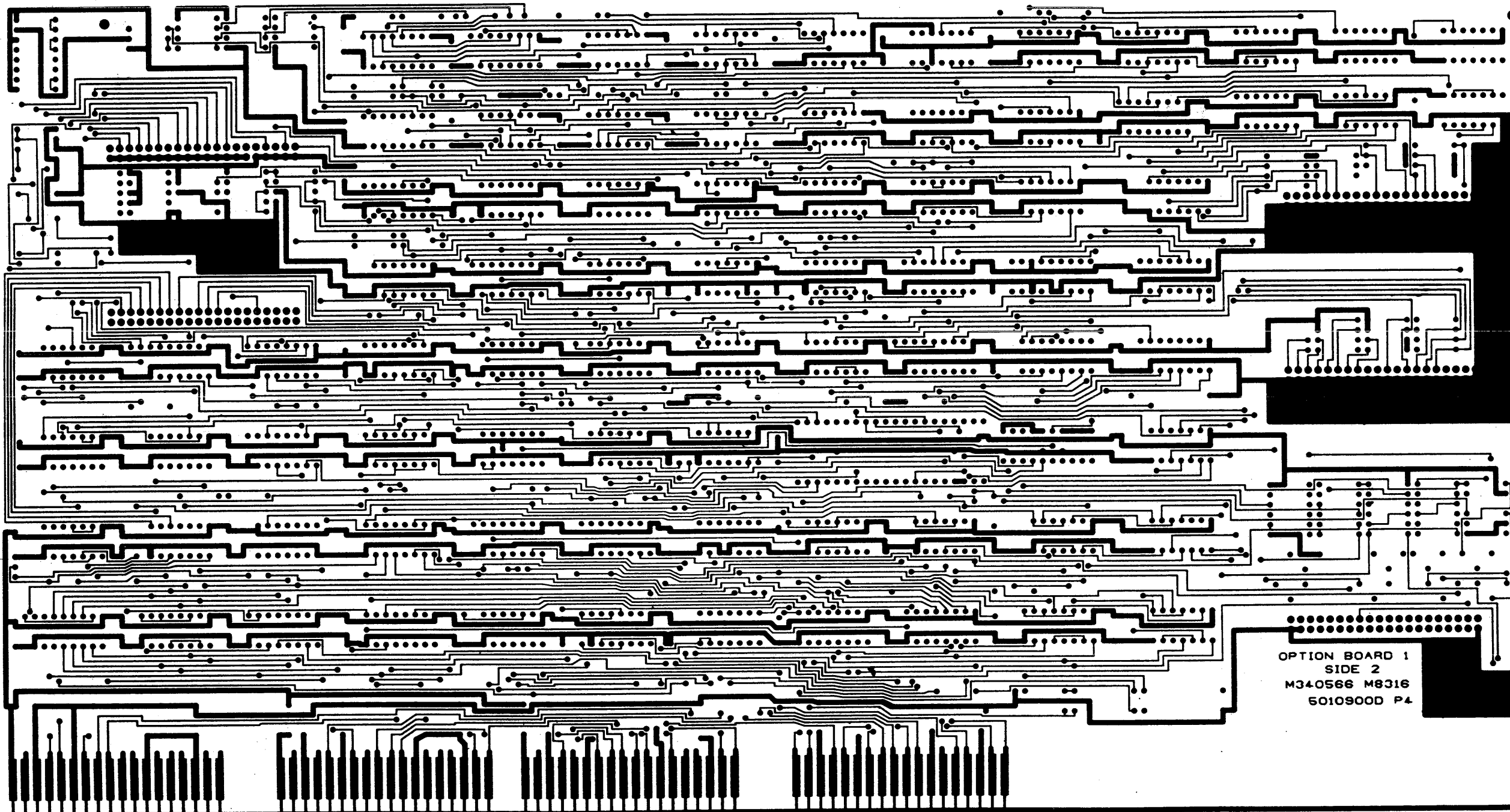
REVISIONS		
CHK	CHANGE NO	REV.

TITLE	OPTION BOARD *1	SIZE CODE	DUA	NUMBER	M8316-0-0	REV.	*
SCALE	2/1	SHEET	2 OF 3	DIST.			

8 7 6 5 4 3 2 1

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* 0-0-9186W YU D 2
3302 325



OPTION BOARD 1
 SIDE 2
 M340566 M8316
 6010900D P4

REVISIONS		
CHK	CHANGE NO	REV.

TITLE	OPTION BOARD *1	SIZE CODE	D UA	NUMBER	M8316-0-0	REV.	*
SCALE	2/1	SHEET	3 OF 3	DIST.			

DUA M8316-0-0
 *

DIGITAL EQUIPMENT CORPORATION				QUANTITY / VARIATION												NOTES:		
PARTS LIST																		
MADE BY BOB KOPPENAL		CHECKED <i>K. Koppenal</i>																
DATE 3-31-76		DATE 3-30-76																
ENG <i>P. Blum</i>		PROD <i>X. Blum</i>																
DATE 7-2-76		DATE 7-2-76																
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316- ϕ - ϕ														REF DESIGNATION
1	D-MD-5010900-0-0	5010900	ETCHED CIRCUIT BOARD	1														
2		1001610-01	CAP, .01 Uf, 100V, 10%	78														C11-C30, C32-C40, C43-C88, C93, C95, C97
3		1000023	CAP, 330 Pf, 100V, 5%	2														C89, C92
4		1002431	CAP, 2.2 UF, 35V, 10%	1														C90
5		1005306	CAP. 6.8 Uf, 35V, 10%	3														C91, C94, C96, C99-C100, C1
6		1109502	DIODE, IN4742	3														D1-D3
7		1100114	DIODE, D664	1														D4
8		1211164-06	SWITCH PACK (10 POS DIP)	1														S1
9		1211813-02	IC SOCKET (16 PIN)	1														J6
10		1210711-02	HANDLE ASSY	1														
11		1209941-02	CONNECTOR 40 P. RT. ANG. HD	5														J1-J5
12		1209941-03	LATCH LEFT 40 P. RT. ANG. HD	5														
13		1209941-04	LATCH RIGHT 40 P RT. ANG. HD	5														
14		1300202	RES., 47, 1/4W, 5%	5														R122-R125, R138
15		1300219	RES., 68, 1/4W, 5%	3														R129, R133, R137
16		1301424	RES., 680, 1/4W, 5%	13														R22, R24, R26, R28, R30, R32, R34, R36 R38, R40, R42, R44, R46
17		1300417	RES., 2.2K, 1/4W, 5%	5														R118, R126, R136, R139, R141
18		1301874	RES., 5.6K, 1/4W, 5%	1														R127
19		1301423	RES., 6.8K, 1/4W, 5%	1														R131
20		1301522	RES., 27, 1/4W, 5%	4														R2, R3, R46, R57

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				SHEET 1 OF 4				INSERTION PARTS LIST DATA BASE REV D					

DIGITAL EQUIPMENT CORPORATION				QUANTITY / VARIATION										NOTES:		
PARTS LIST																
MADE BY BOB KOPPENAL		CHECKED														
DATE 3-31-76		DATE														
ENG		PROD														
DATE		DATE														
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316- ϕ - ϕ												REF DESIGNATION
21		1301322	RES., 18 ϕ , 1/4W, 5%	8												R5, R8, R12, R15, R77, R78, R144, R145
22		1300295	RES., 33 ϕ , 1/4W, 5%	15												R21, R23, R25, R27, R29, R31, R33, R35, R37, R39, R41, R43, R147, R2 $\phi\phi$, R72
23		1300309	RES., 39 ϕ , 1/4W, 5%	4												R4, R9, R11, R16
24		1300316	RES., 47 ϕ , 1/4W, 5%	2												R6, R64
25		1300365	RES., 1K, 1/4W, 5%	83												R1, R7, R1 ϕ , R13, R14, R17-R20, R45, R47-R56, R71, R58-R63, R65-R69, R73-R76, R79-R81, R82-R94, R117, R96-R1 $\phi\phi$, R104-R115, R119-R121, R128, R13 ϕ , R132, R135, R14 ϕ , R142, R143, R146, R168, R134
26		1309405	RES., 68, 1/2W, 5%	2												R116, R95
27		1300250	RES., 15 ϕ , 1/4W, 5%	1												R199
28		1509338	TRANSISTOR, DEC 6531B	5												Q1, Q2, Q6, Q8, Q9
29		1503409-01	TRANSISTOR, DEC 6534B	5												Q3, Q4, Q5, Q7, Q1 ϕ
30		1811660-02	CRYSTAL OSCILLATOR, 5. ϕ 688 MHZ	1												Y1
31		1912824	IC., 74LS74	6												E41, E43, E52, E64, E48, E49
32		1912799	IC., 74LS $\phi\phi$	3												E32, E35, E65
33		1912807	IC., 74LS1 ϕ	1												E12
34		1912815	IC., 74LS3 ϕ	1												E17
35		1912801	IC., 74LS ϕ 2	4												E8, E36, E51, E57
36		1909053	IC., 7492	2												E23, E37
37		1909054	IC., 7493	1												E59
38		1910537	IC., 74S11	1												E7
39		1912803	IC., 74LS ϕ 4	5												E4, E27, E33, E74, E68,

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TITLE
OPTION BOARD #1

ASSY NO.
D-UA-M8316- ϕ - ϕ
SHEET 2 OF 4

SIZE CODE NUMBER REV.
B PL M8316- ϕ - ϕ *

INSERTION PARTS LIST DATA BASE REV D

DIGITAL EQUIPMENT CORPORATION

PARTS LIST

QUANTITY / VARIATION

NOTES:

MADE BY BOB KOPPENAL	CHECKED _____	SECTION 1
DATE 4 NOV 76	DATE _____	ISSUED SECTION _____
ENG _____	PROD _____	
DATE _____	DATE _____	

ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316-0-0	QUANTITY / VARIATION										REF DESIGNATION	
40		1909705	IC., 8881	10												E40, E24, E54, E42, E46, E50, E66, E61, E75, E58
41		1909928	IC., 7416	2												E71, E76
42		1909929	IC., 7417	5												E6, E26, E31, E29, E21
43		1909935	IC., 8235	3												E5, E15, E60
44		1912819	IC., 74LS42	3												E19, E28, E25
45		1912805	IC., 74LS08	2												E34, E44
46		1910322	IC., 1488L	1												E38
47		1910323	IC., 1489L	1												E78
48		1909704	IC., 314A	1												E10
49		1911469	IC., 8640	1												E77
50		1910459	IC., 1602A (UART)	1												E47
51		1912853	IC., 74LS175	1												E56
52		1912697	IC., 74LS174	3												E55, E70, E72
53		1910091	IC., 7437	1												E39
54		1911315	IC., 8234	6												E1, E9, E14, E13, E45, E53
55		1911116	IC., 8837	6												E11, E16, E30, E22, E69, E73
56		2112623	IC., 5016 (BAUD RATE GEN.)	1												E62
57		23062A1	ROM #1 KLDK #1, 32 X8	1												E18
58		23063A1	ROM #2 KLDK #2, 32 X 8	1												E20
59		1905575	IC., 7400	1												E3

E.C.O. NO. _____

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EN-01140A-16-R276(325) DRB 125				SHEET 3 OF 4		INSERTION PARTS LIST DATA BASE REV D							

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES:		
MADE BY JACK MASON		CHECKED		M8316-Ø-Ø														
DATE 13 APRIL 76		DATE																
ENG		PROD																
DATE		DATE														REF DESIGNATION		
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION															
60		1905576	IC. , 7410	1														E2
61		1909686	IC. , 7404	2														E63, E67
62		9006732	EYELET	12														

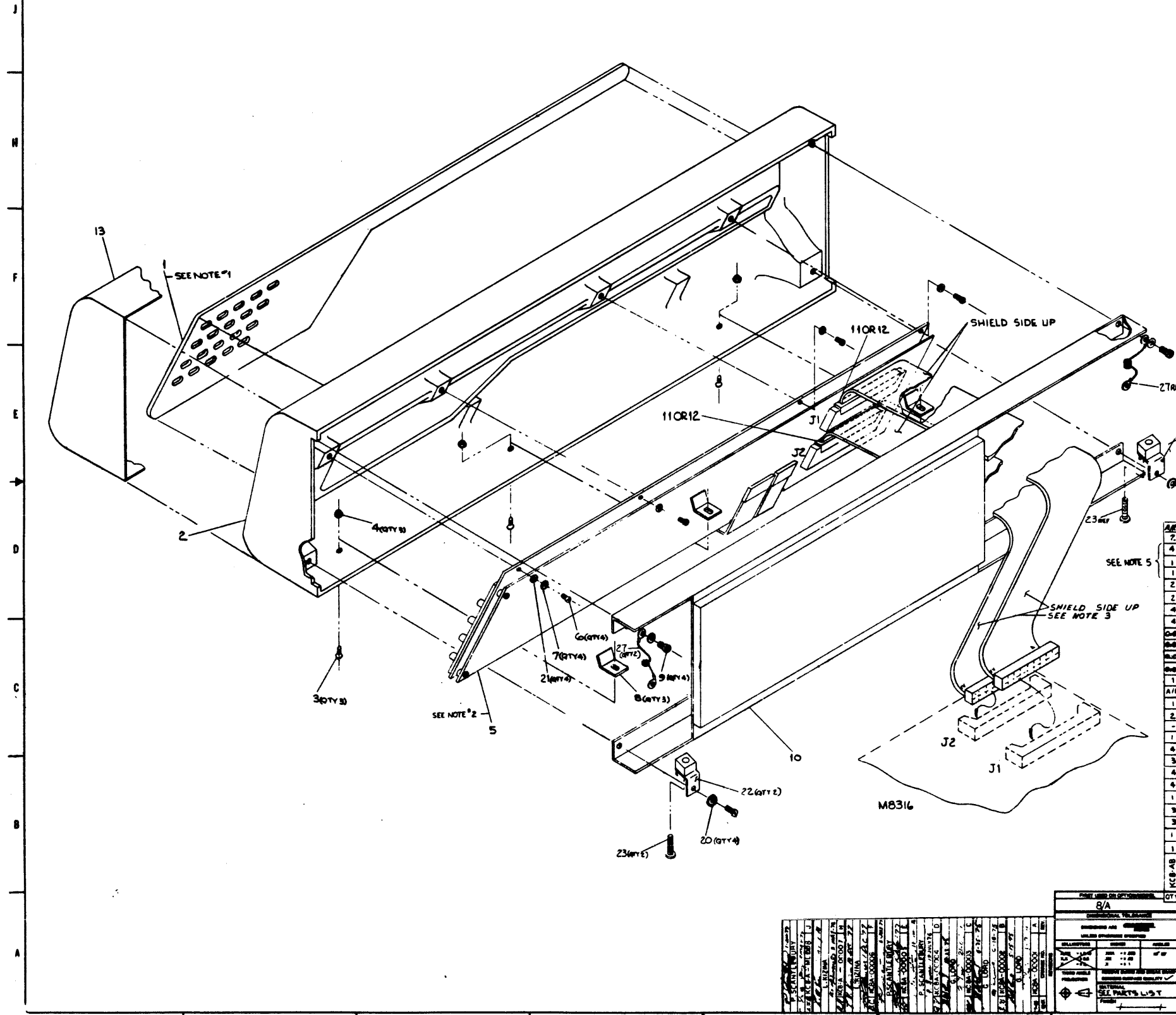
E.C.O. NO.

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								SHEET 4 OF 4		INSERTION PARTS LIST DATA BASE REV D				

DO NOT SCALE DRAWING

LEGEND	
PART NO	VARIATION
KCB-AA	PROG'S CONSOLE (BEZEL)
KCB-AB	REMOTE PROG'S CONSOLE (BEZEL)

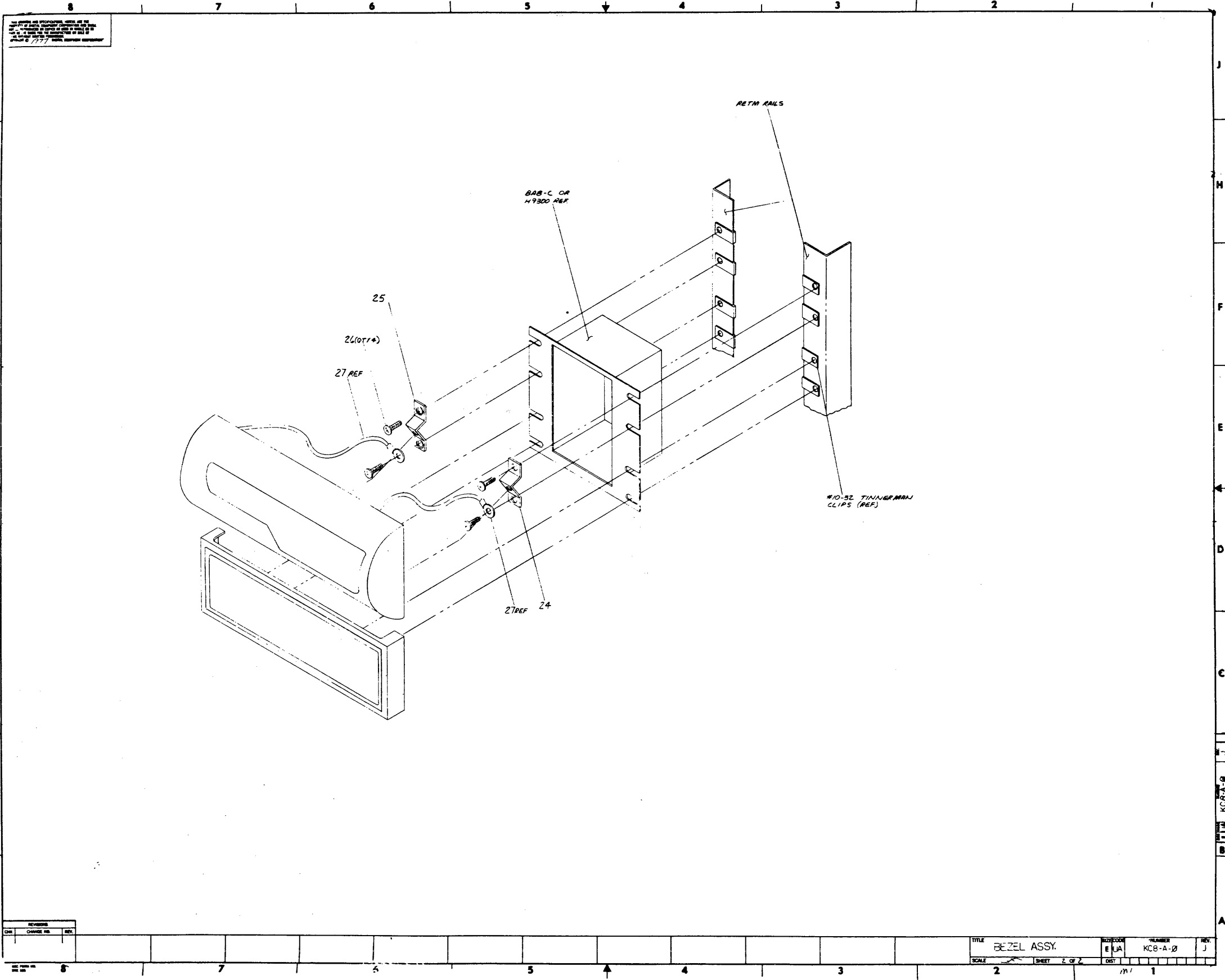
- NOTES:**
- ASSEMBLE ITEM #1 (PANEL) TO ITEM #2 (BEZEL) USING SILASTIC ADHESIVE ITEM #4.
 - FOR PROPER ALIGNMENT OF KEYPAD OF MODULE ASSEMBLY (ITEM #5) INTO BEZEL (ITEM #2), DO THE FOLLOWING:
 - PLACE MODULE ASSEMBLY (ITEM #5) INTO BEZEL (ITEM #2).
 - START MOUNTING HARDWARE, ITEMS 6, 7 AND 21 INTO BEZEL.
 - BEFORE TIGHTENING DOWN ITEMS 6, 7 AND 21, PUSH MODULE ASSEMBLY TOWARDS THE TOP OF BEZEL. THE KEYPAD, WHEN VIEWED FROM THE FRONT OF BEZEL, SHOULD JUST TOUCH THE TOP EDGE OF THE KEYPAD CUTOUTS IN BEZEL.
 - TIGHTEN ITEMS 6, 7 AND 21.
 - NOW SECURE MODULE ASSEMBLY BOTTOM TO BEZEL WITH ITEMS 3, 4 AND 8; BEING SURE THAT CLIPS (ITEM #8) ARE PRESSED FIRMLY AGAINST MODULE ASSEMBLY.
 - MATE BC08R CABLES J1 (KCB-A) TO J1 (M8316) J2 (KCB-A) TO J2 (M8316)
 - SEE SHEET 2 FOR INSTRUCTIONS OF MOUNTING BEZEL TO BOX AND CABINET
 - LOOSE SHIP ITEMS 24, 25 & 26 WITH ASSY



QTY	DESCRIPTION	PART NO.	REV.
28	INSTRUCTIONS, PROG.	1212707-01	28
27	CABLE ASSY, FLEX	1212707-01	27
26	SCR PPH 10-28X.50	9006073-03	26
25	BRKT CABINET	741704-1	25
24	BRKT CABINET	741704-0	24
23	SCR, SOC. HD, D 3/16 X 1/2	9006350-08	23
22	BRKT, BEZEL	C-1A-701524	22
21	WASHER FLAT	9006653	21
20	WASH. EXT TOOTH LOCK #8	9008151	20
19	WASHER FLAT	9006653	19
18	WASHER FLAT	9006653	18
17	WASHER FLAT	9006653	17
16	WASHER FLAT	9006653	16
15	SHIPPING LIST	A-PL-KCB-A-2	15
14	ADHESIVE, SILASTIC	9009158	14
13	COVER, PROTECTIVE, BEZEL	D-4B-741236-1-0-0	13
12	I/O CABLE, BC08R	C-1A-BC08R-1E	12
11	I/O CABLE, BC08R	C-1A-BC08R-01	11
10	BRACKET, BEZEL	C-1A-7411957-0-0	10
9	SCR, PH. HD, PAN 3/32 X 1/2	9006037-1	9
8	CLIP	B-4B-7412352-0-0	8
7	WASH EXT TOOTH LOCK #6	9007649	7
6	SCR, PH. HD, PAN 3/32 X 1/2	9006020-1	6
5	KEYBOARD ASSY	D-4B-7010644-0-0	5
4	NUT KEPS #4-40	9006587	4
3	SCR, PH. HD, FLAT 4.40 X .375	9006011-2	3
2	BEZEL (PROG) HDG DIE CHISEL	L-4B-7412349-3-0	2
1	PANEL	D-1A-7412398-1-0	1

B/A		PARTS LIST	
DATE	BY	DATE	BY
12/1/68	J	12/1/68	J
TITLE		TITLE	
BEZEL ASSY		BEZEL ASSY	
B-DD-KCB-A		E UA KCB-A-0	
SCALE		M/C	

REV.	DESCRIPTION	DATE	BY
1	ISSUED FOR PRODUCTION	12/1/68	J
2	REVISED TO ADD PARTS LIST	12/1/68	J
3	REVISED TO ADD PARTS LIST	12/1/68	J
4	REVISED TO ADD PARTS LIST	12/1/68	J
5	REVISED TO ADD PARTS LIST	12/1/68	J
6	REVISED TO ADD PARTS LIST	12/1/68	J
7	REVISED TO ADD PARTS LIST	12/1/68	J
8	REVISED TO ADD PARTS LIST	12/1/68	J
9	REVISED TO ADD PARTS LIST	12/1/68	J
10	REVISED TO ADD PARTS LIST	12/1/68	J
11	REVISED TO ADD PARTS LIST	12/1/68	J
12	REVISED TO ADD PARTS LIST	12/1/68	J
13	REVISED TO ADD PARTS LIST	12/1/68	J
14	REVISED TO ADD PARTS LIST	12/1/68	J
15	REVISED TO ADD PARTS LIST	12/1/68	J
16	REVISED TO ADD PARTS LIST	12/1/68	J
17	REVISED TO ADD PARTS LIST	12/1/68	J
18	REVISED TO ADD PARTS LIST	12/1/68	J
19	REVISED TO ADD PARTS LIST	12/1/68	J
20	REVISED TO ADD PARTS LIST	12/1/68	J
21	REVISED TO ADD PARTS LIST	12/1/68	J
22	REVISED TO ADD PARTS LIST	12/1/68	J
23	REVISED TO ADD PARTS LIST	12/1/68	J
24	REVISED TO ADD PARTS LIST	12/1/68	J
25	REVISED TO ADD PARTS LIST	12/1/68	J
26	REVISED TO ADD PARTS LIST	12/1/68	J
27	REVISED TO ADD PARTS LIST	12/1/68	J
28	REVISED TO ADD PARTS LIST	12/1/68	J



ALL DIMENSIONS UNLESS OTHERWISE SPECIFIED ARE IN INCHES. DIMENSIONS IN PARENTHESES ARE IN MILLIMETERS. DIMENSIONS IN PARENTHESES ARE TO BE USED ONLY IN CASE OF DISCREPANCY BETWEEN THE TWO UNITS.

REV.	CHANGED BY	DATE

TITLE	BEZEL ASSY.	REVISION	E (A)	NUMBER	KCB-A-0	REV.	J
SCALE	1/2"	SHEET	2 OF 2	DIST.			

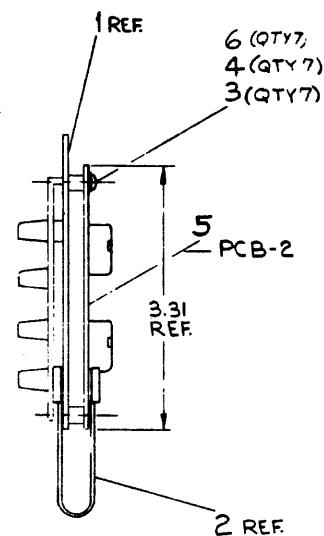
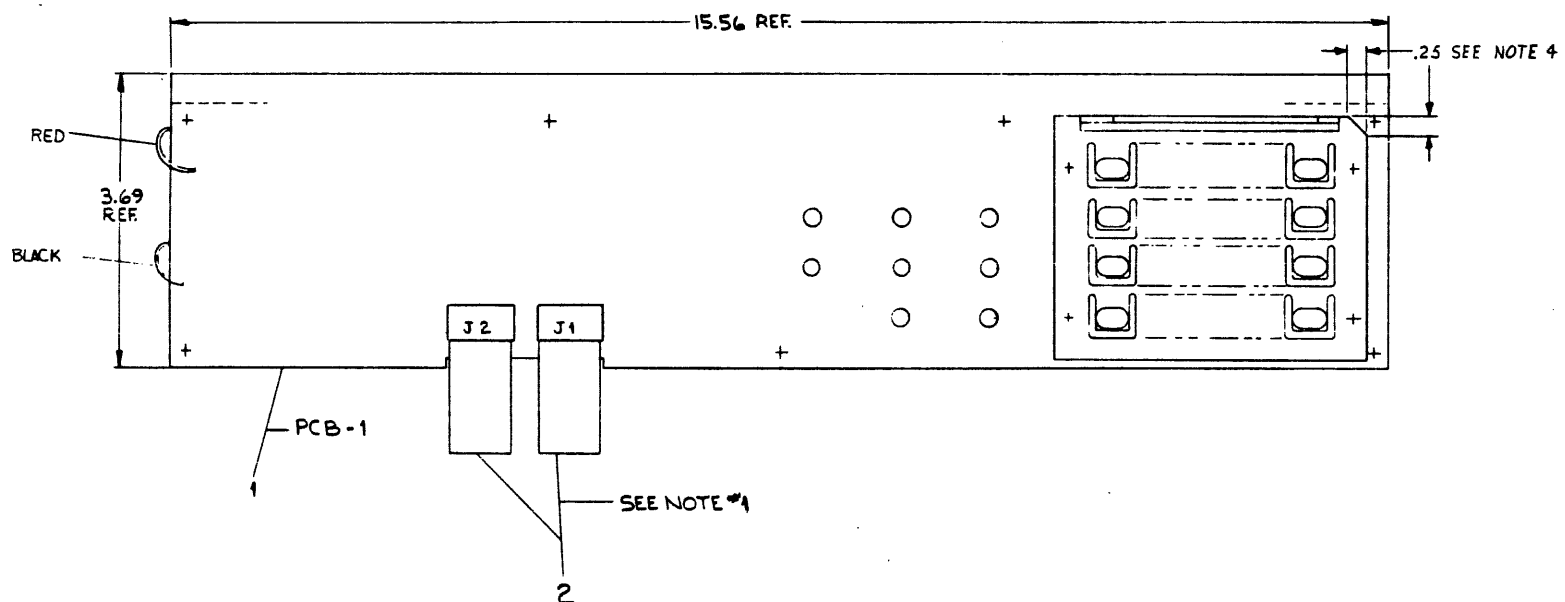
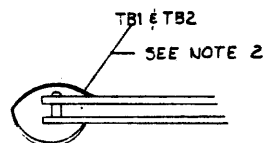
KCB-A-0

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NOTES:

1. INSTALL ITEM #2 (CABLE) AS FOLLOWS.
PCB-1-J1 TO PCB-2-J3
PCB-1-J2 TO PCB-2-J4
2. CONNECT RED WIRE FROM PCB-1 TO TB1 ON PCB-2. CONNECT BLACK WIRE FROM PCB-1 TO TB2 ON PCB-2.
3. ITEM #7 MAY BE RECYCLED WHEN USED FOR INTERPLANT SHIPMENT.
4. CLIP UPPER RIGHT HAND LIP OF KEYPAD OFF TO ALLOW PROPER ALIGNMENT INTO BEZEL.



SEE NOTE 3

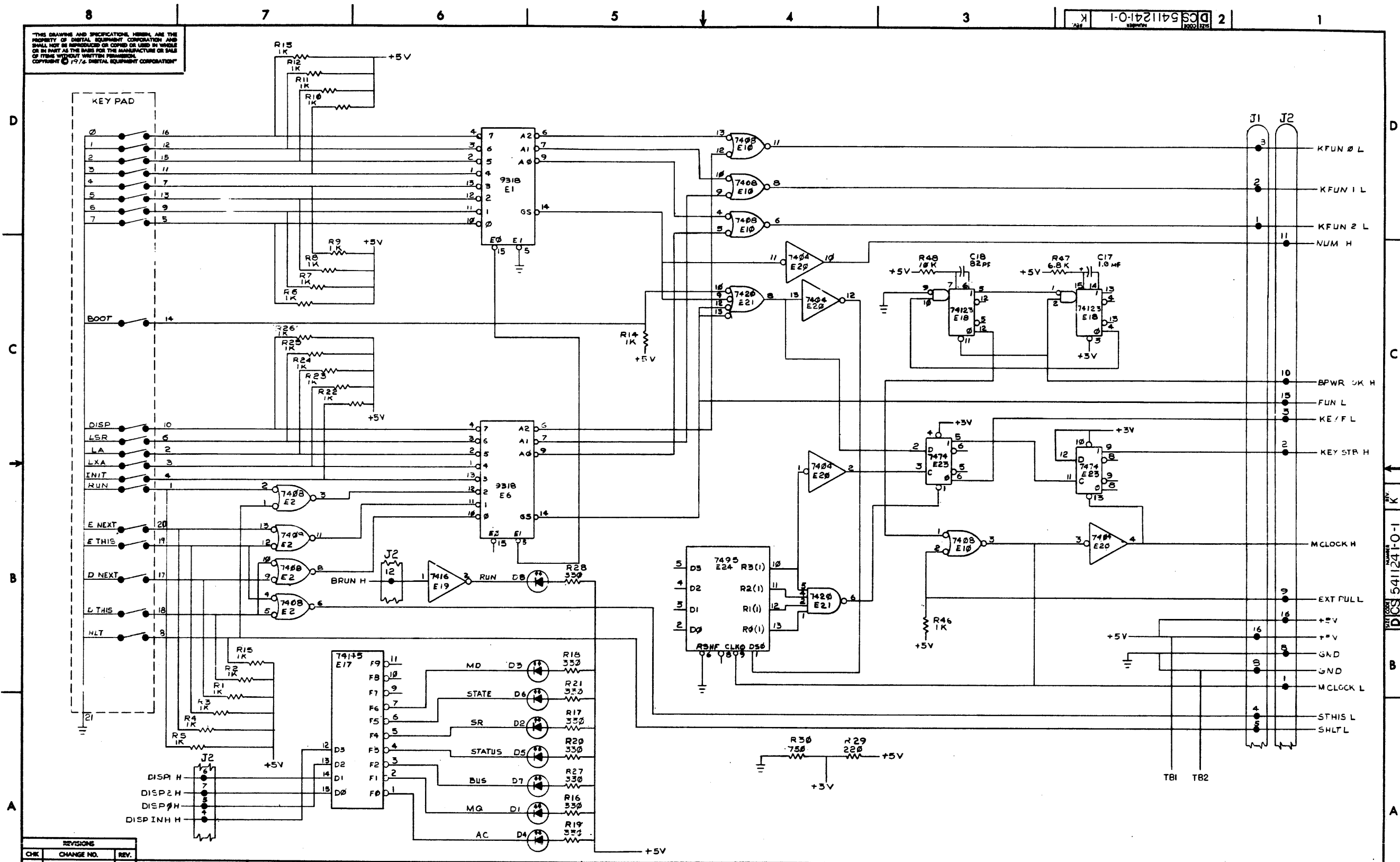
QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	PACKAGING INSTRUCTIONS	A-SP-3700188-0-0	7
7	WASHER, FLAT #4	9006655	6
1	REGISTERS AND CONTROL	D-CS-5411316-0-1	5
7	WASH INT LOCK #4	9006632	4
7	SCR PHL HD PAN #4-40X.25	9008301-1	3
2	CABLE, KEYBOARD	C-1A-2008612-0D	2
1	INDICATOR DISPLAY	D-CS-5411241-0-1	1

REV.	CHG	NO.	DATE	BY	APP
A		1	12-18-74	P. Scantlebury	
B		1	1-23-75	P. Scantlebury	
C		1	1-23-75	P. Scantlebury	

FIRST USED ON OPTION/MODEL 8/A		PARTS LIST	
DIMENSIONAL TOLERANCE DIMENSIONS ARE UNLESS OTHERWISE SPECIFIED		TITLE KEYBOARD ASSY.	
MILLIMETERS XX = ±0.25 XX = ±0.5 X = ±0.1	INCHES XXX = ±.005 XX = ±.002 X = ±.01	ANGLES #° ±'	DATE 12-18-74
THIRD ANGLE PROJECTION REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE 1-23-75	
MATERIAL SEE PARTS LIST		DATE 1-23-75	
FINISH		DATE 1-23-75	
SCALE 1/1		DATE 1-23-75	
SHEET OF		DATE 1-23-75	

PART NO. DAD 7010644-0-0 C

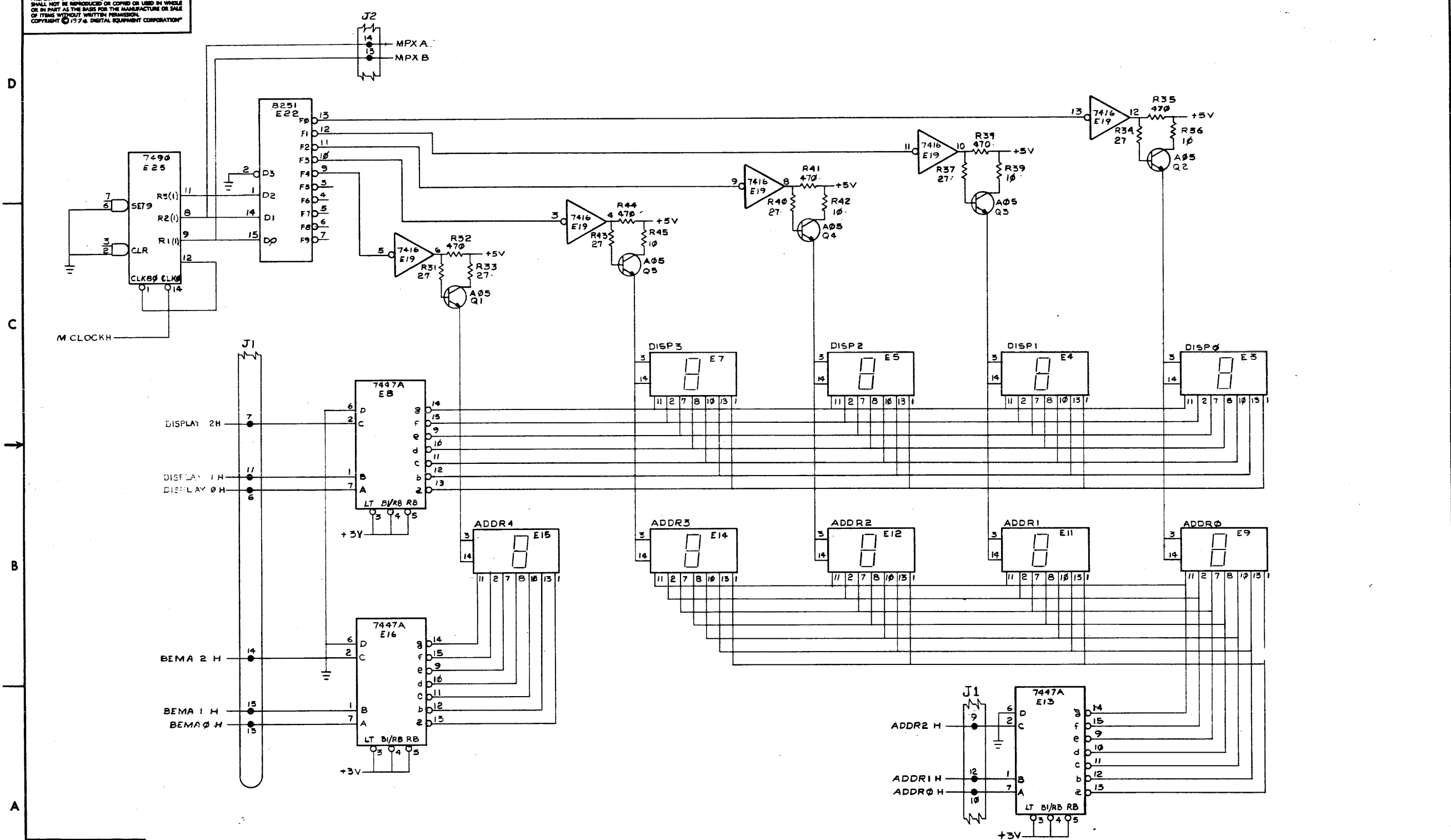
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REVISIONS		
CHK	CHANGE NO.	REV.

MK

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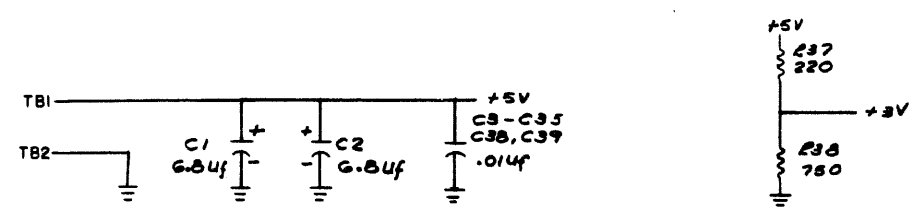
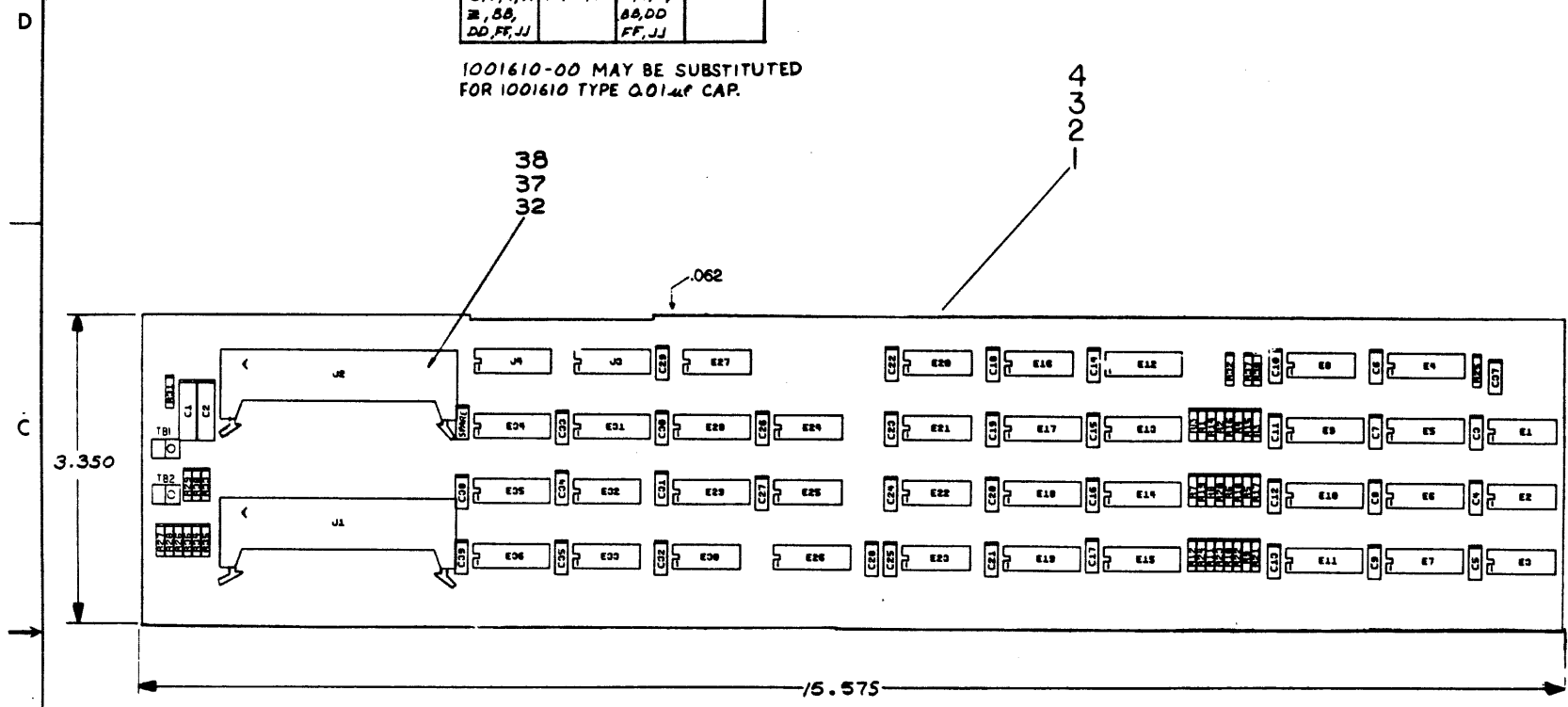
REVISIONS		
CHK	CHANGE NO.	REV.

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NOTES:

J1		J2	
GND	+5V	GND	+5V
B, D, F	LL, NN	B, D, F, J	LL, NN
J, L, N	RE, SS	L, N, R	EE, TT
C, T, V, X	TT, UU, VV	T, V, X, Z	VV
W, BB	DD, DD	AA, DD	
DD, FF, JJ		FF, JJ	

1001610-00 MAY BE SUBSTITUTED FOR 1001610 TYPE 0.01μF CAP.



IC TYPE	GND	+5V
I.C DEC 74174	8	16
I.C DEC 7475	12	5
I.C DEC 8251	8	16
I.C DEC 8235	8	16
I.C DEC 74153	8	16
I.C DEC 74123	8	16
I.C DEC 74175	8	16
I.C DEC 8097	8	16
IC TYPE	GND	+5V

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

IC PIN LOCATIONS

CHK	CHANGE NO.	REV
P. SCANTLEBURY	1	10/17/75
P. SCANTLEBURY	2	1/23/76
P. SCANTLEBURY	3	7/27/76
G. LORD	4	8/17/75
G. LORD	5	9/11/75
G. LORD	6	11/11/75
G. LORD	7	1/14/76
G. LORD	8	1/14/76
G. LORD	9	1/14/76
G. LORD	10	1/14/76
G. LORD	11	1/14/76
G. LORD	12	1/14/76
G. LORD	13	1/14/76
G. LORD	14	1/14/76
G. LORD	15	1/14/76
G. LORD	16	1/14/76
G. LORD	17	1/14/76
G. LORD	18	1/14/76
G. LORD	19	1/14/76
G. LORD	20	1/14/76
G. LORD	21	1/14/76
G. LORD	22	1/14/76
G. LORD	23	1/14/76
G. LORD	24	1/14/76
G. LORD	25	1/14/76
G. LORD	26	1/14/76
G. LORD	27	1/14/76
G. LORD	28	1/14/76
G. LORD	29	1/14/76
G. LORD	30	1/14/76
G. LORD	31	1/14/76
G. LORD	32	1/14/76
G. LORD	33	1/14/76
G. LORD	34	1/14/76
G. LORD	35	1/14/76
G. LORD	36	1/14/76
G. LORD	37	1/14/76
G. LORD	38	1/14/76

REF	DESCRIPTION	PART NO.	ITEM NO.	
REF	X-YCOORDINATE HOLE LOCATION	K-CO-5411316-0-4	1	
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-5411316-0-5	2	
REF	MODULE ECO HISTORY	B-MH-5411316-0-6	3	
1	ETCHED CIRCUIT BOARD	5011315	4	
1	C37	CAP 56MMF 100V	1000012	5
35	C3-C35, C38, C39	CAP .01UF 100V 20%	1001610-01	6
2	C1, C2	CAP 6.8UF 35V	1005306	7
2	E16, E20	I.C DEC 7474	1905547	8
2	E8, E30	I.C DEC 7400	1905575	9
2	E22, E33	I.C DEC 7402	1909004	10
1	E26	I.C DEC 7475	1909050	11
3	E1, E2, E3	I.C DEC 7495	1909055	12
2	E29, E35	I.C DEC 8251	1909594	13
2	E24, E27	I.C DEC 7404	1909686	14
1	E23	I.C DEC 7416	1909928	15
3	E9, E10, E11	I.C DEC 8235	1909935	16
3	E17, E18, E19	I.C DEC 74153	1909937	17
1	E32	I.C DEC 7408	1910155	18
1	E4	I.C DEC 74123	1910436	19
3	E5, E6, E7	I.C DEC 74175	1910651	20
5	E12, E13, E14, E15, E36	I.C DEC 74174	1910652	21
2	E21, E25	I.C DEC 7427	1910878	22
3	E28, E31, E34	I.C DEC 8097	1911527	23
12	R5-R16	RES 330 1/4W 5%	1300295	24
1	R37	RES 220 1/4W 5%	1300271	25
2	R27, R30	RES 390 1/4W 5%	1300309	26
7	R28, R31-R36	RES 1K 1/4W 5%	1300365	27
2	R26, R29	RES 180 1/4W 5%	1301322	28
1	R38	RES 750 1/4W 5%	1301401	29
1	R25	RES 6.8K 1/4W 5%	1301423	30
12	R1-R4, R17-R24	RES 600 1/4W 5%	1301424	31
2	J1, J2	CONN 40 PIN	1209941-02	32
2	J3, J4	SOCKET I.C 16 PIN	1211813-02	33
NR	WIRE, #30AWG, GREEN	9105740-55	34	
2	TB1, TB2	TAB FASTON (OFFSET)	9007112	35
2	TB1, TB2	EYELET	9009000	36
2		LATCH, LEFT 40 P. RT. ANG. HD.	1209941-03	37
2		LATCH, RIGHT 40 P. RT. ANG. HD.	1209941-04	38

FIRST USED ON OPTION MODEL: KCB-AA

ETCH BOARD REV: B C

DRN: [Signature] DATE: 1/10/75

DES: [Signature] DATE: 1/23/75

ENG: [Signature] DATE: 7/27/76

PROJ ENG: [Signature] DATE: 9/11/75

PROJ: [Signature] DATE: 1/14/76

NEXT HIGHER ASSY: D-AD-7010644-0-0

SCALE: [Blank]

SHEET 1 OF 3

SEMICONDUCTOR CONVERSION CHART

DEC NO. EIA NO. DEC NO. EIA NO.

digital EQUIPMENT CORPORATION

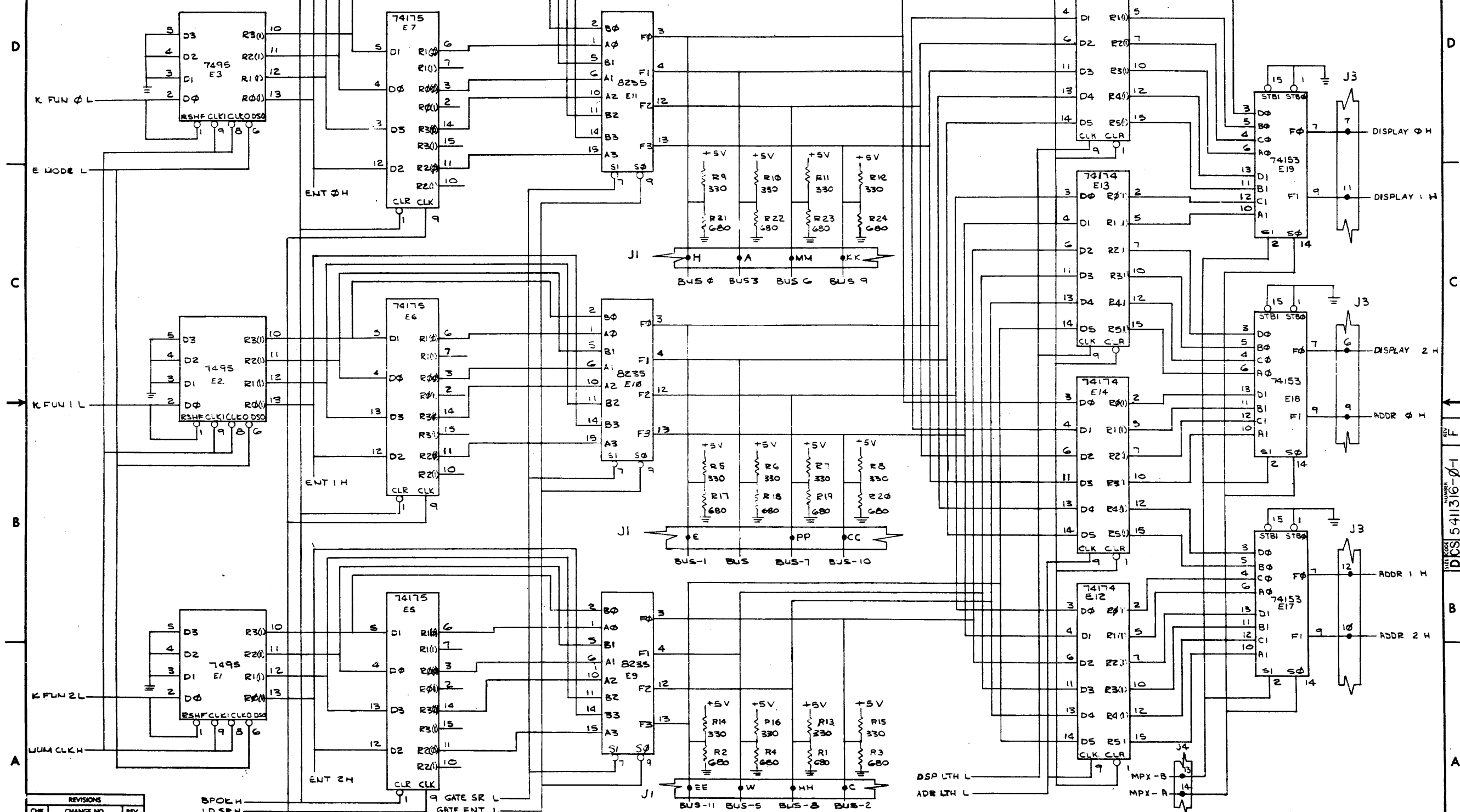
TITLE: REGISTERS & CONTROL

SIZE CODE: DCS 5411316-0-1

NUMBER: [Blank]

REV: F

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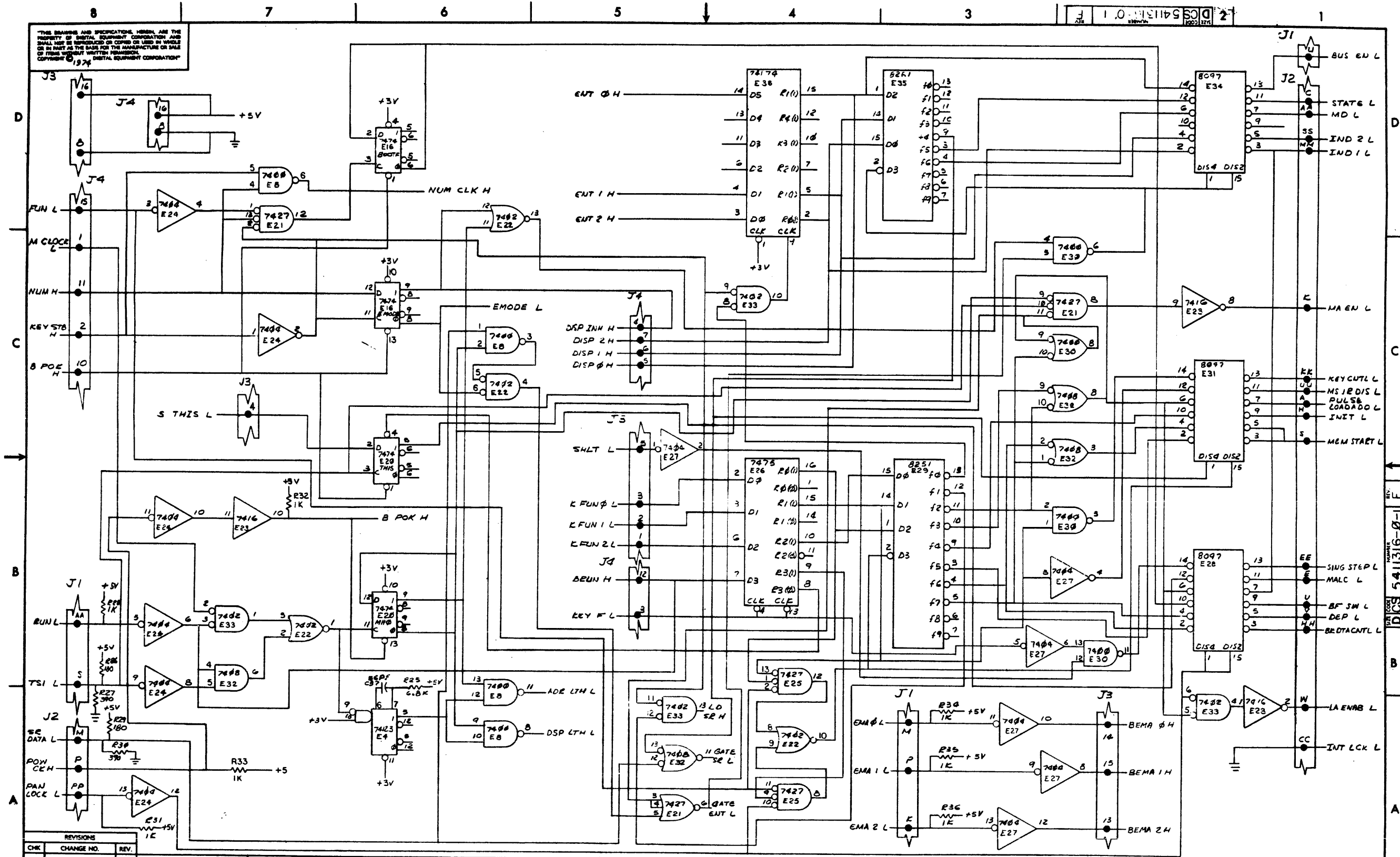


REVISIONS		
CHK	CHANGE NO.	REV.

REV F NUMBER DCS 5411316-0-1

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DCS 5411316-01



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	REGISTER CONTROL	SIZE CODE	D C S 5411316-01	NUMBER	1	REV.	F
SCALE		SHEET	B OF 3	DIST.			

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DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		DATE 22 MARCH 78	
ENGINEERING SPECIFICATION			
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE			
REVISIONS			
REV	DESCRIPTION	CHG NO	DATE
A	ECO CHANGE	00001A	STUDY 4-78
B	ECO CHANGE	ML002	PCARDNER 12-78

REV	DATE	APPD BY
B	3-2-78	<i>[Signature]</i>
A	12-78	<i>[Signature]</i>

ENG	<i>[Signature]</i>	APPD	R. B. Ryan	SIZE	CODE	NUMBER	REV
EN-10	18-0022-16-N370-081			A	SP	KT8A-3	B

SHEET 1 OF 12

ENGINEERING SPECIFICATION		CONTINUATION SHEET
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE		
I General	<p>This document will define the hardware requirements and tests to be performed to: #1) install, #2) configure and #3) accept a KT8-AA system or KT8-AB add-on to an existing system.</p> <p>Because the KT8-A Memory Management options has several possible hardware configurations, the Hardware Rules/Restrictions (appendix A), General Configuration Guide (appendix B), and Configuration Examples (appendix C) should be referenced before installing this option.</p> <p>A. If the KT8-AA was shipped as part of a system, refer only to the Acceptance procedure.</p> <p>B. If the KT8-AB is an add-on installation to upgrade an existing system, then refer to the Installation and Acceptance Procedures.</p>	REV B
II Hardware	<p>This section defines the required hardware to install and accept a KT8-A and also defines the three hardware designations of the KT8-A option.</p> <p>A. The KT can be installed and accepted on any 8A/420 or 620 machine.</p> <p>B. The KCPA Programmer's Console is not required, as the KT diagnostics have a console package.</p> <p>C. Program loading media is via: Paper tape, Floppy, or RK05.</p> <p>D. The Three designations of the KT are as follows:</p> <ol style="list-style-type: none"> KT8A-A - the KT Memory Management option shipped as part of a system configured by a DEC Manufacturing facility. KT8A-B - the required hardware to upgrade an 8A/420 or 8A/620 system. The KMB-AC (M8317YB or YC) is part of this option. 	REV B

SHEET 2 OF 12

ENGINEERING SPECIFICATION		CONTINUATION SHEET
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE		
III Installation	<p>Before proceeding with your installation refer to Appendix A and B to familiarize yourself with the rules and configurations. Also refer to the configuration example that most represents your particular installation.</p> <ol style="list-style-type: none"> Install all memory in the system, refer to Configuration guide (appendix B). Install the KT8-AB in any vacant OMNIBUS slot with an "E" connector. If the system is comprised of two (2) BA8C boxes and memory will be located in each box than install the M9020, terminator module, in any available "E" connector of the box not containing the KT8-A (M8416). Now connect the cable (78-11411-1J) between the two berg connectors of the M8416 and M9020. 	REV B

SHEET 3 OF 12

ENGINEERING SPECIFICATION		CONTINUATION SHEET
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE		
IV Acceptance	<p>The time to accept a KT8-A configuration depends upon the amount of memory installed.</p> <ol style="list-style-type: none"> Load and run the KT8-A Memory Management Diagnostic, Maindec 08-DJKTA-A, for five min. with NO errors. Load and run the Extended Address Test Maindec 08-DHKMC-C, for one pass with NO errors. Load and run the Extended Memory Data and Checkerboard Test, Maindec 08-DHKMA-D, for one pass with NO errors. To insure system integrity, load and build a DEC/X8 program using version 2, which will exercise up to 128K of memory. It is important that the program is build using the latest DEC/X8 modules. <p>NOTE: Reference should be made to the latest write-up for DEC/X8 (version 2) as further parameters must be inputted to support break devices.</p>	REV B

SHEET 4 OF 12

ENGINEERING SPECIFICATION

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

ENGINEERING SPECIFICATION

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

APPENDIX A

HARDWARE RULES/RESTRICTIONS

- Any OMNIBUS CPU (KK8A or KK8F) using a BA8C box (20 slot box) is acceptable.
- The KT6-A system can only be configured using any combination of MR8AB (16K core) and MS8C (16K or 32K MOS) memories.
NOTE: MR8AA, MR8A, MS8A, MR8E, MR8EJ and MR8F memories cannot be used to configure a KT8A system.
- If the system is made up of MR8AB core memories (16K), then they must be modified per ECO MR8AB #7, refer to table 1 for instructions.
- If the system is made up of MS8C type memories (16K or 32K MOS), then refer to table 2 for switch configuration.
- The PDP/8E chassis cannot be used as part of a KT8-A system.

- If Power Fail/Auto Restart and/or Bootstraps are required as part of the system, then a KM8-AC (M8317YE or YC) must be used with the Memory Extension and Timeshare option disabled via the jumper. Configuration in table 3.

NOTE: The M8317 and M8317YA are incompatible with the KT8A system.

TABLE 1 MR8-AB 16K CORE MEMORY CONNECTIONS

MEMORY CONNECTIONS		JUMPER	
I BANK	I FIELD	I WIRE	I
0	0-3 (0-16K)	AB1 to EB2	1-3, 3-4 in
	4-7 (16-32)	AB1 to EB2	2-4, 3-4 in
1	0-3 (32-48)	AB1 to ED2	1-3, 3-4 in
	4-7 (48-64)	AB1 to ED2	2-4, 3-4 in
2	0-3 (64-80)	AB1 to EL2	1-3, 3-4 in
	4-7 (80-96)	AB1 to EL2	2-4, 3-4 in
3	0-3 (96-112)	AB1 to ER2	1-3, 3-4 in
	4-7 (112-128)	AB1 to ER2	2-4, 3-4 in

TABLE 2A MS8-CA 16K MOS MEMORY SWITCH SETTINGS

MEMORY		SWITCHES SET TO "OFF"	
I BANK	I FIELD	I	I
0	0-3 (0-16K)	S1-1	
	4-7 (16-32K)	S1-2	
1	0-3 (32-48K)	S1-3	
	4-7 (48-64K)	S1-4	
2	0-3 (64-80K)	S1-5	
	4-7 (80-96K)	S1-6	
3	0-3 (96-112)	S1-7	
	4-7 (112-128)	S1-8	

DEC FORM NO EN-01022-14-370-(381)
DRA 108

DEC FORM NO EN-01022-14-370-(381)
DRA 108

SIZE CODE SP A
NUMBER KT8A-3
REV B

SIZE CODE SP A
NUMBER KT8A-3
REV B

SHEET 5 OF 12

SHEET 6 OF 12

ENGINEERING SPECIFICATION

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

ENGINEERING SPECIFICATION

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

TABLE 2B MS8-CB 32K MOS MEMORY SWITCH SETTING

MEMORY		SWITCHES SET TO "OFF"	
I BANK	I FIELD	I	I
0	0-7 (0-32K)	S1-1 and S1-2	
1	0-7 (32-64K)	S1-3 and S1-4	
2	0-7 (64-96K)	S1-5 and S1-6	
3	0-7 (96-128)	S1-7 and S1-8	

TABLE 3 JUMPER CONFIGURATION TO DISABLE MEMORY EXTENSION AND TIMESHARE

JUMPERS
W1 OUT
W2 IN
W3 IN
W4 IN

APPENDIX B

General Configuration Rules

- All memories must be physically located in the OMNIBUS where an "B" connector is present.
- Remembering the above rule, place the memories as far away as possible from the CPU.
- Direct Memory Address interfaces can only be located between the CPU and the first memory element. With one exception, in a two box system (2 BA8C's) where memory is located in both boxes a DMA interface may be located in any vacant slot of the box containing the CPU.
- Programmed I/O interfaces may be located in any vacant slot of the system.
- When memories are located in two BA8C chassis then the KT8-EX option must be used to extend the memory management option bank bits. The M9020 terminator card must be located in an "E" connector of the BA8C not containing the M8416. The 70-11411-JJ cable is then connected between the M9020 and the M8416.

DEC FORM NO EN-01022-14-370-(381)
DRA 108

DEC FORM NO EN-01022-14-370-(381)
DRA 108

SIZE CODE SP A
NUMBER KT8A-3
REV B

SIZE CODE SP A
NUMBER KT8A-3
REV B

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ENGINEERING SPECIFICATION 00000000 CONTINUATION SHEET

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

APPENDIX C

Configuration Examples

Because the KT8-A is limited to use in the BA8C chassis (20 slot box) there are only four possible configurations.

- The entire system located in one BA8C with a KK8A CPU as shown below.

SLOT OPTION	DEFINITION
1	KK8A CPU (M8315)
2	DKC8A OPTION ONE (M8316), IF REQUIRED
3	KM8-AC OPTION TWO (M8317B or YC), IF REQUIRED
4	KT8-A MEMORY MANAGEMENT OPTION (M8416)
5	DMA DEVICES CONFIGURED FROM THIS POINT TOWARD MEMORY
6	-----
7	-----
8	-----
9	-----
10	-----
11	MEMORY CONFIGURED FROM THIS POINT TOWARD THE CPU ONLY I/O INTERFACES
12	-----
13	-----
14	-----
15	-----
16	-----
17	-----
18	-----
19	-----
20	ONLY I/O INTERFACES

DEC FORM NO EN-01022-16-N370-(881)
DRA.108

SIZE CODE A SP
NUMBER KT8A-3
REV B
SHEET 9 OF 12

ENGINEERING SPECIFICATION 00000000 CONTINUATION SHEET

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

- The entire system located in one BA8C with a KK8F CPU as shown below.

SLOT OPTION	DEFINITION
1	KK8F TERMINATOR, M8320
2	DKC8A OPTION ONE (M8316), IF REQUIRED
3	KM8-AC OPTION TWO (M8317B or YC), IF REQUIRED
4	KT8-A MEMORY MANAGEMENT OPTION (M8416)
5	MEMORY CONFIGURED FROM THIS POINT TOWARD CPU
6	-----
7	-----
8	-----
9	-----
10	-----
11	-----
12	-----
13	-----
14	-----
15	-----
16	-----
17	-----
18	DMA AND I/O INTERFACES CONFIGURED FROM THIS POINT TOWARD MEMORY
19	KK8F CPU, M8310
20	KK8F CPU, M8330

DEC FORM NO EN-01022-16-N370-(881)
DRA.108

SIZE CODE A SP
NUMBER KT8A-3
REV B
SHEET 10 OF 12

ENGINEERING SPECIFICATION 00000000 CONTINUATION SHEET

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

- The KT8-A system made up of two BA8C boxes with the KK8F CPU in one box and all the memory located in the other box as shown below.

SLOT OPTION	DEFINITION
1	KK8F TERMINATOR, M8320
2	ANY I/O INTERFACE
3	ANY I/O INTERFACE
4	KT8-A MEMORY MANAGEMENT OPTION (M8416)
5	MEMORY CONFIGURED FROM THIS POINT TOWARD THE CPU
6	-----
7	-----
8	-----
9	-----
10	-----
11	LAST POSSIBLE MEMORY IN THIS CONFIGURATION
12	ANY DMA OR I/O INTERFACES
13	-----
14	-----
15	-----
16	-----
17	-----
18	-----
19	-----
20	BC08H-3 OMNIBUS EXPANDER CABLES (BOTTOM BA8C)
1	BC08H-3 OMNIBUS EXPANDER CABLES (BOTTOM BA8C)
2	DKC8A OPTION ONE (M8316), IF REQUIRED
3	KM8-AC OPTION TWO (M8317B or YC), IF REQUIRED
4	-----
5	-----
6	-----
7	-----
8	-----
9	-----
10	-----
11	-----
12	-----
13	-----
14	-----
15	-----
16	-----
17	-----
18	DMA AND I/O INTERFACES CONFIGURED FROM THIS POINT TOWARD MEMORY
19	KK8F CPU, M8310
20	KK8F CPU, M8330

DEC FORM NO EN-01022-16-N370-(881)
DRA.108

SIZE CODE A SP
NUMBER KT8A-3
REV B
SHEET 11 OF 12

ENGINEERING SPECIFICATION 00000000 CONTINUATION SHEET

TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

- The KT8-A system made up of two BA8C boxes with a KK8F in one box and memories located in both boxes as shown below:

SLOT OPTION	DEFINITION
1	KK8F TERMINATOR, M8320
2	ANY I/O INTERFACE
3	ANY I/O INTERFACE
4	M8020 KT8A TERMINATOR, LOCATE IN SLOT "E" OF OMNIBUS MEMORY CONFIGURED FROM THIS POINT TOWARD CPU
5	-----
6	-----
7	-----
8	-----
9	-----
10	-----
11	MEMORY
12	LAST MEMORY ELEMENT IN THIS BA8C
13	ANY I/O INTERFACES
14	-----
15	-----
16	-----
17	-----
18	-----
19	ANY I/O INTERFACES
20	BC08H-3 OMNIBUS EXPANDER CABLES (BOTTOM BA8C)
1	BC08H-3 OMNIBUS EXPANDER CABLES (BOTTOM BA8C)
2	DKC8A OPTION ONE (M8316), IF REQUIRED
3	KM8-AC OPTION TWO (M8317B or YC), IF REQUIRED
4	KT8-A MEMORY MANAGEMENT OPTION, M8416
5	MEMORY CONTINUE CONFIGURING MEMORY FROM THIS POINT TOWARD THE CPU
6	-----
7	-----
8	-----
9	-----
10	-----
11	-----
12	-----
13	-----
14	-----
15	-----
16	-----
17	-----
18	DMA AND I/O INTERFACES CONFIGURED FROM THIS POINT TOWARD MEMORY
19	KK8F CPU, M8310
20	KK8F CPU, M8330

DEC FORM NO EN-01022-16-N370-(881)
DRA.108

SIZE CODE A SP
NUMBER KT8A-3
REV B
SHEET 12 OF 12

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2-1

COMPONENT SIDE VIEW



VUYSRPNMLKJHFEDCBA VUYSRPNMLKJHFEDCBA VUYSRPNMLKJHFEDCBA VUYSRPNMLKJHFEDCBA VUYSRPNMLKJHFEDCBA VUYSRPNMLKJHFEDCBA

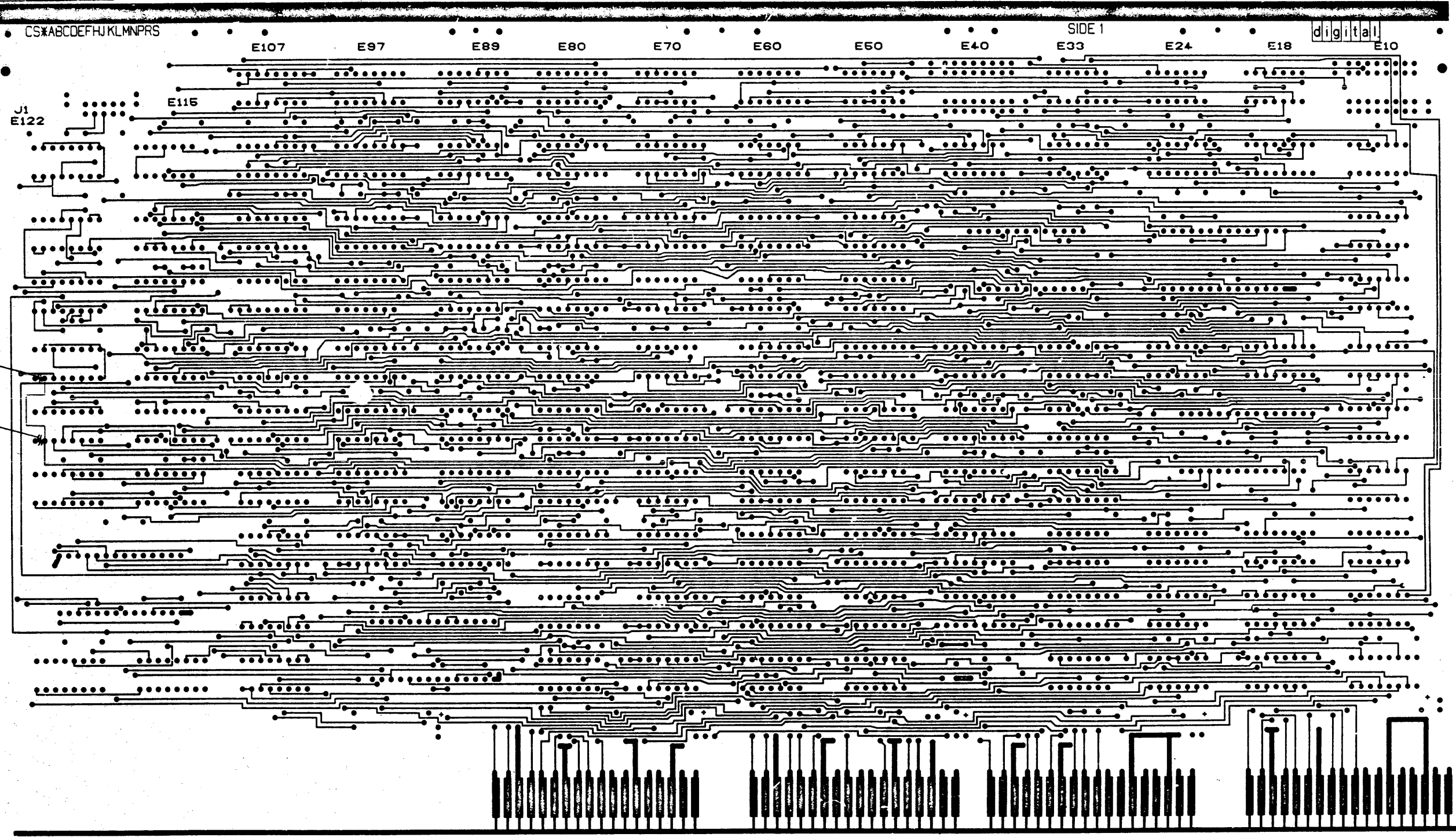
NOTES: MODULE REWORK AT RELEASE
ETCH CUT-SIDE 2 O-1 BETWEEN
DA2 & 2 FEEDTHRU'S NEAR C16

CHANGE NO.	REV.	DATE	BY	CHK'D.
1	C	10/17	JA	MB
2	D	11/77	S.KLIEN	
3				
4				

ETCH REV.	REV.
P.C. DESIGN DATA BASE REV.	C

SIGNATURES	DATE	TITLE
DRN. <i>Bob K...</i>	10/17	digital FDP8 MEMORY MANAGEMENT BOARD
CHK'D. <i>Bob K...</i>	11/77	
ENG. <i>R. P...</i>	11/77	
PROJ. ENG. <i>R. P...</i>	11/77	
SCALE 2/1		SIZE CODE NUMBER REV
SHT. 1 OF 6		D UA M8416-0-0 D
NEXT HIGHER ASSY. KM88		

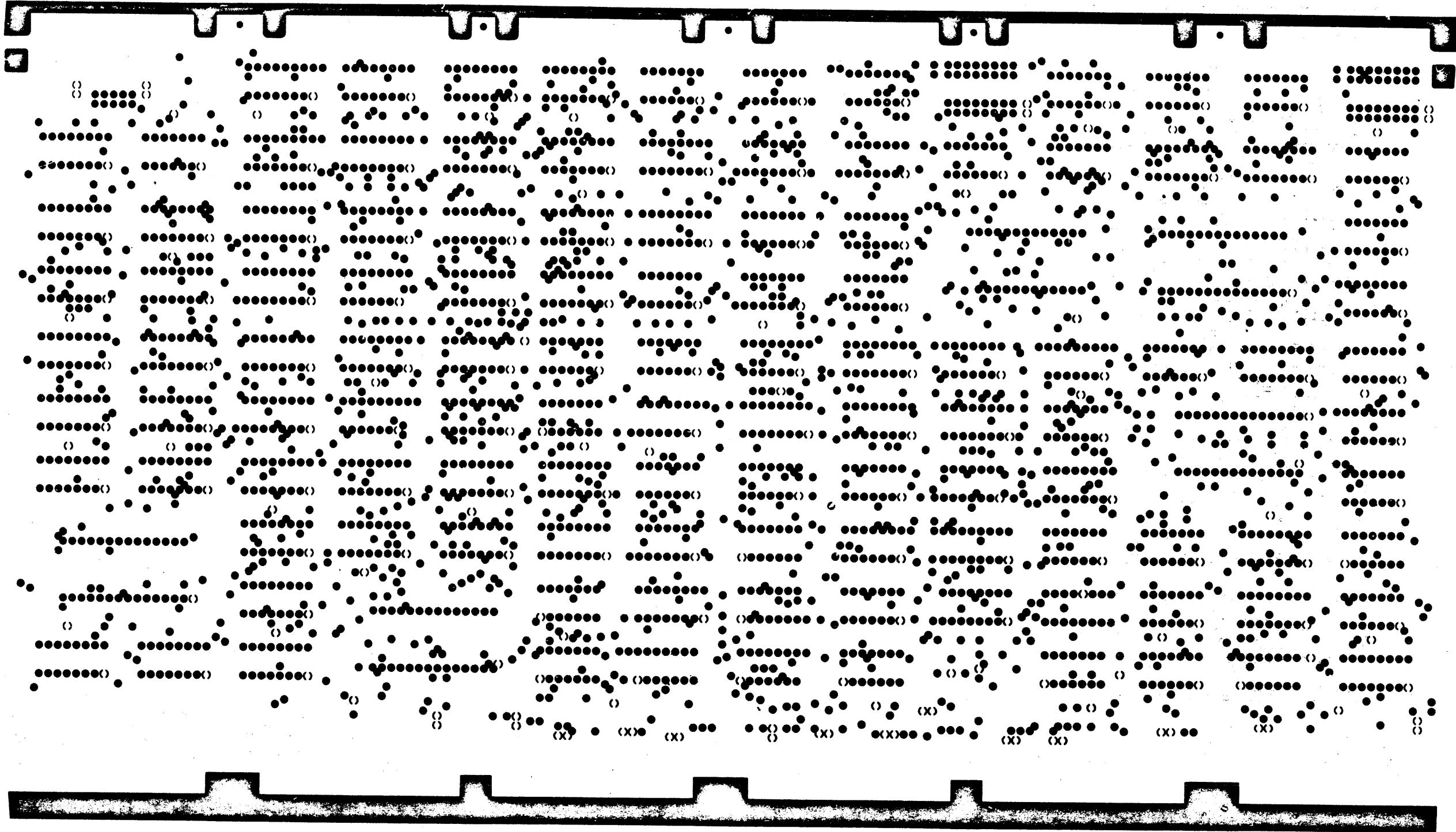
CS*ABCDEFGHIJKLMNPRS



VIEWED FROM SIDE 1

REV-SIGNS		
CHK	CHANGE NO	REV

TITLE	MANAGEMENT BOARD	SIZE CODE	DUA	NUMBER	M8416-4-D	REV	D
SCALE	1:1	SHEET	5	OF	6	DIST	



VIEWED FROM SIDE 1

REVISIONS	
CHK	CHANGED

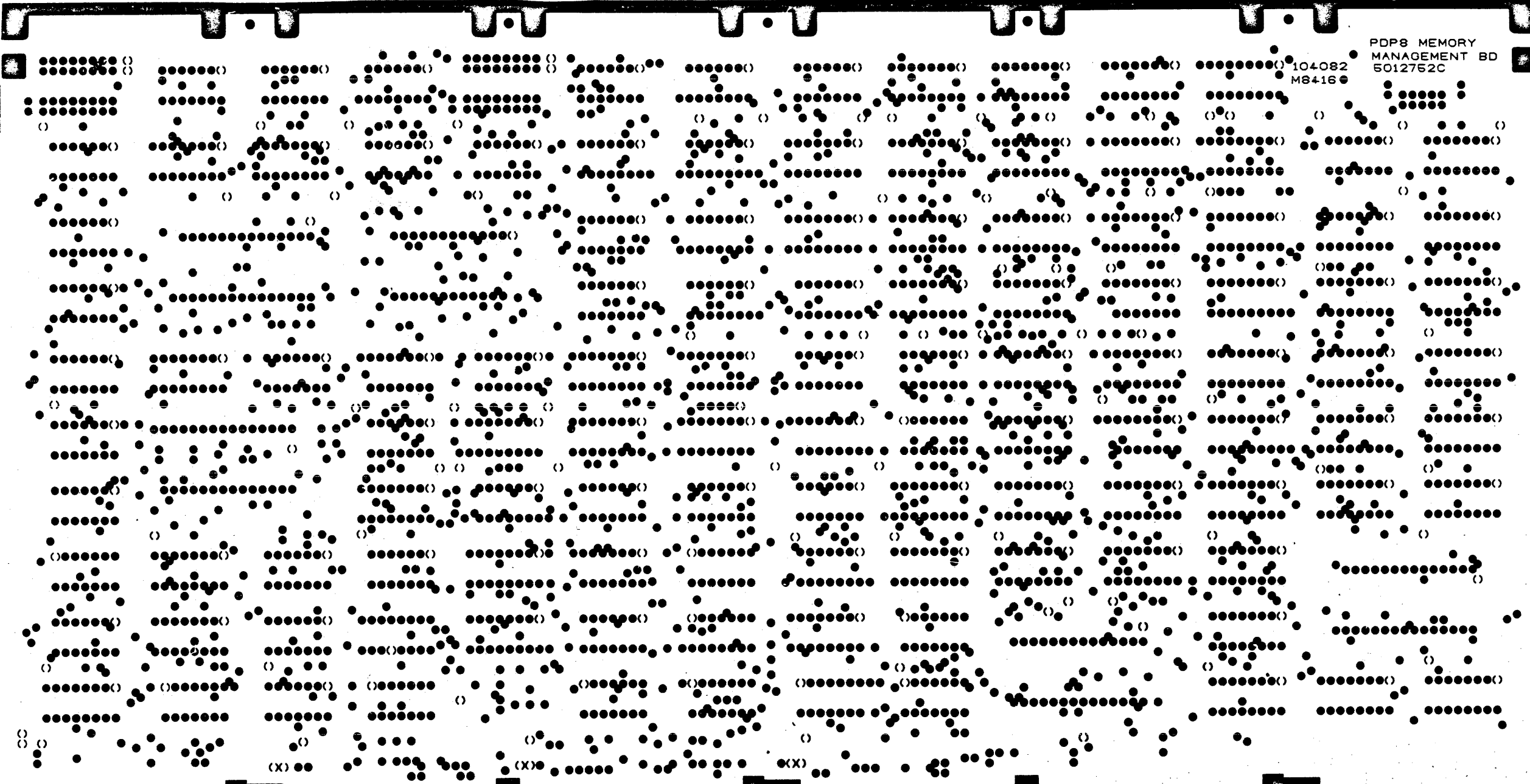
TITLE		SIZE		NO. OF SHEETS		DATE	
SCALE		SHEET	3	OF	6		

D UA M8416-0-0 D

6

PDP8 MEMORY
MANAGEMENT BD
5012752C

104082
M8416



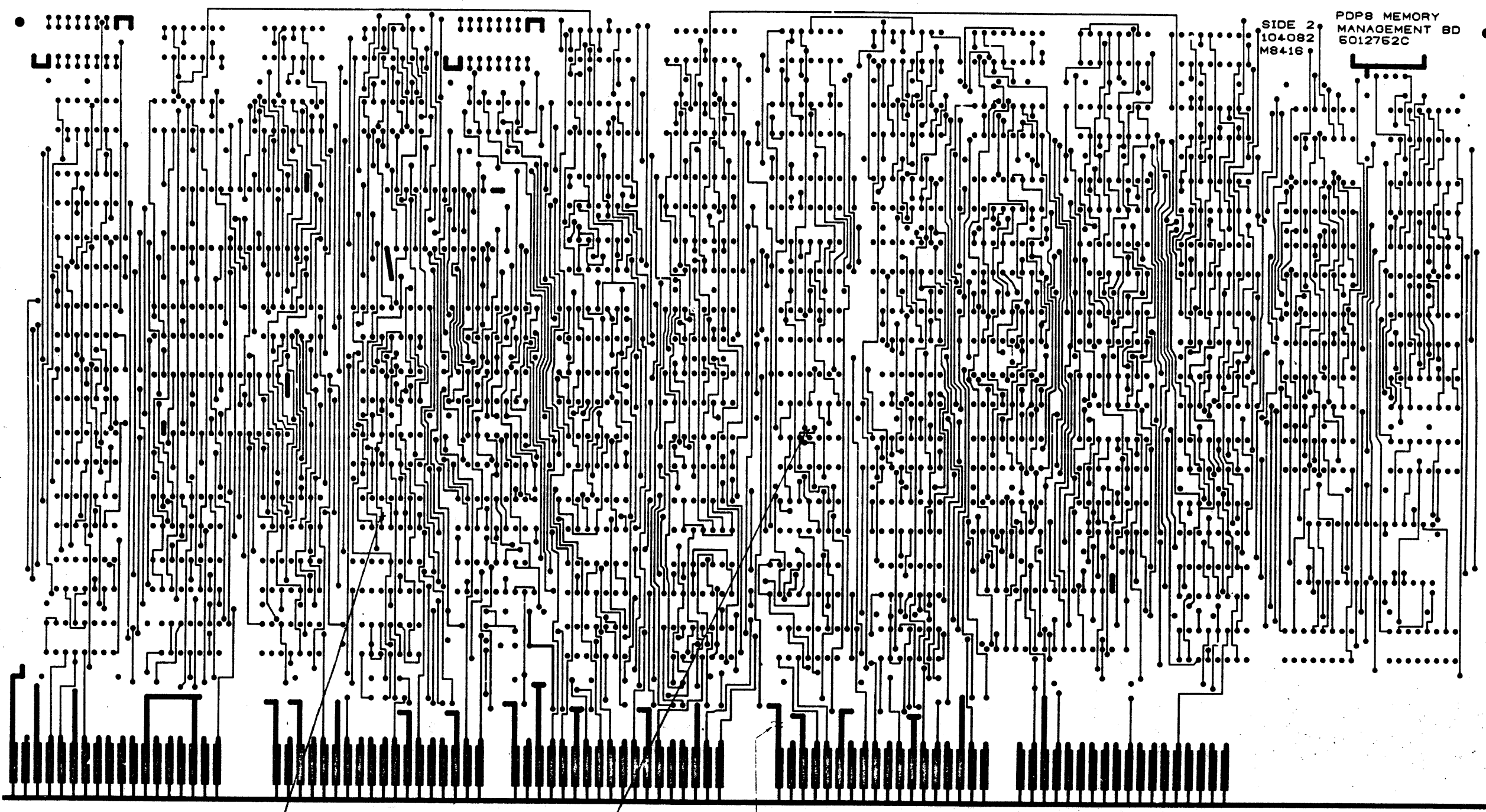
VIEWED FR. M. SIDE Z

REV.	CHANGE NO.	DATE	TITLE	SCALE	SHEET	OF	DATE	DESIGNER	CHECKER	APP.
			PDP8 MEMORY MANAGEMENT BOARD		4	6				

DUA M8416 - 4-6

PDP8 MEMORY
MANAGEMENT BD
6012762C

SIDE 2
104082
M8416



2-2

WEAVER FR. M SIDE 2

1-3

0-1

CHA	CHANGE NO	REV

--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

TITLE	PDP8 MEMORY MANAGEMENT BOARD	SIZE/COU	DUA	NUMBER	M8416	REV.	-0-0	D
SCALE	1:1	SHEET	5	OF	6	DIST		

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REWORK INSTRUCTIONS

E.C.O. #1
 ETCH CUTS SIDE 1
 I-1: BETWEEN E118 PIN1 & E118 PIN2
 I-2: BETWEEN E119 PIN1 & E119 PIN2
 ETCH CUTS SIDE 2
 I-3: BETWEEN E64 PIN10 & FEEDTHRU ABOVE AND BETWEEN E64 PINS11,12
 WIRE ADDS SIDE 1
 I-4: FROM E118 PIN2 TO E119 PIN2
 I-5: FROM E118 PIN1 TO E65 PINS 5-7
 I-6: FROM E65 PIN6 TO FEED THRU THAT WAS CUT FROM E64-10

ECO #2:
 ETCH CUTS SIDE2
 2-1: E27-5
 WIRE ADDS SIDE1
 2-2: E33-11 TO E27-5

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP 8 MEMORY MANAGEMENT BOARD	SIZE CODE	D JA	NUMBER	M8416-0-C	REV.	D
SCALE	1:1	SHEET	6 OF 6	DIST.			

8 7 6 5 4 3 2 1

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	REFERENCE DESIGNATOR
1	D-UD-5012752-0-0	5012752-00	M8416	1	
2		1000016-00	100.0 MMF 100V 5%200PPM DM155	2	C12,C19
3		1005306-00	6.0MFD 35V 10% S.TANT	5	C1,C8,C14,C16,C24
4		1012784-00	.047 MFD 50V -20+80 CER	20	C2-C7,C9,C10,C11,C13,C15,C17,C18,C20-C23, C25-C29,C31-C36
5		1110603-00	1N 5711 TM=100PS PIV=70V HMB	1	D1
6		1209941-05	HEADER,100 10POS RT ANGLE	1	J1
7		1210711-02	HANDLE,MODULE,HEX	1	
8		1300316-00	470 1/4W 5% CC	1	R5
9		1300365-00	1 K 1/4W 5% CC	25	R1,R2,R3,R6-R10,R12,R14,R16,R17,R22-R29, R31-R35
10		1300479-00	10 K 1/4W 5% CC	12	R4,R11,R13,R15,R18,R19,R21,R30,R36-R39
11		1302941-00	14.7 K 1/4W 1% PN550-F 100PPM	1	R20
12		1501999-00	DEC3009A NPN 300MW SI 20 25 M	1	Q1
13		1909701-00	74154 1 OF 16,RINA	1	E31
14		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	4	E4,E8,E19,E42
15		1909934-00	8266 MUX 1 OF 2 (QUAD)	2	E69,E79
16		1910393-00	DEC 7384 OR GATE-QUAD 2IN,UTI	4	E52,E53,E72,E75
17		1910537-00	74S11 AND GATE-TRIPLE 3INP	1	E27
18		1910544-00	74874 FF-D DUAL,EDGE TRIGG	3	E32,E63,E65
19		1911330-01	74173N FF-D QUAD,TRI-STATF	11	E1,E62,E73,E90,E97,E98,E99,E110,E119,E121, E122
20		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	7	E3,E11,E25,E41,E51,E61,E71
21		1911527-00	8097 BUFFER GATE-HEX 2INP	10	E37,E55,E83,E84,E91,E100,E101,E104,E105, E107
22		1911579-00	8641 TRANSCIEVER,BUS,QUA	3	E12,E13,E45
23		1911676-00	74S139 DECODER-DUAL TWO-INP	1	E120
24		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E64
25		1912649-00	LS75 LATCH 4BIT,BISTABLE	1	E26
26		1912661-00	74S189 MEMORY READ/WRITE	1	E82

REVISION HISTORY		SECTION 1 OF 1	RESP.ENG.:	R. REGAN	DATE:	27-OCT-77	D I G I T A L				
ENG:	ECO NUMBER	REV	SECTION. VARIATION INDEX:		MADE BY:	DATE:	TITLE				
J.A.	00001	IC			TED KELLEY	29-AUG-77	PARTS LIST				
A.T.	M8416-ML002	D	1.00				POP8 MEMORY MANAGEMENT BOARD				
			2.								
			3.		CHECKED:	N. GELARDERS	DATE:				
			4.								
			5.								
			6.		DSN.ENG.:	R. REGAN	DATE:	14-NOV-77			
			7.				SIZE:	CODE:	DOCUMENT NUMBER	REV	
			8.				K	PL	M8416-0-DBP	D	
			9.		PROD.:	MELVIN SCHENKE	DATE:	14-NOV-77			
			10.								
			11.								
			12.		ASSEMBLY NUMBER:	D-UA-M8416-0-0	PART NUMBER:	M8416		EDIT	
										36	

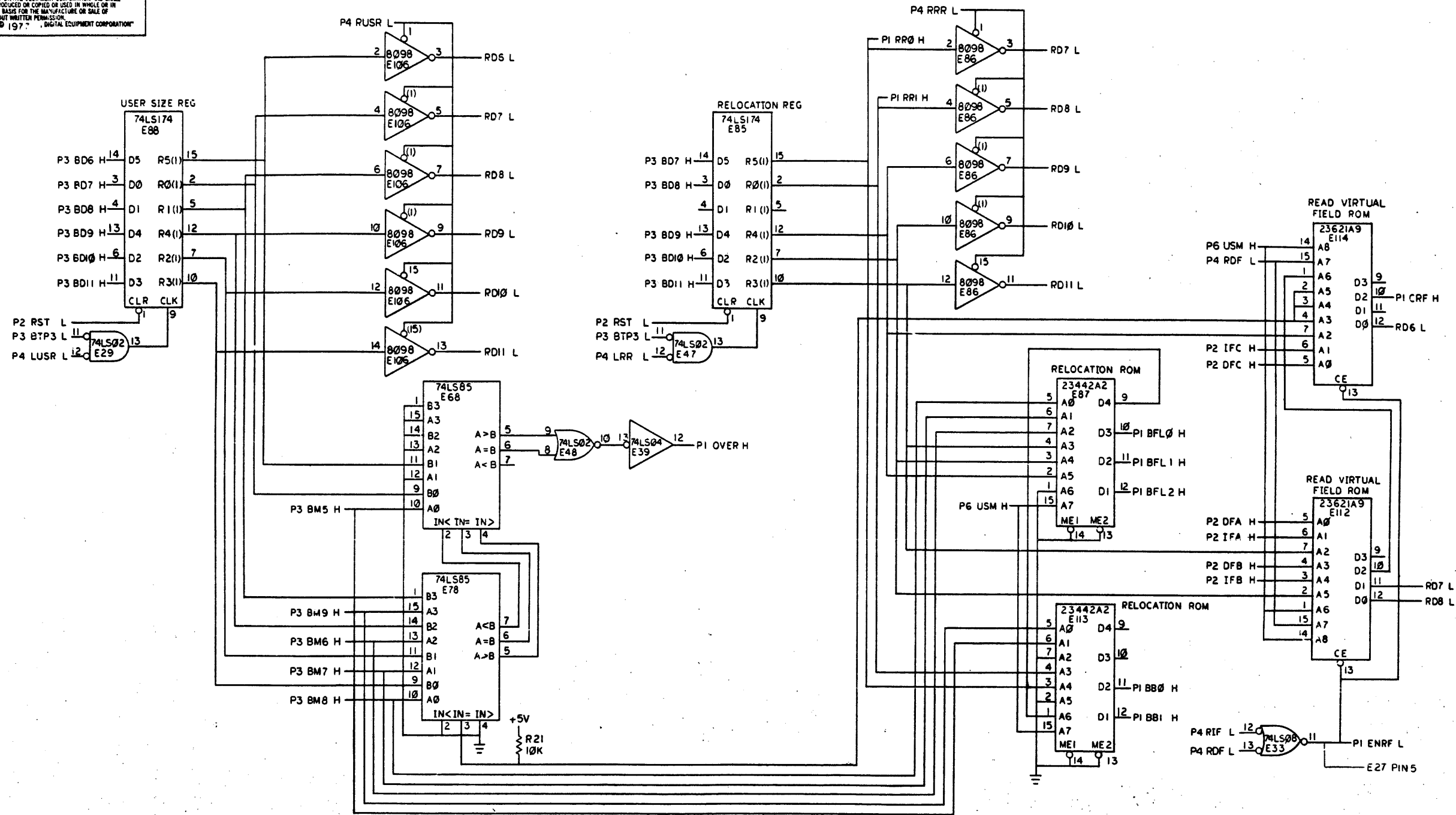
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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	REFERENCE DESIGNATOR
27	27		1912697-00	LS174 FF-D HEX W/CLEAR	5	E15,E17,E85,E88,E103
28	28		1912796-00	74148 EXCODER,PRIORITY,8 T	1	E89
29	29		1912799-00	LS004 NAND-GATE-QUAD 2IN,P	8	E6,E18,E50,E54,E57,E66,E70,E94
30	30		1912800-00	LS01 NAND-GATE-QUAD 2IN,P	1	E20
31	31		1912801-00	LS02 NOR-GATE-QUAD 2IN	3	E29,E47,E48
32	32		1912803-00	LS04 INVERTER GATE-HEX 1I	8	E2,E5,E7,E22,E39,E44,E58,E115
33	33		1912805-00	LS08 AND GATE-QUAD 2IN,PO	2	E33,E67
34	34		1912807-00	LS10 NAND GATE-TRIPLE 3IN	5	E21,E24,E49,E56,E92
35	35		1912810-00	LS20 NAND GATE-DUAL 4IN	3	E9,E36,E43
36	36		1912815-00	LS30 NAND GATE-SINGLE 8IN	1	E30
37	37		1912817-00	LS37 NAND GATE-QUAD 2IN,P	1	E38
38	38		1912819-00	LS42 DECODER,BCD-DECIMAL	1	E34
39	39		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	3	E59,E60,E76
40	40		1912828-00	LS85 COMPARATOR,4BIT MAGN	2	E68,E78
41	41		1912853-00	LS175 FF-D QUAD	8	E35,E46,E93,E102,E108,E110,E116,E117
42	42		1912858-00	LS221 ONE SHOT-DUAL,SCHMIT	1	E77
43	43		1912859-00	LS256 MUX 1 OF 2 (DUAL)	1	E23
44	44		1914087-00	8098 BUFFER GATE-HEX 2IN,	4	E86,E95,E96,E106
45	45		23211A1-00	A1-07	1	E80
46	46		23440A2-00	A2-05	1	E28
47	47		23441A2-00	A2-05	1	E74
48	48		23442A2-00	A2-05	2	E87,E113
49	49		23621A9-00	A9-01	3	E111,E112,E114
50	50		23007C6-00	C6-01	1	E14
51	51		23008C6-00	C6-01	1	E16
52	52		23009C6-00	C6-01	1	E81
53	53		23010C6-00	C6-01	1	E109
54	54		9000024-01	EYFLEF, ROLLED FLANGE, .121 OD X	12	
55	55		9105740-55	WIRE(WRAP)30AWG UL1423	A/R	

56 NOTE: LINE 18: PARTS SUBSTITUTION LIST
 57 NOTE: ITEM #18 1910544-01 74S74 FF-D DUAL (60 VERSION) QTY 3
 58 NOTE: ITEM #18 1910950-00 74S74 FF-D DUAL (45 VERSION) QTY 3

D	I	G	I	T	A	L	TITLE	PU88 MEMORY MANAGEMENT BOARD	SECTION 1 OF 1	SIZE	CODE	DOCUMENT NUMBER	R
										K	PL	M8416-0-DBP	D

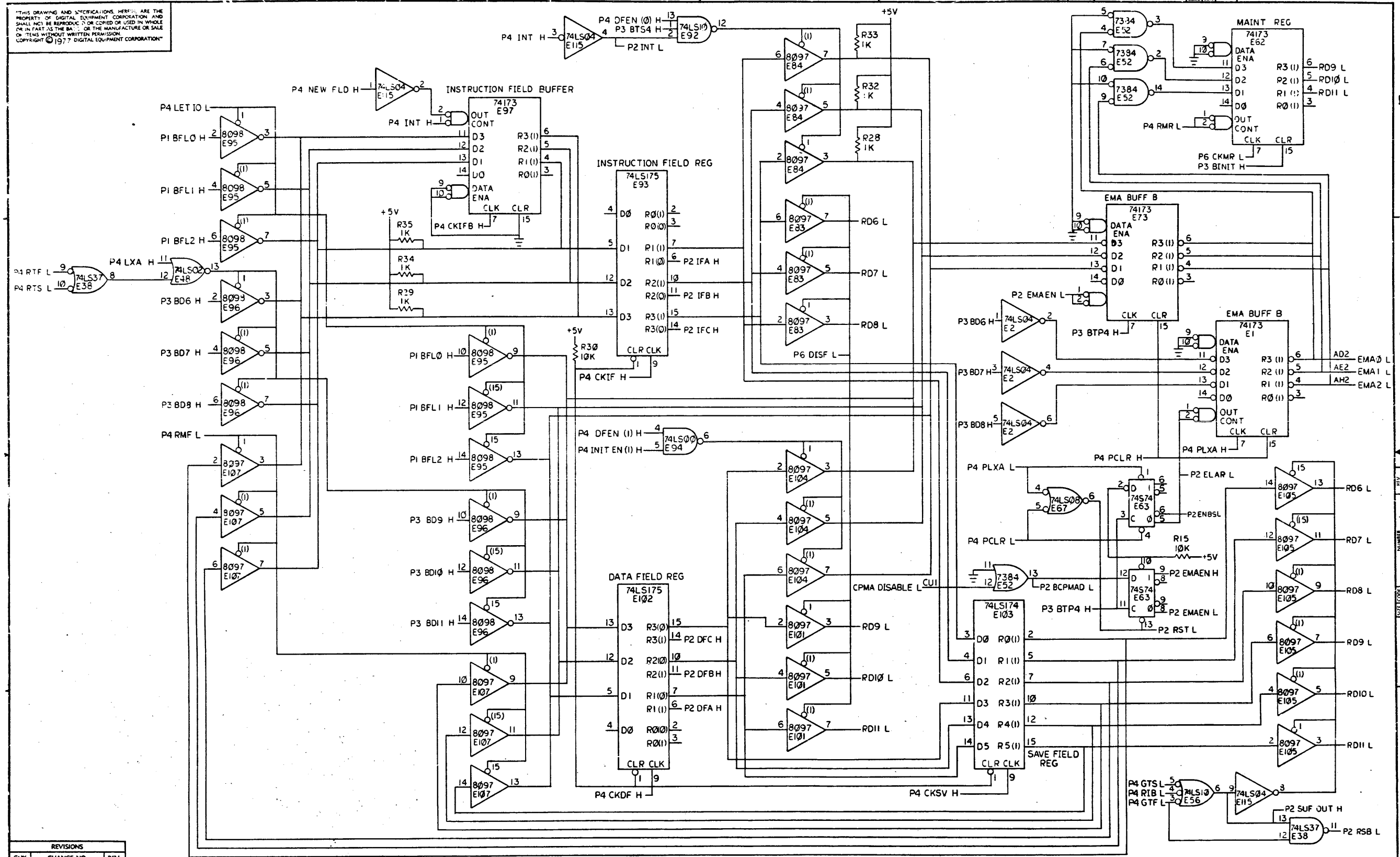
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REV.	CHG.	BY	DATE
C	1	JA	11-11-77
D	2	SK	12-7-77
		AM	12-7-77
		AS	12-7-77

DRN. TEST	11-4-77	FIRST USED ON	KT8-A	
CHKD	12/1/77	TITLE	PDP8 MEMORY MANAGEMENT BOARD	
ENGR	12-7-77	SCALE	D CS	NUMBER
PROJ. ENG.	12-7-77	SCALE	M8416-0-1	REV.
PROD.	12-7-77	SHEET	1 OF 7	D
NEXT HIGHER ASSY.		D-UA-M8416-0-0		(P1)
SCALE		D CS		D
SHEET		1 OF 7		DIST.

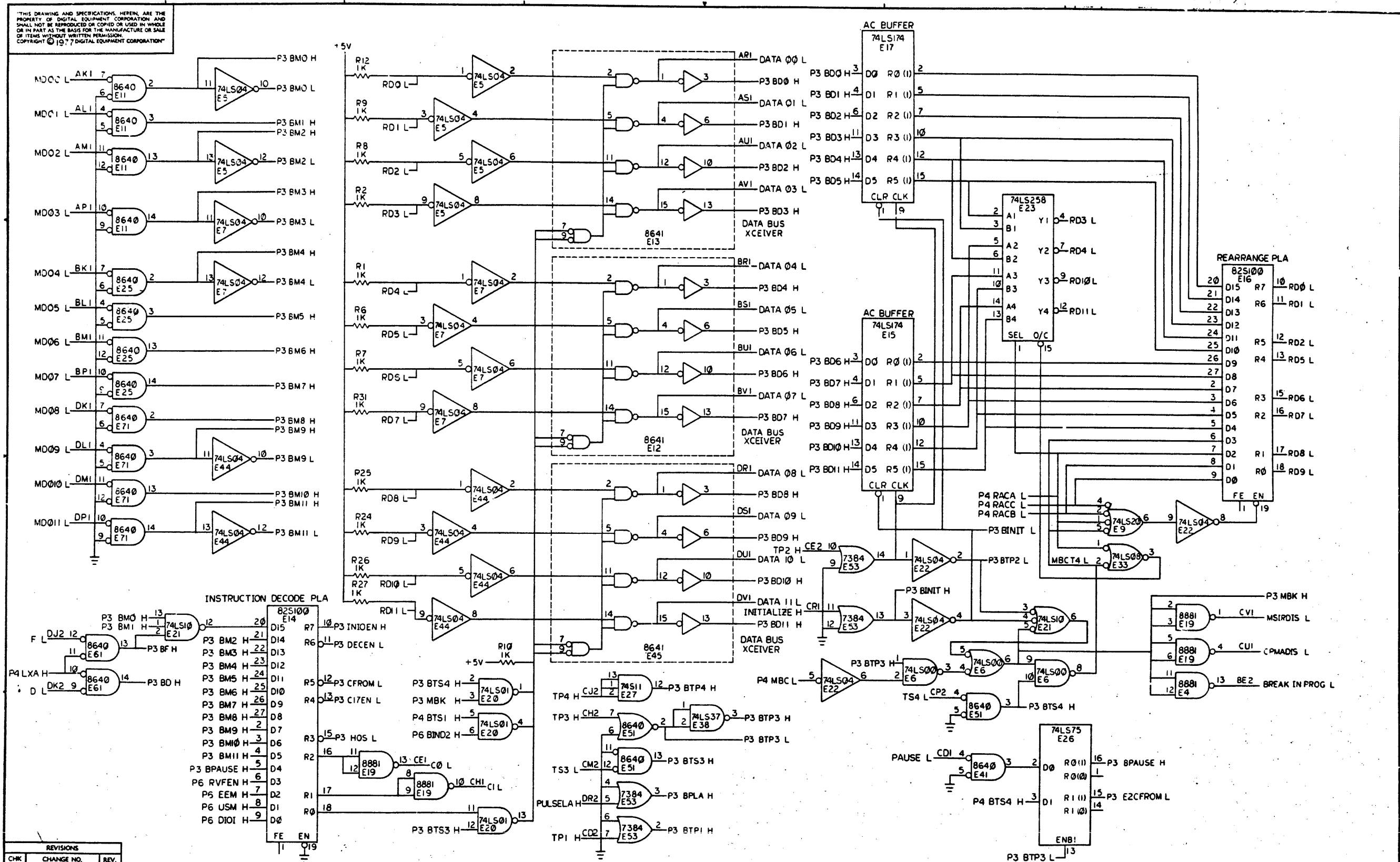
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MEMORY MANAGEMENT BOARD (P2)	SIZE/SCALE	D CS	NUMBER	M8416-0-1	REV.	D
SHEET	2 OF 7	DIST.					

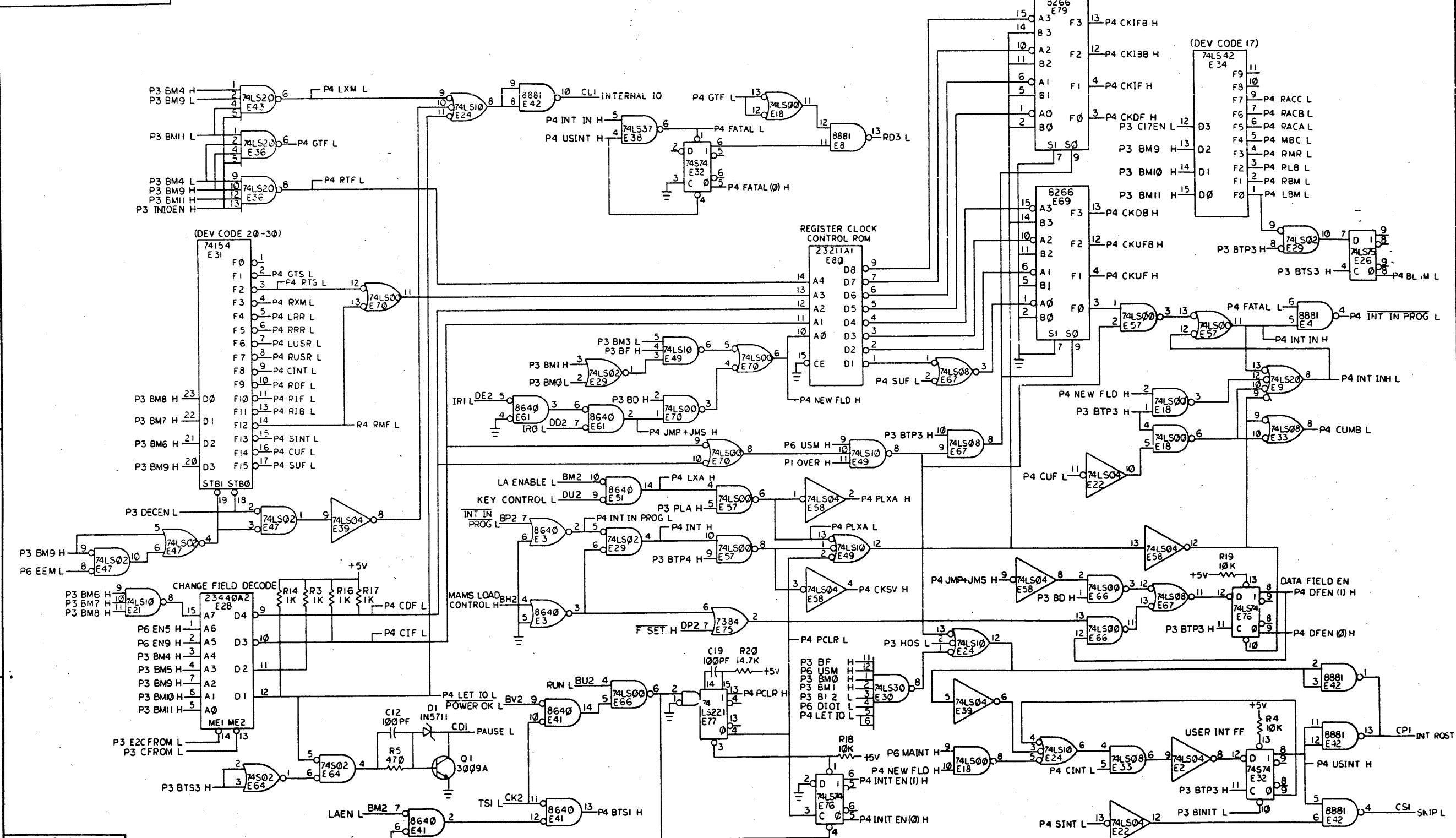
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MEMORY MANAGEMENT BOARD (P3)	SIZE CODE	D CS	NUMBER	M8416-0-1	REV.	0
SCALE	SHEET 3 OF 7		DIST.				

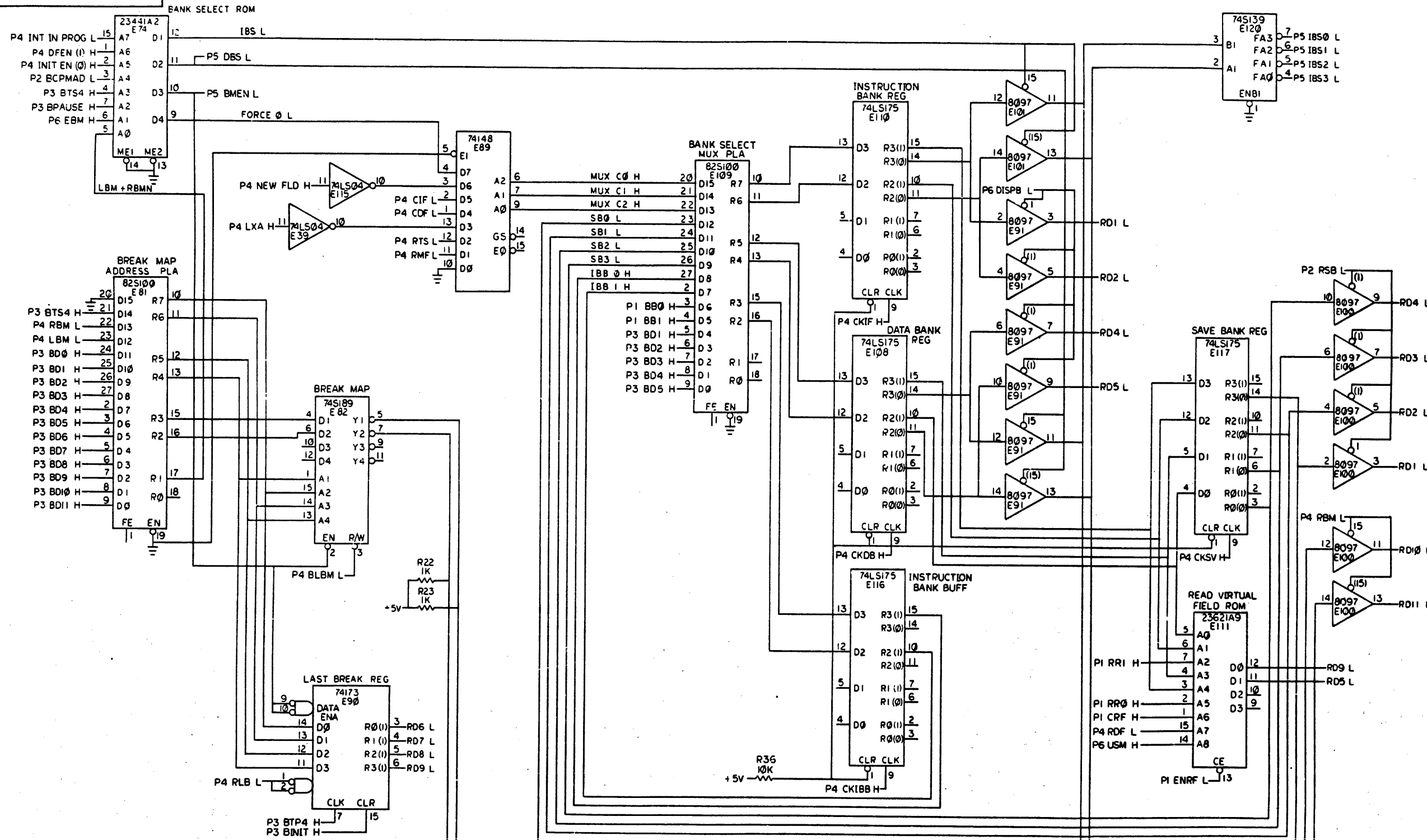
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REVISIONS		
CHK	CHANGE NO.	REV.

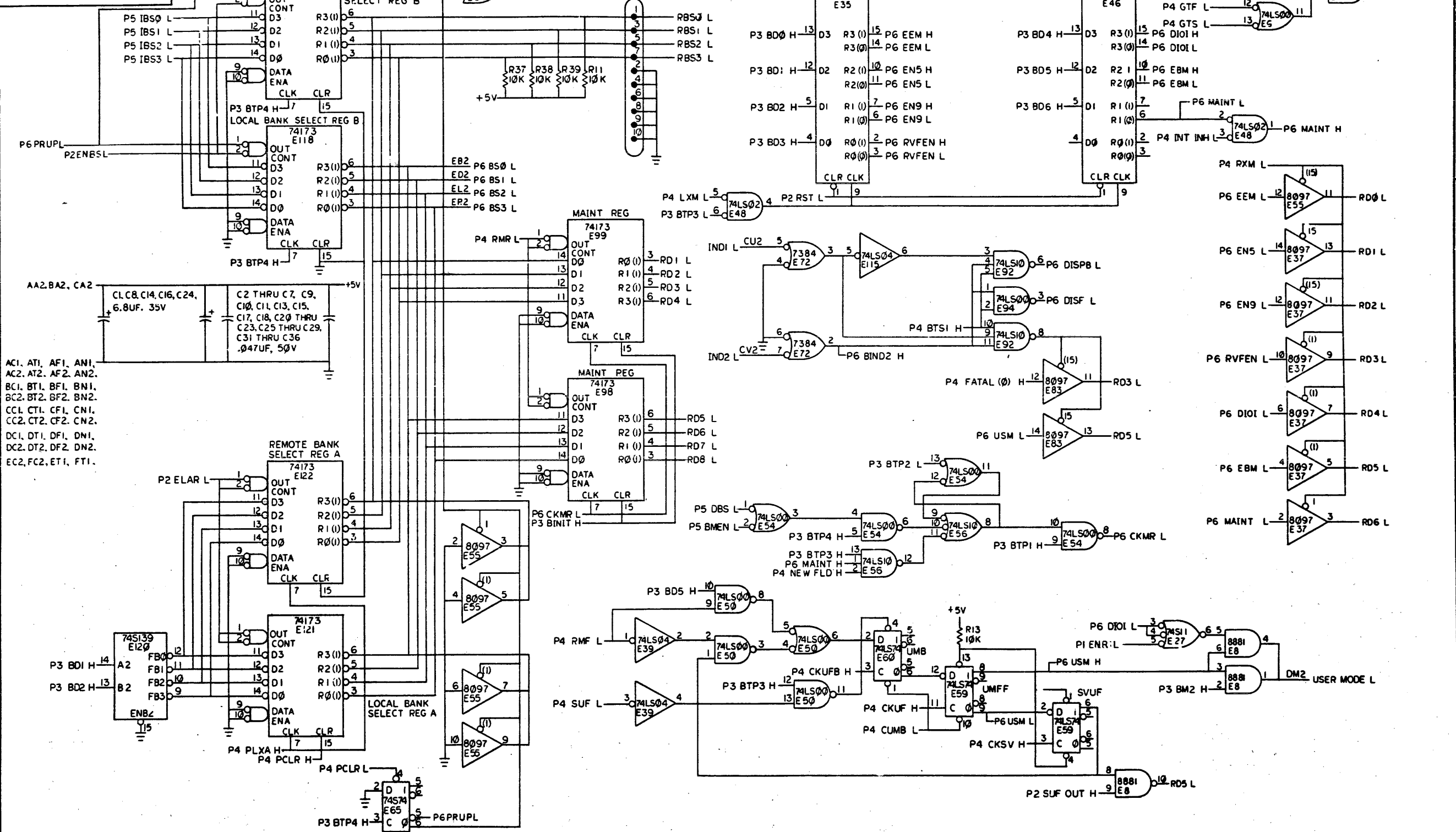
TITLE		PDP8 MEMORY MANAGEMENT BOARD (P4)		SIZE	CODE	NUMBER	REV.
SCALE		SHEET 4 OF 7		D	CS	M8416-0-1	0
DIST.							

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REVISIONS		
CHK	CHANGE NO.	REV.

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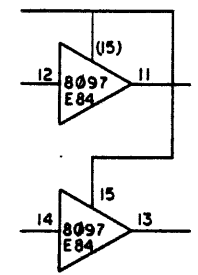
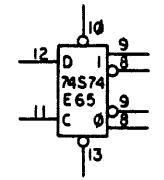
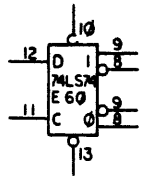
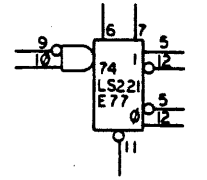
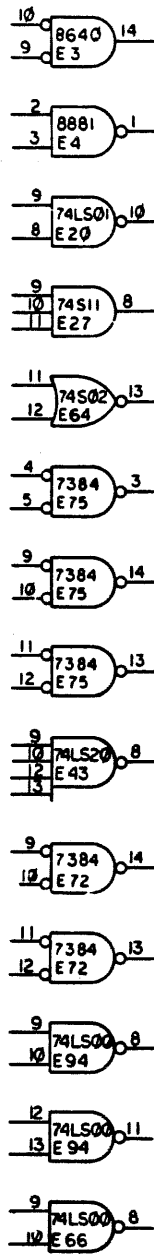
- AC1, AT1, AF1, AN1,
- AC2, AT2, AF2, AN2,
- BC1, BT1, BF1, BN1,
- BC2, BT2, BF2, BN2,
- CC1, CT1, CF1, CN1,
- CC2, CT2, CF2, CN2,
- DC1, DT1, DF1, DN1,
- DC2, DT2, DF2, DN2,
- EC2, FC2, ET1, FT1.

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MEMORY MANAGEMENT BOARD (PG)	SIZE CODE	D CS	NUMBER	M8416-0-1	REV.	D
SCALE		SHEET	6 OF 7	DIST.			

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SPARES

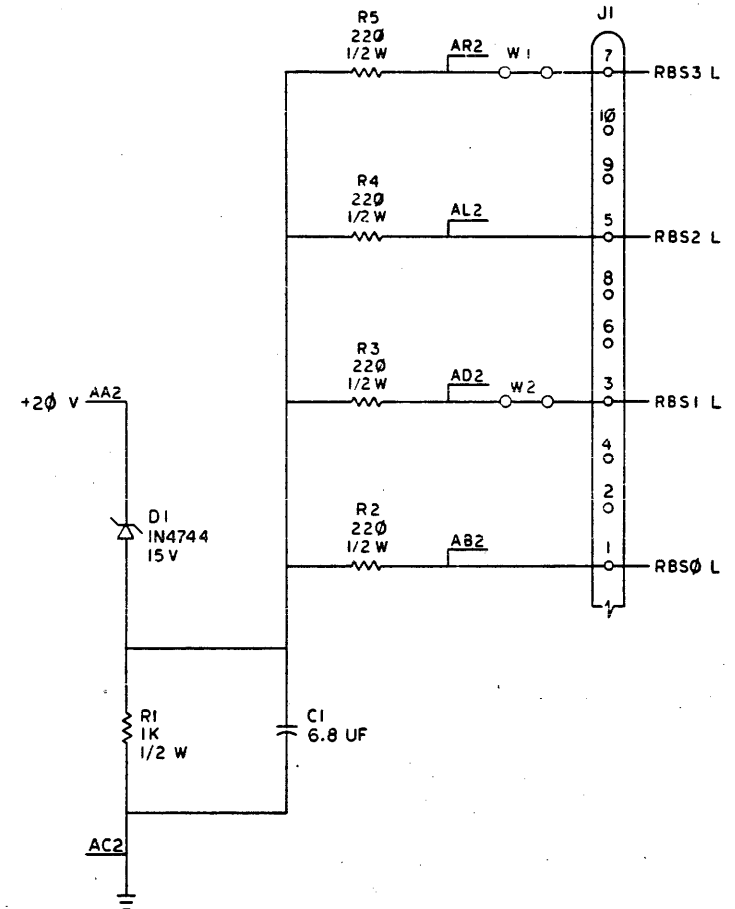


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		PDP8 MEMORY MANAGEMENT BOARD		SIZE CODE	NUMBER	REV.
SCALE		SHEET 7 OF 7		D CS	M8416-0-1	D

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1-0-0206W 2



REV.	
CHANGE NO.	
CHK	

DRN.	11-9-77	FIRST USED ON	KT8-A
CHKD.	12-5-77	TITLE	KT8-A TERMINATOR
ENG.	12-7-77		
PROJ. ENGR.	12-7-77		
PROD.	12-7-77		
NEXT HIGHER ASSY.			
D-1A-M9020-0-0	SIZE	CODE	NUMBER
SCALE	D	CS	M9020-0-1
SHEET	OF	DIST.	REV.
1	1		*

M9020-01

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**DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS**

ENGINEERING SPECIFICATION

DATE 14 JUNE 77

TITLE MS8C Field Installation and Acceptance Procedure

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>Bill Eads</i> 8/24/77	APPD <i>J. Stearns</i> 8/25/77	SIZE A	CODE SP	NUMBER MS8-C-3	REV
------------------------------	--------------------------------	------------------	------------	-------------------	-----

DEC 16-(392)-1079A-R873
DRA 107A

SHEET 1 OF 3

ENGINEERING SPECIFICATION

000116

CONTINUATION SHEET

TITLE MS8C Field Installation and Acceptance Procedure

I. General

This procedure defines the performance standards required of the MS8CA and MS8CB Memories. The MS8C can be installed in any PDP8A series computers. The Memory may be an add-on to PDP8E systems if a BA8C expander is used.

II. Inspection Add-on

After removing the M8417 from the packing material, inspect the module for the following:

1. Loose or broken components.
2. Inventory against shipping lists.

III. Installation

1. Switch/Field Selection

MS8-CA (1) One switch(only) must be off.
MS8-CB (2) Two switch(only) must be off.

Set-up switch as defined in the following table. Switches off are the enable.

	Address	Bank	Field
S1-1	0-16K	0	0-3
S1-2	16-32K	0	4-7
*S1-3	32-48K	1	0-3
*S1-4	48-64K	1	4-7
*S1-5	64-80K	2	0-3
*S1-6	80-96K	2	4-7
*S1-7	96-112K	3	0-3
*S1-8	112-128K	3	4-7

*NOTE: KT8A must be installed for these settings. Refer to the KT8A Installation and Acceptance Procedures for KT8A systems acceptance.

2. Install

Ensure power to PDP8A is off.
Insert the M8417 into slot 4 of the PDP8A.
If two memories are to be installed, install the second in slot 5.

IV. Acceptance

Perform the acceptance test as indicated in the following table. If problems are encountered, refer to the diagnostic listings for the type of error and for information on how to read the error printouts.

SIZE A	CODE SP	NUMBER MS8-C-3	REV
------------------	------------	-------------------	-----

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

SHEET 2 OF 3

TITLE MS8C Field Installation and Acceptance Procedure

Equipment Required:

- 1. 03,04 Console Terminal
- 2. PDP8A with MS8C Memory
- *3. Input device, either paper tape or OS8.
- 4. Diagnostics and listings.

*Programmers console is required to make Switch register settings on the paper tape version of the diagnostic.

<u>Program Name</u>	<u>Maindec #</u>	<u>Accept Time</u>
Extended Memory Address Test	08-DHKMA	30 Minutes
Extended Memory Checkerboard	08-DHKMC	30 Minutes

No Errors are Acceptable.

SIZE A	CODE SP	NUMBER MS8-C-3	REV
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M8417-AA (16K)
X = SWITCH OPEN OR OFF

	E62-1	E62-2	E62-3	E62-4	E62-5	E62-6	E62-7	E62-8
0-16	X							
16-32		X						
32-48			X					
48-64				X				
64-80					X			
80-96						X		
96-112							X	
112-128								X

M8417-BA (32K)
X = SWITCH OPEN OR OFF

	E62-1	E62-2	E62-3	E62-4	E62-5	E62-6	E62-7	E62-8
0-32	X	X						
16-48			X					
32-64				X	X			
48-80					X	X		
64-96						X	X	
80-112							X	X
96-128								X

- NOTES:**
- MODULE DESIGNATION: M8417-AA = M8417-AB, -AC, -AD, -AE, ETC.
16K MOS MEMORY
REFERENCE DESIGNATIONS NOT USED:
E101 E201 E301 E401
E103 E203 E303 E403
E105 E205 E305 E405
E107 E207 E307 E407
E109 E209 E309 E409
E111 E211 E311 E411
E113 E213 E313 E413
E115 E215 E315 E415
E117 E217 E317 E417
E119 E219 E319 E419
E121 E221 E321 E421
E123 E223 E323 E423
 - MODULE DESIGNATION: M8417-BA = M8417-BB, -BC, -BD, -BE, ETC.
32K MOS MEMORY
 - ALL 8837'S HAVE PINS 7 & 9 GROUNDED.
 - TIMING RESISTORS R87 AND R92 MAY BE REMOVED AT MODULE TEST FOR TIMING ADJUSTMENTS.
 - TIMING RESISTORS R88, R100, R140 MAY BE INSTALLED AT MODULE TEST FOR TIMING ADJUSTMENTS.

JUMPER CONFIGURATIONS

JUMPER	M8417-AA (16K)	M8417-BA (32K)
W2	X	X
W3	X	X
W4	X	X
W5	X	X
W10	X	
W11	X	X

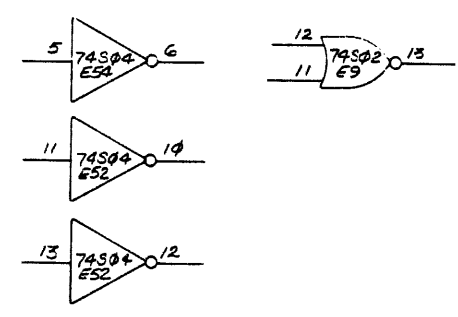
ALL JUMPERS ARE MACHINE INSERTABLE.
THESE JUMPERS ARE REMOVED:
W1, W5, W6, W7, W8, W12, W13, W14, W15, W16
X = JUMPER INSTALLATION

IC PIN LOCATIONS

IC TYPE	GND	+5	-5	+12
555	1	8		
7493	10	5		
74LS75	12	5		
8640	1	8		
75107B	7	14	13	
MK4027	16	9	1	8
OTHER 16 PIN IC'S	8	16		
OTHER 14 PIN IC'S	7	14		
75451	4	8		

SPARES

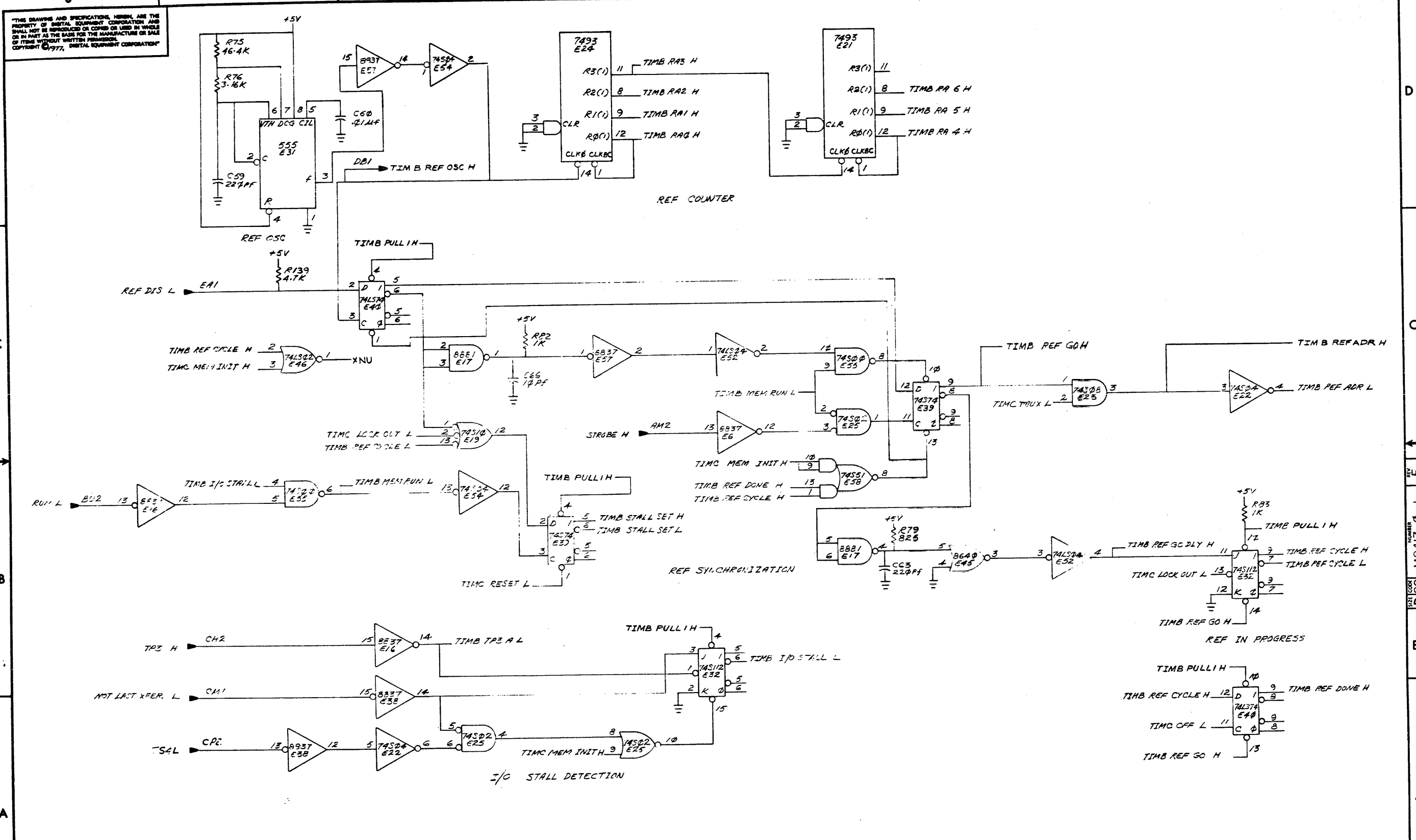
R88	3.16K 1/4W 1% MF	13-03045-00
R100	6040 1/4W 1% MF	13-13155-00
R140	1.21K 1/4W 1% MF	13-02871-00



REV.	CHG.	BY	DATE
A	1	J. STEGEMAN	7/17/77
B	1	J. STEGEMAN	7/17/77
C	1	J. STEGEMAN	7/17/77
D	1	J. STEGEMAN	7/17/77
E	1	J. STEGEMAN	7/17/77
F	1	J. STEGEMAN	7/17/77

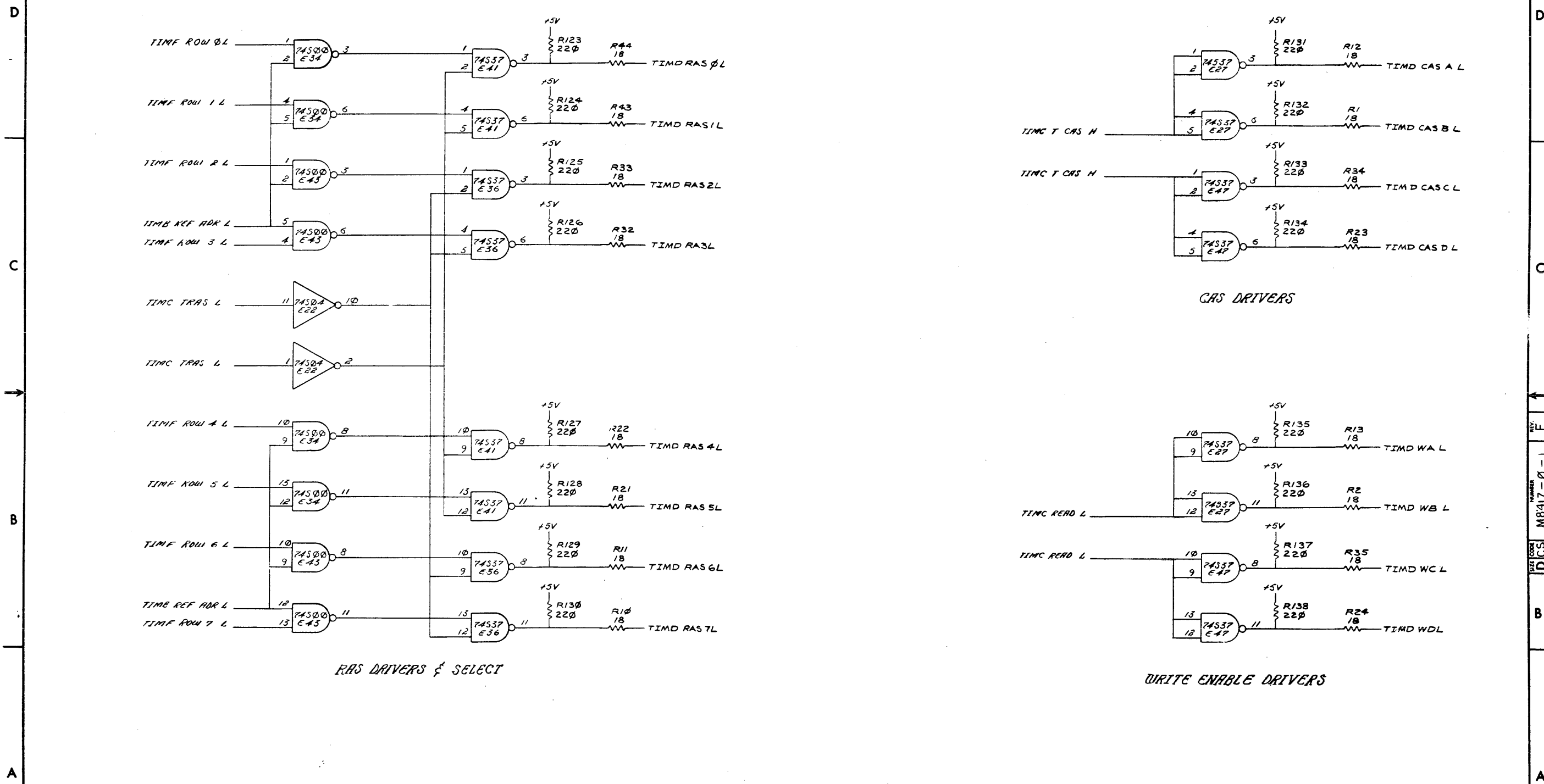
DRN. ANGEL CALD	4/25/77	FIRST USED ON	MS8-C
CHKD	7/17/77	TITLE	PDF8 MOS MEMORY
ENG.	7/17/77	SIZE	D
PROJ. ENG.	7/17/77	CODE	CS
PROD.	7/17/77	NUMBER	M8417-0-1
NEXT HIGHER ASSY.		REV.	F
B-DD-M8417-0		DIST.	
SCALE NCNE			
SHEET 1 OF 14			

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REVISIONS			TITLE		SIZE CODE		NUMBER		REV.
CHK	CHANGE NO.	REV.	PDP8 MOS MEMORY (TIM B)		DCS		M8417-0-1		F
			SCALE	NONE	SHEET	2	OF	14	DIST.
									ML

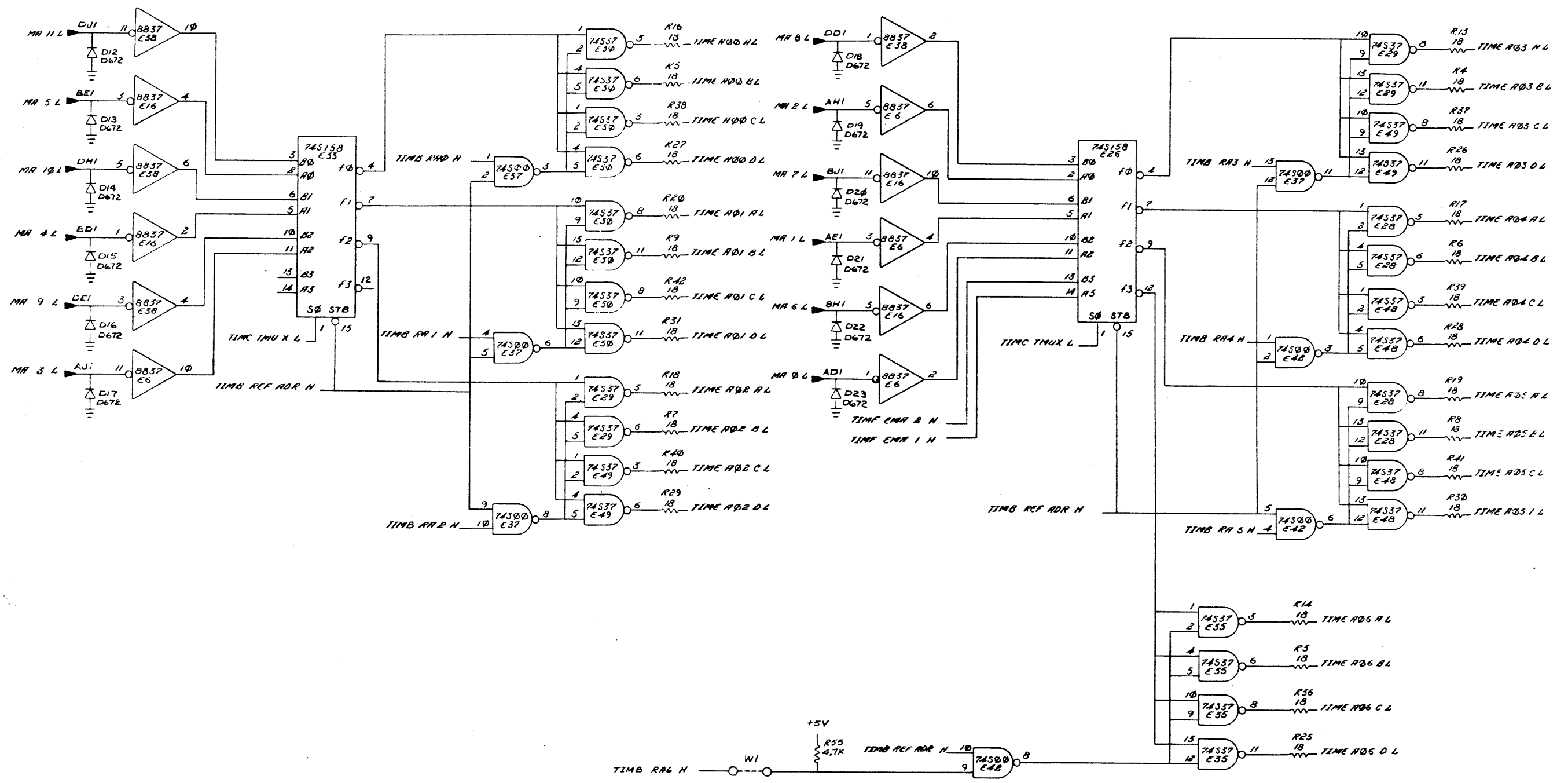
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REVISIONS			TITLE		SIZE	CODE	NUMBER	REV.
CHK	CHANGE NO.	REV.	PDP-8 MOS MEMORY (TIMD)		D	CS	M8417-0-1	E
			SCALE	NONE	SHEET	4 OF 14	DIST.	

REV. 1
DCS M8417-0-1

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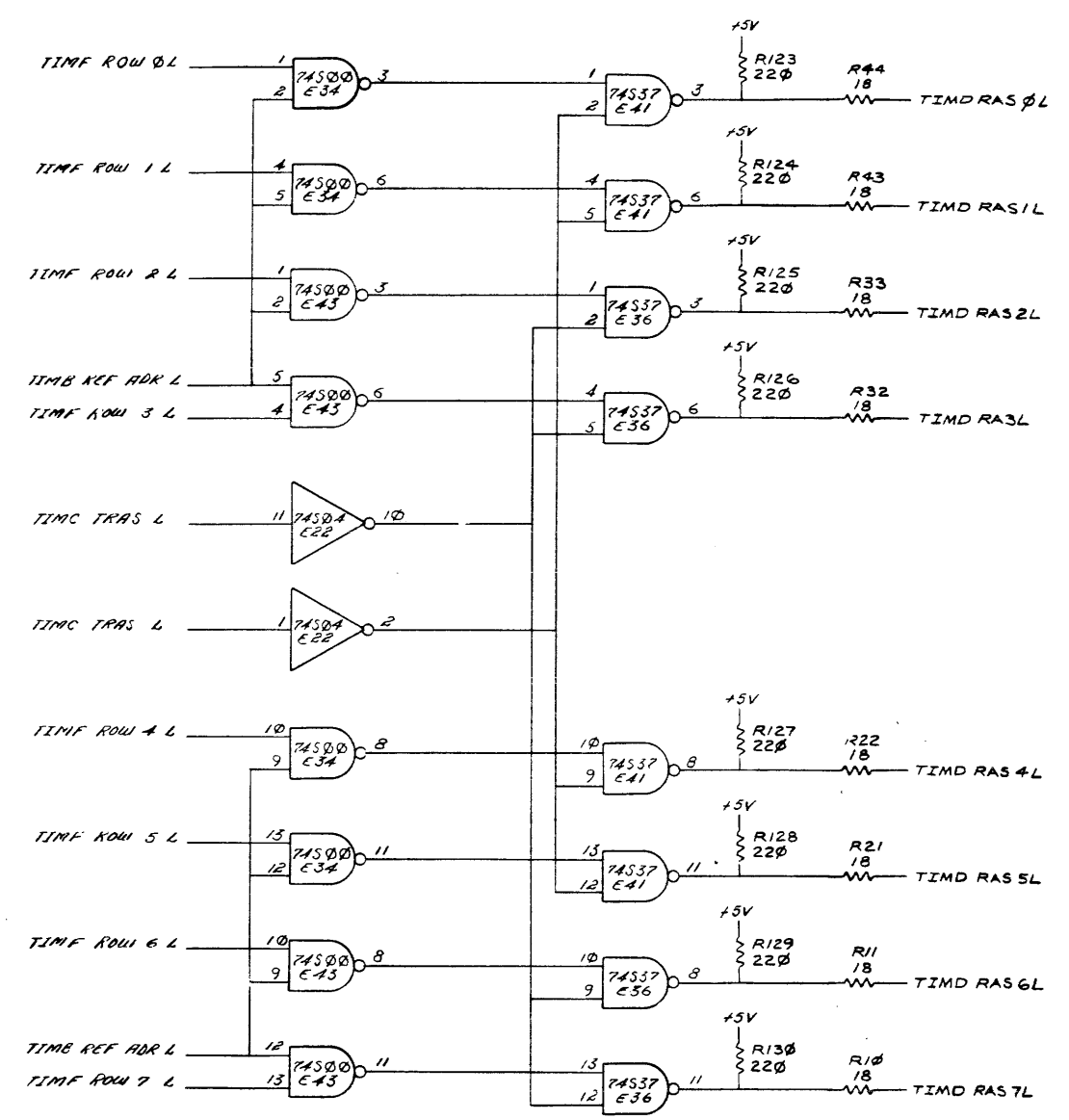


ROW, COLUMN & REF ADR MUX & ADR DRIVERS

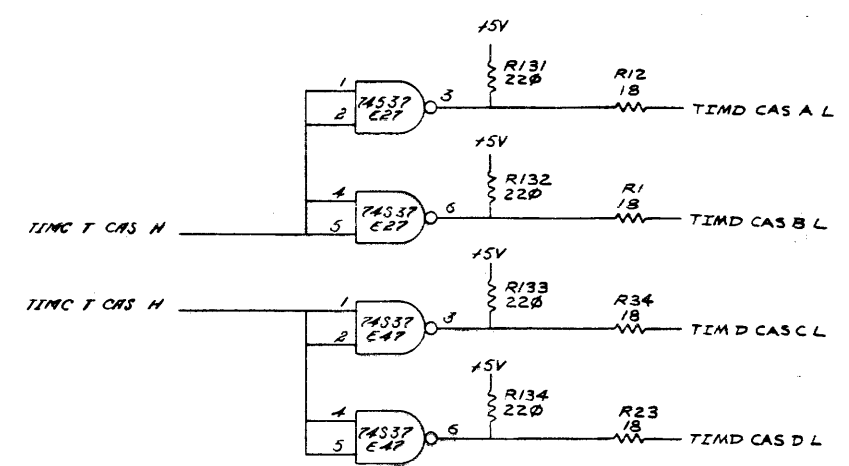
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	(TIME)	SIZE CODE	NUMBER	REV.
PDP8 MOS MEMORY	D CS	M8417-0-1	F	
SCALE	NONE	SHEET	5 OF 14	DIST.

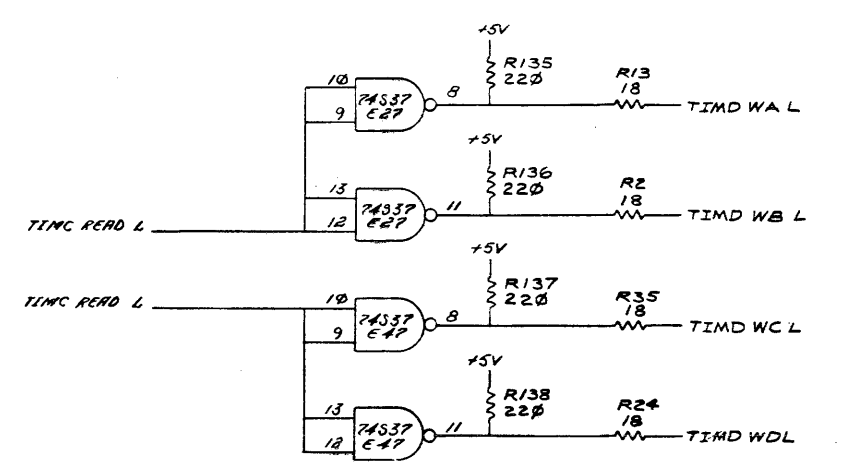
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RAS DRIVERS & SELECT



CAS DRIVERS

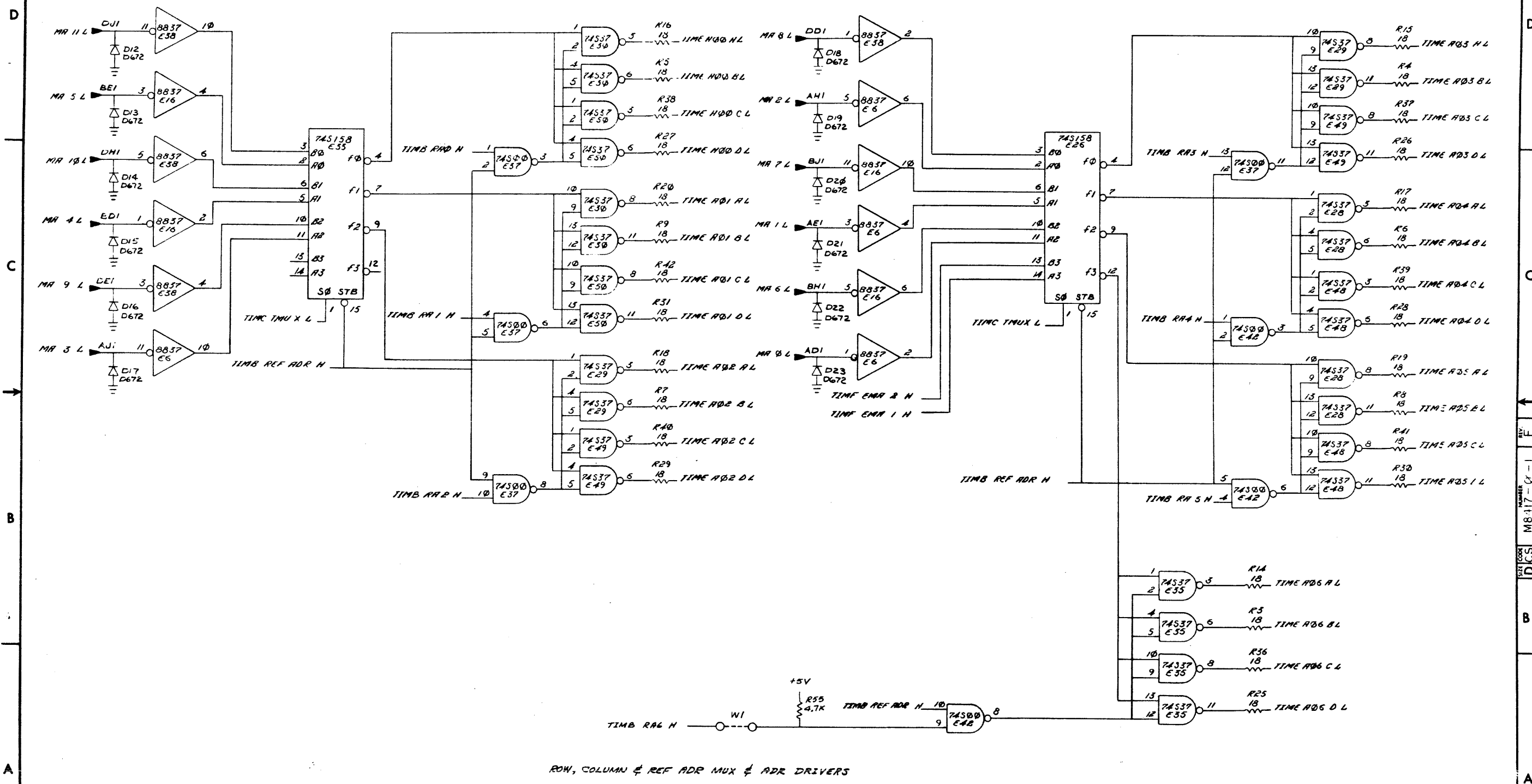


WRITE ENABLE DRIVERS

REVISIONS		
CHK	CHANGE NO.	REV.

8	7	6	5	4	3	2	1		
TITLE PDP-8 MOS MEMORY (TIMD)							SIZE CODE DCS	NUMBER M8417-0-1	REV. E
SCALE NONE		SHEET 4 OF 14		DIST.					

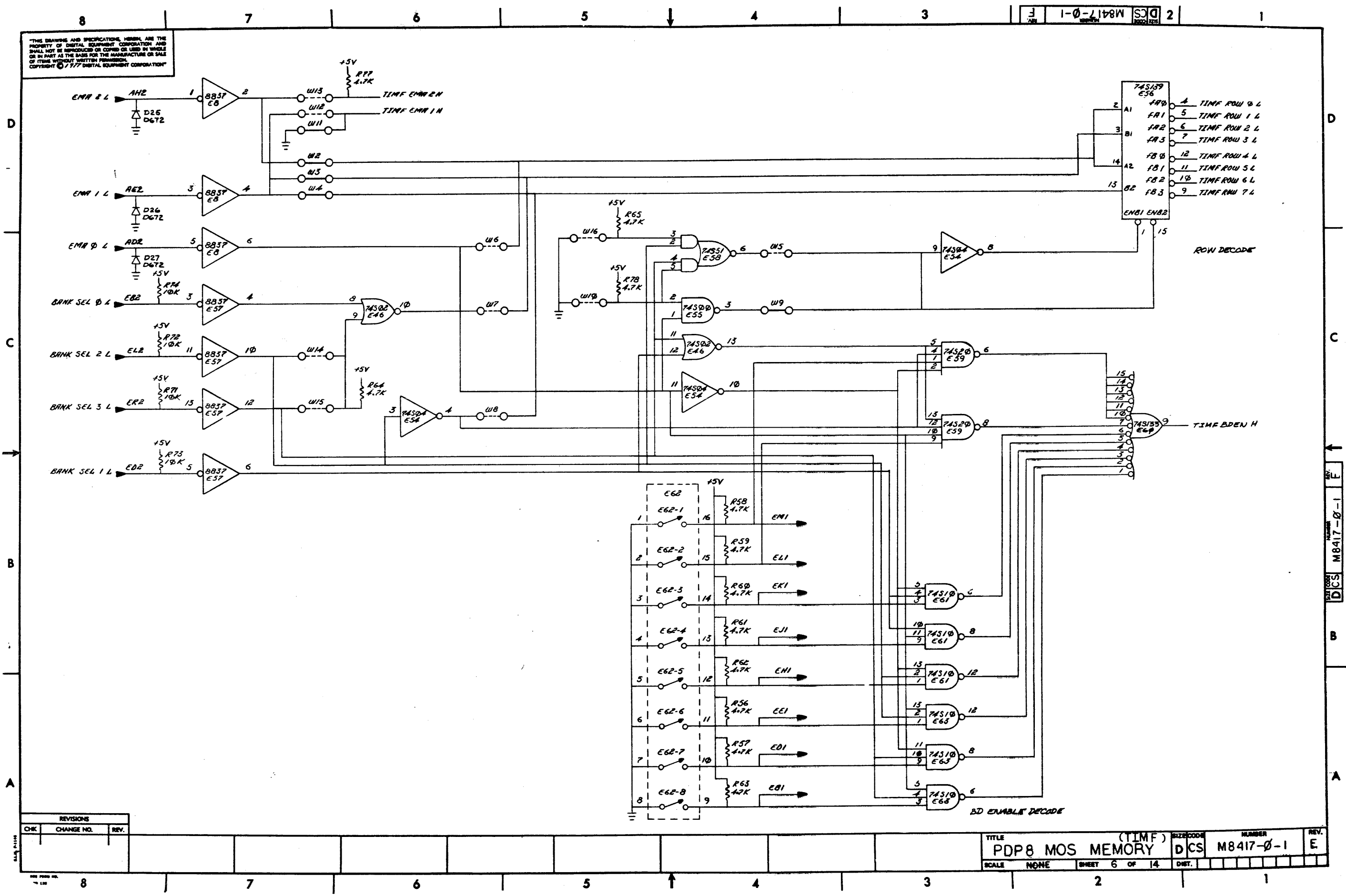
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ROW, COLUMN & REF ADDR MUX & ADDR DRIVERS

REVISIONS		
CHK	CHANGE NO.	REV.

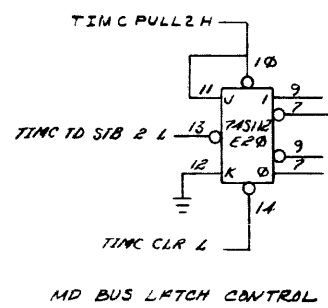
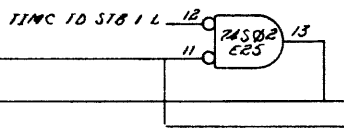
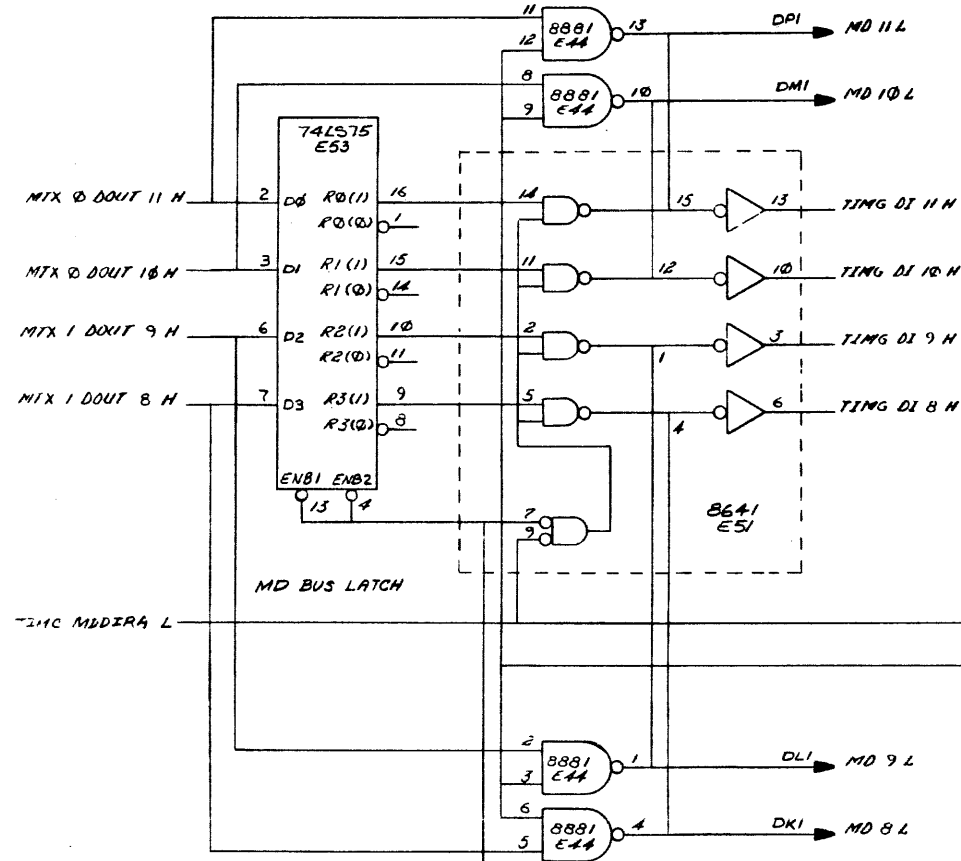
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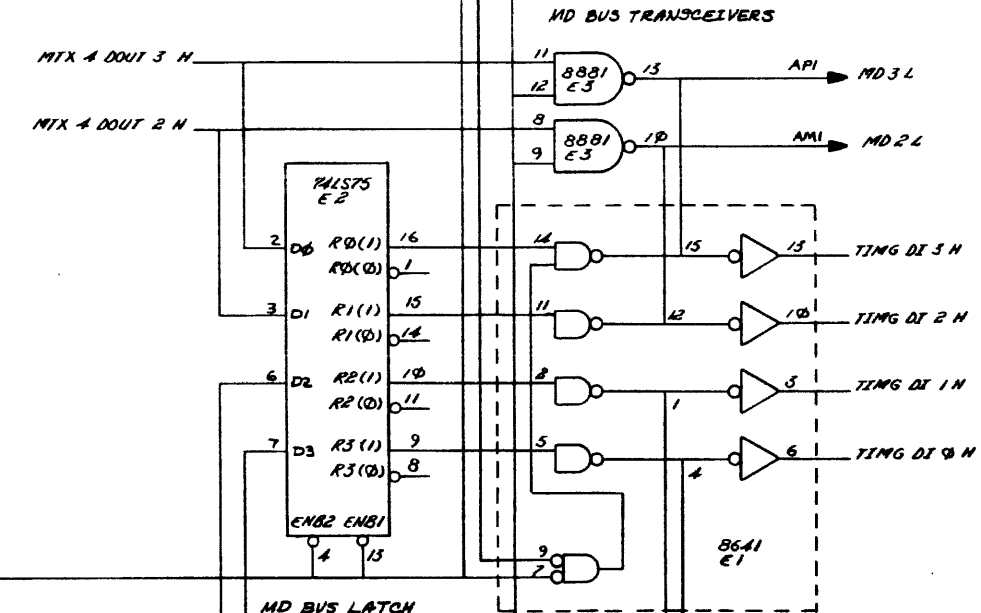
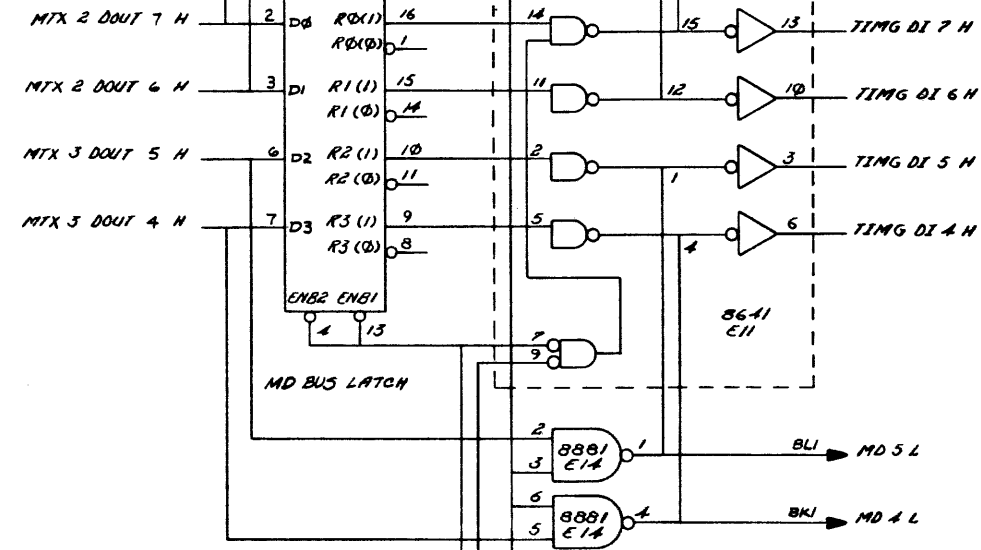
REVISIONS		
CHK	CHANGE NO.	REV.

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MD BUS TRANSCEIVERS



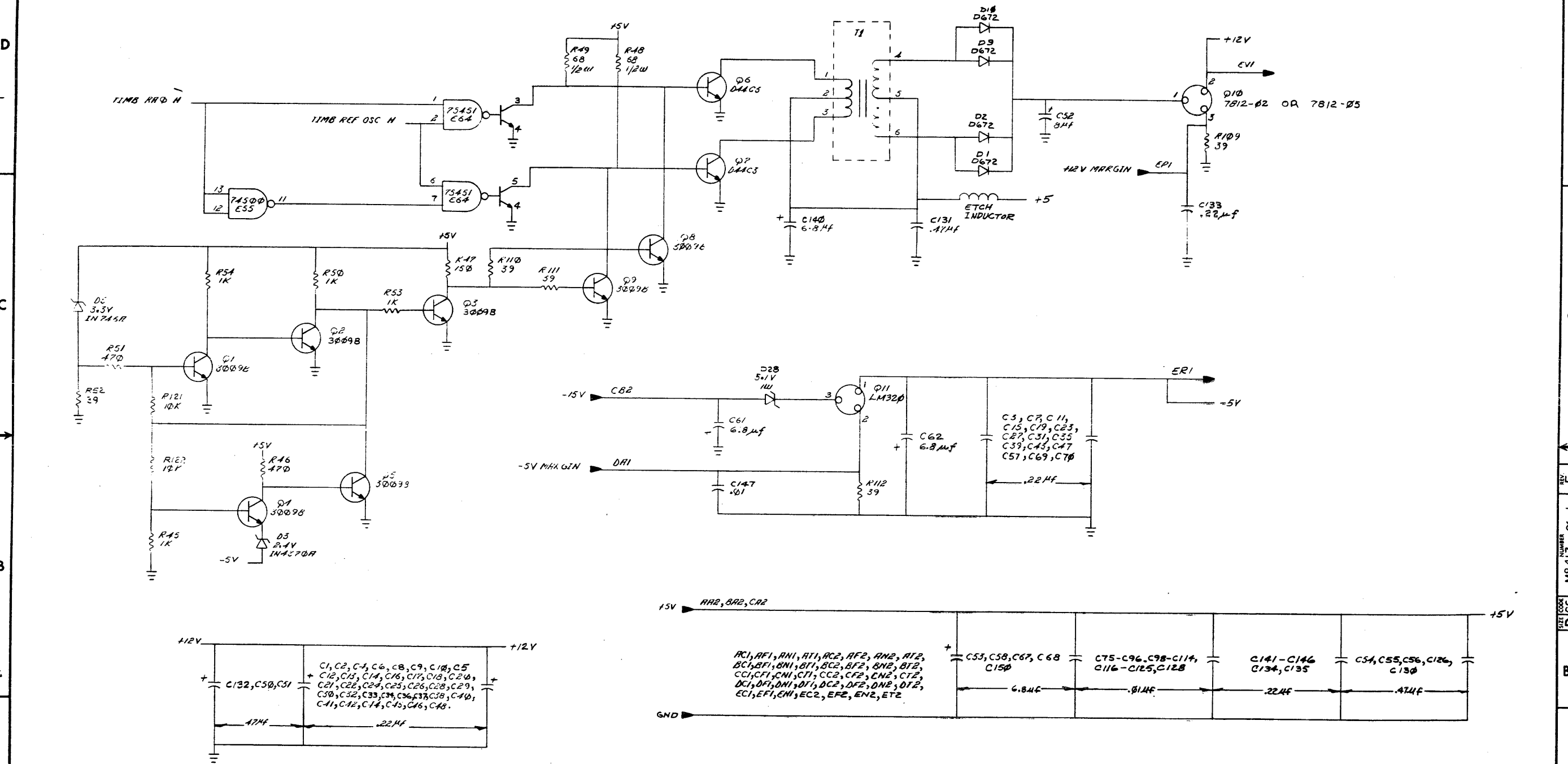
MD BUS LATCH CONTROL



REVISIONS			TITLE		SIZE CODE	NUMBER	REV.
CHK	CHANGE NO.	REV.	PDP8 MOS MEMORY (TIMG)		DCS	M8417-0-1	F.
			SCALE	NONE	SHEET	7 OF 14	DIST.

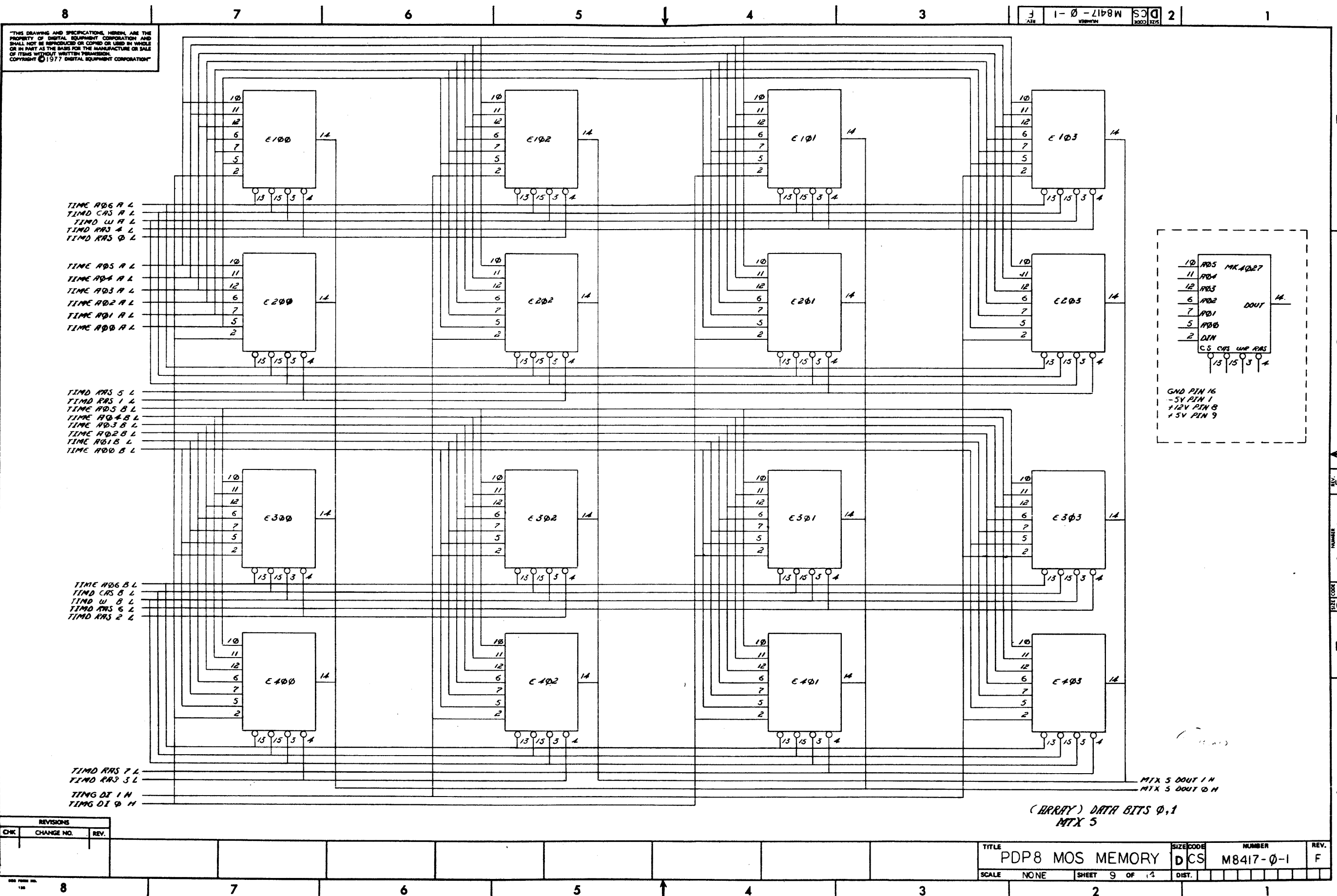
REVISIONS		
CHK	CHANGE NO.	REV.

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REVISIONS		
CHK	CHANGE NO.	REV.

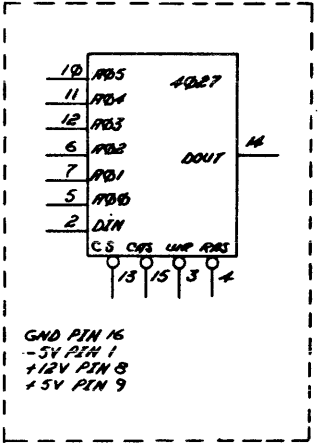
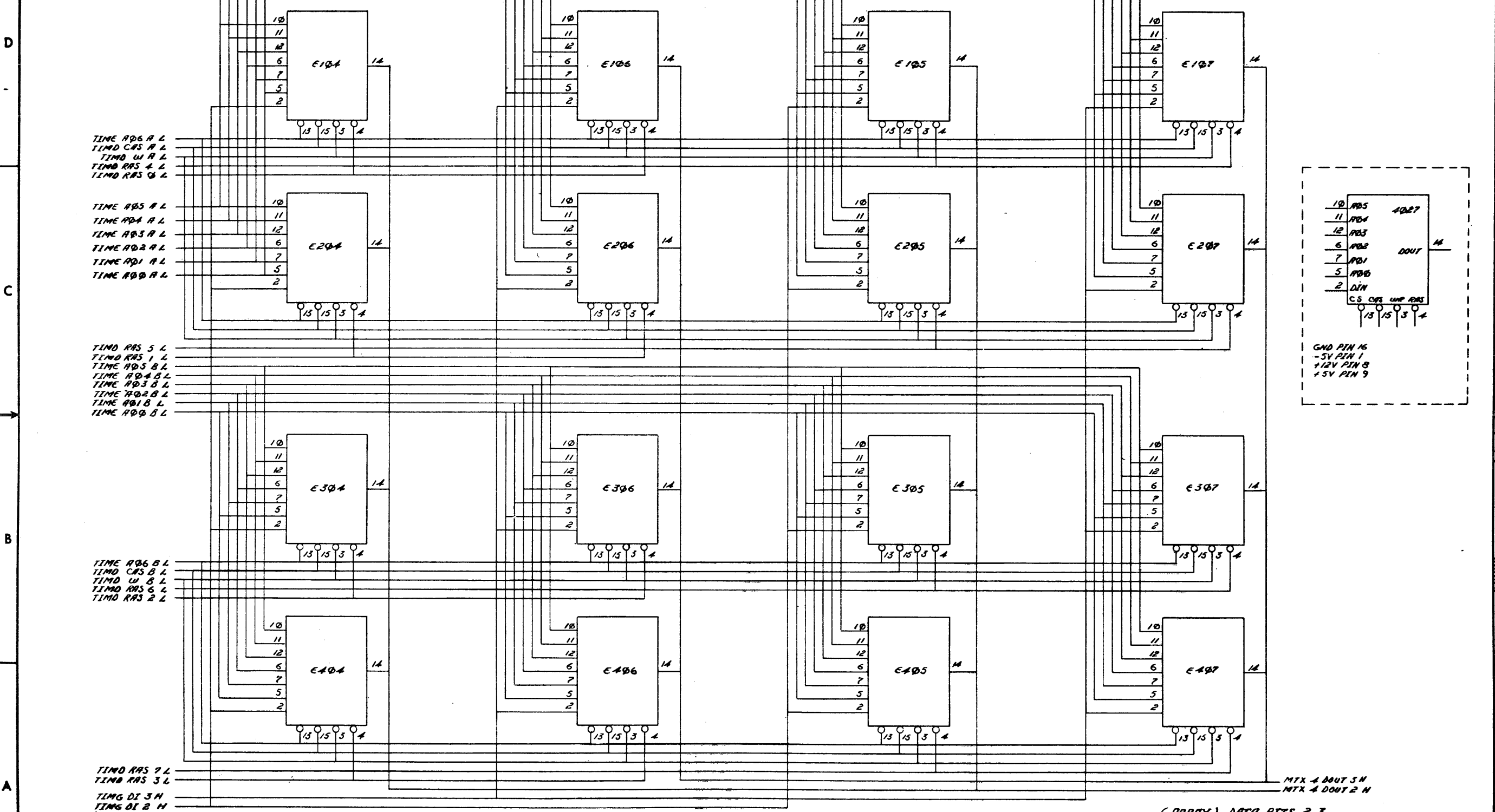
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REVISIONS		
CHK	CHANGE NO.	REV.

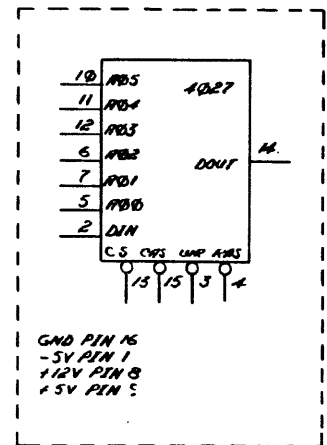
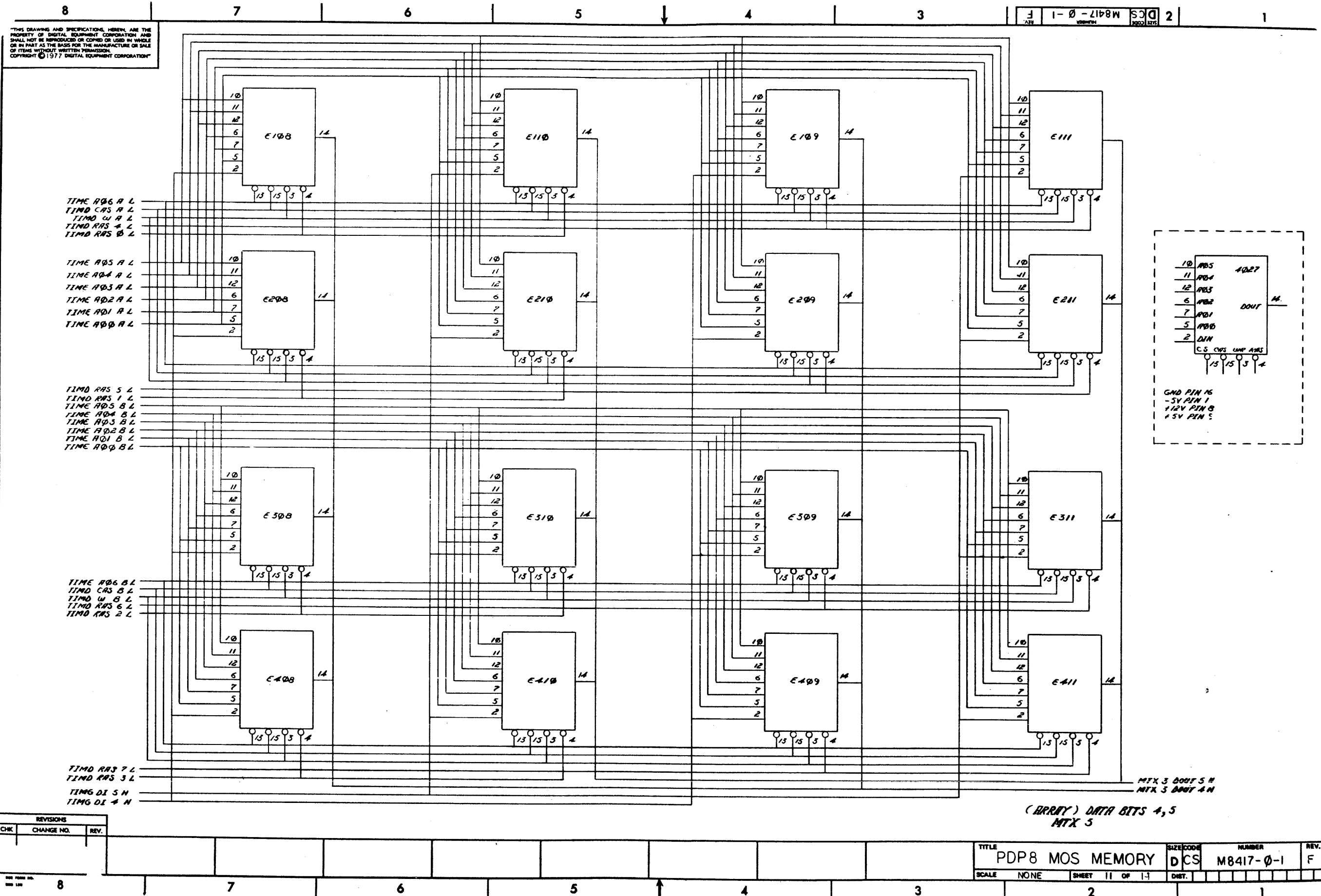
TITLE	PDP8 MOS MEMORY	SIZE CODE	DCS	NUMBER	M8417-0-1	REV.	F
SCALE	NONE	SHEET	9 OF 14	DIST.			

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REVISIONS		
CHK	CHANGE NO.	REV.

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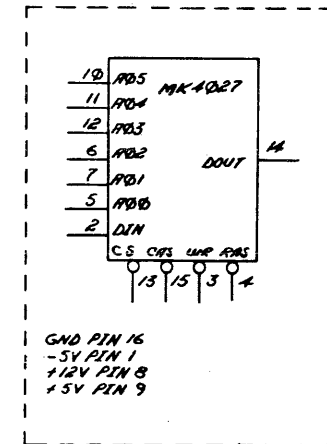
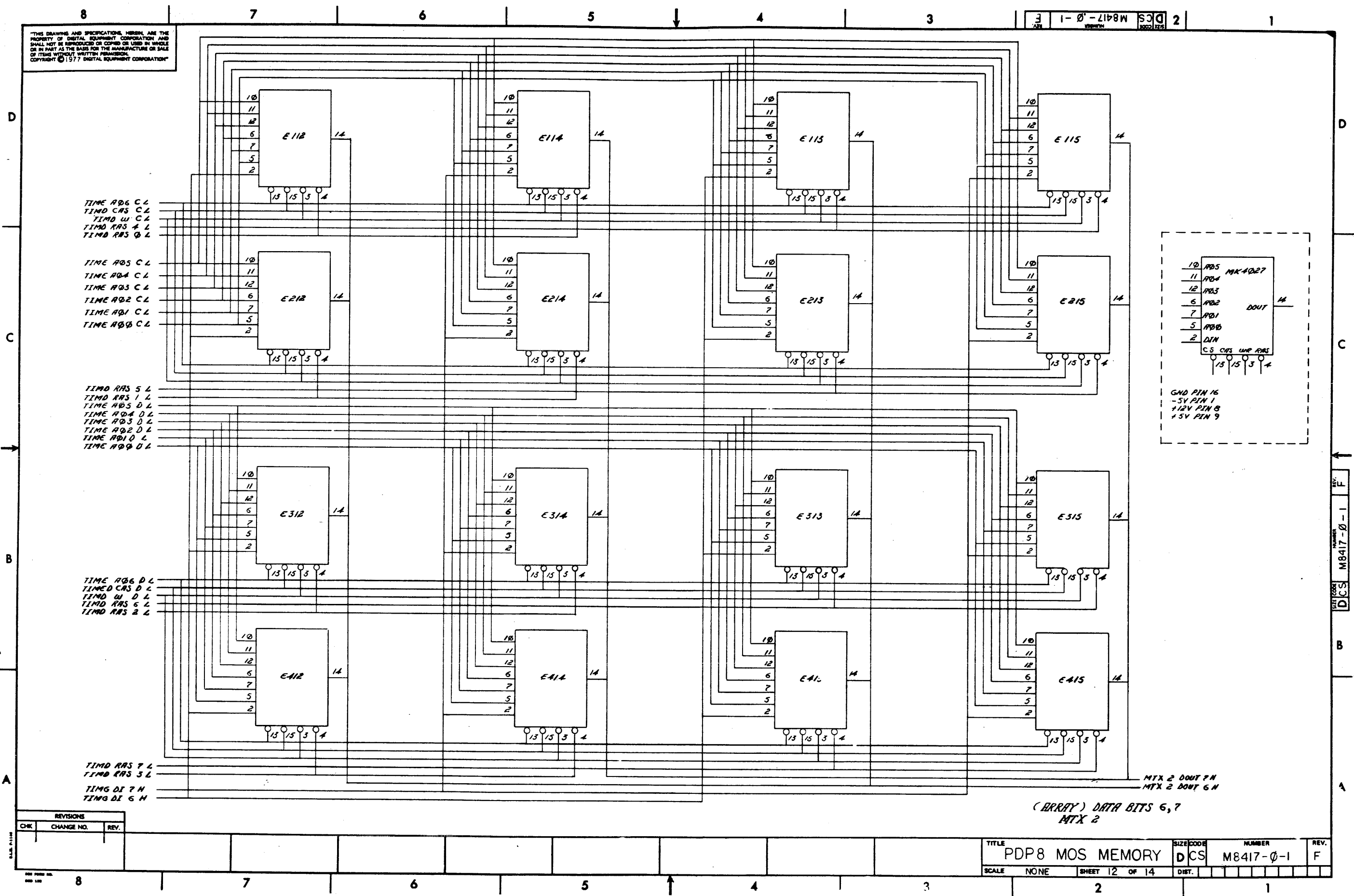


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MOS MEMORY	SIZE CODE	DCS	NUMBER	M8417-0-1	REV.	F
SCALE	NONE	SHEET	11 OF 13	DIST.			

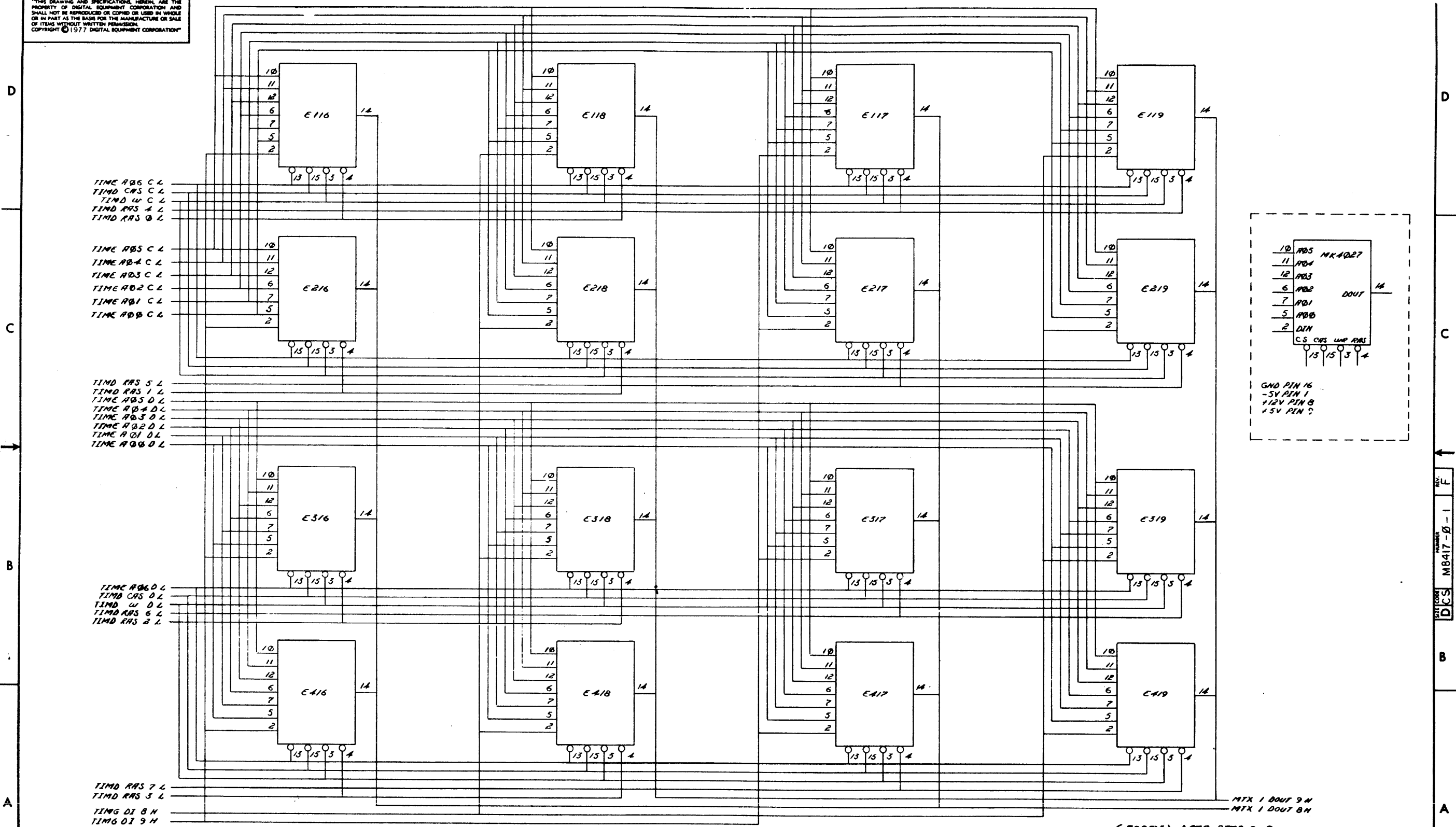
DRAWING NO. M8417-0-1
 SHEET 11 OF 13
 DATE 1977

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REVISIONS		
CHK	CHANGE NO.	REV.

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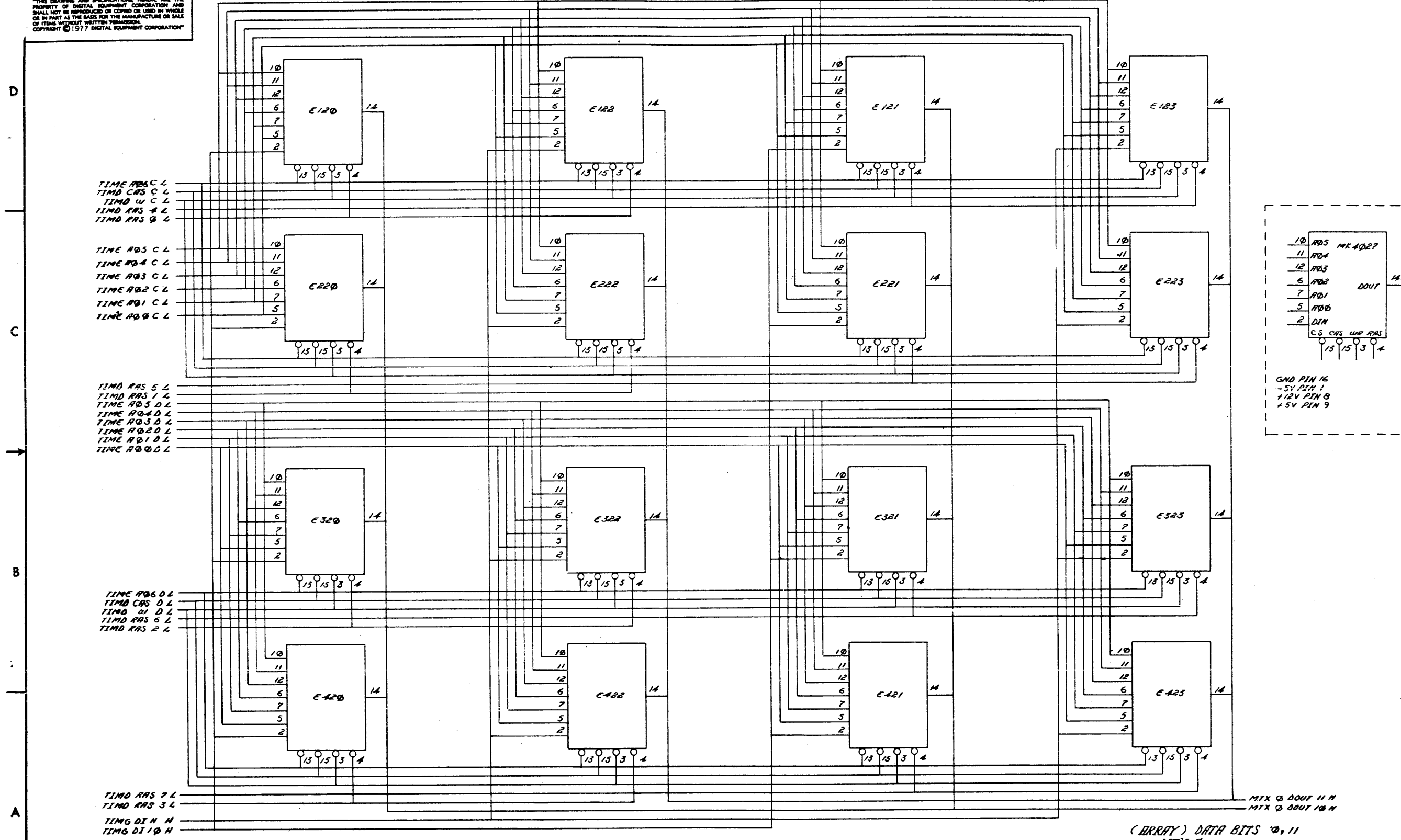


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MOS MEMORY	SIZE CODE	DCS	NUMBER	M8417-0-1	REV.	F
SCALE	NONE	SHEET	13 OF 14	DIST.			

REV F
M8417-0-1

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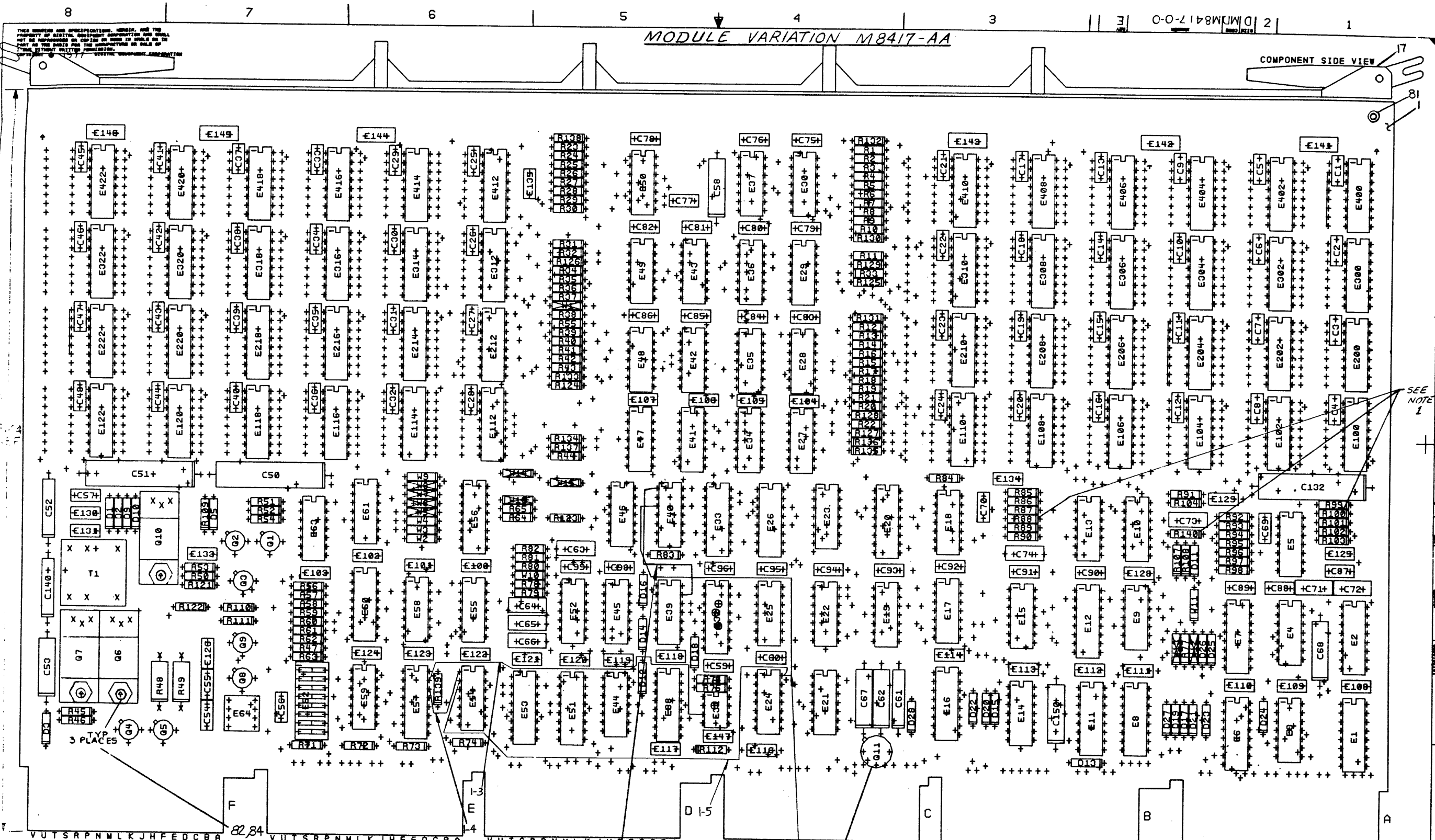


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP8 MOS MEMORY	SIZE CODE	DCS	NUMBER	M8417-0-1	REV.	F
SCALE	NONE	SHEET	14 OF 14	DIST.			

MODULE VARIATION M8417-AA

COMPONENT SIDE VIEW



NOTES: 1. R100, R140 & R88 WILL NOT BE INSTALLED AT ASSEMBLY, BUT AT MEMORY TEST, IF NEEDED.
 2. M8417-AA = M8417-AB, AC, ETC. (6KX12 BIT MEMORY).

CHK	CHANGE NO	REV	DATE	BY
01	M8417-0001	B	5-19-77	J. STEGEMAN
02	M8417-0002	C	5-19-77	J. STEGEMAN
03	M8417-0003	D	5-19-77	J. STEGEMAN
04	M8417-0004	E	5-19-77	J. STEGEMAN
05	M8417-0005	F	5-19-77	J. STEGEMAN

FOR PARTS LIST, SEE B-PL-M8417-0-0

ETCH REV. B
P.C. DESIGN DATA BASE REV. B1

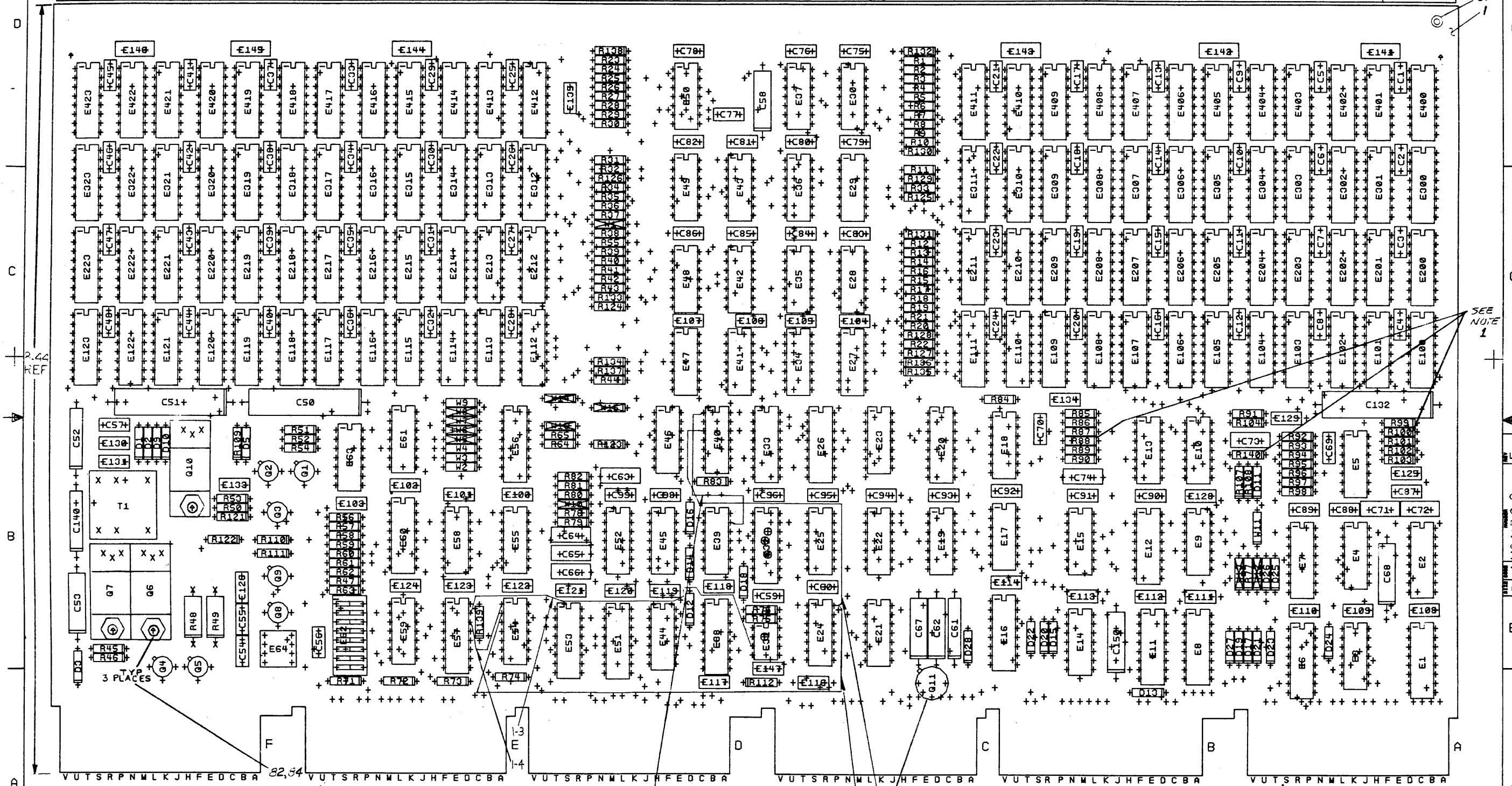
SIGNATURES	DATE	TITLE
DRN. P. Sullivan	5-19-77	PDP 8 MOS MEMORY
CHK. D. Andrew Jackson	5-19-77	
ENG. J. Stegeman	2-2-77	
PROD. ENG. P. Sullivan	2-2-77	
SCALE 2:1	7-8-77	
SHT. 1 OF 3		SIZE CODE NUMBER REV
NEXT HIGHER ASSY. B-DD-M8417-0		D UA M8417-0-0 E

digital

MODULE VARIATION M8417-BA

COMPONENT SIDE VIEW

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SEE NOTE 1

NOTES: 1. R100, R140 & R88 WILL NOT BE INSTALLED AT ASSEMBLY, BUT FIT MEMORY TEST, IF NEEDED.
2. M8417-BA = M8417-55, BC, ETZ = (32K X 12 BIT MEMORY).

CHK	CHANGE NO	REV

FOR PARTS LIST
SEE B-PL-M8417-0-0

ETCH REV.	B
P.C. DESIGN DATA BASE REV.	B1

SIGNATURES		DATE
DRN. <i>B. Sullivan</i>		5-19-72
CHK'D. <i>John J. ...</i>		5-19-72
ENG. <i>...</i>		7-2-72
PROJ. ENG. <i>...</i>		7-2-72
PROD. <i>...</i>		7-2-72
SCALE	2:1	
SHT.	2 OF 8	
NEXT HIGHER ASSY.	D-LD-M8417-0	
TITLE	PDP 8 MOS MEMORY	
SIZE	CODE	NUMBER
0	UA	M8417-0-0
REV	E	

digital

PDP 8 MOS MEMORY

0 UA M8417-0-0 E

8

7

6

5

4

3

3 0-1-1-1 0 2

1

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L1

M8417

LAYER 1
5012701B

CSXABCDEFGHIJKLMPRS

SIDE 1

digital

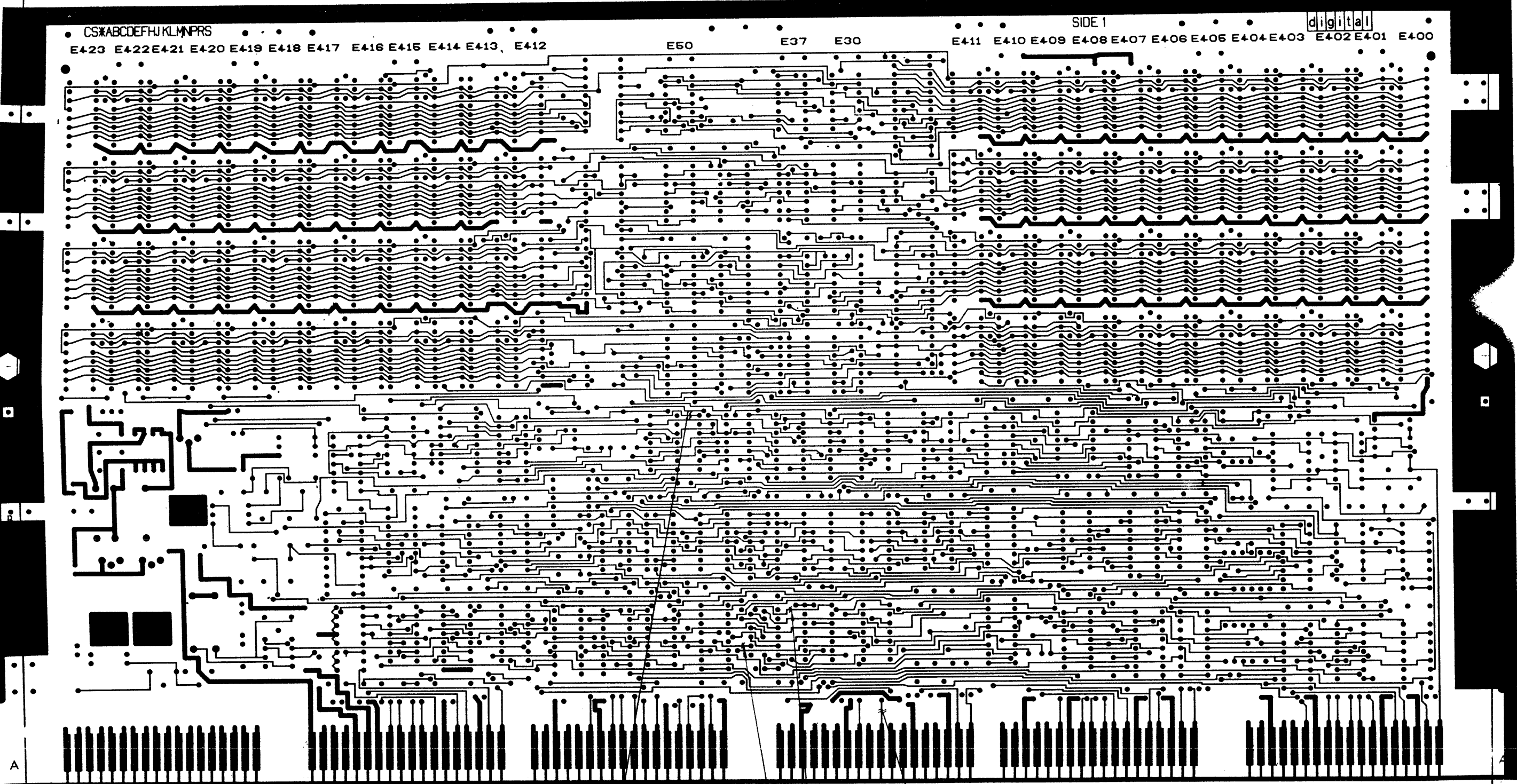
E4-23 E4-22 E4-21 E4-20 E4-19 E4-18 E4-17 E4-16 E4-15 E4-14 E4-13 E4-12

E50

E37

E30

E4-11 E4-10 E4-09 E4-08 E4-07 E4-06 E4-05 E4-04 E4-03 E4-02 E4-01 E4-00



3-1

1-1

1-2

2-1

REVISIONS		
CHK	CHANGE NO	REV

TITLE	PDP8 1105 MEMORY	SIZE CODE	D UA	NUMBER	M8417-0-0	REV.	E
SCALE	2/1	SHEET	3 OF 3	DIST.			

2 ml

8

7

6

5

4

3

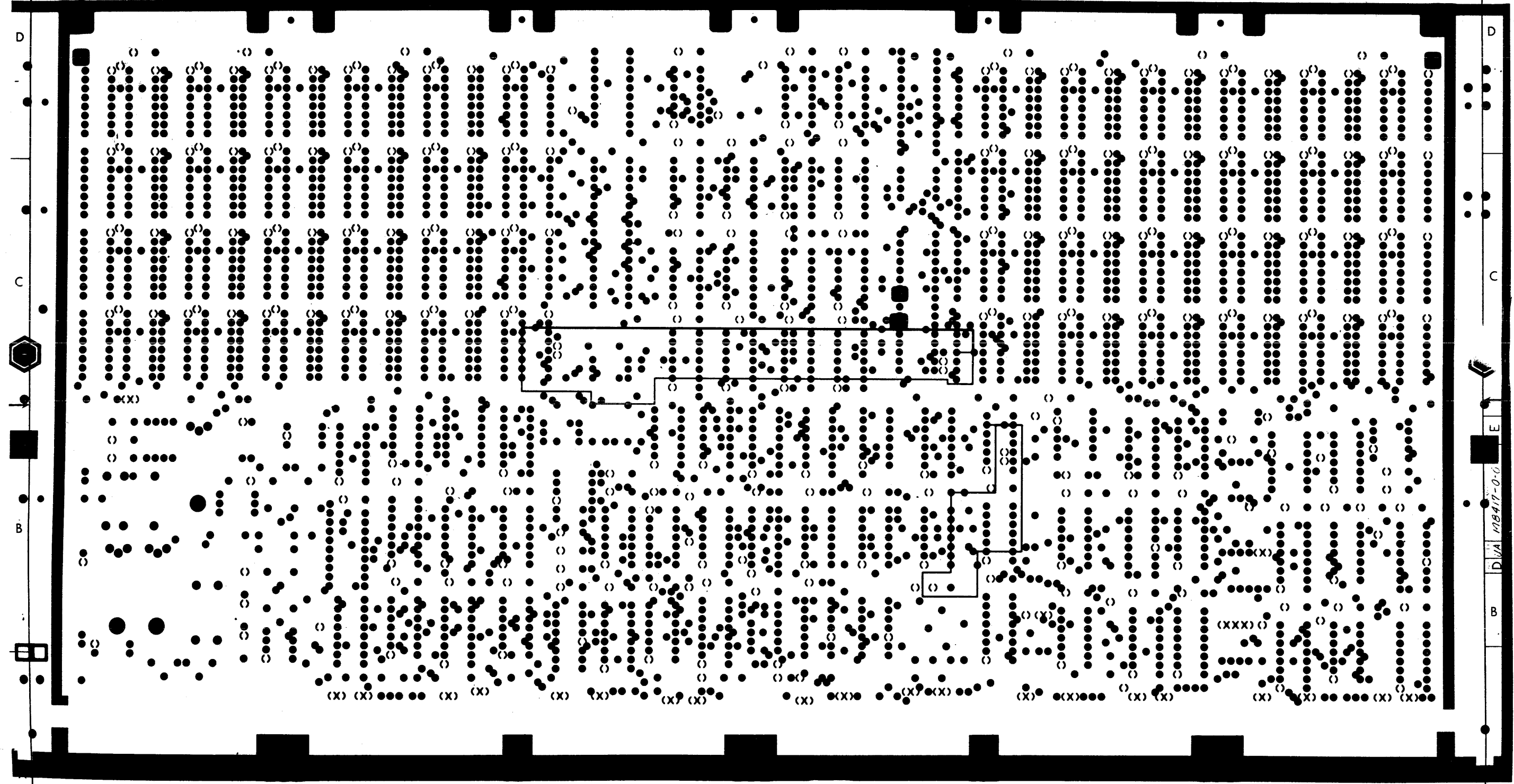
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LAYER 12

M8417 6012701B

L2 M8417B



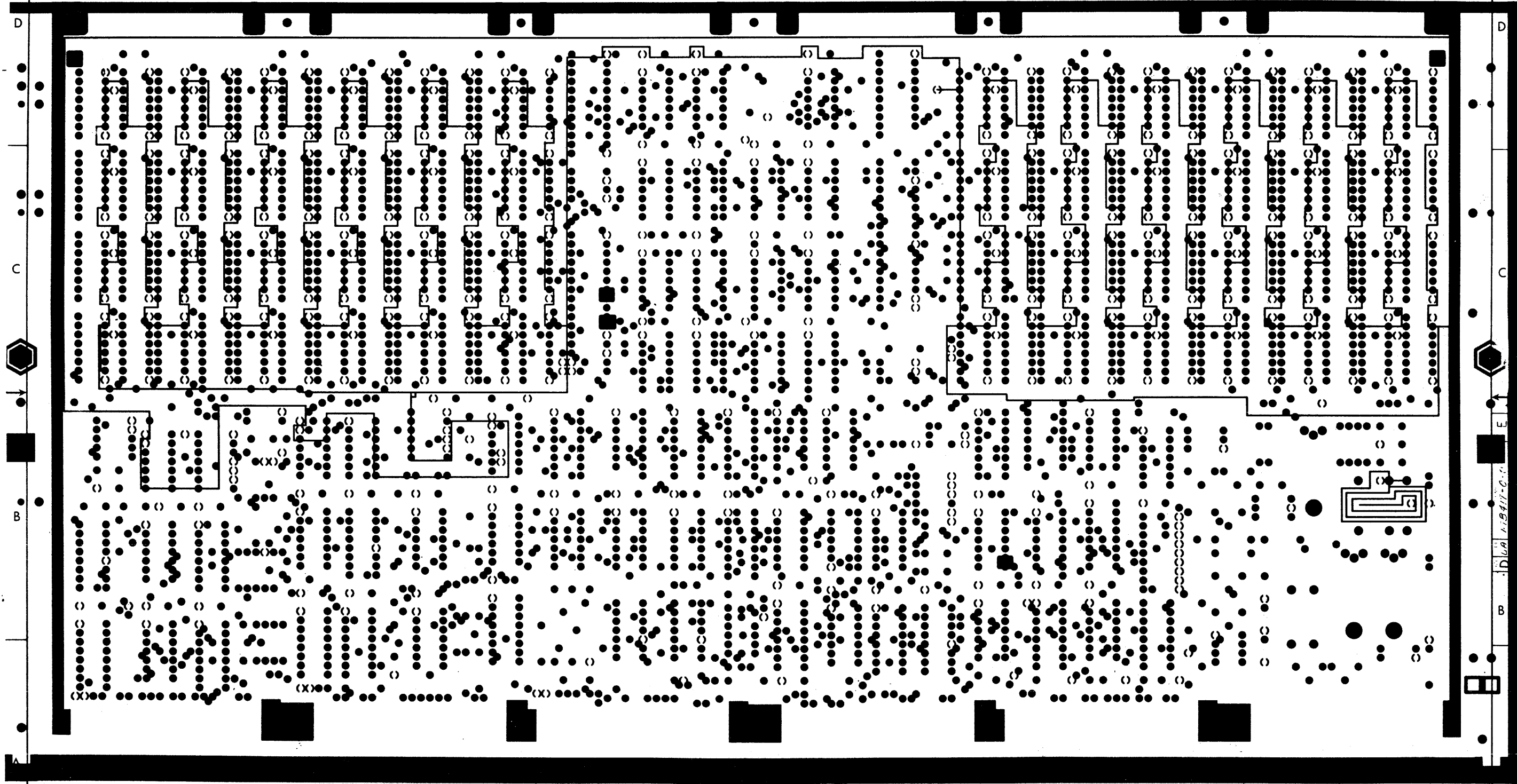
REVISIONS		
CHK	CHANGE NO	REV

TITLE	PDP8 MOS MEMORY	SIZE CODE	DUA	NUMBER	M8417-0-0	REV.	E
SCALE	2/1	SHEET	4 OF 8	DIST.			

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E 834AJ

M8417B L3



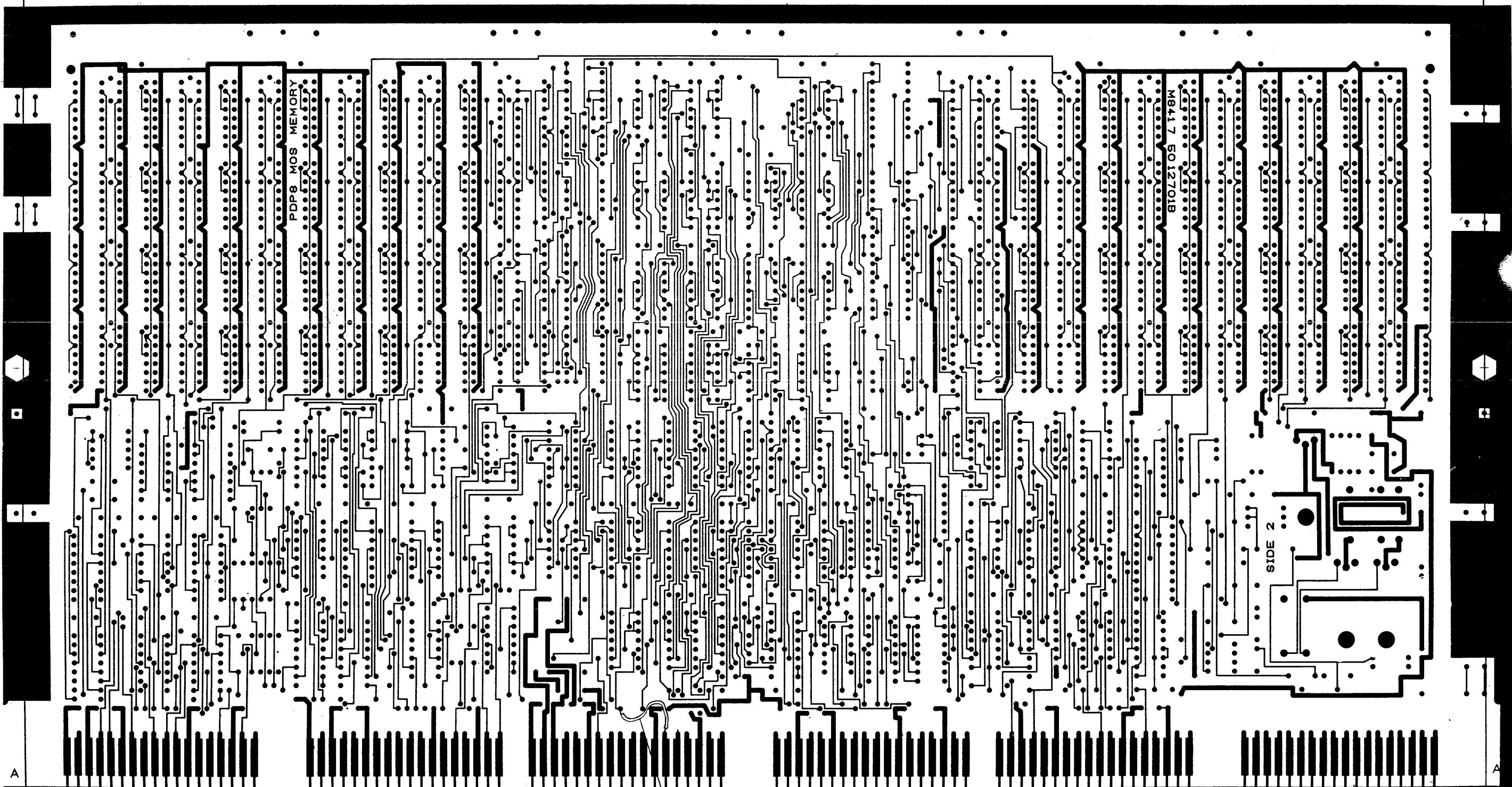
REVISIONS		
CHK	CHANGE NO	REV

TITLE	PDP8 MOS MEMORY	SIZE CODE	D UA	NUMBER	M8417-0-0	REV.	E
SCALE	E/1	SHEET	5 OF 8	DIST.			

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4 834AJ

0-0-1193W 47 D 2



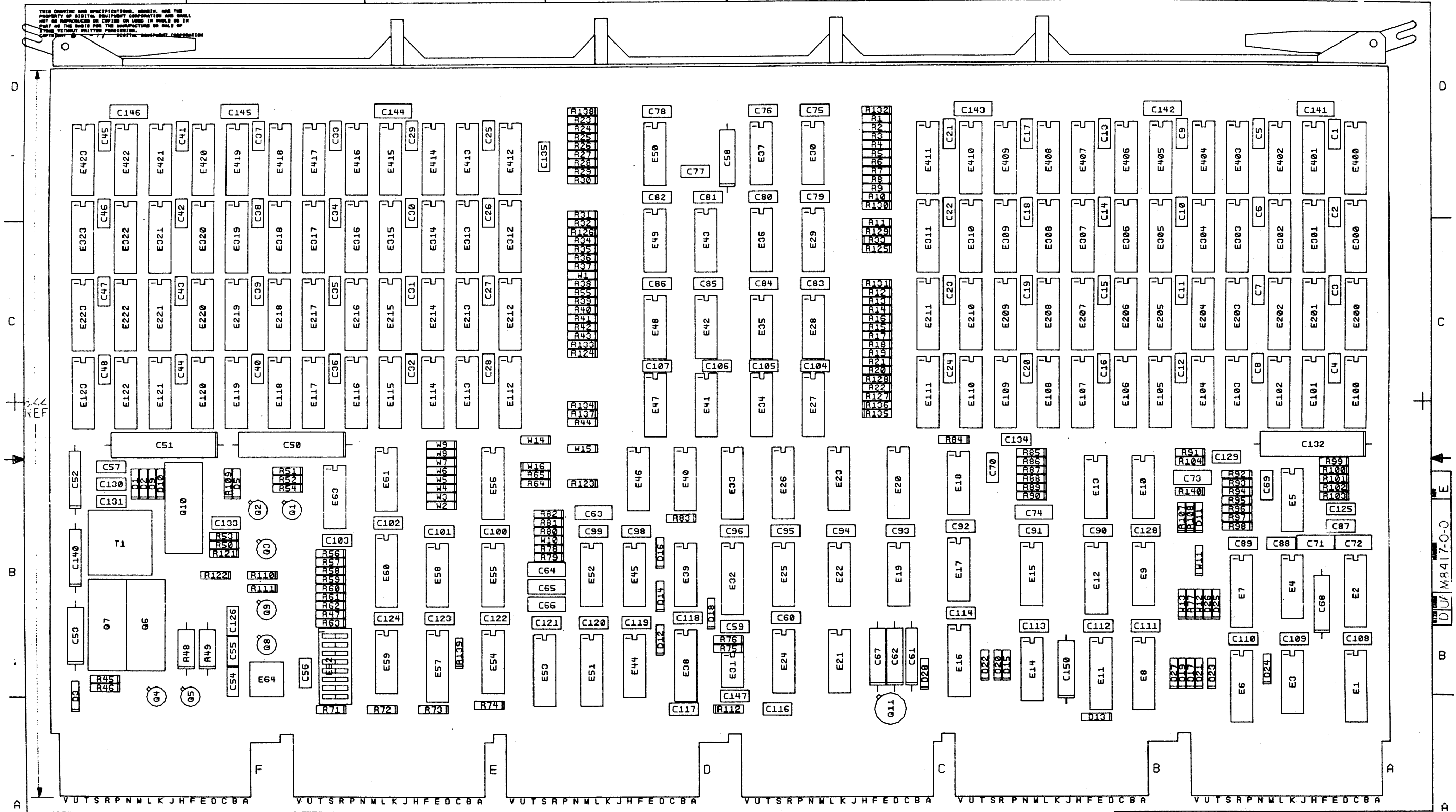
REVISIONS		
CHK	CHANGE NO	REV

TITLE	PDP-8 MOS MEMORY		SIZE CODE	D UA	NUMBER	MB417-0-0	REV.	E
SCALE	2/1	SHEET	6	OF	8	DIST.		

8 7 6 5 4 3 2 1

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2 1
 0417-0-0



NOTES:

CHK	CHANGE NO	REV

DWG-REF ONLY

SIGNATURES		DATE	digital
DRN. <i>J. Sullivan</i>		5-18-77	
CHK'D. <i>A. B. Garcia</i>		5-18-77	
ENG. <i>[Signature]</i>		7/8/77	
PROJ. ENG. <i>[Signature]</i>		7/8/77	
PROD. <i>[Signature]</i>		7-8-77	
SCALE 2:1			TITLE PDP 8 MOS MEMORY
SHT. 7 OF 8			SIZE CODE NUMBER REV
NEXT HIGHER ASSY. B-1017-0-0			0 UA M8417-0-0 E

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REWORK INSTRUCTIONS

ECO #1

- ETCH CUTS SIDE 1:*
 1-1 CUT ETCH TO FREE E31-3
 1-2 CUT ETCH TO FREE E31-3
WIRE ADDS SIDE 1:
 1-3 WIRE E31-3 TO E57-15
 1-4 WIRE E57-14 TO E54-1
 1-5 WIRE E54-2 TO E24-14
 1-6 WIRE E24-14 TO E40-3

ECO #2

- ETCH CUTS SIDE 1:*
 2-1 CUT ETCH BETWEEN PIN CK1 & FEED THRU
WIRE ADDS SIDE 2:
 2-2 WIRE PIN CP2 TO FEEDTHRU ABOVE
 CK2 PER PROCEDURE IN MODULE REWORK
 SPECIFICATION A-SP-7665265-0-0

ECO #3

- ETCH CUTS SIDE 1:*
 3-1 CUT ETCH TO FREE E40-1
WIRE ADDS SIDE 1:
 3-2 FROM E40-1 TO E39-13.

REVISIONS		
CHK	CHANGE NO.	REV.

REV. E
M8417-0-0
DUA

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
					AB	AD	AC	AE	
1	1	D-UA-M8417-0-0		UNIT ASSY	REF	REF	REF	REF	
2	2	D-UA-M8417-0-0		UNIT ASSY	REF	-	-	-	
3	3		5012701-00	ETCH BOARD (M8418)	1	1	1	1	
4	4		1000006-00	10.0 MMF 100V 5%200PPM MICA	1	1	1	1	C66
5	5		1000016-00	100.0 MMF 100V 5%200PPM MICA	1	1	1	1	C64
6	6		1000042-00	1000.0 MMF 100V 5%200PPM MICA	1	1	1	1	C65
7	7		1001610-01	.01 MFD50/100V +80-20% DISC	53	53	53	53	C60, C75-C96, C98-C114, C116-C125, C128, C129, C147
8	8		1005306-0C	6.8MFD 35V 10% S.TANT	8	8	8	8	C53, C58, C67, C61, C62, C63, C140, C150
9	9		1010274-00	.22 MFD 50V +80-20% Z5U CER	60	60	60	60	C1-C48, C57, C69, C70, C133-C135, C141-C146
10	10		1010279-00	.47 MFD 25V 20% CER	6	6	6	6	C54-C56, C126, C130, C131
11	11		1012084-01	8 MFD 25V +75-10% AL EL	1	1	1	1	C52
12	12		1012121-00	220.0 MMF 100V 1%200PPM MICA	6	6	6	6	C59, C63, C71-C74
13	13		1012219-00	47 MFD 30V +75-10% AL EL	3	3	3	3	C50, C51, C132
14	14		1101938-00	1N 4370A VZ= 2.4 5% .40W	1	1	1	1	D3
15	15		1104860-00	1N 746A VZ= 3.3 5%	1	1	1	1	D5
16	16		1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21	21	21	D1, D2, D9-D27
17	17		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	1	1	1	D28
18	18		1211164-04	SW DIP 1P 1A 8POS	1	1	1	1	E62
19	19		1210711-02	/REPLACED BY 12-16988-02	1	1	1	1	
20	20		1300250-00	150.0 .25 W 5.0 % CC	1	1	1	1	R47
21	21		1300271-00	220.0 .25 W 5.0 % CC	16	16	16	16	R123-R138
22	22		1300316-00	470.0 .25 W 5.0 % CC	2	2	2	2	R46, R51
23	23		1300365-00	1.0 K .25 W 5.0 % CC	9	9	9	9	R45, R50, R53, R54, R82-R84, R103, R104
24	24		1300447-00	4.70 K .25 W 5.0 % CC	15	15	15	15	R55-R65, R77, R78, R81, R139
25	25		1300479-00	10.0 K .25 W 5.0 % CC	6	6	6	6	R71-R74, R121, R122
26	26		1302124-00	18.0 .25 W 5.0 % CC	46	46	46	46	R1-R44, R90, R98

REVISION HISTORY			BASIC PART NO: M8417		DRN: L. METZGER		DATE: 30-MAY-78		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF C		CHK'D: P. BOSSMAN		DATE: 30-MAY-78		TITLE PARTS LIST			
E.R.	CJ003	D	SECTION VARIATION INDEX		DES. ENG: J. STEGEMAN <td colspan="2">DATE: 30-MAY-78 <td colspan="4">DOCUMENT NUMBER</td> </td>		DATE: 30-MAY-78 <td colspan="4">DOCUMENT NUMBER</td>		DOCUMENT NUMBER			
J.S.	M8417-ML004	E	[A]	AB, AD, AC, AE	RESP. ENG.: J. STEGEMAN <td colspan="2">DATE: 30-MAY-78 <td colspan="4">SIZE CODE NUMBER REV</td> </td>		DATE: 30-MAY-78 <td colspan="4">SIZE CODE NUMBER REV</td>		SIZE CODE NUMBER REV			
JS	M8417-ML005	F	[B]	BB, BC, BD, BE	MFG. ENG.: C. TANNEK <td colspan="2">DATE: 30-MAY-78 <td>K</td> <td>PL</td> <td>M8417-0-DBP</td> <td>H</td> </td>		DATE: 30-MAY-78 <td>K</td> <td>PL</td> <td>M8417-0-DBP</td> <td>H</td>		K	PL	M8417-0-DBP	H
JS	M8417-ML006	H	[C]	AF, BF	ASSEMBLY NUMBER: D-UA-M8417-0-0 <td colspan="2">TOP DOCUMENT NUMBER: MS8-C <td colspan="2">FILE NAME: Z0189H.PLS <td colspan="2">EDIT # 6</td> </td></td>		TOP DOCUMENT NUMBER: MS8-C <td colspan="2">FILE NAME: Z0189H.PLS <td colspan="2">EDIT # 6</td> </td>		FILE NAME: Z0189H.PLS <td colspan="2">EDIT # 6</td>		EDIT # 6	
			[D]									
			[E]									
			[F]									
			[H]									
			[J]									
			[K]									
			[L]									
			[M]									
			[N]									

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR						
					AB	AD	AC	AE							
27	27		1302377-00	39.0	.25	W	5.0	%	CC	5	5	5	5	R52,R109-R112	
28	28		1302859-00	5.76	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R96
29	29		1302871-00	1.21	K	.25	W	1.0	%	RN550-F10	2	2	2	2	R80,R108
30	30		1302872-00	681.0		.25	W	1.0	%	RN550-F10	1	1	1	1	R89
31	31		1302956-00	196.0		.25	W	1.0	%	RN550-F10	1	1	1	1	R94
32	32		1302957-00	121.0		.25	W	1.0	%	RN550-F10	1	1	1	1	R101
33	33		1303045-00	3.16	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R76
34	34		1303067-00	422.0		.25	W	1.0	%	RN550-F10	1	1	1	1	R99
35	35		1303110-00	19.60		.25	W	1.0	%	RN550-F10	1	1	1	1	R93
36	36		1303226-00	68.10		.25	W	1.0	%	RN550-F10	1	1	1	1	R86
37	37		1303311-00	46.40	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R75
38	38		1303313-00	12.10	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R97
39	39		1304725-00	300.0		.25	W	1.0	%	RN550-F10	1	1	1	1	R107
40	40	SEE NOTE 90	1304833-00	1.96	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R92
41	41		1305122-00	51.10		.25	W	1.0	%	RN550-F10	1	1	1	1	R85
42	42		1305123-00	215.0		.25	W	1.0	%	RN550-F10	2	2	2	2	R91,R102
43	43		1305143-00	825.0		.25	W	1.0	%	RN550-F10	2	2	2	2	R95,R79
44	44	SEE NOTE 90	1305253-00	7.15	K	.25	W	1.0	%	RN550-F10	1	1	1	1	R87
45	45		1309405-00	68.0		.50	W	5.0	%	CC	2	2	2	2	R48,R49
46	46		1503100-00	DEC3009B	NPN 200MW SI 20 25						7	7	7	7	Q1-Q5,Q8,Q9
47	47		1510171-00	D 44C3	NPN 30WT SI 30 20 Y						2	2	2	2	Q6,Q7
48	48		1614234-00	XFMR, CONVERTER	RATIO 1:3 300UH						1	1	1	1	T1
49	49		1909054-00	7493	COUNTER, ASYNCH UP, BI						2	2	2	2	E21, E24
50	50		1909705-00	DEC 8881	NAND GATE-QUAD 2IN 0						4	4	4	4	E3, E14, E17, E44
51	51		1910268-01	DEC 75107B-01	RECEIVER, LINE, DUA						3	3	3	3	E5, E10, E18
52	52		1910406-00	75451	DRIVER, PERIPH, DUAL,						1	1	1	1	E64
53	53		1910532-00	74500	NAND GATE-QUAD 2IN						6	6	6	6	E4, E34, E37, E42, E43, E55
54	54		1910533-00	74503	NAND GATE-QUAD 2IN 0						1	1	1	1	E13
55	55		1910534-00	74504	INVERTER GATE-HEX 1I						2	2	2	2	E22, E54
56	56		1910536-00	74510	NAND GATE-TRIPLE 3IN						3	3	3	3	E19, E61, E63
57	57		1910539-00	74520	NAND GATE-DUAL 4INPU						1	1	1	1	E59
58	58		1910542-00	74564	A-0-I GATE 4-2-3-2						1	1	1	1	E15
59	59		1910544-01	74574-6	GGG-D DUAL, EDGE TRIG						1	1	1	1	E39
60	60		1910545-00	745112	FF-JK DUAL, EDGE TRIG						3	3	3	3	E7, E20, E32
61	61		1910549-00	745158	MUX 1 OF 2 (QUAD)						2	2	2	2	E26, E33
62	62		1911116-00	DEC 8837	RECEIVER, BUS, HEX, UN						5	5	5	5	E6, E8, E16, E38, E57
63	63		1911469-00	DEC 8640	RECEIVER, BUS, QUAD, U						1	1	1	1	E45
64	64		1911579-00	8641	TRANSCEIVER, BUS, QUA						3	3	3	3	E1, E11, E51
65	65		1911676-00	745139	DECODER-DUAL TWO-INP						1	1	1	1	E56
66	66		1911712-00	74551	AND-OR GATE-INVERT D						1	1	1	1	E58
67	67		1911944-00	555CN	TIMER, FUNCT. BLOCK						1	1	1	1	E31
68	68		1911983-00	745133	NAND GATE-POSITIVE 1						1	1	1	1	E60
69	69	SEE NOTE 93	1912048-06	DEC 7812	VOLT REG, FIX +12V						1	1	1	1	Q10
70	70		1912388-00	74502	NOR GATE-QUAD 2IN, PO						3	3	3	3	E9, E25, E46
71	71		1912389-00	74508	AND GATE-QUAD 2IN, PO						1	1	1	1	E23
72	72		1912541-00	79M05	VOLT REG, FIX -5V						1	1	1	1	Q11
73	73		1912649-00	LS75	LATCH 4BIT, BISTABLE						3	3	3	3	E2, E12, E53
74	74		1912746-00	DEC 74537	NAND GATE-QUAD 2IN						11	11	11	11	E27-E30, E35, E36, E41, E47-E50

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A3 OF A4

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
					AB	AD	AC	AE	
75	75		1912803-00	74LS04 INVERTER GATE HEX	1	1	1	1	E52
76	76		1912824-00	LS74 FF-D DUAL EDGE TRIGG	1	1	1	1	E40
77	77		2113735-01	4K MOS RAM 200NS 1	48	-	-	-	E100, E102, E104, E106, E108, E110, E112, E114, E116, E118, E120, E122, E200, E202, E204, E206, E208, E210, E212, E214, E216, E218, E220, E222, E300, E302, E304, E306, E308, E310, E312, E314, E316, E318, E320, E322, E400, E402, E404, E406, E408, E410, E412, E414, E416, E418, E420, E422
78	78		2114114-01	4K MOS RAM 200NS 1	-	-	48	-	E100, E102, E104, E106, E108, E110, E112, E114, E116, E118, E120, E122, E200, E202, E204, E206, E208, E210, E212, E214, E216, E218, E220, E222, E300, E302, E304, E306, E308, E310, E312, E314, E316, E318, E320, E322, E400, E402, E404, E406, E408, E410, E412, E414, E416, E418, E420, E422
79	79		2113914-01	4K MOS RAM 200NS 1	-	48	-	-	E100, E102, E104, E106, E108, E110, E112, E114, E116, E118, E200, E210, E212, E214, E216, E218, E220, E222, E300, E302, E304, E306, E308, E310, E312, E314, E316, E318, E320, E322, E400, E402, E404, E406, E408, E410, E412, E414, E416, E418, E420, E422
80	80	SEE NOTE 91	2114475-01	4K MOS RAM 200NS 1	-	-	-	48	E100, E102, E104, E106, E108, E110, E112, E114, E116, E118, E120, E122, E200, E202, E204, E206, E208, E210, E212, E214, E216, E218, E220, E222, E300, E302, E304, E306, E308, E310, E312, E314, E316, E318, E320, E322, E400, E402, E404, E406, E408, E410, E412, E414, E416, E418, E420, E422
81	81		9000024-01	EYELET, ROLL FLANGE .1210DX .192	12	12	12	12	
82	82	USE WITH Q6, Q7, Q10	9006557-00	NUT, KEP 4-40X 1/4 AF	3	3	3	3	
83	83	USE WITH Q11	9007254-00	TRANSIPADS #10146	1	1	1	1	
84	84	USE WITH Q6, Q7, Q10	9008301-01	SCREW, PAN, PHIL 4-40X 1/4 SS	3	3	3	3	
85	85		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	6	6	6	-	W2, W3, W4, W9, W10, W11
			CONT		-	-	-	5	W2, W3, W4, W9, W11
86	86		9105740-55	WIRE(WRAP)30AWG UL1423	A/R	A/R	A/R	A/R	
87	87		2113789-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	

88 NOTE: M8417-AA=M8417-AB, AC, AD, AE, AF, (16KX12)

D	I	G	I	T	A	L	TITLE	SECTION	OF	SIZE	CODE	DOCUMENT NUMBER	REV
							PDP8 MOS MEMORY	SECTION A	OF C	K	PL	M8417-D-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A4 OF A4

LINE ITEM DOCUMENT NUMBER PART NUMBER DESCRIPTION QTY PER VARIATION
AB AD AC AE REFERENCE DESIGNATOR

89 NOTE: M8417-BA=M8417-BB,BC,BD,BE,BF(32KX12)
90 NOTE: USED ON:OPTION/MODEL MS8-CA,MS8-CB
91 NOTE: R87,R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.
92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.
93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.
94 NOTE: -----

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
					BB	BC	BD	BE	
1	1	D-UA-M8417-0-0		UNIT ASSY	REF	REF	REF	REF	
2	2	D-UA-M8417-0-0		*** THIS ITEM IS NOT USED ***	-	-	-	-	
3	3		5012701-00	ETCH BOARD (M8418)	1	1	1	1	
4	4		1000006-00	10.0 MMF 100V 5%200PPM MICA	1	1	1	1	C66
5	5		1000016-00	100.0 MMF 100V 5%200PPM MICA	1	1	1	1	C64
6	6		1000042-00	1000.0 MMF 100V 5%200PPM MICA	1	1	1	1	C65
7	7		1001610-01	.01 MFD50/100V +80-20% DISC	53	53	53	53	C60, C75-C96, C98-C114, C116-C125, C128, C129, C147
8	8		1005306-00	6.8MFD 35V 10% S.TANT	8	8	8	8	C53, C58, C67, C61, C62, C68, C140, C150
9	9		1010274-00	.22 MFD 50V +80-20% Z5U CER	60	60	60	60	C1-C48, C57, C69, C70, C133-C135, C141-C146
10	10		1010279-00	.47 MFD 25V 20% CER	6	6	6	6	C54-C56, C126, C130, C131
11	11		1012084-01	8 MFD 25V +75-10% AL EL	1	1	1	1	C52
12	12		1012121-00	220.0 MMF 100V 1%200PPM MICA	6	6	6	6	C59, C63, C71-C74
13	13		1012219-00	47 MFD 30V +75-10% AL EL	3	3	3	3	C50, C51, C132
14	14		1101938-00	1N 4370A VZ= 2.4 5% .10W	1	1	1	1	D3
15	15		1104860-00	1N 746A VZ= 3.3 5%	1	1	1	1	D5
16	16		1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21	21	21	D1, D2, D9-D27
17	17		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	1	1	1	D28
18	18		1211164-04	SW,DIP 1P 1A 8POS	1	1	1	1	E62
19	19		1210711-02	/REPLACED BY 12-16988-02	1	1	1	1	
20	20		1300250-00	150.0 .25 W 5.0 % CC	1	1	1	1	R47
21	21		1300271-00	220.0 .25 W 5.0 % CC	16	16	16	16	R123-R138
22	22		1300316-00	470.0 .25 W 5.0 % CC	2	2	2	2	R45, R51
23	23		1300365-00	1.0 K .25 W 5.0 % CC	9	9	9	9	R45, R50, R53, R54, R82-R84, R103, R104
24	24		1300447-00	4.70 K .25 W 5.0 % CC	15	15	15	15	R55-R65, R77, R78, R81, R139
25	25		1300479-00	10.0 K .25 W 5.0 % CC	6	6	6	6	R71-R74, R121, R122
26	26		1302124-00	18.0 .25 W 5.0 % CC	46	46	46	46	R1-R44, R90, R98

REVISION HISTORY		BASIC PART NO: M8417		DRN: L. METZGER		DATE: 30-MAY-78		D I G I T A L	
ENG:	ECO NUMBER	REV	SECTION B OF C	CHK'D:	P. BOSSMAN	DATE:	30-MAY-78	TITLE PARTS LIST	
E.R.	CJ003	D	SECTION VARIATION INDEX	DES.ENG:	J. STEGEMAN	DATE:	30-MAY-78	DOCUMENT NUMBER	
J.S.	M8417-ML004	E	[A] AB, AD, AC, AE	RESP.ENG.:	J. STEGEMAN	DATE:	30-MAY-78	SIZE!CODE!	NUMBER
JS	M8417-ML005	F	[B] BB, BC, BD, BE	MFG.ENG.:	C. TANNER	DATE:	30-MAY-78	K	PL M8417-0-DBP
JS	M8417-ML006	H	[C] AF, BF	ASSEMBLY NUMBER:	D-UA-M8417-0-0	TOP DOCUMENT NUMBER:	MS8-C	FILE NAME:	Z0189H.PLS
			[D]					EDIT #	6
			[E]						
			[F]						
			[H]						
			[J]						
			[K]						
			[L]						
			[M]						
			[N]						

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR					
					BB	BC	BD	BE						
27	27		1302377-00	39.0	.25	W	5.0	%	CC	5	5	5	5	R52,R109-R112
28	28		1302859-00	5.76 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R96
29	29		1302871-00	1.21 K	.25	W	1.0	%	RN550-F10	2	2	2	2	R80,R108
30	30		1302872-00	681.0	.25	W	1.0	%	RN550-F1	1	1	1	1	R89
31	31		1302956-00	196.0	.25	W	1.0	%	RN550-F10	1	1	1	1	R94
32	32		1302957-00	121.0	.25	W	1.0	%	RN550-F10	1	1	1	1	R101
33	33		1303045-00	3.16 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R76
34	34		1303067-00	422.0	.25	W	1.0	%	RN550-F10	1	1	1	1	R99
35	35		1303110-00	19.60	.25	W	1.0	%	RN550-F10	1	1	1	1	R93
36	36		1303226-00	68.10	.25	W	1.0	%	RN550-F10	1	1	1	1	R86
37	37		1303311-00	46.40 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R75
38	38		1303313-00	12.10 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R97
39	39		1304725-00	300.0	.25	W	1.0	%	RN550-F10	1	1	1	1	R107
40	40	SEE NOTE 90	1304833-00	1.96 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R92
41	41		1305122-00	51.10	.25	W	1.0	%	RN550-F10	1	1	1	1	R85
42	42		1305123-00	215.0	.25	W	1.0	%	RN550-F10	2	2	2	2	R91,R102
43	43		1305143-00	825.0	.25	W	1.0	%	RN550-F10	2	2	2	2	R95,R79
44	44	SEE NOTE 90	1305253-00	7.15 K	.25	W	1.0	%	RN550-F10	1	1	1	1	R87
45	45		1309405-00	68.0	.50	W	5.0	%	CC	2	2	2	2	R48,R49
46	46		1503100-00	DEC30098	NPN 200MW SI 20 25					7	7	7	7	Q1-Q5,Q8,Q9
47	47		1510171-00	D 44C3	NPN 30WT SI 30 20 Y					2	2	2	2	Q6,Q7
48	48		1614234-00	XFMR,	CONVERTER, RATIO 1:3 300UH					1	1	1	1	T1
49	49		1909054-00	7493	COUNTER ASYNCH UP, BI					2	2	2	2	E21,E24
50	50		1909705-00	DEC 8881	NAND GATE-QUAD 2IN 0					4	4	4	4	E3,E14,E17,E44
51	51		1910268-01	DEC 751078-01	RECEIVER LINE, DUA					3	3	3	3	E5,E10,E18
52	52		1910406-00	75451	DRIVER, PERIPH, DUAL,					1	1	1	1	E64
53	53		1910532-00	74500	NAND GATE-QUAD 2IN, 0					6	6	6	6	E4,E34,E37,E42,E43,E55
54	54		1910533-00	74503	NAND GATE-QUAD 2IN, 0					1	1	1	1	E13
55	55		1910534-00	74504	INVERTER GATE-HEX 1I					2	2	2	2	E22,E54
56	56		1910536-00	74510	NAND GATE-TRIPLE 3IN					3	3	3	3	E19,E51,E63
57	57		1910539-00	74520	NAND GATE-DUAL 4INPU					1	1	1	1	E59
58	58		1910542-00	74564	A-0-1 GATE 4-2-3-2					1	1	1	1	E15
59	59		1910544-01	74574-60GG-D	DUAL, EDGE TRIG					1	1	1	1	E39
60	60		1910545-00	745112	FF-JK DUAL, EDGE TRIG					3	3	3	3	E7,E20,E32
61	61		1910549-00	745158	MUX 1 OF 2 (QUAD)					2	2	2	2	E26,E33
62	62		1911116-00	DEC 8837	RECEIVER, BUS, HEX, UN					5	5	5	5	E6,E8,E16,E38,E57
63	63		1911469-00	DEC 8640	RECEIVER, BUS, QUAD, U					1	1	1	1	E45
64	64		1911579-00	8641	TRANSCEIVER, BUS, QUA					3	3	3	3	E1,E11,E51
65	65		1911676-00	745139	DECODER-DUAL TWO-INP					1	1	1	1	E56
66	66		1911712-00	74551	AND-OR GATE-INVERT D					1	1	1	1	E58
67	67		1911944-00	555CN	TIMER, FUNCT. BLOCK					1	1	1	1	E31
68	68		1911983-00	745133	NAND GATE-POSITIVE 1					1	1	1	1	E60
69	69	SEE NOTE 93	1912048-06	DEC 7812	VOLT REG, FIX +12V					1	1	1	1	Q10
70	70		1912388-00	74502	NOR GATE-QUAD 2IN, PO					3	3	3	3	E9,E25,E46
71	71		1912389-00	74508	AND GATE-QUAD 2IN, PO					1	1	1	1	E23
72	72		1912541-00	79M05	VOLT REG, FIX -5V					1	1	1	1	Q11
73	73		1912649-00	LS75	LATCH 4BIT, BISTABLE					3	3	3	3	E2,E12,E53
74	74		1912746-00	DEC 74537	NAND GATE-QUAD 2IN					11	11	11	11	E27-E30,E35,E36,E41,E47-E50

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION B OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET B3 OF B3

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
					BB	BC	BD	BE	
75	75		1912803-00	74LS04 INVERTER GATE, HEX	1	1	1	1	E52
76	76		1912824-00	LS74 FF-D DUAL, EDGE TRIGG	1	1	1	1	E40
77	77		2113735-01	4K MOS RAM 200NS 1	96	-	-	-	E100-E123, E200-E223, E300-E323, E400-E423
78	78		2114114-01	4K MOS RAM 200NS 1	-	96	-	-	E100-E123, E200-E223, E300-E323, E400-E423
79	79		2113914-01	4K MOS RAM 200NS 1	-	-	96	-	E100-E123, E200-E223, E300-E323, E400-E423
80	80	SEE NOTE 91	2114475-01	4K MOS RAM 200NS 1	-	-	-	96	E100-E123, E200-E223, E300-E323, E400-E423
81	81		9000024-01	EYELET, ROLL FLANGE .1210DX .192	12	12	12	12	
82	82	USE WITH Q6, Q7, Q10	9006557-00	NUT, KEP 4-40X 1/4 AF	3	3	3	3	
83	83	USE WITH Q11	9007254-00	TRANSIPADS #10146	1	1	1	1	
84	84	USE WITH Q5, Q7, Q10	9008301-01	SCREW PAN, PHIL 4-40X 1/4 SS	3	3	3	3	
85	85		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	5	5	5	5	W2, W3, W4, W9, W11
86	86		9105740-55	WIRE(WRAP)30AWG UL1423	A/R	A/R	A/R	A/R	
87	87		2113789-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	

- 88 NOTE: M8417-AA=M8417-AB, AC, AD, AE, AF, (16KX12)
- 89 NOTE: M8417-BA=M8417-BB, BC, BD, BE, BF (32KX12)
- 90 NOTE: USED ON: OPTION/MODEL M58-CA, M58-CB
- 91 NOTE: R87, R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.
- 92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.
- 93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.
- 94 NOTE: -----

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION B OF C	SIZE! CODE!	DOCUMENT NUMBER	REV
										K	PL M8417-0-DBP	H

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER	VARIATION	REFERENCE DESIGNATOR
					AF	BF		
1	1	D-UA-M8417-0-0		UNIT ASSY	REF	REF		
2	2	D-UA-M8417-0-0		UNIT ASSY	REF	REF		
3	3		5012701-00	ETCH BOARD (M8418)	1	1		
4	4		1000006-00	10.0 MMF 100V 5%200PPM MICA	1	1		C66
5	5		1000016-00	100.0 MMF 100V 5%200PPM MICA	1	1		C64
6	6		1000042-00	1000.0 MMF 100V 5%200PPM MICA	1	1		C65
7	7		1001610-01	.01 MFD50/100V +80-20% DISC	53	53		C60, C75-C96, C98-C114, C116-C125, C128, C129, C147
8	8		1005306-00	6.8MFD 35V 10% S.TANT	8	8	CONT	C53, C58, C67, C61, C62, C68, C140, C150
9	9		1010274-00	.22 MFD 50V +80-20% Z5U CER	60	60	CONT	C1-C48, C57, C69, C70, C133-C135, C141-C146
10	10		1010279-00	.47 MFD 25V 20% CER	6	6	CONT	C54-C56, C126, C130, C131
11	11		1012084-01	8 MFD 25V +75-10% AL EL	1	1		C52
12	12		1012121-00	220.0 MMF 100V 1%200PPM MICA	6	6		C59, C63, C71-C74
13	13		1012219-00	47 MFD 30V +75-10% AL EL	3	3		C50, C51, C132
14	14		1101938-00	1N 4370A VZ= 2.4 5% .40W	1	1		D3
15	15		1104860-00	1N 746A VZ= 3.3 5%	1	1		D5
16	16		1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21		D1, D2, D9-D27
17	17		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	1		D28
18	18		1211164-04	SW, DIP 1P 1A 8POS	1	1		E62
19	19		1210711-02	/REPLACED BY 12-16988-02	1	1		
20	20		1300250-00	150.0 .25 W 5.0 % CC	1	1		R47
21	21		1300271-00	220.0 .25 W 5.0 % CC	16	16		R123-R138
22	22		1300316-00	470.0 .25 W 5.0 % CC	2	2		R46, R51
23	23		1300365-00	1.0 K .25 W 5.0 % CC	9	9	CONT	R45, R50, R53, R54, R82-R84, R103, R104
24	24		1300447-00	4.70 K .25 W 5.0 % CC	15	15		R55-R65, R77, R78, R81, R139
25	25		1300479-00	10.0 K .25 W 5.0 % CC	6	6		R71-R74, R121, R122
26	26		1302124-00	18.0 .25 W 5.0 % CC	46	46		R1-R44, R90, R98

REVISION HISTORY		BASIC PART NO: M8417		DRN: L. METZGER		DATE: 30-MAY-78		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION C OF C	CHK'D:	P. BOSSMAN	DATE:	30-MAY-78	TITLE	PARTS LIST
E.R.	00003	D	SECTION VARIATION INDEX	DES. ENG.:	J. STEGEMAN <td>DATE:</td> <td>30-MAY-78 <td>DOCUMENT NUMBER</td> <td></td> </td>	DATE:	30-MAY-78 <td>DOCUMENT NUMBER</td> <td></td>	DOCUMENT NUMBER	
J.S.	M8417-ML004	E	[A] AB, AD, AC, AE	RESP. ENG.:	J. STEGEMAN <td>DATE:</td> <td>30-MAY-78 <td>SIZE</td> <td>CODE</td> </td>	DATE:	30-MAY-78 <td>SIZE</td> <td>CODE</td>	SIZE	CODE
JS	M8417-ML005	F	[B] BB, BC, BD, BE	MFG. ENG.:	C. TANNER <td>DATE:</td> <td>30-MAY-78 <td>PL</td> <td>M8417-0-DBP</td> </td>	DATE:	30-MAY-78 <td>PL</td> <td>M8417-0-DBP</td>	PL	M8417-0-DBP
JS	M8417-ML006	H	[C] AF, BF	ASSEMBLY NUMBER:	D-UA-M8417-0-0 <td>TOP DOCUMENT NUMBER:</td> <td>MSB-C</td> <td>FILE NAME:</td> <td>Z0189H.PLS</td>	TOP DOCUMENT NUMBER:	MSB-C	FILE NAME:	Z0189H.PLS
			[D]					REV	6
			[E]						
			[F]						
			[H]						
			[J]						
			[K]						
			[L]						
			[M]						
			[N]						

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY AF	PER BF	VARIATION	REFERENCE DESIGNATOR
27	27		1302377-00	39.0	.25	W	5.0 % CC	R52, R109-R112
28	28		1302859-00	5.76 K	.25	W	1.0 % RN55D-F10	R96
29	29		1302871-00	1.21 K	.25	W	1.0 % RN55D-F10	R80, R108
30	30		1302872-00	681.0	.25	W	1.0 % RN55D-F10	R89
31	31		1302956-00	196.0	.25	W	1.0 % RN55D-F10	R94
32	32		1302957-00	121.0	.25	W	1.0 % RN55D-F10	R101
33	33		1303045-00	3.16 K	.25	W	1.0 % RN55D-F10	R76
34	34		1303067-00	422.0	.25	W	1.0 % RN55D-F10	R99
35	35		1303110-00	19.60	.25	W	1.0 % RN55D-F10	R93
36	36		1303226-00	68.10	.25	W	1.0 % RN55D-F10	R86
37	37		1303311-00	46.40 K	.25	W	1.0 % RN55D-F10	R75
38	38		1303313-00	12.10 K	.25	W	1.0 % RN55D-F10	R97
39	39	SEE NOTE 90	1304725-00	300.0	.25	W	1.0 % RN55D-F10	R107
40	40		1304833-00	1.96 K	.25	W	1.0 % RN55D-F10	R92
41	41		1305122-00	51.10	.25	W	1.0 % RN55D-F10	R85
42	42		1305123-00	215.0	.25	W	1.0 % RN55D-F10	R91, R102
43	43	SEE NOTE 90	1305143-00	825.0	.25	W	1.0 % RN55D-F10	R95, R79
44	44		1305253-00	7.15 K	.25	W	1.0 % RN55D-F10	R87
45	45		1309405-00	68.0	.50	W	5.0 % CC	R48, R49
46	46		1503100-00	DEC30098	NPN	200MW	SI 20 25	Q1-Q5, Q8, Q9
47	47		1510171-00	D	44C3	NPN	30WT SI 30 20 Y	Q6, Q7
48	48		1614234-00	XFMR,	CONVERTER,	RATIO	1:3 300UH	T1
49	49		1909054-00	7493	COUNTER,	ASYNCH	UP, BI	E21, E24
50	50		1909705-00	DEC 8881	NAND	GATE-QUAD	2IN 0	E3, E14, E17, E44
51	51		1910268-01	DEC 75107B-01	RECEIVER,	LINE	DUA	E5, E10, E18
52	52		1910406-00	75451	DRIVER,	PERIPH,	DUAL,	E64
53	53		1910532-00	74500	NAND	GATE-QUAD	2IN	E4, E34, E37, E42, E43, E55
54	54		1910533-00	74503	NAND	GATE-QUAD	2IN, 0	E13
55	55		1910534-00	74504	INVERTER	GATE-HEX	1I	E22, E54
56	56		1910536-00	74510	NAND	GATE-TRIPLE	3IN	E19, E61, E63
57	57		1910539-00	74520	NAND	GATE-DUAL	4INPU	E59
58	58		1910542-00	74564	A-0-I	GATE	4-2-3-2	E15
59	59		1910544-01	74574-6	OGG-D	DUAL,	EDGE TRIG	E39
60	60		1910545-00	745112	FF-JK	DUAL,	EDGE TRIG	E7, E20, E32
61	61		1910549-00	745158	MUX	1 OF 2	(QUAD)	E26, E33
62	62		1911116-00	DEC 8837	RECEIVER,	BUS,	HEX, UN	E6, E8, E16, E38, E57
63	63		1911469-00	DEC 8640	RECEIVER,	BUS,	QUAD, U	E45
64	64		1911579-00	8641	TRANSCEIVER,	BUS,	QUA	E1, E11, E51
65	65		1911676-00	745139	DECODER-DUAL	TWO-INP		E56
66	66		1911712-00	74551	AND-OR	GATE-INVERT	D	E58
67	67		1911944-00	555CN	TIMER,	FUNCT.	BLOCK	E31
68	68		1911983-00	745133	NAND	GATE-POSITIVE	1	E60
69	69	SEE NOTE 93	1912048-06	DEC 7812	VOLT	REG.FIX	+12V	Q10
70	70		1912388-00	74502	NOR	GATE-QUAD	2IN, PO	E9, E25, E46
71	71		1912389-00	74508	AND	GATE-QUAD	2IN, PO	E23
72	72		1912541-00	79M05	VOLT	REG.FIX	-5V	Q11
73	73		1912649-00	LS75	LATCH	4BIT,	BISTABLE	E2, E12, E53
74	74		1912746-00	DEC 74537	NAND	GATE-QUAD	2IN	E27-E30, E35, E36, E41, E47-E50

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION C OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.20(16)

PARTS LIST

SHEET C3 OF C3

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER	VARIATION	REFERENCE DESIGNATOR
					AF	BF		
75	75		1912803-00	74LS04 INVERTER GATE, HEX	1	1		E52
76	76		1912824-00	LS74 FF-D DUAL, EDGE TRIGG	1	1		E40
77	77		2113735-01	*** THIS ITEM IS NOT USED ***	-	-		
78	78		2114114-01	*** THIS ITEM IS NOT USED ***	-	-		
79	79		2113914-01	*** THIS ITEM IS NOT USED ***	-	-		
80	80	SEE NOTE 91	2114475-01	*** THIS ITEM IS NOT USED ***	-	-		
81	81		9000024-01	EYELET, ROLL FLANGE .1210DX .192	12	12		
82	82	USE WITH Q6, Q7, Q10	9006557-00	NUT, KEP 4-40X 1/4 AF	3	3		
83	83	USE WITH Q11	9007254-00	TRANSIPADS #10146	1	1		
84	84	USE WITH Q6, Q7, Q10	9008301-01	SCREW, PAN, PHIL 4-40X 1/4 SS	3	3		
85	85		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	6	6		W2, W3, W4, W9, W10, W11
86	86		9105740-55	WIRE(WRAP)30AWG ULI423	A/R	A/R		
87	87		2113789-01	4K MOS RAM 200NS 1	48	-		
CONT					-	96		E100, E102, E104, E106, E108, E110, E112, E114, E116, E118, E120, E122, E200, E202, E204, E206, E208, E210, E212, E214, E216, E218, E220, E222, E300, E302, E304, E306, E308, E310, E312, E314, E316, E318, E320, E322, E400, E402, E404, E406, E408, E410, E412, E414, E416, E418, E420, E422, E100-E123, E200-E223, E300-E323, E400-E423

- 88 NOTE: M8417-AA=M8417-AB, AC, AD, AE, AF. (16KX12)
- 89 NOTE: M8417-BA=M8417-BB, BC, BD, BE, BF (32KX12)
- 90 NOTE: USED ON: OPTION/MODEL MS8-CA, MS8-CB
- 91 NOTE: R87, R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.
- 92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.
- 93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.
- 94 NOTE: -----

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION C OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

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DIGITAL EQUIPMENT CORPORATION																					
MAYNARD, MASSACHUSETTS																					
ENGINEERING SPECIFICATION				DATE 10/3/73																	
TITLE MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE																					
REVISIONS																					
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE															
A	ECO CHANGE	MR8F-00001	ADAMS	1-74	<i>David Adams</i>	2/27/74															
B	UPDATE TO ADD PDP8A	MR8F-00004	REGAN	12-74	<i>R. Regan</i>	12/30/74															
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">ENG</td> <td style="width: 25%;">Richard Morris</td> <td style="width: 15%;">APPRO</td> <td style="width: 15%;"><i>David Adams</i> 10/11/73</td> <td style="width: 10%;">SIZE</td> <td style="width: 10%;">CODE</td> <td style="width: 15%;">NUMBER</td> <td style="width: 10%;">REV</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td>A</td> <td>SP</td> <td>MR8-F-2</td> <td>B</td> </tr> </table>						ENG	Richard Morris	APPRO	<i>David Adams</i> 10/11/73	SIZE	CODE	NUMBER	REV					A	SP	MR8-F-2	B
ENG	Richard Morris	APPRO	<i>David Adams</i> 10/11/73	SIZE	CODE	NUMBER	REV														
				A	SP	MR8-F-2	B														

DEC FORM NO.
DRA 107

SHEET 1 OF 3

ENGINEERING SPECIFICATION	CONTINUATION SHEET								
TITLE MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE									
<ol style="list-style-type: none"> 1. Shipping Hardware <ol style="list-style-type: none"> 1.1 See A-PL-MR8-F-5 (Shipping List) 2. Shipping Software <ol style="list-style-type: none"> 2.1 See A-PL-MR8-F-6 (Software List) <p>NOTE: Prom Diagnostics are not used in this Acceptance Procedure. These Maindecs are used to diagnose the MR8-FB after the MR8-FB has been programmed to the customers specifications.</p> 3. Equipment required for acceptance <ol style="list-style-type: none"> 3.1 PDP8E, 8M, or 8A with a programmers console. If 4K of read/write memory is present, the system must have a KM8-E (M837), or KM8-A (M8317), extended memory control. All these options must be customer supplied. 4. Unpacking and Installation <ol style="list-style-type: none"> 4.1 Unpack and inspect the modules for physical damage. 4.2 Make sure all four top edge connectors on the M8349 are fitted correctly. 4.3 Turn power off in the PDP8E, 8M, 8F, or 8A. 4.4 If the MR8-FB is a PDP8E, 8F, or 8M, add-on remove M8330 and insert the new M8330-YB in the same slot. The MR8-FB requires an M8330-YB in order to operate. 4.5 Insert the M8349 in the OMNIBUS behind the RFI shield (M849) in the PDP8E, 8F, or 8M, and in the lowest available Omnibus slot in the PDP8A. 4.6 Remove or disable all other options in PDP8E, 8M, 8F, or 8A that use the "SW" or "BOOT" switch option. 									
Sheet 2 of 3	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">SIZE</td> <td style="width: 15%;">CODE</td> <td style="width: 15%;">NUMBER</td> <td style="width: 10%;">REV</td> </tr> <tr> <td>A</td> <td>SP</td> <td>MR8-F-2</td> <td>B</td> </tr> </table>	SIZE	CODE	NUMBER	REV	A	SP	MR8-F-2	B
SIZE	CODE	NUMBER	REV						
A	SP	MR8-F-2	B						

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE

MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

5. Acceptance

- 5.1 The MR8-FB is shipped with Prom Internal test Maindec-08-DHMRE programmed in the Prom chips.
- 5.2 Turn PDP8E, 8M, 8F, or 8A, power on.
- 5.3 If the MR8-FB is an add-on and an M8330-YB was installed, run all basic 8E diagnostics and EAE diagnostics if applicable.
- 5.4 Toggle "SW" or "BOOT" switch. The Prom Internal Test should be running. Refer to MAINDEC-08-DHMRE writeup if there are any errors. With the switch register = 0000 the test will halt in approx. 3 min. Repeat the test 4 times.
- 5.5 If no errors have occurred the MR8-FB is ready to be erased and reprogrammed by the customer.

Sheet 3 of 3

SIZE
ACODE
SPNUMBER
MR8-F-2REV
B

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DCS M8349-0-1 2

QTY	QTY	QTY	QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
12	φ	φ	φ	SEE Y VARIATION CHART	I.C. DEC 74200 (3100)	1810018-2	56
4	φ	φ	φ	EB, E10, E36, E80	I.C. DEC 74157	1810005	57
1	φ	φ	φ	.002	CAP SMDFF 100V 5%	1000025	58
1	φ	φ	φ	.000	RES. 10K POT 3/4 W 10%	1309143-10	59
1	φ	φ	φ		HEAT SINK: TRANSISTOR	1210001	61
REF					X-Y COORDINATE HOLE LOCATION	K-CO-M8349-0-4	62
REF					ASSY/DRILLING HOLE LAYOUT	D-AH-M8349-0-5	63
REF					ECO MODULE HISTORY	B-MH-M8349-0-6	64
1	φ	φ	φ		ETCH CIRCUIT BOARD	5010426	65

M8349-YA
M8349-YC
M8349-YD

Y VARIATION CHART

COMPONENTS	M8349 YA	M8349 YC	M8349 YD	M8349
I.C. DEC 1702A	E26, E50	E26, E32, E38, E42, E50, E55	E26, E32, E50	E20, E26, E32, E38, E42, E50, E55
I.C. DEC 5384	E4, E24, E51	E4, E24, E51	E4, E24, E51	E4, E10, E24, E27, E43, E51
JUMPER YA1 SEE NOTE 6	IN	IN	IN	OUT
JUMPER YA2	IN	IN	IN	OUT
I.C. DEC 74151	0	0	0	E16
I.C. DEC 74200	0	0	0	E2, E7, E11, E15, E19, E23, E25, E28, E33, E37, E41, E46

* DIODE & JUMPER SETTINGS FOR ADDRESS DEFINITIONS

MEMORY FIELD SELECT	DIODE					
	D3	D4	D5	D6	D9	D10
0	1	-	1	-	-	1
1	-	1	1	-	-	1
2	1	-	1	-	-	1
3	-	1	1	-	1	-
4	1	-	-	1	-	1
5	-	1	-	1	-	1
6	1	-	-	1	1	-
7	-	1	-	1	1	-

FIRST MEMORY ADDRESS	DIODE			
	D7	D8	D11	D12
0000	-	1	-	1
2000	1	-	-	1
4000	-	1	1	-
6000	1	-	1	-

STARTING MEMORY ADDRESS	JUMPERS BELOW		
	R1	R2	R3
0000	1	1	1
0200	1	1	-
2000	1	-	1
2200	1	-	-
4000	-	1	1
4200	-	1	-
6000	-	-	1
6200	-	-	-

* 1 = DIODE OR JUMPER IN
- = DIODE OR JUMPER OUT

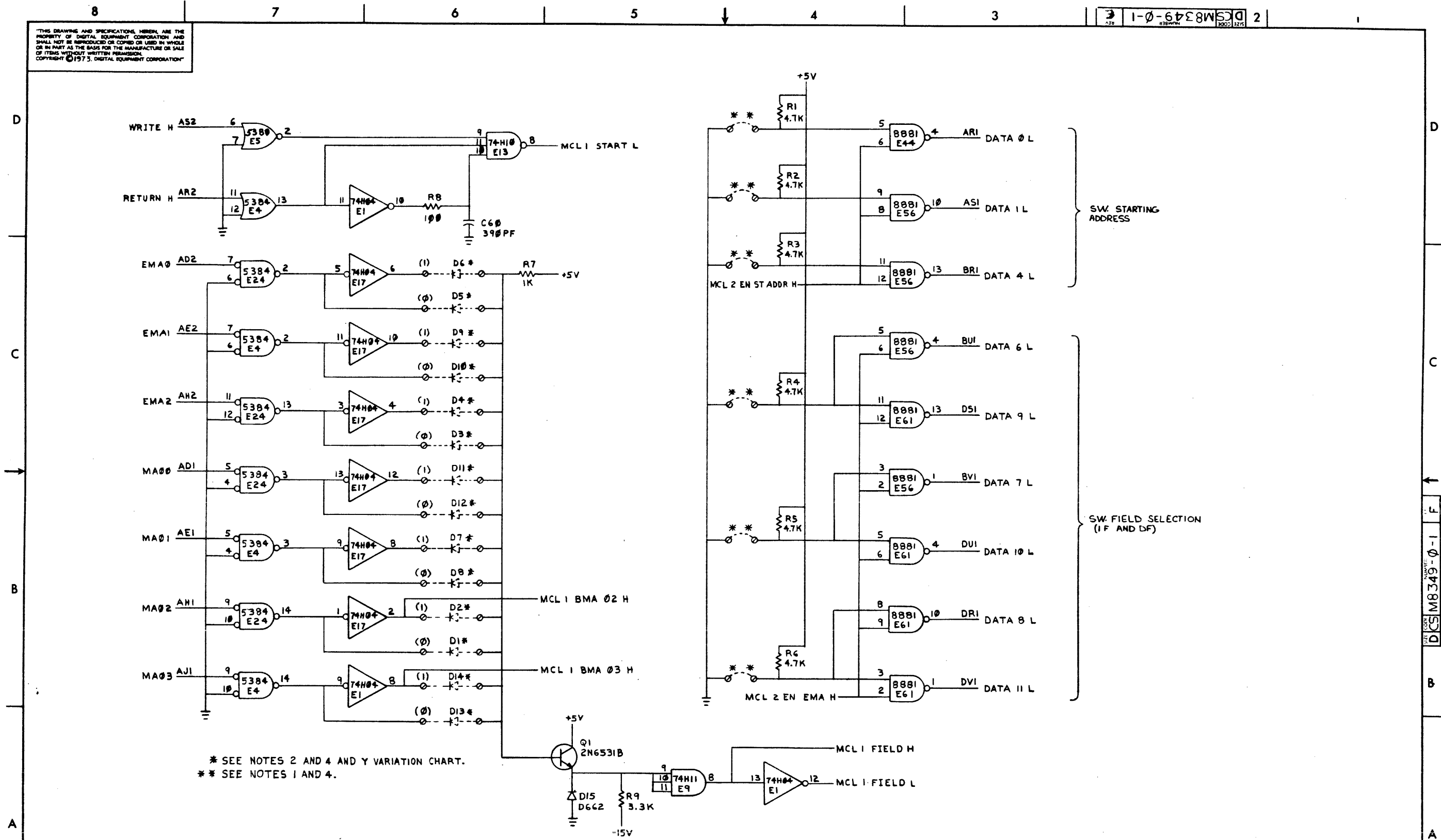
*SW OR *BOOT* FIELD SELECT	JUMPER BELOW		
	R4	R5	R6
0	1	1	1
1	1	1	-
2	1	-	1
3	1	-	-
4	-	1	1
5	-	1	-
6	-	-	1
7	-	-	-

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PROM 1K	SIZE CODE	D CS	NUMBER	M8349-0-1	REV.	F
SCALE	1:1	SHEET	2 OF 7	DIST.			

REV. F DCS M8349-0-1

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* SEE NOTES 2 AND 4 AND Y VARIATION CHART.
 ** SEE NOTES 1 AND 4.

SW STARTING ADDRESS

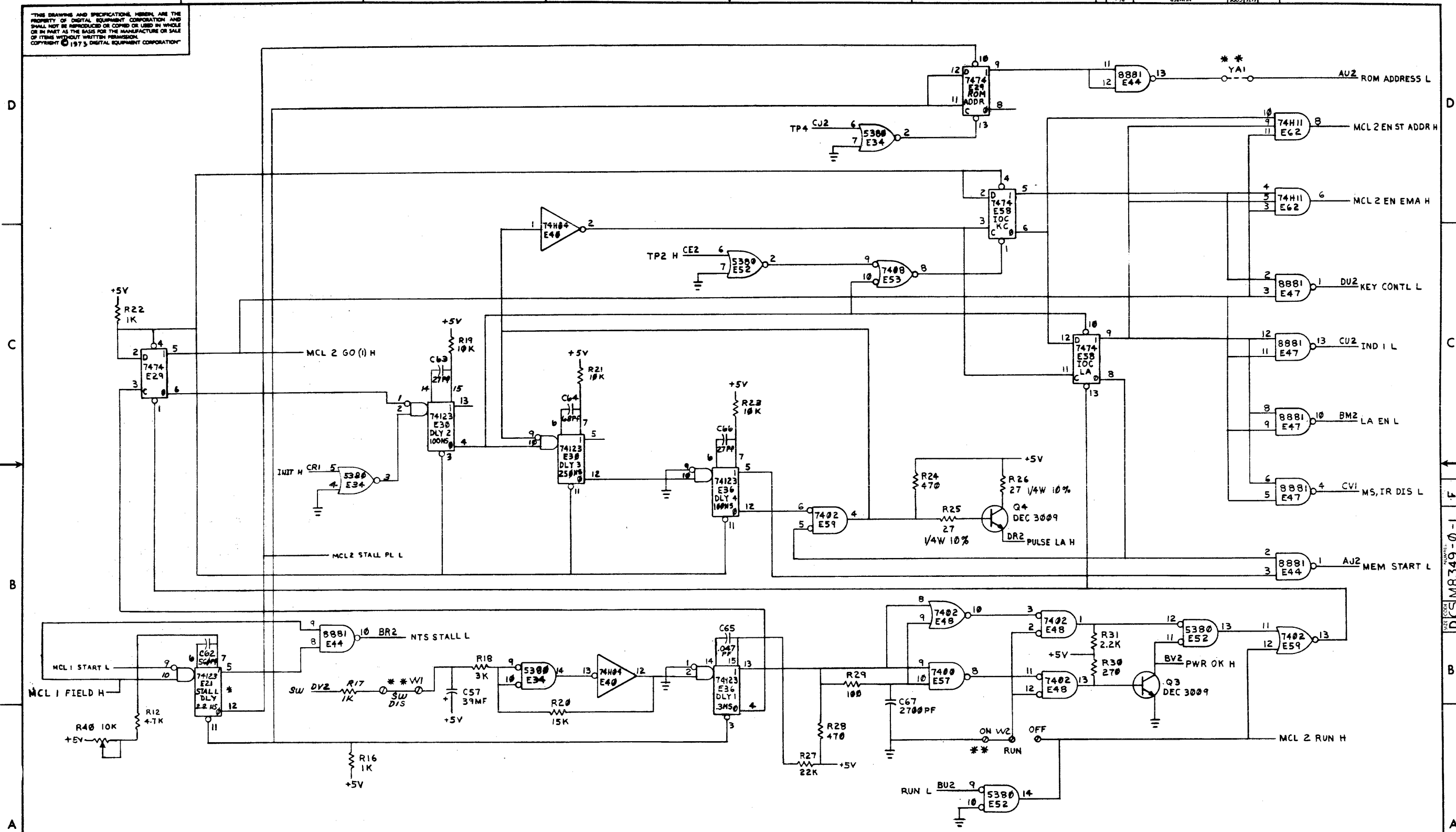
SW FIELD SELECTION (IF AND DF)

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PROM 1K (MCL1)	SIZE CODE	D CS	NUMBER	M8349-0-1	REV.	F
SCALE	+	SHEET	3	OF	7	DIST.	

DCS M8349-0-1 F

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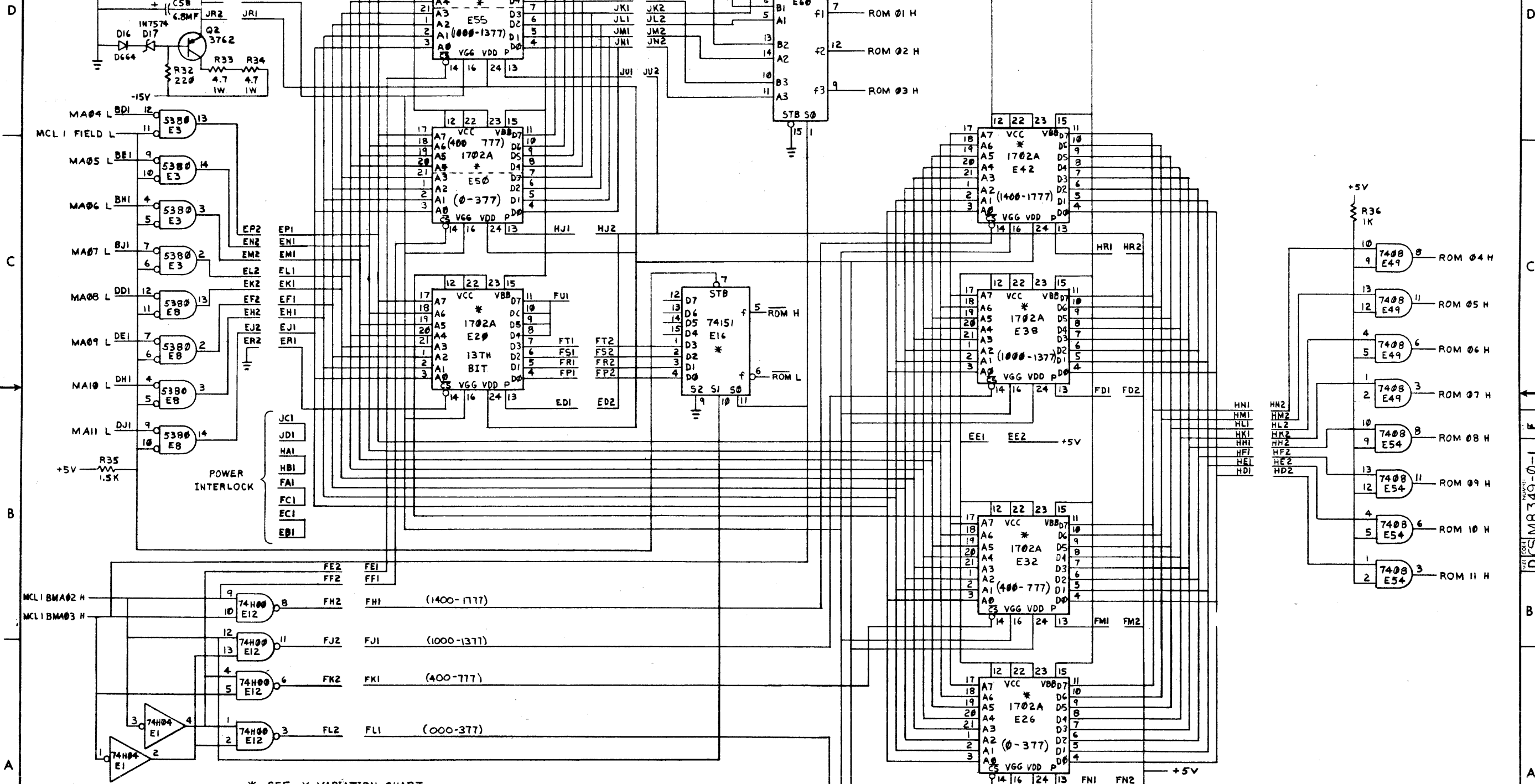
* SEE NOTE 3
** SEE NOTE 4

REVISIONS		
CHK	CHANGE NO	REV.

TITLE	PROM 1K (MCL2)	SIZE CODE	DCS	NUMBER	M8349-0-1	REV.	F
SCALE	1:1	SHEET	4	OF	7	DIST.	

DCS M8349-0-1 F

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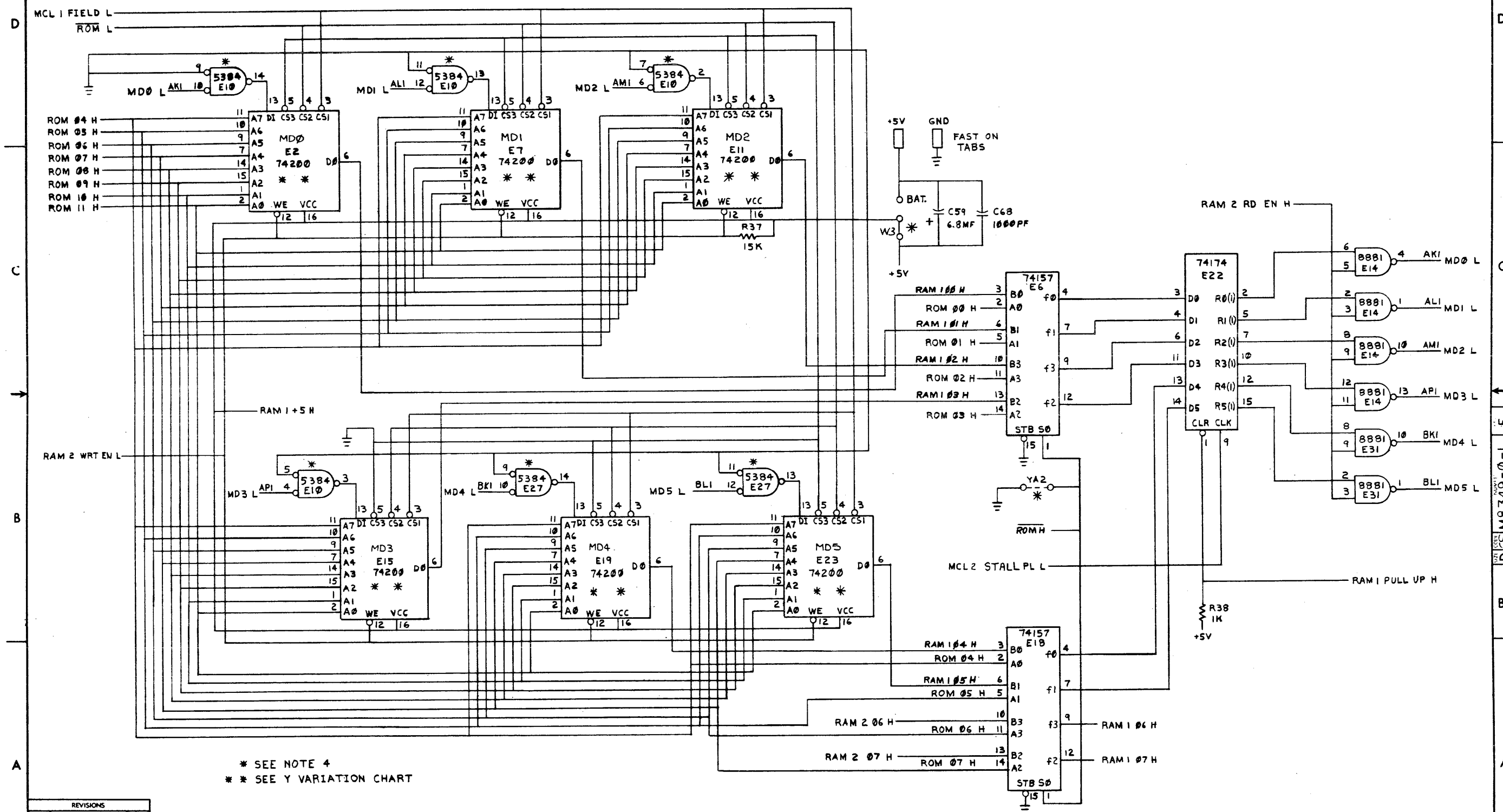


* SEE Y VARIATION CHART

REVISIONS table with columns: CHK, CHANGE NO, REV.

Metadata table with columns: TITLE (PROM 1K (ROM)), SIZE CODE (DCS), NUMBER (M8349-0-1), REV. (F), SHEET 5 OF 7.

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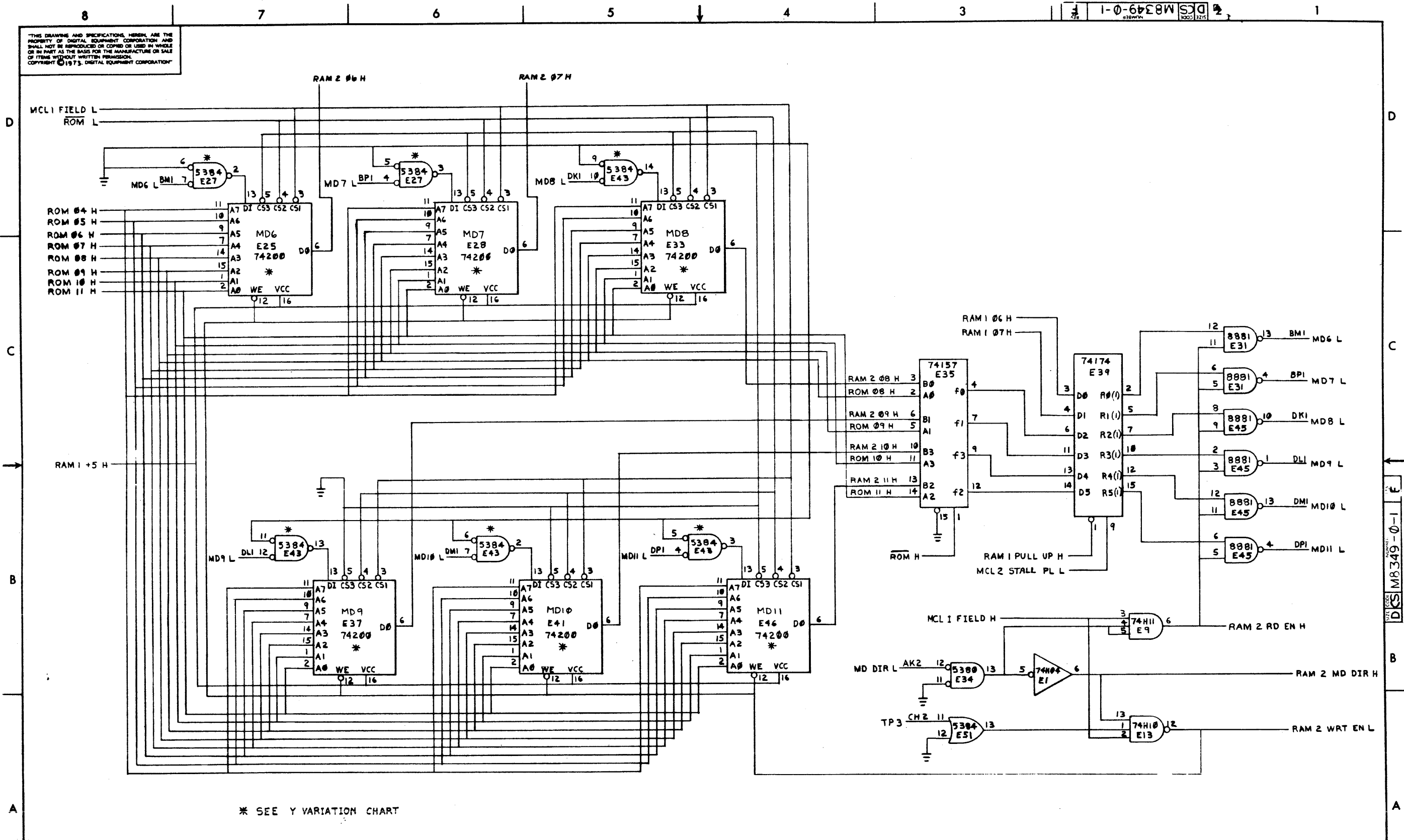
* SEE NOTE 4
 ** SEE Y VARIATION CHART

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PROM 1K (RAM 1)	SIZE CODE	D CS	NUMBER	M8349-0-1	REV.	F
SCALE		SHEET	6 OF 7	DIST.			

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1-0-6038W SCD 2



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PROM 1K (RAM 2)	SIZE CODE	DCS	NUMBER	M8349-0-1	REV.	1
SCALE	1/1	SHEET	7	OF	7	DIST.	

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DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
ENGINEERING SPECIFICATION			DATE 11/19/74
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE			
REV	DESCRIPTION	REVISIONS	
		CHG NO	DATE
APPD BY	DATE	ORIG	DATE

ENG: *John* 11/20/74 APP: *John* 11/20/74
 DEC FORM NO. DRA 108

ENGINEERING SPECIFICATION				CONTINUATION SHEET
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE				
I GENERAL				
This procedure defines the performance standards required of an MS8A RAM memory.				
NOTE: If MS8A was shipped as part of a PDP-8A system, proceed to installation procedure.				
*MSRAA (M8311YA) 1K Semiconductor Random Access Memory MS8AB (M8311YB) 2K Semiconductor Random Access Memory MS8AD (M8311YD) 4K Semiconductor Random Access Memory				
II INSPECTION				
After removing the MS8A from racking material, inspect the module for the following:				
1. Check for loose or broken components.				
2. Inventory software against software list, if ordered.				
3. Inventory prints against shipping list, if ordered.				
4. Inventory hardware against shipping list.				
III INSTALLATION PROCEDURE				
Install the equipment using the following procedure:				
1. Set up switches as indicated below for the particular variation being accepted.				
M8311YA 1K				
M8311YB 2K				
M8311YD 4K				
	M8311 YA	M8311 YB	M8311 YD	
S1-1	ON	ON	ON	= field #
S1-2	ON	ON	ON	= field #
S1-3	ON	ON	ON	= field #
S1-4	ON	ON	ON	ST Add
S1-5	ON	ON	ON	ST Add
S1-6	OFF	OFF	ON	On for 4K
S1-7	OFF	OFF	OFF	OFF
S1-8	OFF	ON	OFF	On for 2K
S1-9	ON	OFF	OFF	On for 1K
S1-10	ON	ON	ON	Normally On
NOTE: Reference Operator's Handbook for complete description of switch settings.				

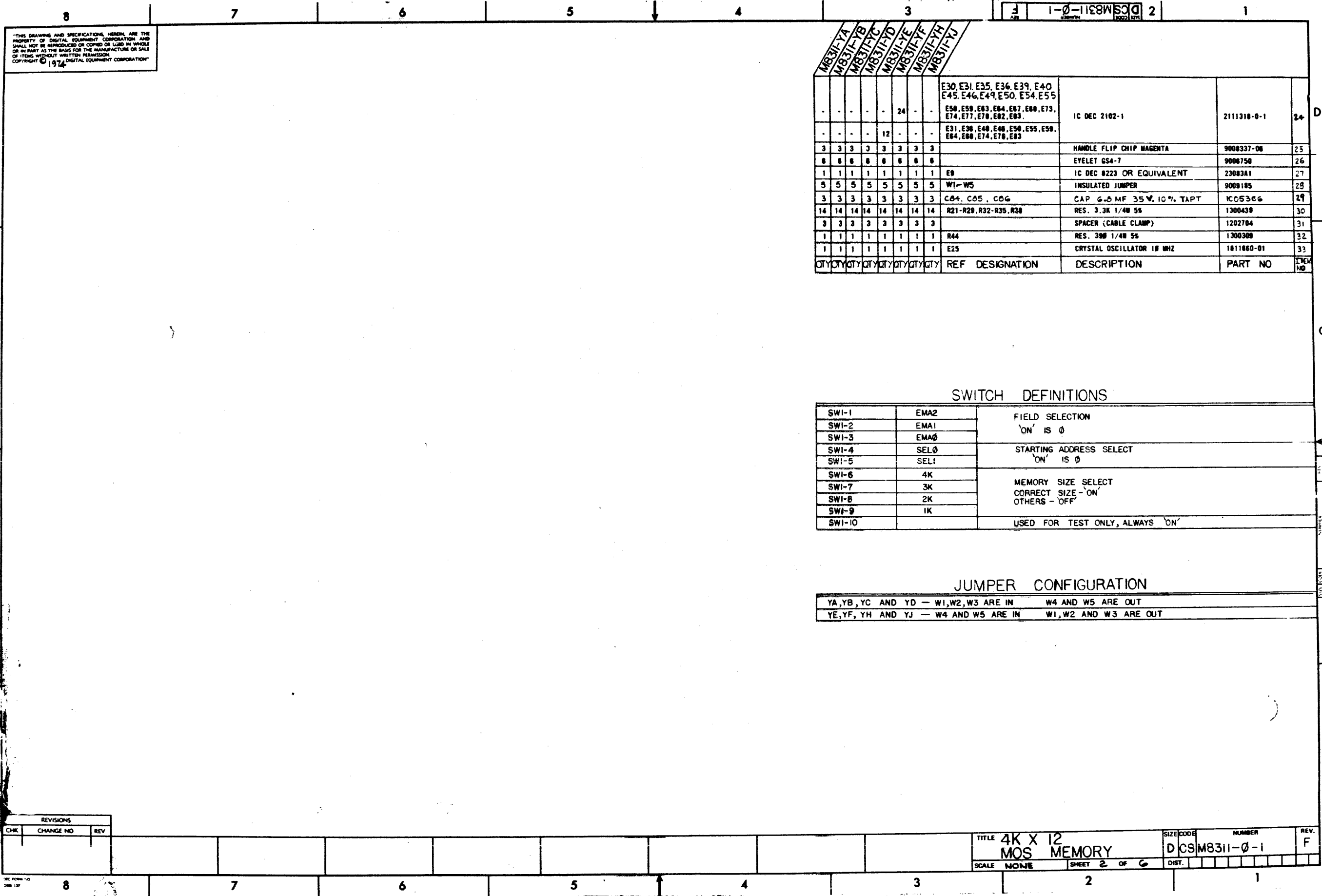
DEC FORM NO. DEC 16-10811-1022-N370
 DRA 108

ENGINEERING SPECIFICATION				CONTINUATION SHEET
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE				
III INSTALLATION PROCEDURE (continued)				
2. Insure that the PDP-8A power is removed from the Omnibus TM.				
3. Insert the MS8A into the last slot vacant in the Omnibus TM.				
4. Turn power on.				
IV ACCEPTANCE PROCEDURE				
Perform the Acceptance Test as indicated in Table B. If problems are encountered, refer to the diagnostic listing for type of error. Reference Operator's Manual and Diagnostic Write-up for instructions on loading diagnostic.				
Equipment required:				
1. PDP-8A with MS8A R/W Memory				
2. Programmer's Console				
3. Paper Tape Input Device				
4. Diagnostic and Listings				
NOTE: If the Programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.				
Table B				
Acceptance of MS8A: YA or YB				
Program Name	Maindec #	Accept Time	Restrictions	
1-4K MOS Memory Test (RIM)	08-DJMSA-PM	30 min	1K or 2K MS8A R/W Memory	
Acceptance of MS8A: YD				
1-4K MOS Memory Test	08-DJMSA-PM	15 min.	4K MS8A R/W Memory	
4-32K Memory Test	08-DJMMA-PB	15 min.	4K MS8A R/W Memory	

DEC FORM NO. DEC 16-10811-1022-N370
 DRA 108

ENGINEERING SPECIFICATION				CONTINUATION SHEET
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE				
III INSTALLATION PROCEDURE (continued)				
2. Insure that the PDP-8A power is removed from the Omnibus TM.				
3. Insert the MS8A into the last slot vacant in the Omnibus TM.				
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IV ACCEPTANCE PROCEDURE				
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Equipment required:				
1. PDP-8A with MS8A R/W Memory				
2. Programmer's Console				
3. Paper Tape Input Device				
4. Diagnostic and Listings				
NOTE: If the Programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.				
Table B				
Acceptance of MS8A: YA or YB				
Program Name	Maindec #	Accept Time	Restrictions	
1-4K MOS Memory Test (RIM)	08-DJMSA-PM	30 min	1K or 2K MS8A R/W Memory	
Acceptance of MS8A: YD				
1-4K MOS Memory Test	08-DJMSA-PM	15 min.	4K MS8A R/W Memory	
4-32K Memory Test	08-DJMMA-PB	15 min.	4K MS8A R/W Memory	

DEC FORM NO. DEC 16-10811-1022-N370
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1-0-1188M8311-0-1 2

M8311-YA
M8311-YB
M8311-YC
M8311-YD
M8311-YE
M8311-YF
M8311-YH
M8311-YJ

QTY	QTY	QTY	QTY	QTY	QTY	QTY	QTY	QTY	QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO
-	-	-	-	24	-	-	-	-	-	E30, E31, E35, E36, E39, E40, E45, E46, E49, E50, E54, E55	IC DEC 2102-1	2111310-0-1	24
-	-	-	-	12	-	-	-	-	-	E50, E50, E63, E64, E67, E68, E73, E74, E77, E78, E82, E83	HANDLE FLIP CHIP MAGENTA	9008337-08	25
-	-	-	-	-	-	-	-	-	-	E31, E36, E40, E46, E50, E55, E59, E64, E68, E74, E78, E83	EYELET GS4-7	9008750	26
3	3	3	3	3	3	3	3	3	3		IC DEC 8223 OR EQUIVALENT	23083A1	27
8	8	8	8	8	8	8	8	8	8	W1-W5	INSULATED JUMPER	9008185	28
1	1	1	1	1	1	1	1	1	1	E8	CAP 6.8 MF 35V 10% TAPT	IC05306	29
5	5	5	5	5	5	5	5	5	5	W1-W5	RES. 3.3K 1/4W 5%	1300439	30
3	3	3	3	3	3	3	3	3	3	C04, C05, C06	SPACER (CABLE CLAMP)	1202704	31
14	14	14	14	14	14	14	14	14	14	R21-R29, R32-R35, R39	RES. 390 1/4W 5%	1300308	32
3	3	3	3	3	3	3	3	3	3		CRYSTAL OSCILLATOR 10 MHZ	1811880-01	33
1	1	1	1	1	1	1	1	1	1	R44			
1	1	1	1	1	1	1	1	1	1	E25			

SWITCH DEFINITIONS

SWI	DEFINITION
SWI-1	EMA2
SWI-2	EMA1
SWI-3	EMA0
SWI-4	SEL0
SWI-5	SEL1
SWI-6	4K
SWI-7	3K
SWI-8	2K
SWI-9	1K
SWI-10	

FIELD SELECTION
'ON' IS 0

STARTING ADDRESS SELECT
'ON' IS 0

MEMORY SIZE SELECT
CORRECT SIZE - 'ON'
OTHERS - 'OFF'

USED FOR TEST ONLY, ALWAYS 'ON'

JUMPER CONFIGURATION

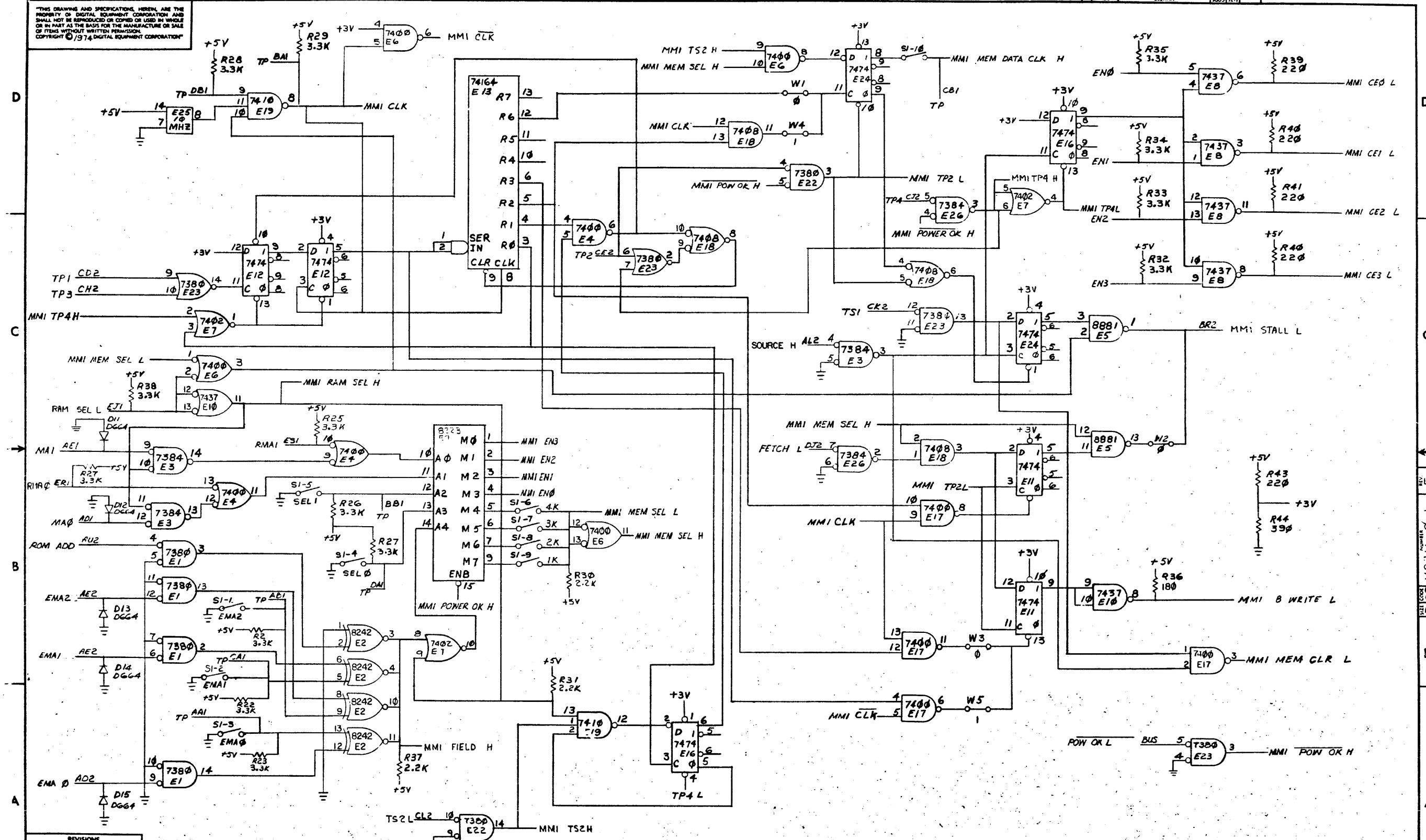
YA, YB, YC AND YD - W1, W2, W3 ARE IN	W4 AND W5 ARE OUT
YE, YF, YH AND YJ - W4 AND W5 ARE IN	W1, W2 AND W3 ARE OUT

REVISIONS		
CHK	CHANGE NO	REV

TITLE	4K X 12 MOS MEMORY	SIZE CODE	D CS M8311-0-1	NUMBER		REV.	F
SCALE	NONE	SHEET	2 OF 6	DIST.			

D CS M8311-0-1

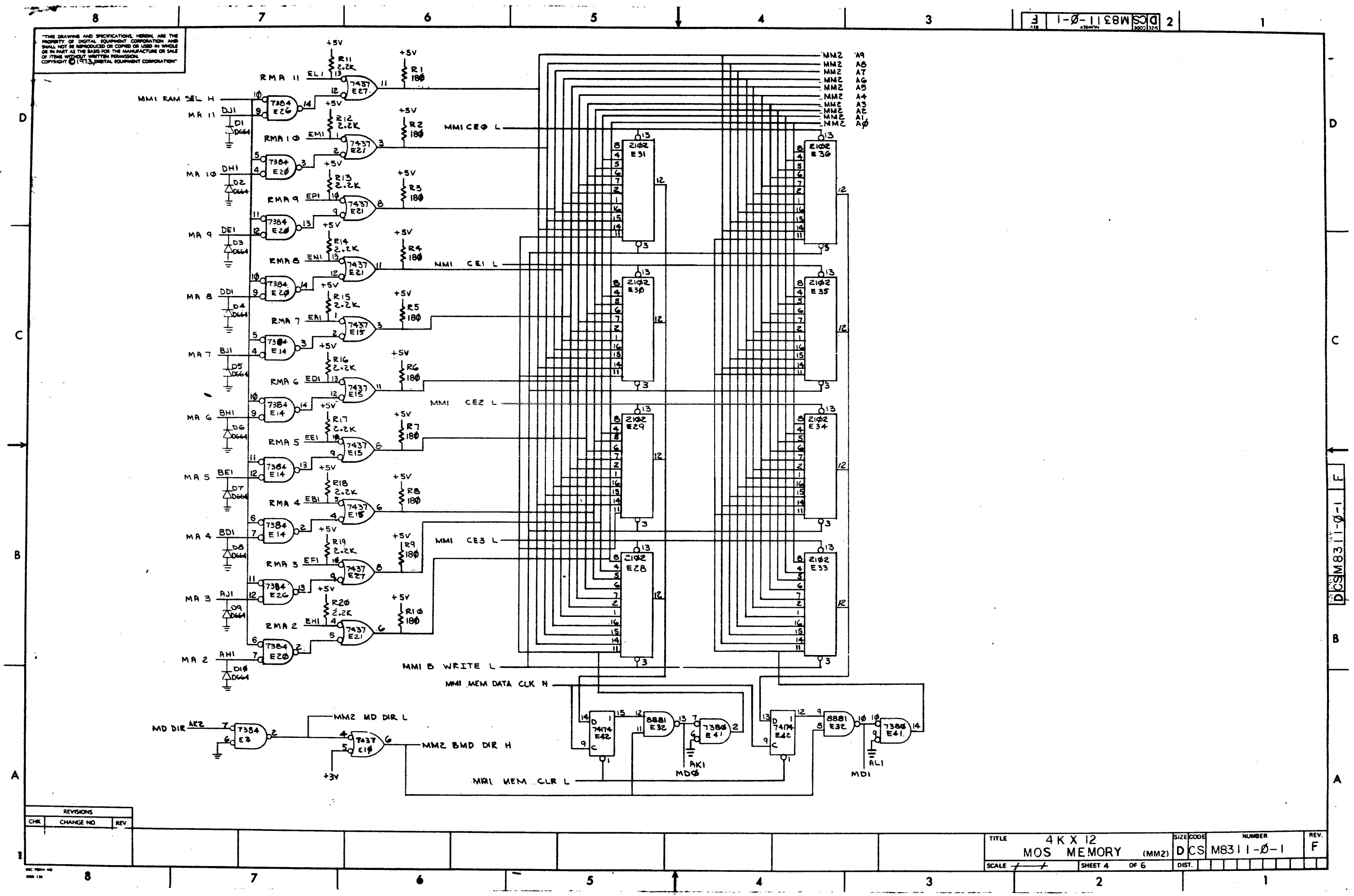
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REVISIONS		
CHK	CHANGE NO.	REV.

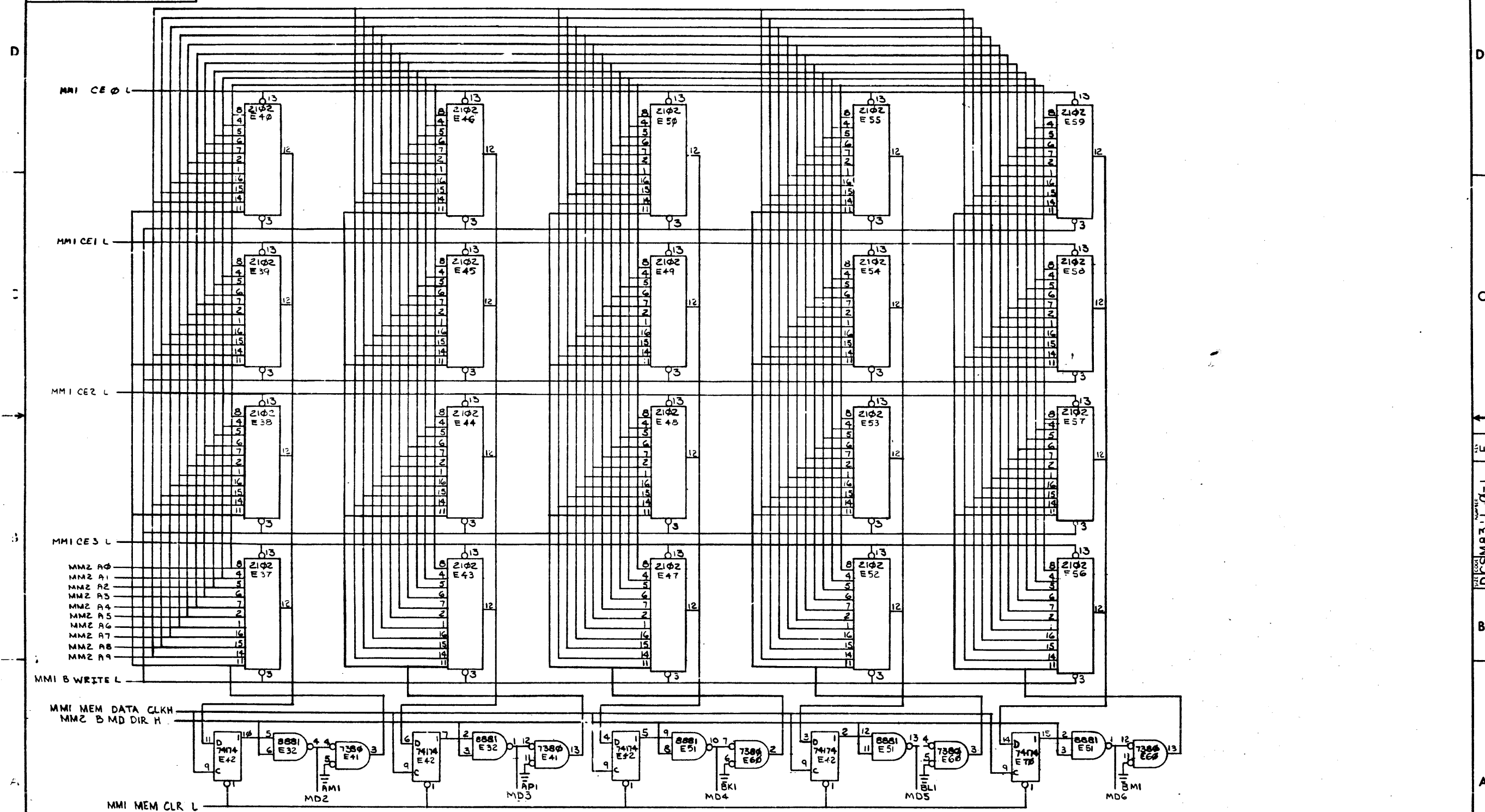
TITLE	4K X 12 MOS MEMORY (MMI)	SIZE CODE	DCS	NUMBER	M8311-0-1	REV.	F
SCALE		SHEET	3	OF	6	DIST.	

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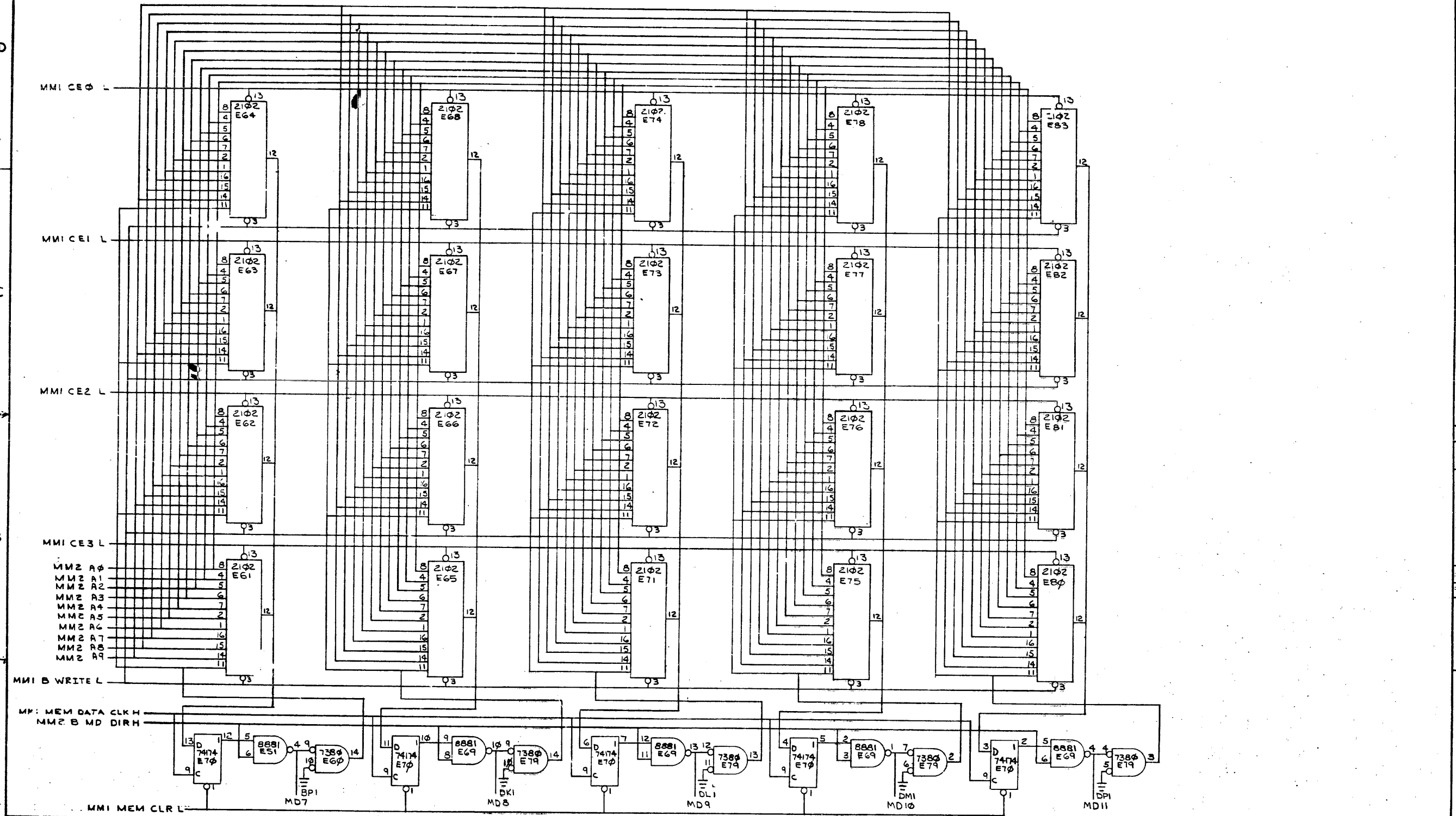


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	4K X 12 MOS MEMORY (MM3)	SIZE CODE	NUMBER	REV.
SCALE	NONE	SHEET	5 OF 6	F
DIST.				

DCS M8311-0-1

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	4K X 12 MOS MEMORY (MN4)	SIZE CODE	NUMBER	REV.
SCALE	NONE	SHEET	6 OF 6	F
DIST.				