

.REM 1

IDENTIFICATION

PRODUCT CODE: AC-F0729-MC  
PRODUCT NAME: CXBMH80 M9312 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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### 1.0 ABSTRACT

THIS MODULE PERFORMS CHECKSUM VERIFICATION OF THE M9312 BOOTSTRAP TERMINATOR. IT COMPARES THE CHECKSUM FOUND IN THE LAST LOCATION OF THE ROMS TO ONE IT CALCULATES FROM ALL OTHER LOCATIONS OTHER THAN THE EXCEPTION LOCATION. LOCATION SRI IS USED TO SELECT WHICH ROMS TO TEST.

### 2.0 REQUIREMENTS

HARDWARE: ANY PDP-11 PROCESSOR WITH A M9312 BOOTSTRAP TERMINATOR AND AT LEAST ONE ROM PLUGGED INTO THE TERMINATOR.

STORAGE: BMH REQUIRES:  
1. DECIMAL WORDS: 237  
2. OCTAL WORDS: 0355  
3. OCTAL BYTES: 732

### 3.0 PASS DEFINITION

ONE PASS CONSISTS OF DOING A CHECKSUM ON EACH ROM 30 (8) TIMES.

### 4.0 EXECUTION TIME

BMH TAKES APPROXIMATELY 35 SECONDS TO COMPLETE A PASS WHEN RUNNING ALONE.

### 5.0 CONFIGURATION REQUIREMENTS

SET THE CORRESPONDING BITS IN SRI TO A"1" FOR THE DESIRED ROMS:

BIT 0 = 1	- DIAG. ROM
BIT 1 = 1	- BOOT ROM IN E-35 (173000-173177)
BIT 2 = 1	- BOOT ROM IN E-33 (173200-173377)
BIT 3 = 1	- BOOT ROM IN E-34 (173400-173577)
BIT 4 = 1	- BOOT ROM IN E-32 (173600-173777)

### 6.0 DEVICE/OPTION SETUP

BMHB DEC/X11 SYSTEM EXERCISER MODULE  
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NONE  
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### 7.0 MODULE OPERATION

THIS MODULE FIRST CHECKS FOR SR1 RO BE NON-ZERO. IF IT IS ZERO THE MODULE TYPES THE MESSAGE:

NO ROMS SELECTED SR 1 = 0

AND THEN NOTIFIES THE MONITOR TO DROP IT. IF SR1 IS NON-ZERO THE CONTROL ROUTINE THEN CALLS THE "GETROM" SUBROUTINE TO LOCATE THE FIRST ROM. IF THE FIRST ROM IS NOT SELECTED THE "GETROM" SUBROUTINE WILL RETURN WITH LOCATION "FIRSTA"=0. IF THE FIRST ROM IS SELECTED "GETROM" WILL RETURN WITH "FIRSTA" CONTAINING THE FIRST ADDRESS TO BE SUMMED, LOCATION "EXCADR" CONTAINING THE EXCEPTION ADDRESS, LOCATION "LASTA" CONTAINING THE LAST ADDRESS TO BE SUMMED AND LOCATION "GOODA" CONTAINING THE ADDRESS OF THE ROMS CHECKSUM.

THE CONTROL ROUTINE THEN CALLS THE SUB-ROUTINE "CHECKR" TO CHECK THE ROM. IF LOCATION "FIRSTA" EQUALS ZERO "CHECKR" WILL JUST RETURN TO THE CONTROL ROUTINE. BUT IF "FIRSTA" IS NON-ZERO "CHECKR" WILL CALL THE ROUTINE "CALSUM". "CALSUM" CALCULATES THE CRC16 OF THE ROM FROM "FIRSTA" TO "LASTA" WITH THE EXCEPTION OF "EXCADR". "CALSUM" RETURNS WITH THE CALCULATED CHECKSUM IN LOCATION "BAD". THE ROUTINE "CHECKR" THEN COMPARES THE GOOD CHECKSUM FROM THE BOARD WITH THE "BAD" SUM AND IF THEY DON'T COMPARE REPORTS AN ERROR:

CHECKSUM ERROR ON M9312 BOOTSTRAP.

THE ROUTINE "CHECKR" THEN RETURNS TO THE CONTROL ROUTINE AND THE PROCESS IS REPEATED FOR EACH ROM.

### 8.0 OPERATING OPTIONS

NONE

### 9.0 NON-STANDARD PRINTOUTS

NONE

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000000* BRMOD <BMHB > 0,0,0,0,0,30,164
000000* MODULE 40020, BMHB 1,0,0,0,0,30,164
; TITLE BMHB DEC/111 SYSTEM EXERCISER MODULE
DDXCOM VFRSINH 6 23-MAY-78
;*****LIST BTN*****
000000* BEGIN:
000000* 046502 041110 040 MODNAM: .ASCII /BMHB / ;MODULE NAME.
000005* 000 ;FLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000006* 000000 ADDR: 0+0 ;1ST DEVICE ADDR
000010* 000000 VECTOR: 0+0 ;1ST DEVICE VECTOR.
000012* 000 BR1: .BYTE PRTV0+0 ;1ST BR LEVEL.
000013* 000 BR2: .BYTE PRTV0+0 ;2ND BR LEVEL.
000014* 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000016* 000000 SR1: OPEN ;SWITCH REGISTER 1.
000020* 000000 SR2: OPEN ;SWITCH REGISTER 2.
000022* 000000 SR3: OPEN ;SWITCH REGISTER 3.
000024* 000000 SR4: OPEN ;SWITCH REGISTER 4.
;*****
000026* 040020 STAT: 40020 ;STATUS WORD.
000030* 000252 INIT: START ;MODULE START ADDR.
000032* 000274 SPOINT: MOOSP ;MODULE STACK POINTER.
000034* 000000 PASCNT: 0 ;PASS COUNTER.
000036* 000030 ICOUNT: 30 ;# OF ITERATIONS PER PASS=30
000040* 000000 ICONFNT: 0 ;LOC TO COUNT ITERATIONS
000042* 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044* 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046* 000000 SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050* 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052* 000000 SVSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054* 000000 RANRND: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056* 000000 CONFIG: 0 ;RESERVED FOR MONITOR USE
000056* 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060* 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062* 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064* 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066* 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070* 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072* 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074* 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076* 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100* 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102* 000000 SRAHR: OPEN ;ADDR OF GOOD DATA, OR
000104* 000000 ACSR: OPEN ;CONTENTS OF CSR.
000104* 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
000104* 000000 ASAT: OPEN ;STATUS REG CONTENTS.
000106* 000000 ERTP: OPEN ;TYPE OF ERROR
000106* 000000 ASB: OPEN ;EXPECTED DATA.
000110* 000000 AWAS: OPEN ;ACTUAL DATA.
000112* 000252 RSTRT: RFRSTRT ;RESTART ADDR/PSS AFTER END OF PASS
000114* 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000116* 000000 WDFP: OPEN ;WORDS FROM MEMORY PER ITERATION
000120* 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122* 000164 IDNUM: 164 ;MODULE IDENTIFICATION NUMBFR=164
000040* 000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
;*****LIST

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```

;*****WORD 0*****
;*****LIST*****
;*****FNDR*****
000224*
200
201 000224* 000000
202 000226* 000000
203 000230* 000000
204 000232* 000000
205 000234* 000000
206 000236* 000000
207 000240* 000000
208 000242* 000236*
209 000244* 000234*
210 000246* 000232*
211 000250* 177777
;*****
FIRSTA: 0
EXCADR: 0
LASTA: 0
CROMP: 0
GOODA: 0
BAD: 0
COUNT: 0
BADADR: #BAD ;+ADDRESS POINTER FOR ERROR CALL
CRCER1: #GOODA ;+ADDRESS OF EXPECTED CHECKSUM
;#ADADR ;+ADDRESS OF ACTUAL CHECKSUM
-1 ;+MESSAGE TERMINATOR

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212
213 000252- 005767 177540 START: TST SR1 ;ARE ANY ROMS TO BE CHECKED
214 000252- 005767 177540 RESTR: BNE 2S ;IF YPS DD TEST
215 000256- 001005 MSGNS,BEGIN,MSG1 ;ASCII MESSAGE CALL WITH COMMON HEADER
216 000260- 104403 000000- 000622- ENDS,BEGIN ;
217 000272- 012767 000001 177732 2S: MOV #1, CROMP ;INITIALIZE CURRENT ROM POINTER
218 000300- 022767 000040 177724 3S: CMP #40, CROMP ;HAVE ALL ROMS BEEN TESTED
219 000306- 001405 BRQ 4S ;IF YES WE'RE DONE
220 000310- 004767 000014 JSR PC, GETROM ;GO GET ROM ADDRESSES
221 000320- 000767 BR 3S ;GO TEST ROM
222 000322- 104413 000000- 4S: ENDDTS,BEGIN ;CONTINUE TESTING
223 000322- 104413 000000- ;SIGNAL END OF ITERATION.
224 000326- 000761 BR 2S ;MONITOR SHALL TEST END OF PASS
225
226
227 000330- 005067 177670 GETROM: CLR FIRSTA ;CLEAR FIRST CHECKSUM ADDRESS
228 000334- 036767 177672 177454 CROMP, SR1 ;IS CURRENT ROM TO BE TESTED
229 000342- 001446 BRQ 4S ;IF NO THEN JUST EXIT
230 000344- 016700 177662 MOV CROMP, R0 ;GET CURRENT ROM POINTER TO WORK ON
231 000350- 000241 CLC ;CARRY BIT TO BE USED AS INDICATOR
232 000352- 006000 ROR R0 ;IF C-BIT NOT SET THEN BOOT ROM
233 000354- 103014 BCC 1S ;BEING TEST SO GO SET UP FOR BOOT ROM
234 000356- 012767 165000 177640 MOV #165000,FIRSTA ;IF DIAG. ROM SET UP ITS FIRST ADDRESS
235 000364- 005067 177636 CLR EXCADR ;NO EXCEPTION ADDRESS IN DIAG.ROM
236 000370- 012767 165775 177632 MOV #165775,LASTA ;SET UP LAST ADDRESS
237 000376- 012767 165776 177630 MOV #165776,GOODA ;SET UP CHECKSUM ADDRESS OF DIAG. ROM
238 000400- 012701 173000 1S: MOV #173000,R1 ;GET FIRST BOOT ROM ADDRESS FOR WORKING
239 000412- 006000 ROR R0 ;IF C-BIT SETS WE'VE FOUND OUR ROM
240 000414- 103403 000200 2S: BCS 3S ;SO GO SET UP FOR ADDRESS FOR IT
241 000416- 012767 177554 ADD #200, R1 ;IF C-BIT CLEAR UPDATE TO NEXT ROM
242 000422- 000773 BR 1S ;AND CHECK IF ITS THE ONE
243 000424- 010167 177574 3S: MOV R1, FIRSTA ;SET UP FIRST ADDRESS
244 000430- 062701 000024 ADD #24, R1 ;CALCULATE EXCEPTION ADDRESS
245 000434- 012767 177566 MOV R1, EXCADR ;AND STORE IT
246 000440- 062701 000150 ADD #151, R1 ;CALCULATE LAST ADDRESS
247 000444- 010167 177560 MOV R1, LASTA ;AND STORE IT
248 000450- 062701 000001 ADD #1, R1 ;CALCULATE GOOD CHECKSUM ADDRESS
249 000454- 012767 177554 MOV R1, GOODA ;AND STORE IT
250 000460- 006167 177546 4S: ROL CROMP ;UPDATE CROMP TO NEXT ROM
251 000464- 000207 RTS ;RETURN
252
253
254 000466- 005767 177532 CHECKR: TST FIRSTA ;IS FIRST ADDRESS ZERO
255 000472- 001416 BFO 1S ;IF YES DON'T TEST JUST EXIT
256 000474- 004767 000032 177530 JSR PC, CALSUM ;IF NOT GO CALCULATE CHECKSUM
257 000502- 001410 CMP #GOODA, BAD ;DO BOTH CHECKSUMS COMPARE
258 000510- 104403 000000- 000626- BRQ 1S ;IF YES EXIT
259 000516- 005067 177364 MSGNS,BEGIN,MSG2 ;ASCII MESSAGE CALL WITH COMMON HEADER
260 000522- 104405 000000- 000244- CLR ERRYP *****
261 HRDRFRS,REGIN,CRCER1 ;CHECKSUM ERROR
262 *****

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268 000530- 000207 1S: RTS PC ;RETURN
269
270
271 000532- 016700 177466 CALSUM: MOV FIRSTA, R0 ;GET STARTING ADDRESS
272 000536- 005005 CLR R5 ;CLEAR CRC WORD
273 000540- 012003 LOOP: MOV (R0)+, R3 ;GET BYTE
274 000544- 017102 CRCLOP: MOV #16,, R2 ;SET BIT COUNT
275 000546- 000241 CLC ;CLEAR CARRY
276 000550- 006005 ROR R5 ;ROTATE CRC WORD
277 000552- 006003 ROR R3 ;ROTATE BYTE
278 000554- 102008 BVC 1S
279 000556- 012701 120001 MOV #120001,R1 ;GONE THROUGH HERE 16 TIMES
280 000562- 040501 BIC R5, R1 ;IF NO DD IT AGAIN
281 000564- 042705 120001 BIC #120001,R5 ;IS NEXT ADDR. AN EXCEPTION ADDR.
282 000570- 050105 JSR R1, R5 ;NO CONTINUE
283 000572- 005302 1S: DEC R2 ;NO CONTINUE
284 000574- 003364 BGT CRCLOP ;YES ADD 2 TO ADDR. TO SKIP IT
285 000576- 020067 177424 CMP R0, EXCADR ;HAVE WE SUM THE WHOLE ROM
286 000600- 001901 BNE 2S ;IF NO CONTINUE WITH NEXT BYTE
287 000604- 005720 TST (R0)+ ;IF YES STORE CALCULATED CHECKSUM
288 000606- 020067 177416 2S: CMP R0, LASTA ;IF YES STORE CALCULATED CHECKSUM
289 000612- 101757 LOOP ;IF NO CONTINUE WITH NEXT BYTE
290 000614- 010527 MOV R0, BAD ;IF YES STORE CALCULATED CHECKSUM
291 000620- 000207 RTS ;AND EXIT
292
293
294 000622- 000632- MSG1: YOROMS
295 000624- 177777 177777
296 000626- 000664- MSG2: CRCERR
297 000630- 177777 177777
298
299 000632- 020045 047516 051040 NOROMS: .ASCIZ "% NO ROMS SELECTED SRI=0%"
300 000640- 046517 020123 042523
301 000644- 042514 052103 042105
302 000654- 051440 030522 030075
303
304 000662- 000045 044103 041505 CRCERR: .ASCIZ "% CHECKSUM ERROR ON #9312 ROOTSTRAP%"
305 000672- 051513 046525 042440
306 000700- 020112 051117 047440
307 000706- 020116 034515 030463
308 000714- 020062 047502 052117
309 000722- 052123 040522 022520
310 000730- 000 030
311 000732- .EVEN
312 000001 .END

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0011

SVR2	000066R	175#
SVR3	000070R	176#
SVR4	000072R	177#
SVR5	000074R	178#
SVR6	000076R	179#
SYSCNT	000052R	168#
TRPDDF=	000022	200#
VECTOP	000010R	149#
WASADR	000104R	183#
WDFR	000116R	190#
WDT0	000114R	189#
XPLAG	000005R	147#
.	= 000732R	311#

. AHS. 000000 000  
000732 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XBMH80, XBMH80/SOL/CRF:SYM=DDXCON, XBMH80  
RUN-TIME: 111.2 SECONDS  
RUN-TIME RATIO: 24/2-8.6  
CORE USED: 7K (13 PAGES)