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.REM \*

IDENTIFICATION  
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PRODUCT CODE: AC-8954E-MC  
PRODUCT NAME: CZM9AE0 BOOTSTRAP/TERMINATOR (M9301, M9400)  
PROGRAM DATE: APRIL, 1979  
MAINTAINER: DIAGNOSTIC ENGINEERING

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\* SUMMARY OF OPERATING INSTRUCTIONS \*  
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THE FOLLOWING PROCEDURE CAN BE USED TO RUN THIS DIAGNOSTIC  
IN A DEVICE VERIFICATION MODE. IF THE PROGRAM DOES NOT  
RUN SUCCESSFULLY CONSULT THE FOLLOWING DOCUMENT FOR ASSISTANCE.

OPERATING PROCEDURE:

1. LOAD THE PROGRAM USING NORMAL PROCEDURES.
2. LOAD ADDRESS 200
3. SET SWITCH REGISTER TO SELECT THE PROPER  
VERSION OF THE ROM UNDER TEST.  
(SEE INSTRUCTIONS IF A SOFTWARE SWITCH REGISTER  
IS TO BE USED.)
4. PRESS START
5. THE PROGRAM SHOULD TAKE ABOUT 1 SEC TO  
COMPLETE THE TEST AND PRINT: "END OF TEST".
6. IF THE PROGRAM DOES NOT RUN AS DESCRIBED  
ABOVE, CONSULT THE FULL OPERATING INSTRUCTIONS  
WHICH FOLLOW.

\* \* CAUTION \* \*

BECAUSE THE CONTENTS READ FROM LOCATION 773024  
OF THE M9301 OPTION IS CONFIGURATION DEPENDANT(SWITCH  
REGISTER DEPENDANT), THIS LOCATION IS NOT INCLUDED IN  
THE DATA CHECK.  
THIS LOCATION CAN BE VERIFIED BY EXAMINING IT OR BY USING  
THE ALTERNATE STARTING ADDRESS(SEE SECTION 2.1.3) TO  
PRINT OUT THIS LOCATION.

91  
92 1.0 GENERAL PROGRAM INFORMATION  
93 -----  
94  
95 1.1 PROGRAM PURPOSE  
96 -----  
97  
98 THIS DIAGNOSTIC PROGRAM IS INTENDED TO VERIFY THE  
99 ROM CONTENTS OF THE ROM BOOTSTRAP MODULES. THE PROGRAM  
100 COMPUTES AND CHECKS A CYCLIC REDUNDANCY CHARACTER  
101 AND A LONGITUDINAL PARITY CHARACTER FOR THE CONTENTS  
102 OF THE ROM STORAGE AVAILABLE IN AN M9301 OR M9400 MODULE.  
103  
104 A SEPARATE ROUTINE INCLUDED ALLOWS THE USER TO TYPE  
105 THE CONTENTS OF THE ROM STORAGE ON THE TELETYPE AS  
106 AN AID TO DEBUGGING.  
107  
108 1.2 SYSTEM REQUIREMENTS  
109 -----  
110  
111 1.2.1 HARDWARE  
112 -----  
113  
114 PDP/11 PROCESSOR  
115 TELETYPE OR EQUIVALENT  
116 4K OF MEMORY  
117 M9301 OR M9400 MODULE  
118  
119 1.2.2 SOFTWARE  
120 -----  
121  
122 THIS PROGRAM IS WRITTEN TO BE RUN AS A STAND-ALONE PROGRAM.  
123 HOWEVER, THE PROGRAM IS DESIGNED TO RUN UNDER AUTOMATED  
124 PRODUCT TEST SYSTEM (APT) IN ALL THREE MODES.  
125  
126 THE PROGRAM CAN ALSO BE RUN UNDER THE ACT 11 MONITOR.  
127  
128 1.3 RELATED DOCUMENTS AND STANDARDS  
129 -----  
130  
131 DIAGNOSTIC ENGINEERING STANDARDS AND CONVENTIONS PROGRAMMING PRACTICES  
132 DOCUMENT NO. 175-003-009-00  
133  
134 APT INTERFACE SPECIFICATION, REV. 13  
135  
136 1.4 DIAGNOSTIC HIERARCHY PREREQUISITES  
137 -----  
138  
139 NONE, HOWEVER THE CPU IS ASSUMED TO BE FUNCTIONING  
140  
141 1.5 FAILURE ASSUMPTIONS  
142 -----  
143  
144 THE PROCESSOR IS ASSUMED TO BE FUNCTIONING PROPERLY.

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2.0 OPERATING INSTRUCTIONS  
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2.1 LOADING AND STARTING PROCEDURES  
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2.1.1 LOADING  
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USE NORMAL PROCEDURES FOR LOADING DIAGNOSTIC PROGRAMS.

2.1.2 NORMAL START  
-----

1. LOAD SOFTWARE SWITCH REGISTER (IF USED) TO SELECT THE ROM VERSION UNDER TEST. (SEE 2.3)

2. LOAD ADDRESS 200

3. SET HARDWARE SWITCH REGISTER (IF AVAILABLE) TO SELECT THE ROM VERSION UNDER TEST (SEE 2.3).

4. START

2.1.3 OPTIONAL START  
-----

THE OPTIONAL STARTING ADDRESS IS USED TO TYPE OUT THE CONTENTS OF THE ROM FOR USE IN VISUAL VERIFICATION OR AS A DEBUGGING TOOL.

USE THE SAME PROCEDURE AS A NORMAL START EXCEPT USE ADDRESS 210 IN STEP 2.

2.2 SPECIAL ENVIRONMENTS  
-----

THIS PROGRAM IS WRITTEN TO COMPLY WITH ALL THE REQUIREMENTS OF THE APT INTERFACE SPECIFICATION. IT WILL RUN UNDER APT IN EITHER QUICK VERIFY, PROGRAM OR RUN-TIME MODES.

THIS PROGRAM IS WRITTEN TO COMPLY WITH THE ACT11/XXDP INTERFACE REQUIREMENTS.

WHEN RUNNING IN ACT11 QUICK VERIFY OR XXDP CHAIN MODE (LOC. 42 NOT 0), OR APT QUICK VERIFY AND PROGRAM MODE THE PROGRAM ATTEMPTS TO RUN WITHOUT OPERATOR INTERVENTION OR SWITCH REGISTER SELECTION. THE COMPUTED CRC IS COMPARED AGAINST THE CRC FOR ALL KNOWN VERSIONS OF THE ROM BOOTSTRAP. WHEN A MATCH IS FOUND THE VERSION OF THE MODULE IS TYPED FOR VISUAL VERIFICATION.

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2.3 PROGRAM OPTIONS

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THE PROGRAM AUTOMATICALLY CHECKS FOR THE PRESENCE OF  
A HARDWARE SWITCH REGISTER. IF NO RESPONSE IS FOUND WHEN  
ADDRESSING THE HARDWARE SWR (177570), THE ADDRESS  
OF THE SOFTWARE SWR (176) IS SUBSTITUTED.

FOR PROCESSORS WITH NO HARDWARE SWITCH REGISTER, THE OPERATOR  
SHOULD SET THE DESIRED SWITCH VALUE IN LOCATION 176

WARNING... IN ORDER TO ALLOW TESTING OF M7942-YB BOARDS ON THE VT71,  
IF LOCATION 176 IS SET TO 6, THE SOFTWARE SWITCH REGISTER WILL BE  
USED REGARDLESS OF HARDWARE SWITCH REGISTER AVAILABILITY.

2.3.1 SWITCH SELECTION

-----  
THE SWITCH REGISTER (HARDWARE OR SOFTWARE) IS USED TO SELECT  
THE VERSION OF THE ROM BOOTSTRAP TO BE TESTED  
ACCORDING TO THE FOLLOWING TABLE. NOTE: THESE SETTINGS ARE  
OCTAL NUMBERS. THEY ARE NOT PARTICULAR SWITCHES SET TO A  
ONE. FOR EXAMPLE, TO SELECT THE M9301-YH VERSION, SET  
SWITCHES #3 AND #1 IN THE SWITCH REGISTER. THIS  
CORRESPONDS TO AN OCTAL 12.

SWR	MODULE VERSION
---	-----
1	M9301-YA
2	M9301-YB
3	M9301-YC
4	M9400-YA (OR YC)
5	M9301-YF
6	M7942-YB
7	M9301-YD
10	M9400-YH (OR YK)
11	M9311 REVISION * --- SEE NOTE BELOW ---
12	M9301-YH
13	M9301-YE
14	M9301-YJ
15	M9400-YN
16	M9311 REVISION A *** SEE NOTE BELOW ***

NOTE: FOR M9311 ONLY.....  
IF ROM IN LOCATION E-5 IS #23-524A9 , THEN  
YOU HAVE REVISION \*. (I.E. USE SWR 11 SETTING)

IF ROM IN LOCATION E-5 IS #23-688A9 , THEN  
YOU HAVE REVISION A .( I.E. USE SWR 16 SETTING)

IF THE CRC AND LPC FOR NEW VERSIONS ARE KNOWN BUT  
NOT IN THE ABOVE TABLE, SET THE SWITCH REGISTER TO ZERO  
AND ANSWER THE TELETYPE DIALOG.

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TO DETERMINE THE CRC AND LPC FOR A NEW VERSION, START THE DIAGNOSTIC AT 200 WITH SWP=0, ANSWER 0 TO THE REQUESTS FOR THE LPC AND CRC, THE RESULTING MESSAGES WILL INDICATE THE CORRECT FUTURE RESPONSES FOR CRC AND LPC PROVIDED THE TEST IS RUN ON A KNOWN-GOOD MODULE.

2.3.2 TELETYPE DIALOG

-----  
SEVERAL QUESTIONS ARE ASKED OF THE OPERATOR IN ORDER TO OBTAIN SUFFICIENT INFORMATION FOR TESTING A ROM MODULE NOT PREVIOUSLY SUPPORTED IN THE DIAGNOSTIC. THE DIALOG IS INITIATED IF THE PROGRAM IS STARTED WITH THE SWR = 0. ALL RESPONSES ARE IN OCTAL AND TERMINATED BY A CARRIAGE RETURN.

ALL RESPONSES ARE CHECKED FOR VALID OCTAL NUMBERS. IF AN ILLEGAL CHARACTER IS TYPED, THE PROGRAM WILL TYPE A "?", CARRIAGE RETURN-LINE FEED AND AWAIT THE PROPER INPUT.

IF A MISTAKE IS NOTICED BEFORE THE CARRIAGE RETURN IS USED TO TERMINATE THE INPUT, A RUBOUT CAN BE USED TO DELETE MISTYPED INPUT.

1. TYPE CRC VALUE:

THIS REQUESTS THE VALUE OF THE CYCLIC REDUNDANCY CHECK PREVIOUSLY CALCULATED FOR THIS VERSION OF THE ROM MODULE. IT IS THE VALUE AGAINST WHICH THE UNIT UNDER TEST'S CRC WILL BE COMPARED.

2. TYPE LPC VALUE:

THIS REQUESTS THE VAULE OF THE LONGITUDINAL PARITY CHECK PREVIOUSLY CALCULATED FOR THIS VERSION OF THE ROM MODULE. IT IS THE VALUE AGAINST WHICH THE UNIT UNDER TEST'S LPC WILL BE COMPARED.

3. TYPE STARTING ADDR. OF 1ST ROM ADDR. SPACE:

THIS QUESTION REFERS TO THE FACT THAT THE ROM SPACE IN AROM BOOTSTRAP MODULE IS DIVIDED INTO 2 DISTINCT ADDRESS SPACES. TYPE THE STARTING ADDRESS OF THE 1ST RANGE OF ADDRESSES. THE STANDARD M9301 & M9400 BEGIN AT 173000.

4. TYPE LENGTH (BYTES) OF 1ST ROM ADDR. SPACE:

THIS REQUESTS THE LENGIH OF THE 1ST GROUP OF ROM ADDRESSES IN BYTES. THE STANDARD M9301 & M9400 HAVE AN INITIAL ADDRESS SPACE OF 1000 BYTES. IF THIS SECTION OF ADDRESSES IS NOT USED BY THIS VERSION, ANSWER 0 TO THIS QUESTION.

5. TYPE STARTING ADDR. OF 2ND ROM ADDR. SPACE:

THIS REFERS TO THE FIRST ADDRESS IN THE SECOND DISTINCT GROUP OF ROM ADDRESSES. THE RESPONSE FOR A STANDARD M9301 & M9400 WOULD BE 165000.

6. TYPE LENGTH (BYTES) OF 2ND ROM ADDR. SPACE:

THIS REQUESTS THE LENGTH OF THE 2ND GROUP OF ROM ADDRESSES IN BYTES. THE STANDARD M9301 & M9400 HAVE A SECOND ADDRESS SPACE

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OF 1000 BYTES. IF THIS SECTION OF ADDRESSES IS NOT USED  
BY THIS VERSION, ANSWER 0 TO THIS QUESTION.

2.4 EXECUTION TIMES

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THE DIAGNOSTIC COMPLETES 1 PASS IN LESS THAN 1 SEC.  
ONCE THE INPUT DIALOG HAS BEEN COMPLETED.  
THE PROGRAM WILL HALT UPON COMPLETION; HOWEVER, IF RUNNING  
UNDER APT THE PROGRAM WILL CYCLE CONTINUOUSLY.

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3.0 ERROR INFORMATION  
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WHEN THE DIAGNOSTIC DETECTS A DISCREPANCY BETWEEN THE EXPECTED AND COMPUTED CRC OR LPC BOTH ARE PRINTED ON THE TELETYPE.

ANY DISCREPANCY IN THE LPC CAN ASSIST IN ISOLATING THE PROBLEM TO THE ROM IC.

UNDER APT THE ERROR IS INDICATED BY DEPOSITING ERROR INFORMATION IN THE APT MAILBOX BEFORE HALTING.

4.0 PROGRESS REPORTS  
-----

AT THE END OF EACH PASS THE PROGRAM INCREMENTS TO THE LOCATION \$PASS WHICH IS IN THE APT MAILBOX. THIS LOCATION WILL ALWAYS CONTAIN THE NUMBER OF PASSES COMPLETED. \$PASS IS RESET WITH EVERY RESTART.

ADDITIONALLY, THE MESSAGE "END OF TEST" IS PRINTED ON THE CONSOLE TELETYPE AFTER EACH PASS. NORMALLY ONLY ONE PASS NEEDS TO RUN TO VERIFY THE MODULE.

5.0 TROUBLE SHOOTING  
-----

THE ALGORITHM FOR COMPUTING THE CRC IS THE SAME AS THAT USED ON 9-TRACK MAGNETIC TAPE WITH ODD PARITY. THE CRC IS CALCULATED ON A BYTE-BY-BYTE BASIS. WHILE THE ALGORITHM IS SUCCESSFUL IN DETECTING MULTIPLE ERRORS, ITS USE AS A DEBUGGING AID IS LIMITED.

THE LPC IS CALCULATED BY ASSEMBLING THE XOR OF EVERY WORD IN THE ROM. WHILE ONLY USEFUL IN CATCHING AN ODD NUMBER OF ERRORS IN EACH BIT POSITION, IT IS VERY USEFUL IN ISOLATING THE PROBLEM TO A CHIP. BY LOCATING WHICH BIT POSITIONS ARE IN DISCREPANCY, THE CORRESPONDING ROM CHIPS CAN BE ISOLATED.

IF NO OTHER CLUES CAN BE OBTAINED, START THE PROGRAM AT 210 AND COMPARE THE PRINTOUT OF THE CODE WITH THE LISTING FOR THE VERSION BEING TESTED.

\* \* CAUTION \* \*

BECAUSE THE CONTENTS READ FROM LOCATION 773024 OF THE M9301 OPTION IS CONFIGURATION DEPENDANT(SWITCH REGISTER DEPENDANT), THIS LOCATION IS NOT INCLUDED IN THE DATA CHECK.  
THIS LOCATION CAN BE VERIFIED BY EXAMINING IT OR BY USING THE ALTERNATE STARTING ADDRESS(SFE SECTION 2.1,3) TO PRINT OUT THIS LOCATION.

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6.0 LISTING  
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.ENABLE ABS
.LIST ME
.MLIST MC,MD,CMD
$SWR=0
.SRTTL BASIC DEFINITIONS

;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR ;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;BASIC DEFINITION OF SCOPE CALL

;*MISCELLANEOUS DEFINITIONS
HT= 11 ;CODE FOR HORIZONTAL TAB
LF= 12 ;CODE FOR LINE FEED
CR= 15 ;CODE FOR CARRIAGE RETURN
CRLF= 200 ;CODE FOR CARRIAGE RETURN+LINE FEED
PS= 177776 ;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLM= 177774 ;STACK LIMIT REGISTER
PIRQ= 177772 ;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;HARDWARE SWITCH REGISTER
DDISP= 177570 ;HARDWARE DISPLAY REGISTER

;*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0 ;GENERAL REGISTER
R1= %1 ;GENERAL REGISTER
R2= %2 ;GENERAL REGISTER
R3= %3 ;GENERAL REGISTER
R4= %4 ;GENERAL REGISTER
R5= %5 ;GENERAL REGISTER
R6= %6 ;GENERAL REGISTER
R7= %7 ;GENERAL REGISTER
SP= %6 ;STACK POINTER
PC= %7 ;PROGRAM COUNTER

;*PRIORITY LEVEL DEFINITIONS
PR0= 0 ;PRIORITY LEVEL 0
PR1= 40 ;PRIORITY LEVEL 1
PR2= 100 ;PRIORITY LEVEL 2
PR3= 140 ;PRIORITY LEVEL 3
PR4= 200 ;PRIORITY LEVEL 4
PR5= 240 ;PRIORITY LEVEL 5
PR6= 300 ;PRIORITY LEVEL 6
PR7= 340 ;PRIORITY LEVEL 7

;*"SWITCH REGISTER" SWITCH DEFINITIONS
SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
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441 000100 SW06= 100
442 000440 SW05= 40
443 000020 SW04= 20
444 000010 SW03= 10
445 000004 SW02= 4
446 000002 SW01= 2
447 000001 SW00= 1
448 .EQUIV SW09,SW9
449 .EQUIV SW08,SW8
450 .EQUIV SW07,SW7
451 .EQUIV SW06,SW6
452 .EQUIV SW05,SW5
453 .EQUIV SW04,SW4
454 .EQUIV SW03,SW3
455 .EQUIV SW02,SW2
456 .EQUIV SW01,SW1
457 .EQUIV SW00,SW0
458
459 ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
460 BIT15= 100000
461 BIT14= 40000
462 BIT13= 20000
463 BIT12= 10000
464 BIT11= 4000
465 BIT10= 2000
466 BIT09= 1000
467 BIT08= 400
468 BIT07= 200
469 BIT06= 100
470 BIT05= 40
471 BIT04= 20
472 BIT03= 10
473 BIT02= 4
474 BIT01= 2
475 BIT00= 1
476 .EQUIV BIT09,BIT9
477 .EQUIV BIT08,BIT8
478 .EQUIV BIT07,BIT7
479 .EQUIV BIT06,BIT6
480 .EQUIV BIT05,BIT5
481 .EQUIV BIT04,BIT4
482 .EQUIV BIT03,BIT3
483 .EQUIV BIT02,BIT2
484 .EQUIV BIT01,BIT1
485 .EQUIV BIT00,BIT0
486
487 ;*BASIC "CPU" TRAP VECTOR ADDRESSES
488 ERRVEC= 4 ;TIME OUT AND OTHER ERRORS
489 RESVEC= 10 ;RESERVED AND ILLEGAL INSTRUCTIONS
490 TRIVEC=14 ;"T" BIT
491 TRIVEC= 14 ;TRAP TRAP
492 BPTVEC= 14 ;BREAKPOINT TRAP (BPT)
493 IOTVEC= 20 ;INPUT/OUTPUT TRAP (IOI) **SCOPE**
494 PWRVEC= 24 ;POWER FAIL
495 EMTVEC= 30 ;EMULATOR TRAP (EMT) **ERROR**
496 TRAPVEC=34 ;"TRAP" TRAP
```

```
497 000000 TKVEC= 60 ;TTY KEYBOARD VECTOR
498 000064 TPVEC= 64 ;TTY PRINTER VECTOR
499 000240 PIRQVEC=240 ;PROGRAM INTERRUPT REQUEST VECTOR
500 000000 .=0
501 .SBTTL TRAP CATCHER
502 .=0
503 000000 ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A "+,2,HALT"
504 ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
505 ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
506
507 .=174
508 000174 000000 DISPREG; .WORD 0 ;SOFTWARE DISPLAY REGISTER
509 000176 000000 SWREG; .WORD 0 ;SOFTWARE SWITCH REGISTER
510 000200 .=200
511 000200 000067 000624 CLP TYP0UT
512 000204 000167 000666 JMP START
513 000210 012767 000001 000612 MOV #1,TYP0UT
514 000216 000167 000654 JMP START
515
516 177776 PS=177776
517 000014 TRAPVEC=34
518
519 001000 .=1000
520 001000 177570 SWR; 177570
521 001002 177570 DISPLAY; 177570
522 001004 173000 ROMSA1; 173000
523 001006 001000 DATLN1; 512,
524 001010 165000 ROMSA2; 165000
525 001012 001000 DATLN2; 512,
526 001014 000000 XORS; 0
527 001016 000000 EXCRC; 0
528 001020 000000 EXLPC; 0
529 001022 000000 ACICRC; 0
530 001024 000000 ACTLPC; 0
531 001026 000000 PARCNT; 0
532 001030 000000 TYP0UT; 0
533
534 .SBTTL ACT11 HOOKS
535
536 ;*****
537 ;HOOKS REQUIRED BY ACT11
538 $SVPC=, ;SAVE PC
539 .=46 ;
540 000046 002060 $ENDAD ;1)SET LOC,46 TO ADDRESS OF $ENDAD IN $SEUP
541 000052 .=52 ;
542 000052 000000 .WORD 0 ;2)SET LOC,52 TO ZERO
543 001032 .=$SVPC ; RESTORE PC
544 .SBTTL APT PARAMETER BLOCK
545
546 ;*****
547 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
548 ;*****
549 001032 .$X=, ;SAVE CURRENT LOCATION
550 000024 .=24 ;SET POWER FAIL TO POINT TO START OF PROGRAM
551 000024 000200 200 ;FOR APT START UP
552 000044 .=44 ;POINT TO APT INDIRECT ADDRESS PNTR,
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553 000044 001032 $APTHDR ;;POINT TO APT HEADER BLOCK
554 001032 .,SX ;;RESET LOCATION COUNTER
555 *****
556 ;;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
557 ;;INTERFACE SPEC.
558
559 001032 $APTHDR
560 001032 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
561 001034 $MBADR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
562 001036 $BSTM: .WORD 2 ;;RUN TIME OF LONGEST TEST
563 001040 $PASTM: .WORD 2 ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
564 001042 $SUNITM: .WORD 0 ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
565 001044 $SUNITL: .WORD $*END-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
566 $SUNITL APT MAILBOX-ETABLE
567
568 *****
569 .EVEN
570 $MAIL: .WORD 0 ;;APT MAILBOX
571 001046 $MSGTY: .WORD $MSGTY ;;MESSAGE TYPE CODE
572 001050 $FATAL: .WORD $FATAL ;;FATAL ERROR NUMBER
573 001052 $TESTN: .WORD $TESTN ;;TEST NUMBER
574 001054 $PASS: .WORD $PASS ;;PASS COUNT
575 001056 $DEVCT: .WORD $DEVCT ;;DEVICE COUNT
576 001060 $SUNIT: .WORD $SUNIT ;;I/O UNIT NUMBER
577 001062 $MSGADR: .WORD $MSGADR ;;MESSAGE ADDRESS
578 001064 $MSGLEN: .WORD $MSGLEN ;;MESSAGE LENGTH
579 001066 $ETABLE: .WORD $ETABLE ;;APT ENVIRONMENT TABLE
580 001068 $ENV: .BYTE $ENV ;;ENVIRONMENT BYTE
581 001070 $ENVM: .BYTE $ENVM ;;ENVIRONMENT MODE BITS
582 001072 $SWREG: .WORD $SWREG ;;APT SWITCH REGISTER
583 001074 $USWR: .WORD $USWR ;;USER SWITCHES
584 001076 $CPUOPT: .WORD $CPUOPT ;;CPU TYPE,OPTIONS
585 ;;
586 ;; 11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
587 ;; 11/70=06,PD0=07,0=10
588 ;; BIT 10=REAL TIME CLOCK
589 ;; BIT 9=FLOATING POINT PROCESSOR
590 ;; BIT 8=MEMORY MANAGEMENT
591 001076 $ETEND: .WORD $ETEND
592 .MEXIT
593
594 001076 005067 177746 START: CLR $FATAL ;CLEAR ERROR NO.
595 001102 005067 177740 CLF $MSGTY ;CLEAR MESSAGE TYPE (APT)
596 001106 012767 000001 177736 MOV #1,$TESTN ;SET TEST NO.
597
598 $SBTTL INITIALIZE THE COMMON TAGS
599 MOV #500,$SP ;;SETUP THE STACK POINTER
600 ;;INITIALIZE A FEW VECTORS
601 MOV #TRAP,$TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
602 MOV #340,$TRAPVEC+2;LEVEL 7
603 MOV #SPWRD,$PWRVEC ;;POWER FAILURE VECTOR
604 MOV #340,$PWRVEC+2;LEVEL 7
605 ;;SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
606 ;;EQUAL TO A "1", SETUP FOR A SOFTWARE SWITCH REGISTER.
607 MOV #ERRVEC,($SP) ;;SAVE ERROR VECTOR
608 MOV #64,$ERRVEC ;;SET UP ERROR VECTOR
609 MOV #DSWR,$SWR ;;SETUP FOR A HARDWARE SWITCH REGISTER
```

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609 001170 012767 177570 177604 MOV #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
610 001176 022777 177777 177574 CMP #-1,$SWR ;;TRY TO REFERENCE HARDWARE SWR
611 001204 001012 BNE 665 ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
612
613 001206 000403 BR 655 ;;AND THE HARDWARE SWR IS NOT = -1
614 001210 012716 001216 648: MOV #656,($SP) ;;BRANCH IF NO TIMEOUT
615 001214 000002 RTI ;;SET UP FOR TRAP RETURN
616 001216 012767 000176 177554 658: MOV $SWREG,$SWR ;;POINT TO SOFTWARE SWR
617 001224 012767 000174 177550 MOV #DISPREG,DISPLAY
618 001232 012637 000104 668: MOV ($SP)+,$ERRVEC ;;RESTORE ERROR VECTOR
619
620 001236 005067 177612 CLR $PASS ;;CLEAR PASS COUNT
621 001242 132767 000200 177617 BITR $APTSIZE,$ENVM ;;TEST USER SIZE UNDER APT
622 001250 001403 BEQ 678 ;;YES,USE NON-APT SWITCH
623 001252 012767 001070 177520 MOV $SWREG,$SWR ;;NO,USE APT SWITCH REGISTER
624 001260
625 001266 026727 176556 002060 678: CMP 42,$ENDAD ;ACT AUTO MODE?
626 001266 001402 BEQ RESTRT ;YIF SO: BR
627 001270 104401 TYPE
628 001272 005276 TITL
629
630 001274 123727 000176 000006 RESTRT: CMPB #176,#6 ;IS SOFTWARE SWITCH REGISTER =6?
631 001302 001003 BNE 18 ;IF NOT, TEST NORMALLY
632 001304 012767 000176 177466 MOV $SWREG,$SWR ;IF SO USE THE SOFTWARE SWITCH REG
633 001312 017700 177462 18: MOV $SWR,#0 ;GET SWR
634 001316 001424 RFQ ;IF ZERO: GET INPUT
635 001320 006300 ST2: ASL R0
636 001322 016067 004714 177470 MOV TXLPC(R0),EXLPC ;FETCH EXPECT, LPC
637 001330 016067 004644 177460 MOV TXCRC(R0),EXCRC ;FETCH EXPECTED CRC
638 001336 016067 004766 177442 MOV TDLN1(R0),DATLN1 ;FETCH 1ST LENGTH
639 001344 016067 005036 177432 MOV TRMSA1(R0),ROMSA1 ;FETCH 1ST STARTING ADDR.
640 001352 016067 005106 177432 MOV TDLN2(R0),DATLN2 ;FETCH 2ND LENGTH
641 001360 016067 005156 177422 MOV TRMSA2(R0),ROMSA2 ;FETCH 2ND STARTING ADDR
642 001366 000450 BR ;GO COMPUTE
643 001370 005767 176446 GETIN: TST 42 ;UNDER ACT AUT ACCEPT?
644 001374 001045 BNE CHECK ;IF SO: BR USE DEFAULT PARAMETERS
645 001376 122767 000001 177462 CMPR #1,$ENV ;UNDER APT?
646 001404 001441 BEQ CHECK ;IF SO: BR
647 001406 005767 177416 TST TYP0UT ;FROM TYPE OPTION
648 001412 001012 BNE GET2 ;IF SO: BR
649 001414 104401 TYPE
650 001416 005311 GETCPC
651 001420 104407 RDOCT ;STORE EXPECT, CRC
652 001422 012667 177370 MOV ($SP)+,EXCRC ;TYPE LPC INPUT REQUEST
653 001426 104401 TYPE
654 001430 005335 GETLPC
655 001432 104407 RDOCT ;STORE EXPECTED LPC
656 001434 012667 177360 MOV ($SP)+,EXLPC ;REQUEST 1ST ADDRESS SPACE
657 001440 104401 GET2: TYPE ;INPUT SA
658 001442 005515 SA1 ;REQUEST 1ST ADDRESS SPACE
659 001444 104407 RDOCT ;INPUT SA
660 001446 012667 177332 MOV ($SP)+,ROMSA1
661 001452 104401 TYPE ;REQUEST LENGTH OF 1ST ADDR, SPACE
662 001454 005655 SIZE1
663 001456 104407 RDOCT ;INPUT LENGTH
664 001460 012667 177322 MOV ($SP)+,DATLN1
```

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665 001464 104401 TYPE ;REQUEST START ADDR, FOR 2ND SPACE
666 001466 005575 SA2
667 001473 104407 RDOCT ;INPUT SA
668 001472 012667 177312 MOV (SP)+,ROMSA2
669 001476 104401 TYPE ;REQUEST LENGTH OF 2ND SPACE
670 001500 005735 SIZE2
671 001502 104407 RDOCT ;INPUT LENGTH
672 001504 012667 177302 MOV (SP)+,DATLN2
673 001510 005767 177314 CHECK: TST TYP0UT ;IS TYP0UT REQUESTED?
674 001514 001402 BEQ 18 ;BRANCH IF NOT
675 001516 000167 000660 JMP TYPROM ;GO TYP OUT POM
676 001522 005067 177274 18: CLR ACTCRC ;CLEAR STORAGE FOR ACTUAL CRC
677 001526 005067 177272 CLR ACTLPC ;CLEAR STORAGE FOR ACTUAL LPC
678 001532 016700 177250 MOV DATLN1,R0 ;SET LENGTH OF 1ST ROM SPACE
679 001536 001413 BEQ CH0 ;IF NO VERSION SELECTED: BR
680 001540 016701 177240 MOV ROMSA1,R1 ;POINT TO START OF 1ST ROM SPACE
681 001544 004767 000324 JSR PC,CRC ;COMPUTE FIRST HALF OF CRC
682 001550 016701 177230 MOV ROMSA1,R1 ;POINT TO START OF 1ST ROM ADDR.
683 001554 016700 177226 MOV DATLN1,R0 ;SET LENGTH OF 1ST ROM ADDR.
684 001560 006200 ASR R0 ;CONVERT TO WORDS
685 001562 004767 000556 JSR PC,LPC ;COMPUTE FIRST HALF OF CRC
686 001566 016701 177216 CH0: MOV ROMSA2,R1 ;POINT TO 2ND ROM ADDR.
687 001572 016700 177214 MOV DATLN2,R0 ;SET LENGTH OF 2ND ROM ADDR.
688 001576 001422 BEQ CH1 ;BR IF THIS SPACE NOT USED
689 001600 004767 000270 JSR PC,CRC ;COMPUTE REMAINDER OF CRC
690 001604 016701 177200 MOV ROMSA2,R1 ;POINT TO START OF 2ND ROM ADDR.
691 001610 016700 177176 MOV DATLN2,R0 ;SET LENGTH OF 2ND ROM ADDR.
692 001614 006200 ASR R0 ;CONVERT TO WORDS
693 001616 004767 000522 JSR PC,LPC ;COMPUTE REMAINDER OF LPC
694 001622 122767 000001 177236 CMPB #1,SEN0 ;UNDER APT?
695 001630 001403 BEQ 18 ;IF SO: BR
696 001632 005767 176204 TST 42 ;UNDER ACT AUTO ACCEPT?
697 001636 001402 BEQ CH1 ;IF NOT: BR
698 001640 000167 000656 18: JMP AUTACT
699 001644 026767 177146 CH1: CMP EXCRC,ACTCRC ;COMPUTED = EXPECTED ?
700 001652 001431 BEQ CM1 ;IF SO: BR
701
702 001654 104401 TYPE ;TYPE CRC ERROR MESSG.
703 001656 005361 EXCRMG
704 001660 016746 177132 MOV EXCRC,-(SP) ;PUT EXPECTED CRC ON STACK
705 001664 104402 TYP0C ;TYPE EXPECTED CRC
706 001666 104401 TYPE ;TYPE ACTUAL CRC MESSG
707 001670 005430 ACCRMG
708 001672 016746 177124 MOV ACTCRC,-(SP) ;PUT ACTUAL CRC ON STACK
709 001676 104402 TYP0C ;TYPE ACTUAL CRC
710 001700 026727 176136 002000 CMP 42,#SENDAD ;UNDEK ACT AUTO MODE?
711 001706 001404 BEQ 18 ;IF SO: BR
712 001710 122767 000001 177150 CMPB #1,SEN0 ;UNDER APT?
713 001716 001007 RNE CK1 ;IF NOT: BR
714 001720 012767 000002 177122 18: MOV #2,$FATAL ;MOVE TO MAILBOX ERROR NO. **** 2 ****
715 001726 012767 000001 177112 MOV #1,$MSGTYP ;SET MAILBOX FOR FATAL ERROR
716 001734 000000 HALT ;CRC ERROR
717
718 001736 026767 177062 177054 CK1: CMP ACTLPC,EXLPC ;COMPARE EXPT. LPC=ACTUAL LPC
719 001744 001431 BEQ CK2 ;IF SO: BR
720 001746 104401 TYPE ;TYPE LPC ERROR MESSG.
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721 001750 005404 EXLPMG
722 001752 016746 177042 MOV EXLPC,-(SP) ;PUT EXPECTED LPC ON STACK
723 001756 104402 TYP0C ;TYPE EXPECTED LPC
724 001760 104401 TYPE ;TYPE ACTUAL LPC MESSG.
725 001762 005453 ACLPMG
726 001764 016746 177034 MOV ACTLPC,-(SP) ;PUT ACTUAL LPC ON STACK
727 001770 104402 TYP0C ;TYPE ACTUAL LPC
728 001772 026727 176044 002000 CMP 42,#SENDAD ;UNDER ACT AUTO MODE?
729 002000 001404 BEQ 18 ;IF SO: BR
730 002002 122767 000001 177056 CMPB #1,SEN0 ;UNDER APT?
731 002010 001007 RNE CK2 ;IF NOT: BR
732 002012 012767 000003 177030 18: MOV #3,$FATAL ;MOVE TO MAILBOX ERROR NO. **** 3 ****
733 002020 012767 000001 177020 MOV #1,$MSGTYP ;SET MAILBOX FOR FATAL ERROR
734 002026 000000 HALT ;LPC ERROR
735
736 002030 026727 176006 002000 CK2: CMP 42,#SENDAD ;ACT AUTO ACCEPT?
737 002036 001402 BEQ 18 ;IF SO: BR
738 002040 104401 TYPE ;TYPE END OF TEST
739 002042 005476 EOTST
740 002044 005267 177004 18: INC $PASS ;BUMP PASS COUNT
741 002050 013700 000042 MOV #42,R0 ;CHECK APT
742 002054 001405 BEQ GOAGIN ;KEEP GOING
743 002056 000005 RESEI
744 002060 004710 0ENDAD: JSR PC,(R0) ;ACT HOOKS
745 002062 000240 NOP
746 002064 000240 NOP
747 002066 000240 NOP
748 002070 000167 177200 GOAGIN: JMP RESIRT ;DO AGAIN
749
750 002074 016767 176722 176712 CRC: MOV ACTCRC,XORS
751 002102 111104 CL0: MOVB (R1),R4 ;GET CHAR.
752 002104 022701 173024 CMP #173024,P1 ;LOCATION EFFECTED BY SWITCHES
753 002110 001004 RNE CL3 ;IF NOT: BR
754 002112 005300 DEC P0 ;FIX COUNTERS
755 002114 005300 DEC R0
756 002116 005721 TST (R1)+ ;FIX POINTER
757 002120 000770 RR CL0 ;CONTINUE
758 002122 004767 000114 CL3: JSR PC,PARITY ;GO GET PARITY
759 002126 004767 000166 JSR PC,XOR ;XOR CHAR
760 002132 000241 CLC
761 002134 006004 ROR R4 ;ROTATE 1 POS. RIGHT
762 002136 103014 BCC CL2 ;IF NO CARRY: BR
763 002140 052704 000400 BIS #400,R4 ;SET BIT NINE
764 002144 000241 CLC
765 002146 010405 CL1: MOV R4,P5 ;SAVE CHAR
766 002150 042705 177703 BIC #177703,P5
767 002154 005105 COM P5
768 002156 042705 177703 BIC #177703,P5
769 002162 042704 000074 BIC #74,R4
770 002166 050504 BIS P5,R4
771 002170 010467 176620 CL2: MOV R4,XORS
772 002174 005300 DEC R0
773 002176 001402 BEQ CLLAST ;IF LAST CAR.: BR
774 002200 000167 177676 JMP CLLAST ;GET NEXT CHAR.
775 002204 016704 176604 CLLAST: MOV XORS,R4
776 002210 005167 176600 COM XORS
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777 002214 042767 177050 176572 BIC #177050,XORS
778 002222 042704 177727 177727 PIC #177727,R4 ;COMPLEMENT ALL BUT BITS 3 & 5
779 002226 050467 176562 RTS R4,XORS
780 002232 016767 176556 176562 MOV XORS,ACTCRC
781 002240 000207 RTS PC
782 002242 005067 176560 PARITY: CLR PARCNT ;CLEAR BIT COUNTER
783 002246 012703 000010 MOV #10,R3 ;SET NO. OF BITS
784 002252 032704 000001 CLP0: BIT #1,R4 ;SEE IF ONE BIT
785 002256 001402 REQ CLP1 ;IF NOT: BR
786 002260 005267 176542 INC PARCNT ;HUMP COUNTER
787 002264 000241 CLP1: CLC
788 002266 006004 POR R4 ;ROTATE TO NEXT BIT
789 002270 005303 DEC R3
790 002272 001367 BNE CLP0 ;CONTINUE FOR ALL BITS
791 002274 112104 MOV# (R1)+,R4
792 002276 042704 177400 BIC #177400,P4
793 002302 032767 000001 176516 BIT #1,PARCNT ;SEE IF ODD # OF ONE BITS
794 002310 001002 BNE CLP2 ;IF SO: BR
795 002312 052704 000400 RTS #400,P4 ;SET PARITY BIT
796 002316 000207 CLP2: PC ;EXIT
797
798 002320 010446 XOR: MOV P4,-(SP) ;XOR SUBROUTINE: R4 WITH XORS
799 002322 046716 176466 RIC XORS,(SP)
800 002326 040467 176462 BIC P4,XORS
801 002332 052667 176456 BIS (SP)+,XORS
802 002336 016704 176452 MOV XORS,P4
803 002342 000207 RTS PC
804
805 002344 016767 176454 176442 LPC: MOV ACTLPC,XORS
806 002352 012104 LPC1: MOV (R1)+,R4
807 002354 022701 173026 CMP #173026,R1 ;LOCATION EFFECTED BY SWITCHES
808 002360 001402 BEQ LPC2 ;IF SO: SKIP LOC. BY BRANCHING
809 002362 004767 177732 JSR PC,XOR
810 002366 005300 LPC2: DEC R0
811 002370 001370 BNE LFC1
812 002372 016767 176416 176424 MOV XORS,ACTLPC
813 002400 000207 RTS PC
814
815 002402 104401 TYPRM: TYPE ;TYPE HEADER
816 002404 006023 TYPHDR
817 002406 016700 176372 MOV #0MSA1,R0 ;POINT TO 1ST RUM SPACE
818 002412 016701 176370 MOV #0ATLN1,R1 ;PUT LENGTH IN R1
819 002416 006201 ASR R1 ;CONVERT TO WORDS
820 002420 001402 BEQ TYPR1 ;BRANCH IF 1ST ROM SPACE NOT USED
821 002422 004767 000026 JSR PC,TYP ;GO TYPE 1ST ADDR. SPACE
822 002426 016700 176356 TYPR1: MOV #0MSA2,R0 ;POINT TO 2ND ADDR. SPACE
823 002432 016701 176354 MOV #0ATLN2,P1 ;PUT LENGTH IN R1
824 002436 006201 ASR P1 ;CONVERT TO WORDS
825 002440 001402 BEQ ENDOT ;BR IF 2ND ADDR. SPACE NOT USED
826 002442 004767 000006 JSR PC,TYP ;GO TYPE 2ND ADDR. SPACE
827 002446 104401 ENDOT: TYPE
828 002450 005476 EOTST
829 002452 000000 HALT
830
831 002454 104401 TY: TYPE
832 002456 006015 CARLF
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833 002460 000403 BR TYP3
834 002462 032700 000003 TYP1: BIT #3,R0 ;ADDRESS MULTIPLE OF 4?
835 002466 001006 BNE TYP2 ;IF NOT: BR
836 002470 104401 TYP3: TYPE
837 002472 006015 CARLF
838 002474 010046 MOV R0,-(SP) ;PUT ADDRESS ON STACK
839 002476 104402 TYPOC ;TYPE ADDR.
840 002500 104401 TYPE
841 002502 006006 COLON
842 002504 012046 TYP2: MOV (P0)+,-(SP) ;PUT DATA ON STACK
843 002506 104402 TYPOC ;TYP DATA
844 002510 104401 TYPE ;TYPE 2 SPACES
845 002512 006020 SP2
846 002514 005301 DFC R1 ;FINISHED?
847 002516 001361 BNE TYP# ;IF NOT: BR
848 002520 000207 RTS PC ;RETURN
849 002522 005000 AUTACT: CLR R0
850 002524 062700 000002 AUT1: ADD #2,R0 ;BUMP TABLE INDEX
851 002530 026027 005226 177777 CMP TMSG(R0),#-1 ;CHECKED ALL KNOWN VERSIONS?
852 002536 001425 BEQ AUTERR ;IF SO: BR
853 002540 026067 004644 176254 CMP TXCRC(R0),ACTCRC ;DOES THIS CRC AGREE?
854 002546 001366 BNE AUT1 ;IF NOT: KEEP LOOKING
855 002550 023727 000042 002000 CMP #42,#SENDAD ;UNDER ACT AUTO ACCEPT?
856 002556 001403 BEQ AUT3 ;IF SO: BR
857 002560 016067 005226 000002 MOV TMSG(R0),AUT2 ;SET UP VERSION MESSAGE
858 002566 104401 TYPE
859 002570 000000 AUT2: 0
860 002572 016067 004644 176216 AUT3: MOV TXCRC(R0),EXCRC ;SET EXPECTED CRC
861 002600 016067 004714 176212 MOV TXLPC(R0),LALPC ;SET EXPECTED LPC
862 002606 000167 177124 JMP CK1 ;CHECK LPC
863
864 002612 104401 AUTERR: TYPE
865 002614 006004 AUTERR:
866 002616 012767 000001 176224 MOV #1,$FATAL ;MOVE TO MAILBOX ERROR NO. **** 1 ****
867 002624 012767 000001 176214 MOV #1,$MSGTYP ;SET MAILBOX FOR FATAL ERROR
868 002632 000000 HALT ;AUTO ACCEPT FAILED
869
870 ;SBTTL TTY INPUT ROUTINE
871
872 ;*****
873 002634 177560 STKS: ,WOPD 177560 ;TTY KBD STATUS
874 002636 177562 STKB: ,WORD 177562 ;TTY KBD BUFFER
875
876 ,ENABL LSB
877
878 ,DSABL LSB
879
880 ;*****
881 ;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
882 ;*CALL:
883 ;* RDCHR ;INPUT A SINGLE CHARACTER FROM THE TTY
884 ;* RETURN HERE ;CHARACTER IS ON THE STACK
885 ;* ;WITH PARITY BIT STRIPPED OFF
886 ;
887
888 002640 011646 BRDCHR: MOV (SP),-(SP) ;PUSH DOWN THE PC
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889 002642 016666 000004 000002      MOV      4(SP),2(SP)      ;;SAVE THE PS
890 002650 105777 177760      TSTB    #ATKS           ;;WAIT FOR
891 002654 100375      BPL     18              ;;A CHARACTER
892 002656 117766 177754 000004      MOVB    #STKB,4(SP)     ;;READ THE TTY
893 002664 042766 177600 000004      BIC     #'C<177>,4(SP)  ;;GET RID OF JUNK IF ANY
894 002672 026627 000004 000023      CMB     4(SP),#23      ;;IS IT A CONTROL-S?
895 002700 001013      BNE     35              ;;BRANCH IF NO
896 002702 105777 177726      TSTB    #ATKS           ;;WAIT FOR A CHARACTER
897 002706 100375      BPL     25              ;;LOOP UNTIL ITS THERE
898 002710 117746 177722      MOVB    #STKB,-(SP)     ;;GET CHARACTER
899 002714 042716 177600      BIC     #'C177,(SP)     ;;MAKE IT 7-BIT ASCII
900 002720 022627 000021      CMB     (SP)+,#21      ;;IS IT A CONTROL-Q?
901 002724 001366      BNE     28              ;;IF NOT DISCARD IT
902 002726 000750      BR      15              ;;YES, RESUME
903 002730 026627 000004 000140 301      CMB     4(SP),#140     ;;IS IT UPPER CASE?
904 002736 000407      BLT     48              ;;BRANCH IF YES
905 002740 026627 000004 000175      CMB     4(SP),#175     ;;IS IT A SPECIAL CHAR?
906 002746 000303      RCT     48              ;;BRANCH IF YES
907 002750 042766 000004 000004      BIC     #40,4(SP)      ;;MAKE IT UPPER CASE
908 002756 000002      RTI     ;               ;;GO BACK TO USER
909
910 ;*****
911 ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
912 ;*CALL:
913 ;*      RDLIN          ;;INPUT A STRING FROM THE TTY
914 ;*      RETURN HERE    ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
915 ;*                    ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
916
916 002760 010346      $RDLIN: MOV     R3,-(SP)      ;;SAVE R3
917 002762 005046      CLR     -(SP)           ;;CLEAR THE RUBOUT KEY
918 002764 012703 003214      MOV     #STTYIN,R3      ;;GET ADDRESS
919 002770 022703 003224      CMP     #STTYIN+R,,R3   ;;BUFFER FULL?
920 002774 101456      BLOS   46              ;;BR IF YES
921 002776 104405      RDCMR  ;               ;;GO READ ONE CHARACTER FROM THE TTY
922 003000 112613      MOVB   (SP)+,(R3)       ;;GET CHARACTER
923 003002 122713 000177 1001      CMB    #177,(R3)        ;;IS IT A RUBOUT
924 003006 001022      BNE    58              ;;RR IF NO
925 003010 005716      TST    (SP)            ;;IS THIS THE FIRST RUBOUT?
926 003012 001007      BNE    68              ;;RR IF NO
927 003014 112767 000134 000170      MOVB   #' \,96         ;;TYPE A BACK SLASH
928 003022 104401 003212      TYPE   ,96             ;;
929 003026 112716 177777      MOV     #-1,(SP)        ;;SET THE RUBOUT KEY
930 003032 005303 681      DEC     R3              ;;BACKUP BY ONE
931 003034 020327 003214      CMB    R3,#STTYIN      ;;STACK EMPTY?
932 003040 103034      BLO    46              ;;BR IF YES
933 003042 111367 000144      MOVB   (R3),96         ;;SETUP TO TYPEOUT THE DELETED CHAR.
934 003046 104401 003212      TYPE   ,96             ;;GO TYPE
935 003052 000746      BR     28              ;;GO READ ANOTHER CHAR.
936 003054 005716      TST    (SP)            ;;RUBOUT KEY SET?
937 003056 001406      REQ    76              ;;BR IF NO
938 003060 112767 000134 000124      MOVB   #' \,96         ;;TYPE A BACK SLASH
939 003066 104401 003212      TYPE   ,96             ;;
940 003072 005016      CLR     (SP)           ;;CLEAR THE RUBOUT KEY
941 003074 122713 000025 701      CMB    #25,(R3)        ;;IS CHARACTER A CTRL U?
942 003100 001003      BNE    86              ;;BR IF NO
943 003102 104401 003230      TYPE   ,#CNTLU        ;;TYPE A CONTROL "U"
944 003106 000726      BR     15              ;;GO START OVER

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945 003110 122713 000022 801      CHPR    #22,(R3)        ;;IS CHARACTER A "R"?
946 003114 001011      BNE    38              ;;BRANCH IF NO
947 003116 105013      CLRB   (R3)           ;;CLEAR THE CHARACTER
948 003120 104401 003225      TYPE   ,#CRLF         ;;TYPE A "CR" & "LF"
949 003124 104401 003214      TYPE   ,#STTYIN       ;;TYPE THE INPUT STRING
950 003130 000717      BR     28              ;;GO PICKUP ANOTHER CHARACTER
951 003132 104401 003224 401      TYPE   ,#QUES         ;;TYPE A "?"
952 003136 000712      BR     18              ;;CLEAR THE BUFFER AND LOOP
953 003140 111367 000046 301      MOVB   (R3),96         ;;ECHO THE CHARACTER
954 003144 104401 003212      TYPE   ,96             ;;
955 003150 122723 000015      CMB    #15,(R3)+       ;;CHECK FOR RETURN
956 003154 001305      BNE    26              ;;LOOP IF NOT RETURN
957 003156 105063 177777      CLPB   -1(R3)         ;;CLEAR RETURN (THE 15)
958 003162 104401 003226      TYPE   ,#LF           ;;TYPE A LINE FEED
959 003166 005726      TST    (SP)+          ;;CLEAN RUBOUT KEY FROM THE STACK
960 003170 012603      MOV    (SP)+,R3       ;;RESTORE R3
961 003172 011646      MOV    (SP)-,(SP)     ;;ADJUST THE STACK AND PUT ADDRESS OF THE
962 003174 016666 000004 000002      MOV    4(SP),2(SP)    ;;FIRST ASCII CHARACTER ON IT
963 003202 012766 003214 000004      MOV    #STTYIN,4(SP)
964 003210 000002      RTI     ;               ;;RETURN
965 003212 000000 901      ,BYTE  0              ;;STORAGE FOR ASCII CHAR, TO TYPE
966 003213 000000      ,BYTE  0              ;;TERMINATOR
967 003214 000010      ,BLKB  8              ;;RESERVE 8 BYTES FOR TTY INPUT
968 003224 000000      ,QUES: ,ASCII  '?'    ;;QUESTION MARK
969 003225 000015      ,CRLF: ,ASCII  <15>  ;;CARRIAGE RETURN
970 003226 000012      ,LF:   ,ASCII  <12>  ;;LINE FEED
971 003230 052536 000015 000      ,CNTLU: ,ASCII  /'U/'<15><12>  ;;CONTROL "U"
972 003233 000016 000012 000012      ,SCNTLG: ,ASCII  /'G/'<15><12>  ;;CONTROL "G"
973 003242 005015 053523 020122      ,MSWR:  ,ASCII  <15><12>/SWR = /
974 003250 020075      ,MNEW:  ,ASCII  / NEW = /
975 003253 000040 053505      ,SHTTL READ AN OCTAL NUMRER FROM THE TTY
976 003260 036440
977
978 ;*****
979 ;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
980 ;*CHANGE IT TO BINARY.
981 ;*THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
982 ;*OCTAL DIGITS, IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
983 ;*FOLLOWED BY A CARRIAGE RETURN-LINE FEED, THE COMPLETE NUMBER MUST
984 ;*THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN,
985 ;*CALL:
986 ;*      RDOCT          ;;READ AN OCTAL NUMBER
987 ;*      RETURN HERE    ;;LOW ORDER BITS ARE ON TOP OF THE STACK
988 ;*                    ;;HIGH ORDER BITS ARE IN $HI0CT
989
989 003264 011646      $RDOCT: MOV     (SP)-,(SP)      ;;PROVIDE SPACE FOR THE
990 003266 016666 000004 000002      MOV     4(SP),2(SP)      ;;INPUT NUMBER
991 003274 010046      MOV     R0,-(SP)        ;;PUSH R0 ON STACK
992 003276 010146      MOV     R1,-(SP)        ;;PUSH R1 ON STACK
993 003300 010246      MOV     R2,-(SP)        ;;PUSH R2 ON STACK
994 003302 104406      RDLIN  ;               ;;READ AN ASCII LINE
995 003304 012600      MOV     (SP)+,R0        ;;GET ADDRESS OF 1ST CHARACTER
996 003306 010067 000100      MOV     R0,#0           ;;AND SAVE IT
997 003312 005001      CLR     R1              ;;CLEAR DATA WORD
998 003314 005002      CLR     R2

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1041 003316 112046 20: MOVB (R0)+,(SP) ;;PICKUP THIS CHARACTER
1042 003320 001420 BEQ 36 ;;IF ZERO GET OUT
1043 003322 122716 000060 CMPB #'0',(SP) ;;MAKE SURE THIS CHARACTER
1044 003326 003026 FGT 48 ;;IS AN OCTAL DIGIT
1045 003330 122716 000067 CMPB #'7',(SP)
1046 003334 002423 RLT 45
1047 003336 006301 ASL R1 ;;*2
1048 003340 006102 ROL P2
1049 003342 006301 ASL R1 ;;*4
1050 003344 006102 ROL R2
1051 003346 006301 ASL R1 ;;*8
1052 003350 006102 ROL R2
1053 003352 042716 177770 BIC #'C7,(SP) ;;STRIP THE ASCII JUNK
1054 003356 062601 ADD (SP)+,R1 ;;ADD IN THIS DIGIT
1055 003360 000756 BR 26 ;;LOOP
1056 003362 005726 30: TST (SP)+ ;;CLEAN TERMINATOR FROM STACK
1057 003364 010166 000012 MOV R1,12(SP) ;;SAVE THE RESULT
1058 003370 010267 000026 MOV R2,#HIOCT
1059 003374 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
1060 003376 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
1061 003400 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
1062 003402 000002 RTI ;;RETURN
1063 003404 005726 40: TST (SP)+ ;;CLEAN PARTIAL FROM STACK
1064 003406 105010 CLMB (R0) ;;SET A TERMINATOR
1065 003410 104401 TYPE ;;TYPE UP THRU THE BAD CHAR.
1066 003412 000000 50: .WORD 0
1067 003414 104401 TYPE ,SQUES ;;?" "CR" & "LF"
1068 003420 000730 BR 13 ;;TRY AGAIN
1069 003422 000000 $HIOCT: .WORD 0 ;;HIGH ORDER BITS GO HERE
1070 $BTTL TYPE ROUTINE
1071
1072 ;;*****
1073 ;;ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
1074 ;;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
1075 ;;NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
1076 ;;NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
1077 ;;NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
1078 ;;
1079 ;;CALL:
1080 ;;1) USING A TRAP INSTRUCTION
1081 ;; TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCII STRING
1082 ;;OR
1083 ;; TYPE
1084 ;; MESADP
1085 ;;
1086
1087 003424 105767 000265 6TYPE: TSTR $TPFLG ;;IS THERE A TERMINAL?
1088 003430 100002 RPL 18 ;;R IF YES
1089 003432 000000 HALT ;;HALT HERE IF NO TERMINAL
1090 003434 000430 BP 38 ;;LEAVE
1091 003436 010046 10: MOV R0,-(SP) ;;SAVE R0
1092 003440 017600 000002 MOV 02(SP),R0 ;;GET ADDRESS OF ASCII STRING
1093 003444 122767 000001 175414 CMPB #APTENV,$ENV ;;RUNNING IN APT MODE
1094 003452 001011 BNE 626 ;;NO,GO CHECK FOR APT CONSOLE
1095 003454 132767 000100 175405 BITB #APTSPOOL,$ENVM ;;SPOOL MESSAGE TO APT
1096 003462 001405 BEQ 626 ;;NO,GO CHECK FOR CONSOLE

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1057 003464 010067 000004 MOV R0,61$ ;;SETUP MESSAGE ADDRESS FOR APT
1058 003470 004767 000230 JSR PC,$ATY3 ;;SPOOL MESSAGE TO APT
1059 003474 000000 618: .WORD 0 ;;MESSAGE ADDRESS
1060 003476 132767 000040 175363 626: BITB #APTCSP,$ENVM ;;APT CONSOLE SUPPRESSED
1061 003504 001003 BNE 605 ;;YES,SKIP TYPE OUT
1062 003506 112046 20: MOVF (R0)+,(SP) ;;PUSH CHARACTER TO BE TYPED ONTO STACK
1063 003510 001005 BNE 46 ;;BR IF IT ISN'T THE TERMINATOR
1064 003512 005726 TST (SP)+ ;;IF TERMINATOR POP IT OFF THE STACK
1065 003514 012600 608: MOV (SP)+,R0 ;;RESTORE R0
1066 003516 062716 000002 30: ADD #2,(SP) ;;ADJUST RETURN PC
1067 003522 000002 RTI ;;RETURN
1068 003524 122716 000011 40: CMPB #'T',(SP) ;;BRANCH IF <HT>
1069 003530 001430 BEQ R6
1070 003532 122716 000200 CMPB #CRLF,(SP) ;;BRANCH IF NOT <CRLF>
1071 003536 001006 BNE 56
1072 003540 005726 TST (SP)+ ;;POP <CR><LF> EQUIV
1073 003542 104401 TYPE ;;TYPE A CR AND LF
1074 003544 003225 $CRLF
1075 003546 105067 000130 CLRR $CHARCNT ;;CLEAN CHARACTER COUNT
1076 003552 000755 BR 26 ;;GET NEXT CHARACTER
1077 003554 004767 000056 50: JSR PC,$TYPEC ;;GO TYPE THIS CHARACTER
1078 003560 126726 000130 60: CMPB #FILLC,(SP)+ ;;IS IT TIME FOR FILLER CHARS.?
1079 003564 001350 BNE 26 ;;IF NO GO GET NEXT CHAR.
1080 003566 016746 000120 MOV #NULL,-(SP) ;;GET # OF FILLER CHARS. NEEDED
1081 AND THE NULL CHAR.
1082 003572 105366 000001 70: DECB 1(SP) ;;DOES A NULL NEED TO BE TYPED?
1083 003576 002770 BLT 68 ;;BR IF NO--GO POP THE NULL OFF OF STACK
1084 003600 004767 000032 JSR PC,$TYPEC ;;GO TYPE A NULL
1085 003604 105367 000072 DECB $CHARCNT ;;DO NOT COUNT AS A COUNT
1086 003610 000770 BR 76 ;;LOOP
1087
1088 ;HORIZONTAL TAB PROCESSOR
1089
1090 003612 112716 000040 R0: MOVB #' '(SP) ;;REPLACE TAB WITH SPACE
1091 003616 004767 000014 90: JSR PC,$TYPEC ;;TYPE A SPACE
1092 003622 132767 000002 000052 BITB #7,$CHARCNT ;;BRANCH IF NOT AT
1093 003630 001372 BNE 96 ;;TAB STOP
1094 003632 005726 TST (SP)+ ;;POP SPACE OFF STACK
1095 003634 000724 BR 28 ;;GET NEXT CHARACTER
1096 003636 105777 000044 $TYPEC: TSTR $STPS ;;WAIT UNTIL PRINTER IS READY
1097 003642 100375 BPL $TYPEC
1098 003644 116677 000002 000036 MOVB 2(SP),#STPB ;;LOAD CHAR TO BE TYPED INTO DATA REG.
1099 003652 122766 000015 000002 CMPB #CR,2(SP) ;;IS CHARACTER A CARRIAGE RETURN?
1100 003660 001003 BNE 18 ;;BRANCH IF NO
1101 003662 105067 000014 CLRR $CHARCNT ;;YES--CLEAR CHARACTER COUNT
1102 003666 000406 BR $TYPEX ;;EXIT
1103 003670 122766 000012 000002 10: CMPB #LF,2(SP) ;;IS CHARACTER A LINE FEED?
1104 003676 001402 BEQ $TYPEX ;;BRANCH IF YES
1105 003700 105227 INCB (PC)+ ;;COUNT THE CHARACTER
1106 003702 000000 $CHARCNT: .WORD 0 ;;CHARACTER COUNT STORAGE
1107 003704 000207 $TYPEX: RTS PC
1108
1109 003706 177564 $TPS: .WORD 177564 ;;TTY PRINTER STATUS REG. ADDRESS
1110 003710 177566 $TPR: .WORD 177566 ;;TTY PRINTER BUFFER REG. ADDRESS
1111 003712 000 $NULL: .BYTE 0 ;;CONTAINS NULL CHARACTER FOR FILLS
1112 003714 002 $FILLS: .BYTE 2 ;;CONTAINS # OF FILLER CHARACTERS REQUIRED

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1113 003714 012 $FILLC: ,BYTE 12 ;;INSERT FILL CHARS, AFTER A "LINE FEED"
1114 003715 000 $TP:LG: ,BYTE 0 ;;"TERMINAL AVAILABLE" FLAG (RIT<07>=0=YES)
1115 $SBTTL APT COMMUNICATIONS ROUTINE
1116
1117 ;;*****
1118 003716 112767 000001 000236 $ATY1: MOV #1,$FFLG ;;TO REPORT FATAL ERROR
1119 003724 112767 000001 000226 $ATY3: MOVB #1,$MFLG ;;TO TYPE A MESSAGE
1120 003732 000403 BR $ATYC
1121 003734 112767 000001 000220 $ATY4: MOV #1,$FFLG ;;TO ONLY REPORT FATAL ERROR
1122 003742 $ATYC:
1123 003742 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
1124 003744 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
1125 003746 105767 000206 TSTB $MFLG ;;SHOULD TYPE A MESSAGE?
1126 003752 001450 EQ 56 ;;IF NOT: BR
1127 003754 122767 000001 175104 CNPB $APTENV,$ENV ;;OPERATING UNDER APT?
1128 003762 001031 BNE 30 ;;IF NOT: BR
1129 003764 132767 000100 175075 BITB $APTSPOOL,$ENVM ;;SHOULD SPOOL MESSAGES?
1130 003772 001425 BFO 30 ;;IF NOT: BR
1131 003774 017600 000004 MOV #4(SP),R0 ;;GET MESSAGE ADDR.
1132 004000 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
1133 004006 005767 175034 10: TST $MSGTYPE ;;SEE IF DONE W/ LAST XMISSION?
1134 004012 001375 BNE 18 ;;IF NOT: WAIT
1135 004014 010067 175042 MOV R0,$MSGAD ;;PUT ADDR IN MAILBOX
1136 004020 105720 20: TSTB (R0)+ ;;FIND END OF MESSAGE
1137 004022 001376 BNE 28
1138 004024 166700 175032 SUR $MSGAD,R0 ;;SUB START OF MESSAGE
1139 004030 006200 ASR R0 ;;GET MESSAGE LNTH IN WORDS
1140 004032 010067 175026 MOV R0,$MSGLGT ;;PUT LENGTH IN MAILBOX
1141 004036 012767 000004 175002 MOV #4,$MSGTYPE ;;TELL APT TO TAKE MSG.
1142 004044 000413 BR 58
1143 004046 017667 000004 000016 30: MOV #4(SP),46 ;;PUT MSG ADDR IN JSR LINKAGE
1144 004054 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDRESS
1145 004062 016746 173710 MOV 177776,-(SP) ;;PUSH 177776 ON STACK
1146 004066 004767 177332 JSR PC,$TYPE ;;CALL TYPE MACRO
1147 004072 000000 40: ,WORD 0
1148 004074 58:
1149 004074 105767 000062 100: TSTB $FFLG ;;SHOULD REPORT FATAL ERROR?
1150 004100 001416 REQ 128 ;;IF NOT: BR
1151 004102 005767 174760 TST $ENV ;;RUNNING UNDER APT?
1152 004106 001413 BEQ 126 ;;IF NOT: BR
1153 004110 005767 174732 110: TST $MSGTYPE ;;FINISHED LAST MESSAGE?
1154 004114 001375 BNE 118 ;;IF NOT: WAIT
1155 004116 017667 000004 174724 MOV #4(SP),$FATAL ;;GET ERROR #
1156 004124 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
1157 004132 005267 174710 INC $MSGTYPE ;;TELL APT TO TAKE ERROR
1158 004136 105067 000020 120: CLRFB $FFLG ;;CLEAR FATAL FLAG
1159 004142 105067 000013 CLRFB $LFLG ;;CLEAR LOG FLAG
1160 004146 105067 000006 CLRFB $MFLG ;;CLEAR MESSAGE FLAG
1161 004152 012600 MOV (SP)+,R1 ;;POP STACK INTO R1
1162 004154 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
1163 004156 000207 RTS PC ;;RETURN
1164 004160 000 $MFLG: ,BYTE 0 ;;MESSG. FLAG
1165 004161 000 $LFLG: ,BYTE 0 ;;LOG FLAG
1166 004162 000 $FFLG: ,BYTE 0 ;;FATAL FLAG
1167 004164 $EVEN
1168 000200 APTSIZE=200

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1169 000001 APTENV=001
1170 000100 APTSPOOL=100
1171 000040 APTCSUP=040
1172 $SBTTL BINARY TO OCTAL (ASCII) AND TYPE
1173
1174 ;;*****
1175 ;;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
1176 ;;OCTAL (ASCII) NUMBER AND TYPE IT.
1177 ;;$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
1178 ;;CALL:
1179 ;; MOV NUM,-(SP) ;;NUMBER TO BE TYPED
1180 ;; TYPOS ;;CALL FOR TYPEOUT
1181 ;; ,BYTE N ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
1182 ;; ,BYTE M ;;M=1 OR 0
1183 ;; ;;1=TYPE LEADING ZEROS
1184 ;; ;;0=SUPPRESS LEADING ZEROS
1185 ;;
1186 ;;$TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
1187 ;;$TYPOS OR $TYPOC
1188 ;;CALL:
1189 ;; MOV NUM,-(SP) ;;NUMBER TO BE TYPED
1190 ;; TYPON ;;CALL FOR TYPEOUT
1191 ;;
1192 ;;$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
1193 ;;CALL:
1194 ;; MOV NUM,-(SP) ;;NUMBER TO BE TYPED
1195 ;; TYPOC ;;CALL FOR TYPEOUT
1196
1197 004164 017646 000000 $TYPOS: MOV #0(SP),-(SP) ;;PICKUP THE MODE
1198 004170 116667 000001 000211 MOVB 1(SP),$FILL ;;LOAD ZERO FILL SWITCH
1199 004176 112667 000207 MOVB (SP)+,$MODE+1 ;;NUMBER OF DIGITS TO TYPE
1200 004202 062716 000002 ADD #2,(SP) ;;ADJUST RETURN ADDRESS
1201 004206 000406 BR $TYPON
1202 004210 112767 000001 000171 $TYPOC: MOVB #1,$FILL ;;SET THE ZERO FILL SWITCH
1203 004216 112767 000006 000165 MOVB #6,$MODE+1 ;;SET FOR SIX(6) DIGITS
1204 004224 112767 000005 000154 $TYPON: MOVB #5,$CNT ;;SET THE ITERATION COUNT
1205 004232 010346 MOV R3,-(SP) ;;SAVE R3
1206 004234 010446 MOV R4,-(SP) ;;SAVE R4
1207 004236 010546 MOV R5,-(SP) ;;SAVE R5
1208 004240 116704 000145 MOVB $MODE+1,R4 ;;GET THE NUMBER OF DIGITS TO TYPE
1209 004244 005404 NEG R4
1210 004246 062704 000006 ADD #6,R4 ;;SUBTRACT IT FOR MAX. ALLOWED
1211 004252 110467 000132 MOVB R4,$MODE ;;SAVE IT FOR USE
1212 004256 116704 000125 MOVB $FILL,R4 ;;GET THE ZERO FILL SWITCH
1213 004260 016605 000012 MOV 12(SP),R5 ;;PICKUP THE INPUT NUMBER
1214 004266 005003 CLR R3 ;;CLEAR THE OUTPUT WORD
1215 004270 006105 10: ROL R5 ;;ROTATE MSB INTO "C"
1216 004272 000404 BR 38 ;;GO DO MSB
1217 004274 006105 20: ROL R5 ;;FORM THIS DIGIT
1218 004276 006105 ROL R5
1219 004300 006105 ROL R5
1220 004302 010503 MOV R5,R3
1221 004304 006103 30: ROL R3 ;;GET LSB OF THIS DIGIT
1222 004306 105367 000076 DECB $MODE ;;TYPE THIS DIGIT?
1223 004312 100016 BPL 78 ;;BR IF NO
1224 004314 042703 177770 RIC #177770,R3 ;;GET RID OF JUNK

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1225 004320 001002 BNE 48 ;TEST FOR 0
1226 004322 005704 TST R4 ;SUPPRESS THIS 0?
1227 004324 001403 REQ 58 ;BR IF YES
1228 004326 005204 48: INC R4 ;DON'T SUPPRESS ANYMORE 0'S
1229 004330 052703 000060 BIS #'0,R3 ;MAKE THIS DIGIT ASCII
1230 004334 052703 000040 58: BIS #' ,R3 ;MAKE ASCII IF NOT ALREADY
1231 004340 110367 000040 MOV# R3,R8 ;SAVE FOR TYPING
1232 004344 104401 004404 TYPE ,R8 ;GO TYPE THIS DIGIT
1233 004350 105367 000032 78: DECA 60CNT ;COUNT BY 1
1234 004354 003347 RGT 28 ;BR IF MORE TO DO
1235 004356 002402 BLT 68 ;BR IF DONE
1236 004360 005204 INC R4 ;INSURE LAST DIGIT ISN'T A BLANK
1237 004362 000744 RF 28 ;GO DO THE LAST DIGIT
1238 004364 012605 68: MOV (SP)+,R5 ;RESTORE R5
1239 004366 012604 MOV (SP)+,R4 ;RESTORE R4
1240 004370 012603 MOV (SP)+,R3 ;RESTORE R3
1241 004372 016666 000002 000004 MOV 2(SP),4(SP) ;SET THE STACK FOR RETURNING
1242 004400 012616 MOV (SP)+,(SP)
1243 004402 000002 RTI ;RETURN
1244 004404 000000 88: .BYTE 0 ;STORAGE FOR ASCII DIGIT
1245 004405 000000 .BYTE 0 ;TERMINATOR FOR TYPE ROUTINE
1246 004406 000000 80CNT: .BYTE 0 ;OCTAL DIGIT COUNTER
1247 004407 000000 80FILL: .BYTE 0 ;ZERO FILL SWITCH
1248 004410 000000 80MODE: .WORD 0 ;NUMBER OF DIGITS TO TYPE
1249 .SBTTL POWER DOWN AND UP ROUTINES
1250
1251
1252 ;*****
1253 004412 012737 004552 000024 ;POWER DOWN ROUTINE
1254 004420 012737 000340 000026 $PWRDN: MOV $FILLUP,$PWRVEC ;SET FOR FAST UP
1255 004426 010046 MOV $340,$PWRVEC+2 ;PPIO:7
1256 004430 010146 MOV R0,-(SP) ;PUSH R0 ON STACK
1257 004432 010246 MOV R1,-(SP) ;PUSH R1 ON STACK
1258 004434 010346 MOV R2,-(SP) ;PUSH R2 ON STACK
1259 004436 010446 MOV R3,-(SP) ;PUSH R3 ON STACK
1260 004440 010546 MOV R4,-(SP) ;PUSH R4 ON STACK
1261 004442 017746 174332 MOV $SWR,-(SP) ;PUSH $SWR ON STACK
1262 004446 010667 000104 MOV SP,$SAVR6 ;SAVE SP
1263 004452 012737 004464 000024 MOV $PWRUP,$PWRVEC ;SET UP VECTOR
1264 004460 000000 HALT
1265 004462 000776 BR ,-2 ;HANG UP
1266
1267
1268 ;*****
1269 004464 012737 004552 000024 ;POWER UP ROUTINE
1270 004472 016706 000060 $PWRUP: MOV $SAVR6,SP ;GET SP
1271 004476 005067 000054 CLR $SAVR6 ;WAIT LOOP FOR THE TTY
1272 004502 005267 000050 18: INC $SAVR6 ;WAIT FOR THE INC
1273 004506 001375 BNE 18 ;OF WORD
1274 004510 012677 174264 MOV (SP)+,$SWR ;POP STACK INTO $SWR
1275 004514 012605 MOV (SP)+,R5 ;POP STACK INTO R5
1276 004516 012604 MOV (SP)+,R4 ;POP STACK INTO R4
1277 004520 012603 MOV (SP)+,R3 ;POP STACK INTO R3
1278 004522 012602 MOV (SP)+,R2 ;POP STACK INTO R2
1279 004524 012601 MOV (SP)+,R1 ;POP STACK INTO R1
1280 004526 012600 MOV (SP)+,R0 ;POP STACK INTO R0
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1281 004530 012737 004412 000024 MOV $PWRDN,$PWRVEC ;SET UP THE POWER DOWN VECTOR
1282 004536 012737 000340 000026 MOV $340,$PWRVEC+2 ;PPIO:7
1283 004544 104401 TYPE ;REPORT THE POWER FAILURE
1284 004546 004560 $PWRMSG: .WORD $POWER ;POWER FAIL MESSAGE POINTER
1285 004550 000002 PTI
1286 004552 000000 $ILLUP: HALT ;THE POWER UP SEQUENCE WAS STARTED
1287 004554 000776 BR ,-2 ; BEFORE THE POWER DOWN WAS COMPLETE
1288 004556 000000 $SAVR6: 0 ;PUT THE SP HERE
1289 004560 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
1290 004566 000122 .EVEN
1291 .SBTTL TPAP DECODER
1292
1293
1294 ;*****
1295 ;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
1296 ;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
1297 ;*OF THE DESIRED ROUTINE, THEN USING THE ADDRESS OBTAINED IT WILL
1298 ;*GO TO THAT ROUTINE.
1299
1300 004570 010046 $TRAP: MOV R0,-(SP) ;SAVE R0
1301 004572 016600 000002 MOV 2(SP),R0 ;GET TRAP ADDRESS
1302 004576 005740 TST -(R0) ;BACKUP BY 2
1303 004600 111000 MOV# (R0),R0 ;GET RIGHT BYTE OF TRAP
1304 004602 006304 ASL R0 ;POSITION FOR INDEXING
1305 004604 016000 004624 MOV $TRPAD(R0),R0 ;INDEX TO TABLE
1306 004610 000200 RTS R0 ;GO TO ROUTINE
1307
1308
1309 ;THIS IS USE TO HANDLE THE "GETPRI" MACRO
1310
1311 004612 011646 $TRAP2: MOV (SP)-,(SP) ;MOVE THE PC DOWN
1312 004614 016666 000004 000002 MOV 4(SP),2(SP) ;MOVE THE PSW DOWN
1313 004622 000002 RTI ;RESTORE THE PSW
1314
1315 .SBTTL TRAP TABLE
1316
1317 ;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
1318 ;*BY THE "TRAP" INSTRUCTION.
1319
1320 ;
1321 ; ROUTINE
1322 ; -----
1323 004624 004612 $TRPAD: .WORD $TRAP2
1324 004626 003424 $TYPE ;CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
1325 004630 004210 $TYPOC ;CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
1326 004632 004164 $TYPOS ;CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
1327 004634 004224 $TYPON ;CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
1328
1329 004636 002640 $RDCHR ;CALL=RDCHR TRAP+5(104405) TTY TYPEIN CHARACTER ROUTINE
1330 004640 002760 $RDLIN ;CALL=RDLIN TRAP+6(104406) TTY TYPEIN STRING ROUTINE
1331 004642 003264 $RDOCT ;CALL=RDOCT TRAP+7(104407) READ AN OCTAL NUMBER FROM TTY
1332
1333 004644 177777 TXCRC: -1
1334 004646 000571 571 ;M9301 = YA VERSION
1335 004650 000457 457 ;M9301 = YB VERSION
1336 004652 000243 243 ;M9301 = YC VERSION
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1337	004654	000635	635	JM9400	= YA(OR YC) VERSION
1338	004656	000207	207	JM9301	= YF VERSION
1339	004660	000670	670	JM7942	= YB VERSION
1340	004662	000132	132	JM9301	= YD VERSION
1341	004664	000374	374	JM9400	= YH (OR YK) VERSION
1342	004666	000630	630	JM9311	VERSION == REVISION * (ORIGINAL REV.)
1343	004670	000536	536	JM9301	= YH VERSION
1344	004672	000752	752	JM9301	= YE VERSION
1345	004674	000633	633	JM9301	= YJ VERSION
1346	004676	000650	650	JM9400	= YN VERSION
1347	004700	000710	710	JM9311	VERSION == REVISION A ( SECOND REV.)
1348	004702	177777	-1		
1349	004704	177777	-1		
1350	004706	177777	-1		
1351	004710	177777	-1		
1352	004712	177777	-1		
1353					
1354	004714	177777			
1355	004716	133725	133725	JM9301	= YA VERSION
1356	004720	017563	17563	JM9301	= YB VERSION
1357	004722	141744	141744	JM9301	= YC VERSION
1358	004724	047613	47613	JM9400	= YA(OR YC) VERSION
1359	004726	114175	114175	JM9301	= YF VERSION
1360	004730	146126	146126	JM7942	= YB VERSION
1361	004732	132161	132161	JM9301	= YD VERSION
1362	004734	143466	143466	JM9400	= YH(OR YK) VERSION
1363	004736	036751	36751	JM9311	VERSION== REVISION * (ORIGINAL REV.)
1364	004740	125411	125411	JM9301	= YH VERSION
1365	004742	066246	066246	JM9301	= YE VERSION
1366	004744	132367	132367	JM9301	= YJ VERSION
1367	004746	030210	30210	JM9400	= YN VERSION
1368	004750	036743	36743	JM9311	VERSION==REVISION A ( SECOND REV.)
1369	004752	177777	-1		
1370	004754	177777	-1		
1371	004756	177777	-1		
1372	004760	177777	-1		
1373	004762	177777	-1		
1374	004764	177777	-1		
1375					
1376	004766	177777			
1377	004770	001000	1000	JM9301	= YA VERSION
1378	004772	001000	1000	JM9301	= YB VERSION
1379	004774	001000	1000	JM9301	= YC VERSION
1380	004776	001000	1000	JM9400	= YA(OR YC) VERSION
1381	005000	001000	1000	JM9301	= YF VERSION
1382	005002	004000	4000	JM7942	= YB VERSION
1383	005004	001000	1000	JM9301	= YD VERSION
1384	005006	001000	1000	JM9400	= YH(OR YK) VERSION
1385	005010	001000	1000	JM9311	VERSION REVISION * (ORIGINAL VERSION)
1386	005012	000734	734	JM9301	= YH VERSION
1387	005014	001000	1000	JM9301	= YE VERSION
1388	005016	001000	1000	JM9301	= YJ VERSION
1389	005020	001000	1000	JM9400	= YN VERSION
1390	005022	001000	1000	JM9311	VERSION= REVISION A (SECOND VERSION)
1391	005024	177777	-1		
1392	005026	177777	-1		

1393	005030	177777	-1		
1394	005032	177777	-1		
1395	005034	177777	-1		
1396					
1397	005036	177777			
1398	005040	173000	173000	JM9301	= YA VERSION
1399	005042	173000	173000	JM9301	= YB VERSION
1400	005044	173000	173000	JM9301	= YC VERSION
1401	005046	173000	173000	JM9400	= YA(OR YC) VERSION
1402	005050	173000	173000	JM9301	= YF VERSION
1403	005052	170000	170000	JM7942	= YB VERSION
1404	005054	173000	173000	JM9301	= YD VERSION
1405	005056	173000	173000	JM9400	= YH(OR YK) VERSION
1406	005060	163000	163000	JM9311	VERSION REVISION * ( ORIGINAL VERSION)
1407	005062	173000	173000	JM9301	= YH VERSION
1408	005064	173000	173000	JM9301	= YE VERSION
1409	005066	173000	173000	JM9301	= YJ VERSION
1410	005070	173000	173000	JM9400	= YN VERSION
1411	005072	163000	163000	JM9311	VERSION REVISION A (SECOND VERSION)
1412	005074	177777	-1		
1413	005076	177777	-1		
1414	005100	177777	-1		
1415	005102	177777	-1		
1416	005104	177777	-1		
1417					
1418	005106	177777			
1419	005110	001000	1000	JM9301	= YA VERSION
1420	005112	001000	1000	JM9301	= YB VERSION
1421	005114	001000	1000	JM9301	= YC VERSION
1422	005116	001000	1000	JM9400	=YA(OR YC) VERSION
1423	005120	001000	1000	JM9301	=YF VERSION
1424	005122	000000	0	JM7942	= YB VERSION
1425	005124	001000	1000	JM9301	= YD VERSION
1426	005126	001000	1000	JM9400	= YH(OR YK) VERSION
1427	005130	001000	1000	JM9311	VERSION REVISION * (ORIGINAL REV.)
1428	005132	000764	764	JM9301	= YH VERSION
1429	005134	001000	1000	JM9301	= YE VERSION
1430	005136	001000	1000	JM9301	= YJ VERSION
1431	005140	001000	1000	JM9400	= YN VERSION
1432	005142	001000	1000	JM9311	VERSION REVISION A (SECOND REV.)
1433	005144	177777	-1		
1434	005146	177777	-1		
1435	005150	177777	-1		
1436	005152	177777	-1		
1437	005154	177777	-1		
1438					
1439	005156	177777			
1440	005160	165000	165000	JM9301	= YA VERSION
1441	005162	165000	165000	JM9301	= YB VERSION
1442	005164	165000	165000	JM9301	= YC VERSION
1443	005166	165000	165000	JM9400	= YA(OR YC) VERSION
1444	005170	165000	165000	JM9301	= YF VERSION
1445	005172	000000	0	JM7942	= YB VERSION
1446					
1447	005174	165000	165000	JM9301	= YD VERSION
1448	005176	165000	165000	JM9400	= YH(OR YK) VERSION

1449 005200 166000 166000 ;M9311 VERSION REVISION \* ( ORIGINAL REV.)  
1450 005202 165000 165000 ;M9301 = YH VERSION  
1451 005204 165000 165000 ;M9301 = YE VERSION  
1452 005206 165000 165000 ;M9301 = YJ VERSION  
1453 005210 165000 165000 ;M9400 = YN VERSION  
1454 005212 166000 166000 ;M9311 VERSION REVISION A ( SECOND REV.)  
1455 005214 177777 -1  
1456 005216 177777 -1  
1457 005220 177777 -1  
1458 005222 177777 -1  
1459 005224 177777 -1  
1460  
1461 005226 177777 TMSG1 = -1  
1462 005230 006106 MSG1  
1463 005232 006123 MSG2  
1464 005234 006140 MSG3  
1465 005236 006155 MSG4  
1466 005240 006175 MSG5  
1467 005242 006212 MSG6 ;M7942 = YB VERSION  
1468 005244 006227 MSG7  
1469 005246 006244 MSG10  
1470 005250 006270 MSG11  
1471 005252 006347 MSG12 ;M9301 = YH VERSION  
1472 005254 006364 MSG13 ;M9301 = YE VERSION  
1473 005256 006401 MSG14 ;M9301 = YJ VERSION  
1474 005260 006416 MSG15 ;M9400 = YN VERSION  
1475 005262 006433 MSG16 ; M9311 REVISION A ( SECOND REV.)  
1476 005264 177777 -1  
1477 005266 177777 -1  
1478 005270 177777 -1  
1479 005272 177777 -1  
1480 005274 177777 -1  
1481  
1482  
1483 005276 005015 030122 020115 TITL: ,ASCIZ <15><12>/POM TEST/  
1484 005304 042524 052123 0000  
1485 005311 015 052012 050131 GETCRC: ,ASCIZ <15><12>/TYPE CRC VALUE: /  
1486 005316 020105 051103 020103  
1487 005324 040526 052514 035105  
1488 005332 020040 0000  
1489 005335 015 052012 050131 GETLPC: ,ASCIZ <15><12>/TYPE LPC VALUE: /  
1490 005342 020105 050114 020103  
1491 005350 040526 052514 035105  
1492 005356 020040 0000  
1493 005361 015 042412 050130 EXCRMG: ,ASCIZ <15><12>/EXPECTED CRC = /  
1494 005366 041505 042524 020104  
1495 005374 051103 020103 020075  
1496 005402 000040 0000  
1497 005404 005015 042412 050130 EXLPMG: ,ASCIZ <15><12><12>/EXPECTED LPC = /  
1498 005412 041505 042524 020104  
1499 005420 050114 020103 020075  
1500 005426 000040 0000  
1501 005430 005015 047503 050115 ACCRMG: ,ASCIZ <15><12>/COMPUTED CRC = /  
1502 005436 052125 042105 041440  
1503 005444 041522 036440 020040  
1504 005452 0000

1505 005453 015 041412 046517 ACLPMG: ,ASCIZ <15><12>/COMPUTED LPC = /  
1506 005460 052524 042524 020104  
1507 005466 050114 020103 020075  
1508 005474 000040 0000  
1509 005476 005015 042412 042116 EOTST: ,ASCIZ <15><12><12>/END OF TEST/  
1510 005504 047440 020106 042524  
1511 005512 052123 0000  
1512 005515 015 052012 050131 SA1: ,ASCIZ <15><12>/TYPE STARTING ADDR. OF 1ST ROM ADDR. SPACE: /  
1513 005522 020105 052123 051101  
1514 005530 044524 043516 040440  
1515 005536 042104 027122 047440  
1516 005544 020106 051461 020124  
1517 005552 047522 020115 042101  
1518 005560 051104 020056 050123  
1519 005566 041501 035105 020040  
1520 005574 0000  
1521 005575 015 052012 050131 SA2: ,ASCIZ <15><12>/TYPE STARTING ADDR. OF 2ND ROM ADDR. SPACE: /  
1522 005602 020105 052123 051101  
1523 005610 044524 043516 040440  
1524 005616 042104 027122 047440  
1525 005624 020106 047062 020104  
1526 005632 047522 020115 042101  
1527 005640 051104 020056 050123  
1528 005646 041501 035105 020040  
1529 005654 0000  
1530 005655 015 052012 050131 SIZE1: ,ASCIZ <15><12>/TYPE LENGTH (BYTES) OF 1ST ROM ADDR. SPACE: /  
1531 005662 020105 042514 043516  
1532 005670 044124 024040 054502  
1533 005676 042524 024523 047440  
1534 005704 020106 051461 020124  
1535 005712 047522 020115 042101  
1536 005720 051104 020056 050123  
1537 005726 041501 035105 020040  
1538 005734 0000  
1539 005735 015 052012 050131 SIZE2: ,ASCIZ <15><12>/TYPE LENGTH (BYTES) OF 2ND ROM ADDR. SPACE: /  
1540 005742 020105 042514 043516  
1541 005750 044124 024040 054502  
1542 005756 042524 024523 047440  
1543 005764 020106 047062 020104  
1544 005772 047522 020115 042101  
1545 006000 051104 020056 050123  
1546 006006 041501 035105 020040  
1547 006014 0000  
1548 006015 015 000012 CARLFF: ,ASCIZ <15><12>  
1549 006020 020040 0000 SP2: ,ASCIZ / /  
1550 006023 015 040412 042104 TYPHDR: ,ASCIZ <15><12>/ADDRESS DATA/  
1551 006030 042522 051523 020040  
1552 006036 020040 020040 020040  
1553 006044 020040 020040 020040  
1554 006052 042040 052101 000101  
1555 006060 020072 000040 COLON: ,ASCIZ / /  
1556 006064 005015 047125 047113 AUTERM: ,ASCIZ <15><12>/UNKNOWN MODULE /  
1557 006072 053517 020116 047515  
1558 006100 052504 042514 000040  
1559  
1560 006106 005015 034515 030063 MSG11: ,ASCIZ <15><12>/M9301 = YA/



AUTERR	002612	852	864#						
AUT1	002524	850#	854						
AUT2	002570	857#	859#						
AUT3	002572	856	860#						
AVECT1	000000	569							
AVECT2	000000	569							
BIT0	000001	485#							
BIT00	000001	475#	485						
BIT01	000002	474#	484						
BIT02	000004	473#	483						
BIT03	000010	472#	482						
BIT04	000020	471#	481						
BIT05	000040	470#	480						
BIT06	000100	469#	479						
BIT07	000200	468#	478						
BIT08	000400	467#	477						
BIT09	001000	466#	476						
BIT1	000002	484#							
BIT10	002000	465#							
BIT11	004000	464#							
BIT12	010000	463#							
BIT13	020000	462#							
BIT14	040000	461#							
BIT15	100000	460#							
BIT2	000004	483#							
BIT3	000010	482#							
BIT4	000020	481#							
BIT5	000040	480#							
BIT6	000100	479#							
BIT7	000200	478#							
BIT8	000400	477#							
BIT9	001000	476#							
BPTVEC	000014	492#							
CARLF	006015	832	837	1548#					
CHECK	001510	642	644	646	673#				
CH0	001566	679	686#						
CH1	001644	688	697	699#					
CK1	001736	700	713	718#	862				
CK2	002030	719	731	736#					
CLLAST	002204	773	775#						
CLP0	002252	784#	790						
CLP1	002264	785	787#						
CLP2	002316	794	796#						
CL0	002102	751#	757	774					
CL1	002146	765#							
CL2	002170	762	771#						
CL3	002122	753	758#						
COLON	006060	841	1555#						
CR	000015	400#	1099	1109					
CRC	002074	681	689	750#					
CRLF	000200	401#	1070	1109					
DATLN1	001006	523#	638#	664#	678	683	818		
DATLN2	001012	525#	640#	672#	687	691	823		
DDISP	177570	407#	609						
DISPLA	001002	521#	609#	617#					
DISPRE	000174	508#	617						

DSWR	177570	406#	608						
EMTVEC	000030	495#							
ENDOT	002446	825	827#						
EOTST	005476	739	928	1509#					
ERRVEC	000004	488#	606	607#	618#				
EXCRC	001016	527#	637#	652#	699	704	860#		
EXCRNG	005361	703	1493#						
EXLPC	001020	528#	636#	656#	718	722	861#		
EXLPMG	005404	721	1497#						
GETCRC	005311	650	1485#						
GETIN	001370	634	643#						
GETLPC	005335	654	1489#						
GET2	001440	648	657#						
GNS	***** U	507	1323	1324	1325	1326	1329	1330	1331
GOAGIN	002070	742	748#						
HT	000011	398#	1068	1109					
IOTVEC	000020	493#							
LF	000012	399#	1103	1109					
LPC	002344	805	693	805#					
LPC1	002352	806#	811						
LPC2	002366	808	810#						
MSG1	006106	1462	1560#						
MSG10	006244	1469	1581#						
MSG11	006270	1470	1585#						
MSG12	006347	1471	1593#						
MSG13	006364	1472	1596#						
MSG14	006401	1473	1599#						
MSG15	006416	1474	1602#						
MSG16	006433	1475	1605#						
MSG2	006123	1463	1563#						
MSG3	006140	1464	1566#						
MSG4	006155	1465	1569#						
MSG5	006175	1466	1572#						
MSG6	006212	1467	1575#						
MSG7	006227	1468	1578#						
PARCNT	001026	531#	782#	786#	793				
PARITY	002242	758	782#						
PIPO	177772	405#							
PIROVE	000240	499#							
PR0	000000	422#							
PR1	000040	423#							
PR2	000100	424#							
PR3	000140	425#							
PR4	000200	426#							
PR5	000240	427#							
PR6	000300	428#							
PR7	000340	429#							
PS	177776	402#	403	516#					
PSW	177776	403#							
PWRVEC	000024	494#	602#	603#	1253#	1254#	1263#	1269#	1281#
RDCMR	104405	921	1329#						
RDLIN	104406	96	1330#						
RDOCT	104407	651	655	659	663	667	671	1331#	
RESVRT	001274	626	630#	748					
RESVEC	000010	489#							
ROMSA1	001004	522#	639#	660#	680	682	817		



