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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DFP-A-D
 PRODUCT NAME: PDP-11/34 FPP DIAGNOSTIC PART 1
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 AUTHOR: ANTHONY VEZZA

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1. ABSTRACT

 THE THREE PROGRAMS:

DFFPA DFFPB DFFPC

ARE DESIGN TO DETECT AND REPORT LOGIC FAULTS IN THE PDP 11/34 FP11-A FLOATING POINT PROCESSOR. THE DESIGN IS AN ATTEMPT TO REACH ALL ROM STATES, TAKE ALL BRANCH MICRO TESTS (BUT'S) AND VERIFY ALL THE LOGIC. THEY CONSIST OF 155 (OCT) INDIVIDUAL TESTS SEQUENCED TO DETECT AND ATTEMPT TO IDENTIFY FAULTS WITH A MINIMUM HARDWARE OR SOFTWARE LEVEL. THE TESTS ARE PARTIONED INTO THREE STAND-ALONE PROGRAMS DESCRIBED BELOW.

NOTE THAT ERROR REPORTS IN THESE PROGRAMS ARE BASED UPON THE KNOWLEDGE THAT ALL PREVIOUS TESTS HAVE BEEN RUN AND IN MOST CASE THAT THERE IS ONLY A SINGLE POINT FAULT IN THE FP11-A. IF THE PROGRAMS OR TESTS ARE NOT RUN IN ORDER THEN ERROR MESSAGES MAY NOT BE ACCURATE.

A. DFFPA

DFFPA TESTS:

LDFPS
 STFPS
 CFCC
 SETF, SETD, SETI AND SETL
 STST
 LDF AND LDD (ALL SOURCE MODES)
 STD (MODE 0 AND 1)
 ADDF, ADDD AND SUBD (MOST CONDITIONS)

B. DFFPB

DFFPB TESTS:

ADDF, ADDD AND SUBD (ALL CONDITIONS NOT TESTED IN DFFPA)
 CMPD AND CMPF
 DIVD AND DIVF
 MULD AND MULF
 MODD AND MODF

C. DFFPC

DFFPC TESTS:

STF AND STD (ALL MODES)
 STCFD AND STCDF
 CLRD AND CLRf
 NEGF AND NEGd

ABSF AND ABSO
 TSTF AND TSTD
 NEGF, ABSF AND TSTF (ALL SOURCE MODES)
 NEGF, ABSF AND TSTF (ALL SOURCE MODES)
 LDFFS (ALL SOURCE MODES)
 LDCIF AND LDCLF
 LDCID AND LDCLD
 LDEXP
 STFPS (ALL DESTINATION MODES)
 STCFL AND STCFI
 STCDL AND STCDI
 STEXP
 S*ST

2. REQUIREMENTS

2.1 EQUIPMENT

A POP 11/34 (WITH OR WITHOUT CONSOLE), LA30 (OR EQUIVALENT) AND AN FP11-A FLOATING POINT PROCESSOR. NOTE THAT A SPECIAL INTERRUPTS TEST MODULE IS BEING DESIGNED FOR USE IN THE MANUFACTURING ENVIRONMENT. WHEN THIS DEVICE IS PRESENT THE PROGRAM DFFPB WILL MAKE USE OF IT TO TEST THE FPP INTERRUPT ON BLE REQUEST FUNCTIONS.

2.2 STORAGE

ALL THREE PROGRAMS REQUIRE A MEMORY SYSTEM OF AT LEAST 16K TO LOAD AND RUN.

2.3 PRELIMINARY PROGRAMS

THESE THREE DIAGNOSTICS WILL ASSUME THAT THE POP 11/34 CENTRAL PROCESSOR IS FAULTLESS. THEREFORE WHEN IN OBJECT RUN THE POP 11/34 PROCESSOR DIAGNOSTICS BEFORE THESE FP11-A DIAGNOSTICS.

3. LOADING PROCEDURE

THE PROGRAMS WILL BE SUPPLIED ON THE 11/34 DIAGNOSTIC MEDIA. REFER TO THE XXDP OPERATING MANUAL FOR FURTHER INFORMATION.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE SECTION 5.1

4.2 PROGRAM AND OPERATOR ACTION

LOAD PROGRAM INTO MEMORY
 LOAD ADDRESS 200
 SET CONSOLE SWITCHES (IF CONSOLE IS PRESENT).
 PRESS START
 ON FIRST PASS THE PROGRAM
 WILL IDENTIFY ITSELF. NOTE THAT IF THERE IS
 NO PHYSICAL CONSOLE THE PROGRAM WILL REQUEST
 THE OPERATOR FOR INITIAL VALUE FOR THE
 SOFTWARE SWITCH REGISTER (SEE SECTION B.5).
 IF RUNNING UNDER ACT, APT OR CHAIN THIS DOES
 NOT APPLY.
 THE PROGRAM WILL LOOP AND AN END OF PASS AND
 ERROR SUMMARY WILL BE TYPED AT THE END OF
 EVERY PASS.

5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

THE SWITCH SETTING ARE:

	OCTAL	
SW<15>=1...	100000	HALT ON ERROR
SW<14>=1...	40000	LOOP ON CURRENT TEST
SW<13>=1...	20000	INHIBIT ERROR TYPE OUTS
SW<12>=1...	10000	INHIBIT 7-BIT TRAPPING
SW<11>=1...	4000	INHIBIT ITERATIONS
SW<10>=1...	2000	RING TTY BELL ON ERROR
SW<9>=1....	1000	LOOP ON ERROR
SW<8>=1....	400	LOOP ON TEST SPECIFIED IN SW<5> THROUGH SW<0>
SW<7>=1....	200	PRINT ERROR SUMMARY EVEN IF SW<13>=1. THIS APPLIES ONLY TO PROGRAM DFFPA.
SW<7>=1....	200	DESELECT CORRECT INTERRUPT TEST IN PROGRAM DFFPB. NOTE THAT THIS TEST WILL AUTOMATICALLY BE DESELECTED BY THE ABSENCE OF THE SPECIAL TEST EQUIPMENT DESIGNED TO CONDUCT THIS TEST. IF THIS EQUIPMENT IS NOT INSTALLED THERE IS NO NEED TO DESELECT THIS TEST. THIS APPLIES ONLY TO PROGRAM DFFPB!

6. ERRORS

6.1 SUMMARIES

IN PROGRAM DFFPA TESTS 1 AND 11 HAVE A SPECIAL ERROR
 SUMMARY FEATURE. THESE TWO TEST RUN MANY TEST
 PATTERNS THROUGH THE LOGIC. AFTER AN ERROR IS
 ENCOUNTERED, ONLY THE FIRST FIVE ERRORS ARE REPORTED

EACH OF THE THREE PROGRAMS WILL RUN WITH OR WITHOUT A CONSOLE SWITCH REGISTER. IF A PHYSICAL CONSOLE SWITCH REGISTER IS PRESENT ON THE SYSTEM, THEN THESE PROGRAMS WILL GO AHEAD AND USE IT FOR THE SWITCH FUNCTIONS DESCRIBED IN 5.1 ABOVE. IF HOWEVER THERE IS NO CONSOLE SWITCH REGISTER ON THE SYSTEM A SOFTWARE SWITCH REGISTER WILL BE USED. THIS SOFTWARE SWITCH REGISTER CAN BE EXAMINED OR MODIFIED AT ANY TIME BY THE USER IF HE TYPES CONTROL G WHILE THE PROGRAM IS RUNNING. THIS CONTROL G WILL CAUSE THE CONTENTS OF THE SOFTWARE SWITCH REGISTER TO BE TYPED ON THE TTY AND ASK THE USER FOR A NEW VALUE. WHEN THE USER TYPES A VALUE AND CARRIAGE RETURN THEN THE PROGRAM WILL RESUME TESTING AT THE SAME POINT AT WHICH IT LEFT OFF WHEN THE USER TYPED CONTROL G. NOTE THAT WHEN NOT RUNNING UNDER ACT, APT OR CHAIN THE USER WILL BE ASKED FOR A SOFTWARE SWITCH REGISTER VALUE AFTER LOADING ADDRESS 200 AND STARTING THE PROGRAM THE FIRST TIME THE PROGRAM IS RUN AFTER LOADING (ONLY IF NO CONSOLE SWITCH REGISTER IS ON THE SYSTEM).

8.5 INTERRUPTS TEST

IN PROGRAM DFFPB THERE IS A SPECIAL TEST FOR CHECKING THE CORRECT FLOWS OF THE FPP. THIS TEST CAN BE RUN ONLY IF A SPECIAL TEST MODULE IS IN THE SYSTEM. THIS MODULE WILL PROBABLY ONLY BE USED IN MANUFACTURING. IF THIS MODULE IS NOT IN THE SYSTEM THIS TEST WILL AUTOMATICALLY BE DESELECTED. IF THIS TEST MODULE IS ON THE SYSTEM AND SW(7)=0 THIS TEST WILL BE RUN. IF SW(7)=1 THIS TEST WILL BE DESELECTED.

8.7 ACT, APT AND XXDP COMPATIBILITY

THESE PROGRAMS ARE FULLY COMPATIBLE WITH:
 APT
 ACT
 XXDP MONITOR AND CHAIN PROGRAMS.

9. PROGRAM DESCRIPTION

TEST 1 LDFPS, STFPS AND DATA PATHS TEST

THIS IS A TEST OF THE LDFPS (LOAD FLOATING POINT STATUS) AND STFPS (STORE FLOATING POINT STATUS) INSTRUCTIONS. A COUNT PATTERN IS GENERATED AND RUN THROUGH THE FLOATING POINT STATUS REGISTER. THIS WILL TEST THE 16-BIT TRI STATE BUS WHICH CONNECTS THE CPU WITH THE FPP AND ALSO RUNS INTERNALLY WITHIN THE FPP. ONLY DMO AND SMO ARE USED. NOTE THAT A MASK MUST BE USED BECAUSE SOME OF THE FPS BITS CANNOT BE SET.

ONLY THE FIRST FIVE ERRORS WILL BE REPORTED INDIVIDUALLY. THIS IS TO PREVENT LOCKING OUT THE COMPLETION OF THE TEST BECAUSE OF VIRTUALLY ENDLESS NUMBER OF ERRORS. ONLY FIVE INDIVIDUAL ERRORS WILL BE REPORTED THEN THE TEST WILL BE COMPLETED AND AN ERROR SUMMARY GIVEN (SEE NOTE BELOW).

NOTE THAT THIS TEST KEEPS A DYNAMIC RECORD OF THE LOGICAL 'AND' AND 'OR' OF THE FAILING DATA PATTERNS. THESE CAN BE VERY USEFUL IN DETERMINING STUCK BITS. IF THE USER HAS THE INHIBIT ERROR TYPE OUT SWITCH (SWR13) OFF, THEN THE USER WILL RECIEVE EACH INDIVIDUAL ERROR MESSAGE PLUS AN ERROR SUMMARY AT THE END OF THE TEST. INHIBITING ERROR PRINT OUT WILL INHIBIT ERROR SUMMARY PRINT OUT, EXCEPT IN THE CASE DESCRIBED BELOW. TO GET JUST THE ERROR SUMMARY WITH NO INDIVIDUAL ERROR REPORTS, SET SWITCH REGISTER BIT13 AND SWITCH REGISTER BIT7 BOTH ON.

TEST 2 CFCC TEST

THIS IS A TEST OF THE COPY CONDITION CODES INSTRUCTION, CFCC.

TEST 3 SETF, SETD, SETI AND SETL TEST

THIS IS A TEST OF THE SETF, SETD, SETI AND SETL INSTRUCTIONS. EACH INSTRUCTION IS EXECUTED WITH THE FPS CONTAINING ALL ONES AND ALSO WITH THE FPS CLEAR. THE RESULT OF EACH SITUATION IS CHECKED.

TEST 4 ILLEGAL FPP OP CODES AND STST TEST

THIS IS A TEST OF THE FPP OPERATION CODES:

170004
:
170010
170013
170014
:
170077

THESE ARE ILLEGAL INSTRUCTIONS AND WITH INTERRUPTS ENABLED SHOULD CAUSE A TRAP TO 244. ALSO TESTED HERE IS THE INSTRUCTION: STST R1, WHICH SHOULD PUT THE FEC CODE 2 IN R1, AFTER ANY OF THE ABOVE CP CODES IS EXECUTED.

TEST 5 FID, INTERRUPT DISABLE, BIT TEST

THIS IS A TEST OF FPS BIT 14 (FID) OR FLOATING INTERRUPT DISABLE. AN ILLEGAL INSTRUCTION IS EXECUTED WITH FID=1. NO INTERRUPT SHOULD OCCUR.

TEST 6 LDD AND STD, WITH SRC AND DST MODE 1. TEST

THIS IS A TEST OF BOTH THE INSTRUCTION:
LDD (RD).ACD
AND THE INSTRUCTION:
STD ACD,(RD)
MOST OF THE FAILURES ARE ISOLATED TO THE SRC OR DST FLOWS. NOTE THAT THE INTEGRITY OF ACD HAS NOT BEEN ASSURED. THIS MEANS THAT IN SOME CASES IT WILL BE IMPOSSIBLE TO ISOLATE CERTAIN DATA PATTERN FAILURES TO EITHER THE FLOWS OR THIS ACCUMULATOR.

TEST 7 FSRC MODE 0 TEST

THIS IS A TEST OF FSRC MODE ZERO USING THE LDD AND LDF INSTRUCTIONS.

TEST 10 FDST MODE 0 TEST

THIS IS A TEST OF THE STORE INSTRUCTIONS. STD AND STF, WITH FDST MODE 0.

TEST 11 ACCUMULATORS DATA PATTERNS TEST

THIS IS A TEST OF THE FLOATING POINT PROCESSOR ACCUMULATORS.

EACH ACCUMULATOR IS TESTED IN TWO WAYS:
1 TEST PATTERN GENERATED BY FLOATING A ONE ACROSS A FIELD OF ZEROES.

JFFP R.P.1 01-NOV-76 21:03
MACHINE: JFFPA-A
POP : 34
FPP DIAGNOSTIC PART 1
MACY11 27(1006)
01-NOV-76 21:09
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STILL APPLY!! IF THE FAILURE MOVES FROM ONE BIT TO ANOTHER, OR FROM ONE GROUP OF BITS TO ANOTHER GROUP OF BITS THEN THE FAULT WILL PROBABLY NOT SHOW UP IN THE 'AND' OR THE 'OR' PATTERNS: IN THIS CASE THE 'AND' PATTERN WILL BE ALL 0'S AND THE 'OR' PATTERN WILL BE ALL 1'S. WHEN THIS OCCURS SOME OTHER METHOD OF REPAIR MUST BE FOUND (SUCH AS INSPECTION OF EACH INDIVIDUAL ERROR REPORT RATHER THAN USING THE SUMMARY).

MAP THE FOLLOWING NOTATION ONTO EACH BIT POSITION IN THE 'AND' AND THE 'OR' PATTERNS WHICH ARE TYPED IN THE ERROR SUMMARY.

A15,A14,...A1,A0 B15,B14,...B1,B0
C15,C14,...C1,C0 D15,D14,...D1,D0

IN THIS NOTATION A15 THROUGH A0 IS THE FIRST OF THE FOUR 16 BIT OCTAL NUMBERS TYPED. B15 THROUGH B0 IS THE SECOND, ETC.

THIS TABLE SHOWS THE CORRESPONDING AM2901 CHIP (E' NUMBER) WHICH IS RESPONSIBLE FOR EACH BIT POSITION USING THE ABOVE NOTATION. NOTE THAT ECO'S TO THE HARDWARE MIGHT MAKE THIS TABLE OBSOLETE IF IT IS NOT UP DATED. NOTE ALSO THAT THERE ARE FOUR BITS FOR EACH AM2901 CHIP:

BITS ----	AM2901 CHIP NUMBER -----
A15,A14,A13,A12	E61
A11,A10,A9,A8	E62
A7,A6,A5,A4	E90
A3,A2,A1,A0	E81
B15,B14,B13,B12	E86
B11,B10,B9,B8	E85
B7,B6,B5,B4	E83
B3,B2,B1,B0	E88
C15,C14,C13,C12	E79
C11,C10,C9,C8	E84
C7,C6,C5,C4	E89
C3,C2,C1,C0	E87
D15,D14,D13,D12	E78
D11,D10,D9,D8	E77
D7,D6,D5,D4	E82
D3,D2,D1,D0	E80

NOW FIVE IMPORTANT CASES WHICH WILL ARRISE WHEN A FAULTY AM2901 IS PRESENT CAN BE DESCRIBED:

- 1.) IF ONLY ONE BIT OF THE 64 BITS IS INCORRECT

THE CHIP INDICATED IN THE ABOVE TABLE IS MOST PROBABLY AT FAULT. BUT IF THAT CHIP IS REPLACED AND THE ERROR PERSISTS THEN SUPPOSE THAT BIT IS.

LN WHERE 'L' IS A, B, C OR D
AND N IS 15, 14, ... OR 0

THEN IN GENERAL ANY OF THE FOUR CHIPS RESPONSIBLE FOR AN, BN, CN OR DN COULD BE AT FAULT, WITH LN BEING MOST PROBABLE.

FOR EXAMPLE IF BIT C12 IS FAULTY, THEN CHIP E79 IS THE MOST PROBABLE SOURCE OF THE ERROR. IF REPAIRING THAT CHIP DOES NOT REMOVE THE FAULT THEN TRY EACH OF THE CHIPS ASSOCIATED WITH BITS A12, B12 AND D12 SHOULD BE TRIED WITH EQUAL PROBABILITY OF THE FAULT BEING IN ANY ONE OF THESE OTHER THREE CHIPS, TRY CHIPS E61, E36 AND E78.

- 2.) IF THERE ARE FOUR CONSECUTIVE BITS IN ERROR, FOLLOWING THE PATTERN:

LN, LN+1, LN+2 AND LN+3 WHERE 'L' IS A, B, C OR D
N=0, 4, 8 OR 12

THEN THE ABOVE TABLE SHOULD DIRECTLY IDENTIFY THE FAILING CHIP.

- 3.) IF FOUR BITS ARE DROPPED WHICH FIT THE PATTERN:

AN, BN, CN AND DN WHERE N=15, 14, ... OR 0
OR 0

THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH EACH OF THE BITS AN, BN, CN AND DN COULD BE AT FAULT WITH EQUAL PROBABILITY.

- 4.) IF 16 BITS ARE IN ERROR, FITTING THE PATTERN:

AN, AN+1, AN+2, AN+3 WHERE N=0, 4, 8 OR 12
BN, BN+1, BN+2, BN+3
CN, CN+1, CN+2, CN+3
AND
DN, DN+1, DN+2, AN+3

THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH THESE BITS COULD BE AT FAULT WITH EQUAL PROBABILITY.

- 5.) IF THE FAILING BIT PATTERNS DISPLAYED IN THE 'AND' AND THE 'OR' DATA TYPED IN THE SUMMARY DOES NOT CONFORM EXPLICITELY TO ANY OF THE

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ABOVE PATTERNS, THEN THE TROUBLE SHOOTER MUST INTUITIVELY TRY TO FIND WHICH OF THE ABOVE CASES (1 THROUGH 4) IS A 'BEST FIT' OF THE SYMPTOMS.

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TEST 12 FPP ACCUMULATORS DUAL ADDRESS TEST

THIS TEST PERFORMS A DUAL ADDRESSING TEST ON THE FLOATING ACCUMULATORS. NOTE THAT ACCUMULATOR ZERO IS USED TO ACCESS ALL THE OTHERS.

TEST 13 FSRC MODE 0 WITH ILLEGAL ACCUMULATOR TEST

THIS IS A TEST OF FSRC MODE 0 WITH ACCUMULATORS 6 AND 7. USE OF EITHER OF THESE NON-EXISTENT ACCUMULATORS SHOULD RESULT IN A TRAP TO 244 WITH FEC=2 (ILLEGAL FPP INSTRUCTION).

TEST 14 FSRC MODE 2 TEST

THIS IS A TEST OF FSRC MODE 2, AUTO INCREMENT MODE.

TEST 15 FSRC MODE 4 TEST

THIS IS A TEST OF FSRC MODE 4, AUTO DECREMENT MODE.

TEST 16 FSRC MODE 2, WITH FD=0, TEST

THIS IS A TEST OF FSRC MODE 2 WITH FD=0. (AUTO INCREMENT)

TEST 17 FSRC MODE 2 WITH GR7, IMMEDIATE MODE, TEST

THIS IS A TEST OF FSRC MODE 2 USING GR7 (THE PC). THIS IS IMMEDIATE MODE.

TEST 20 FSRC MODE 3 TEST

THIS IS A TEST OF FSRC MODE 3, AUTO INCREMENT DEFERRED

TEST 21 FSRC MODE 5 TEST

THIS IS A TEST OF FSRC MODE 5, AUTO DECREMENT DEFERRED.

TEST 22 FSRC MODE 6 TEST

THIS IS A TEST OF FSRC MODE 6, INDEX MODE

TEST 23 FSRC MODE 7 TEST

THIS IS A TEST OF FSRC MODE 7, INDEX DEFERRED MODE.

TEST 24 (BUT EZBT YB), (BUT ENBT) AND (BUT FIUV) TEST

THIS IS A TEST OF THE (BUT EZBT YB) FORK, THE (BUT
ENBT) FORK AND (BUT FIUV) FORK IN THE LOAD
INSTRUCTION FLOWS.
EACH OF THE PATTERNS:

0
+NUM
-NUM
-0

IS LOADED TWICE, ONCE WITH AC>0 THEN WITH AC=0.
AFTER EACH LOAD THE FPS IS CHECK TO INSURE THAT
CONTROL WAS PASSED THROUGH WITH THE FORKS PROPERLY.

TEST 25 ADDF, ADD, SUBF AND SUBD WITH FSRC=AC=0 TEST

THIS IS A TEST OF ADD AND SUB WITH FSRC=AC=0

TEST 26 ADD AND SUB WITH FSRC=0

THIS IS A TEST OF ADD AND SUB WITH FSRC=0.

TEST 27 SUBD WITH AC=0 TEST

THIS IS A TEST OF SUBD WITH AC=0. BOTH POSITIVE AND
NEGATIVE FSRC'S ARE TRIED.

TEST 30 ADD WITH AC=0 TEST

POSITIVE AND NEGATIVE FSRC'S ARE TRIED.

TEST 31 ADDF AND ADDD WITH E(AC)=E(FSRC) AND (BUT FT) TEST

THIS IS A TEST OF THE ADD INSTRUCTION WITH THE
OPERANDS HAVING EQUAL EXPONENTS. THE (BUT FT) FORK
IN THE ROUND/TRUNK FLOWS IS ALSO TESTED.

TEST 32 ADDF AND ADDD WITH E(AC) LESS THAN E(FSRC) TEST

THIS IS A TEST OF THE ADD AND ADDF INSTRUCTIONS AND THE ALIGN AC ALGORITHM FLOWS. THE CONSTANT (ES FOR FLOATING, 57 FOR DOUBLE) USED IS CHECKED. THEN SIMPLE AND WORST CASE ALIGNMENT SITUATIONS ARE TRIED. NOTE E(AC) IS LESS THEN E(FSRC).

 TEST 33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST

THIS IS A TEST OF THE ADD AND ADDF INSTRUCTIONS AND THE ALIGN FSRC ALGORITHM FLOWS. FIRST THE CONSTANT USED IS CHECKED. THEN SIMPLE AND WORST CASE ALIGNMENT SITUATIONS ARE TRIED. NOTE E(AC) IS GREATER THAN E(FSRC).

 TEST 34 ADD WITH NEGATIVE OPRANDS TEST

THIS IS A TEST OF THE ADD INSTRUCTION WITH NEGATIVE OPRANDS. EVERY COMBINATION OF OPRAND SIGNS IS TRIED.

 TEST 35 SUBD TEST

THIS IS A TEST OF THE SUBD INSTRUCTION. BOTH A POSITIVE AND A NEGATIVE NUMBER IS SUBTRACTED FROM IT SELF

 TEST 36 NORMALIZE ALGORITHM TEST

THIS IS A TEST OF THE NORMALIZE FLOW ALGORITHM. TWO PATTERNS ARE USED. FIRST THE MINIMUM SITUATION REQUIRING ONE LEFT SHIFT AND THEN THE MAXIMUM SITUATION REQUIRING 56 SHIFTS.

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000001
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000010

3
NUMBER=213
PROGNUM=1

.LIST ME
.NLIST MD,MC,CND

.ENABL ABS

.TITLE MAINDEC-11-DFFPA-A PDP 11/34 FPP DIAGNOSTIC PART 1
:*COPYRIGHT (C) SEP 1976
:*DIGITAL EQUIPMENT CORP.
:*MAYNARD, MASS. 01754
:*
:*PROGRAM BY ANTHONY S. VEZZA
:*
:*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
:*PACKAGE (MAINDEC-11-DZGAC-C2), SEPT 14, 1976.
:*

000001
000002

\$TN=1
\$SWR=160000 ::HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYPOLY

000244
077400

FPVECT=244
\$SWR=.77400

BASIC DEFINITIONS

```

SW05 = 40
SW04 = 20
SW03 = 10
SW02 = 4
SW01 = 2
SW00 = 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

```

::DATA BIT DEFINITIONS (BIT00 TO BIT15)

```

BIT15 = 10000
BIT14 = 4000
BIT13 = 2000
BIT12 = 1000
BIT11 = 400
BIT10 = 200
BIT09 = 100
BIT08 = 40
BIT07 = 20
BIT06 = 10
BIT05 = 4
BIT04 = 2
BIT03 = 1
BIT02 = 4
BIT01 = 2
BIT00 = 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

```

::BASIC "CPU" TRAP VECTOR ADDRESSES

```

ERRVEC = 4          ;; TIME OUT AND OTHER ERRORS
RESVEC = 10         ;; RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC = 14        ;; "T" BIT
TRTVEC = 14         ;; TRACE TRAP
BPTVEC = 14         ;; BREAKPOINT TRAP (BPT)
IOTVEC = 20         ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
PWRVEC = 24         ;; POWER FAIL
EMTVEC = 30         ;; EMULATOR TRAP (EMT) **ERROR**
TRAPVEC = 34        ;; "TRAP" TRAP
TKVEC = 60         ;; TTY KEYBOARD VECTOR

```



```

001300 000000
001301 000000
001302 000000
001303 000000
001304 000000
001305 000000
001306 000000
001307 000000
001308 000000
001309 000000
001310 000000
001311 000000
001312 000000
001313 000000
001314 000000
001315 000000
001316 000000
001317 000000
001318 000000
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001336 000000
001337 000000
001338 000000
001339 000000
001340 000000
001341 000000
001342 000000
001343 000000
001344 000000
001345 000000
001346 000000

```

000377

```

$REG1: .WORD 0 ::CONTAINS (($REGAD,+42)
$REG2: .WORD 0 ::CONTAINS (($REGAD,+44)
$REG3: .WORD 0 ::CONTAINS (($REGAD,+46)
$TMP0: .WORD 0 ::USER DEFINED
$TMP1: .WORD 0 ::USER DEFINED
$TMP2: .WORD 0 ::USER DEFINED
$TMP3: .WORD 0 ::USER DEFINED
$TMP4: .WORD 0 ::USER DEFINED
$TMP5: .WORD 0 ::USER DEFINED
$TMP6: .WORD 0 ::USER DEFINED
$TMP7: .WORD 0 ::USER DEFINED
$TMP10: .WORD 0 ::USER DEFINED
$TMP11: .WORD 0 ::USER DEFINED
$TMP12: .WORD 0 ::USER DEFINED
$TMP13: .WORD 0 ::USER DEFINED
$TMP14: .WORD 0 ::USER DEFINED
$TMP15: .WORD 0 ::USER DEFINED
$TMP16: .WORD 0 ::USER DEFINED
$TMP17: .WORD 0 ::USER DEFINED
$TMP20: .WORD 0 ::USER DEFINED
$TMP21: .WORD 0 ::USER DEFINED
$TMP22: .WORD 0 ::USER DEFINED
$TMP23: .WORD 0 ::USER DEFINED
$TIMES: 0 ::MAX. NUMBER OF ITERATIONS
$ESCAPE: 0 ::ESCAPE ON ERROR ADDRESS
$BELL: .ASCIZ <207><377><377> ::CODE FOR BELL
$QUES: .ASCII '?' ::QUESTION MARK
$CRLF: .ASCII <15> ::CARRIAGE RETURN
$LF: .ASCIZ <12> ::LINE FEED
*****
.SBTTL APT MAILBOX-ETABLE
*****

```

```

.EVEN
$MAIL: .WORD 0 ::APT MAILBOX
$MSGTY: .WORD 0 ::MESSAGE TYPE CODE
$FATAL: .WORD 0 AFATAL ::FATAL ERROR NUMBER
$TESTN: .WORD 0 ATESTN ::TEST NUMBER
$PASS: .WORD 0 APASS ::PASS COUNT
$DEVCT: .WORD 0 ADEVCT ::DEVICE COUNT
$UNIT: .WORD 0 AUNIT ::I/O UNIT NUMBER
$MSGAD: .WORD 0 AMSGAD ::MESSAGE ADDRESS
$MSGLG: .WORD 0 AMSGLG ::MESSAGE LENGTH
$ETABLE: .WORD 0 ::APT ENVIRONMENT TABLE
$ENV: .BYTE 0 AENV ::ENVIRONMENT BYTE
$ENVM: .BYTE 0 AENVM ::ENVIRONMENT MODE BITS
$SWREG: .WORD 0 ASWREG ::APT SWITCH REGISTER
$USWR: .WORD 0 AUSWR ::USER SWITCHES
$CPUOP: .WORD 0 ACPUOP ::CPU TYPE, OPTIONS
*
* BIT 15-11=CPU TYPE
* 11/04=01,11/05=02,11/20=03,11 40=04,11 45=05
* 11/70=06,PDQ=07,Q=10
*
* BIT 10=REAL TIME CLOCK
* BIT 9=FLOATING POINT PROCESSOR
* BIT 8=MEMORY MANAGEMENT
*
$MAMS1: .BYTE 0 AMAMS1 ::HIGH ADDRESS,M.S. BYTE

```


ERROR POINTER TABLE

.SBTTL ERROR POINTER TABLE

*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

* EM ::POINTS TO THE ERROR MESSAGE
* DH ::POINTS TO THE DATA HEADER
* DT ::POINTS TO THE DATA
* DF ::POINTS TO THE DATA FORMAT

1186
1187
1188
1189
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1200
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Table with columns for error codes (e.g., 001442, 043025, 063250, 067664) and pointers (EM, DH, DT, DF). Includes headers like 'SERRTB: ITEM 1' and 'ITEM 2'.

124	001620	066723			: ITEM 17	
124	001622	043754	063740	067730	.WORD	EM17, DH17, DT17, DF17
124	001630	066631			: ITEM 20	
124	001632	044207	064030	070166	.WORD	EM20, DH20, DT20, DF20
124	001640	066732			: ITEM 21	
124	001642	044365	063620	070210	.WORD	EM21, DH21, DT21, DF21
124	001650	066742			: ITEM 22	
124	001652	044516	064116	070222	.WORD	EM22, DH22, DT22, DF22
124	001660	066746			: ITEM 23	
124	001662	044516	064153	070250	.WORD	EM23, DH23, DT23, DF23
124	001670	066760			: ITEM 24	
124	001672	044516	064311	070272	.WORD	EM24, DH24, DT24, DF24
124	001700	066770			: ITEM 25	
124	001702	044603	064450	070316	.WORD	EM25, DH25, DT25, DF25
124	001710	066777			: ITEM 26	
124	001712	044716	064512	070366	.WORD	EM26, DH26, DT26, DF26
124	001720	067023			: ITEM 27	
124	001722	044716	064512	070442	.WORD	EM27, DH27, DT27, DF27
124	001730	067050			: ITEM 30	
124	001732	044764	000000	070504	.WORD	EM30, DH30, DT30, DF30
124	001740	067070			: ITEM 31	
124	001742	045036	064512	070366	.WORD	EM31, DH31, DT31, DF31
124	001750	067023			: ITEM 32	
124	001752	045036	064512	070442	.WORD	EM32, DH32, DT32, DF32
124	001760	067050			: ITEM 33	
124	001762	045104	064600	070536	.WORD	EM33, DH33, DT33, DF33
124	001770	067104			: ITEM 34	
124	001772	045145	064600	070614	.WORD	EM34, DH34, DT34, DF34
124	002000	067132			: ITEM 35	
124	002002	045247	064600	070614	.WORD	EM35, DH35, DT35, DF35
124	002010	067132			: ITEM 36	
124	002012	045351	064600	070614	.WORD	EM36, DH36, DT36, DF36
124	002020	067132			: ITEM 37	
124	002022	045452	064600	070614	.WORD	EM37, DH37, DT37, DF37
124	002030	067132			: ITEM 40	
124	002032	045553	064600	070536	.WORD	EM40, DH40, DT40, DF40
124	002040	067156			: ITEM 41	

1298	002042	045724	000000	070666		.WORD	EM41,DM41,DT41,DF41
1299	002050	067204					
1300					:ITEM 42		
1301	002052	045761	064703	070720		.WORD	EM42,DM42,DT42,DF42
1302	002060	067220					
1303					:ITEM 43		
1304	002062	046102	064703	070720		.WORD	EM43,DM43,DT43,DF43
1305	002070	067220					
1306					:ITEM 44		
1307	002072	046223	000000	070776		.WORD	EM44,DM44,DT44,DF44
1308	002100	067246					
1309					:ITEM 45		
1310	002102	046223	065005	071046		.WORD	EM45,DM45,DT45,DF45
1311	002110	067271					
1312					:ITEM 46		
1313	002112	046266	065024	071122		.WORD	EM46,DM46,DT46,DF46
1314	002120	067316					
1315					:ITEM 47		
1316	002122	046344	065005	071210		.WORD	EM47,DM47,DT47,DF47
1317	002130	067350					
1318					:ITEM 50		
1319	002132	046462	065050	070614		.WORD	EM50,DM50,DT50,DF50
1320	002140	067365					
1321					:ITEM 51		
1322	002142	046560	065050	071242		.WORD	EM51,DM51,DT51,DF51
1323	002150	067411					
1324					:ITEM 52		
1325	002152	046621	063620	071210		.WORD	EM52,DM52,DT52,DF52
1326	002160	067350					
1327					:ITEM 53		
1328	002162	046742	064512	071300		.WORD	EM53,DM53,DT53,DF53
1329	002170	067427					
1330					:ITEM 54		
1331	002172	047137	065122	071320		.WORD	EM54,DM54,DT54,DF54
1332	002200	067436					
1333					:ITEM 55		
1334	002202	047203	063620	071210		.WORD	EM55,DM55,DT55,DF55
1335	002210	067350					
1336					:ITEM 56		
1337	002212	047324	064512	071300		.WORD	EM56,DM56,DT56,DF56
1338	002220	067427					
1339					:ITEM 57		
1340	002222	047521	065122	071320		.WORD	EM57,DM57,DT57,DF57
1341	002230	067436					
1342					:ITEM 60		
1343	002232	047565	064512	071300		.WORD	EM60,DM60,DT60,DF60
1344	002240	067427					
1345					:ITEM 61		
1346	002242	047762	065122	071320		.WORD	EM61,DM61,DT61,DF61
1347	002250	067436					
1348					:ITEM 62		
1349	002252	050026	065122	071320		.WORD	EM62,DM62,DT62,DF62
1350	002260	067436					
1351					:ITEM 63		
1352	002262	050220	065122	071320		.WORD	EM63,DM63,DT63,DF63
1353	002270	067436					

1354					: ITEM 64	
1355	002272	050412	065232	071356	.WORD	EM64, DH64, DT64, DF64
1356	002300	067454				
1357					: ITEM 65	
1358	002302	050412	065163	071356	.WORD	EM65, DH65, DT65, DF65
1359	002310	067454				
1360					: ITEM 66	
1361	002312	050546	065122	071320	.WORD	EM66, DH66, DT66, DF66
1362	002320	067436				
1363					: ITEM 67	
1364	002322	050611	063620	070210	.WORD	EM67, DH67, DT67, DF67
1365	002330	066742				
1366					: ITEM 70	
1367	002332	051042	063620	071376	.WORD	EM70, DH70, DT70, DF70
1368	002340	067463				
1369					: ITEM 71	
1370	002342	051165	064450	071376	.WORD	EM71, DH71, DT71, DF71
1371	002350	067463				
1372					: ITEM 72	
1373	002352	051267	064512	071444	.WORD	EM72, DH72, DT72, DF72
1374	002360	067505				
1375					: ITEM 73	
1376	002362	051343	065122	071320	.WORD	EM73, DH73, DT73, DF73
1377	002370	067436				
1378					: ITEM 74	
1379	0023	051403	063620	070210	.WORD	EM74, DH74, DT74, DF74
1380	002400	066742				
1381					: ITEM 75	
1382	002402	051634	063620	071376	.WORD	EM75, DH75, DT75, DF75
1383	002410	067463				
1384					: ITEM 76	
1385	002412	051757	064450	071376	.WORD	EM76, DH76, DT76, DF76
1386	002420	067463				
1387					: ITEM 77	
1388	002422	052061	064512	071444	.WORD	EM77, DH77, DT77, DF77
1389	002430	067505				
1390					: ITEM 100	
1391	002432	052135	065122	071320	.WORD	EM100, DH100, DT100, DF100
1392	002440	067436				
1393					: ITEM 101	
1394	002442	052175	063620	071376	.WORD	EM101, DH101, DT101, DF101
1395	002450	067463				
1396					: ITEM 102	
1397	002452	052321	064512	071376	.WORD	EM102, DH102, DT102, DF102
1398	002460	067505				
1399					: ITEM 103	
1400	002462	052373	064450	071376	.WORD	EM103, DH103, DT103, DF103
1401	002470	067463				
1402					: ITEM 104	
1403	002472	052476	065122	071320	.WORD	EM104, DH104, DT104, DF104
1404	002500	067436				
1405					: ITEM 105	
1406	002502	052537	063620	071376	.WORD	EM105, DH105, DT105, DF105
1407	002510	067463				
1408					: ITEM 106	
1409	002512	052664	064512	071444	.WORD	EM106, DH106, DT106, DF106

002500	067505								
002502	067507	064450	071376	: ITEM 107	.WORD	EM107,DM107,DT107,DF107			
002503	067508								
002504	067509								
002505	067510			: ITEM 110	.WORD	EM110,DM110,DT110,DF110			
002506	067511								
002507	067512			: ITEM 111	.WORD	EM111,DM111,DT111,DF111			
002508	067513								
002509	067514			: ITEM 112	.WORD	EM112,DM112,DT112,DF112			
002510	067515								
002511	067516			: ITEM 113	.WORD	EM113,DM113,DT113,DF113			
002512	067517								
002513	067518			: ITEM 114	.WORD	EM114,DM114,DT114,DF114			
002514	067519								
002515	067520			: ITEM 115	.WORD	EM115,DM115,DT115,DF115			
002516	067521								
002517	067522			: ITEM 116	.WORD	EM116,DM116,DT116,DF116			
002518	067523								
002519	067524			: ITEM 117	.WORD	EM117,DM117,DT117,DF117			
002520	067525								
002521	067526			: ITEM 120	.WORD	EM120,DM120,DT120,DF120			
002522	067527								
002523	067528			: ITEM 121	.WORD	EM121,DM121,DT121,DF121			
002524	067529								
002525	067530			: ITEM 122	.WORD	EM122,DM122,DT122,DF122			
002526	067531								
002527	067532			: ITEM 123	.WORD	EM123,DM123,DT123,DF123			
002528	067533								
002529	067534			: ITEM 124	.WORD	EM124,DM124,DT124,DF124			
002530	067535								
002531	067536			: ITEM 125	.WORD	EM125,DM125,DT125,DF125			
002532	067537								
002533	067538			: ITEM 126	.WORD	EM126,DM126,DT126,DF126			
002534	067539								
002535	067540			: ITEM 127	.WORD	EM127,DM127,DT127,DF127			
002536	067541								
002537	067542			: ITEM 130	.WORD	EM130,DM130,DT130,DF130			
002538	067543								
002539	067544			: ITEM 131					

002740	055265	065122	071562	.WORD	EM131,DM131,DT131,DF131
002750	067571			: ITEM 132	
002760	055265	065122	071562	.WORD	EM132,DM132,DT132,DF132
002770	067571			: ITEM 133	
002780	055265	066113	071624	.WORD	EM133,DM133,DT133,DF133
002790	067571			: ITEM 134	
002772	055124	066113	071624	.WORD	EM134,DM134,DT134,DF134
003000	067571			: ITEM 135	
003002	055163	066113	071624	.WORD	EM135,DM135,DT135,DF135
003010	067571			: ITEM 136	
003012	055222	066113	071624	.WORD	EM136,DM136,DT136,DF136
003020	067571			: ITEM 137	
003022	055065	066223	071676	.WORD	EM137,DM137,DT137,DF137
003030	067615			: ITEM 140	
003032	055124	066223	071676	.WORD	EM140,DM140,DT140,DF140
003040	067615			: ITEM 141	
003042	055163	066223	071676	.WORD	EM141,DM141,DT141,DF141
003050	067615			: ITEM 142	
003052	055222	066223	071676	.WORD	EM142,DM142,DT142,DF142
003060	067615			: ITEM 143	
003062	055261	066113	071624	.WORD	EM143,DM143,DT143,DF143
003070	067571			: ITEM 144	
003072	055314	066113	071624	.WORD	EM144,DM144,DT144,DF144
003100	067571			: ITEM 145	
003102	055261	066223	071676	.WORD	EM145,DM145,DT145,DF145
003110	067615			: ITEM 146	
003112	055314	066223	071676	.WORD	EM146,DM146,DT146,DF146
003120	067615			: ITEM 147	
003122	055347	065122	071624	.WORD	EM147,DM147,DT147,DF147
003130	067571			: ITEM 150	
003132	055347	066413	071624	.WORD	EM150,DM150,DT150,DF150
003140	067571			: ITEM 151	
003142	055347	066223	071676	.WORD	EM151,DM151,DT151,DF151
003150	067615			: ITEM 152	
003152	055401	066113	071624	.WORD	EM152,DM152,DT152,DF152
003160	067571			: ITEM 153	
003162	055401	066223	071676	.WORD	EM153,DM153,DT153,DF153
003170	067615				

1551	003272	055433	066504	071716	: ITEM 154	.WORD	EM154,DM154,DT154,DF154
1552	003273	067626					
1553	003273	055665	066504	071716	: ITEM 155	.WORD	EM155,DM155,DT155,DF155
1554	003274	067626					
1555	003272	056120	065122	071624	: ITEM 156	.WORD	EM156,DM156,DT156,DF156
1556	003273	067571					
1557	003222	056335	065122	071624	: ITEM 157	.WORD	EM157,DM157,DT157,DF157
1558	003233	067571					
1559	003232	056554	065122	071624	: ITEM 160	.WORD	EM160,DM160,DT160,DF160
1560	003243	067571					
1561	003242	056761	065122	071624	: ITEM 161	.WORD	EM161,DM161,DT161,DF161
1562	003250	067631					
1563	003252	057166	065122	071624	: ITEM 162	.WORD	EM162,DM162,DT162,DF162
1564	003260	067571					
1565	003262	057233	065122	071624	: ITEM 163	.WORD	EM163,DM163,DT163,DF163
1566	003270	067631					
1567	003272	057300	063740	067730	: ITEM 164	.WORD	EM164,DM164,DT164,DF164
1568	003300	066631					
1569	003302	057345	063740	067730	: ITEM 165	.WORD	EM165,DM165,DT165,DF165
1570	003310	066631					
1571	003312	057412	065122	071624	: ITEM 166	.WORD	EM166,DM166,DT166,DF166
1572	003320	067571					
1573	003322	057522	065122	071624	: ITEM 167	.WORD	EM167,DM167,DT167,DF167
1574	003330	067571					
1575	003332	057761	065122	071624	: ITEM 170	.WORD	EM170,DM170,DT170,DF170
1576	003340	067631					
1577	003342	060071	065122	071624	: ITEM 171	.WORD	EM171,DM171,DT171,DF171
1578	003350	067631					
1579	003352	060330	065122	071624	: ITEM 172	.WORD	EM172,DM172,DT172,DF172
1580	003360	067571					
1581	003362	060567	065122	071624	: ITEM 173	.WORD	EM173,DM173,DT173,DF173
1582	003370	067571					
1583	003372	061026	065122	071624	: ITEM 174	.WORD	EM174,DM174,DT174,DF174
1584	003400	067631					
1585	003402	061265	065122	071624	: ITEM 175	.WORD	EM175,DM175,DT175,DF175
1586	003410	067631					
1587	003412	061524	065122	071624	: ITEM 176	.WORD	EM176,DM176,DT176,DF176

003420	067571				
003422	061661	065122	071624	: ITEM 177	
003430	067571			.WORD	EM177,DM177,DT177,DF177
003432	062016	065122	071624	: ITEM 200	
003440	067571			.WORD	EM200,DM200,DT200,DF200
003442	062153	065122	071624	: ITEM 201	
003450	067571			.WORD	EM201,DM201,DT201,DF201
003452	062310	065122	071624	: ITEM 202	
003460	067571			.WORD	EM202,DM202,DT202,DF202
003462	062445	065122	071624	: ITEM 203	
003470	067571			.WORD	EM203,DM203,DT203,DF203
003472	062602	065122	071624	: ITEM 204	
003500	067571			.WORD	EM204,DM204,DT204,DF204
003502	062737	063740	067730	: ITEM 205	
003510	065631			.WORD	EM205,DM205,DT205,DF205
003512	063004	065122	071624	: ITEM 206	
003520	067571			.WORD	EM206,DM206,DT206,DF206
003522	063051	065122	071624	: ITEM 207	
003530	067571			.WORD	EM207,DM207,DT207,DF207
003532	063173	065122	071624	: ITEM 210	
003540	067571			.WORD	EM210,DM210,DT210,DF210
003542	043233	066544	071730	: ITEM 211	
003550	067655			.WORD	EM211,DM211,DT211,DF211
003552	043267	063620	071746	: ITEM 212	
003560	067655			.WORD	EM212,DM212,DT212,DF212
003562	043321	063620	071746	: ITEM 213	
003570	067655			.WORD	EM213,DM213,DT213,DF213

.SBTTL ACT11 HOOKS

::*****

:HOOKS REQUIRED BY ACT11

```

000046      003572      $SVPCL=      ;SAVE PC
000052      000046      .=46
000052      034770      SENDAD      ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SECP
000052      000052      .=52
000052      000000      .WORD 0      ;;2)SET LOC.52 TO ZERO
000052      003572      =$SVPCL      ;; RESTORE PC

```

.SBTTL APT PARAMETER BLOCK

::*****

:SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT

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::*****  
.$X=.   ::SAVE CURRENT LOCATION  
=24    ::SET POWER FAIL TO POINT TO START OF PROGRAM  
200    FOR APT START UP  
=44    ::POINT TO APT INDIRECT ADDRESS PNTR.  
$APTHDR ::POINT TO APT HEADER BLOCK  
=.$X   ::RESET LOCATION COUNTER  
::*****  
::SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDF11 DIAGNOSTIC  
::INTERFACE SPEC.  
  
$APTHDR:  
$SHIBTS: .WORD 0      ::TWO HIGH BITS OF 18 BIT MAILBOX ADDR.  
$MBAADR: .WORD $MAIL  ::ADDRESS OF APT MAILBOX (BITS 0-15)  
$STMT: .WORD 10      ::RUN TIM OF LONGEST TEST  
$PASTM: .WORD 40      ::RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUIK JERF)  
$UNITM: .WORD 0       ::ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT  
        .WORD SETEND-$MAIL/2 ::LENGTH MAILBOX-ETABLE(WORDS)  
  
START:  
.$BTTL INITIALIZE THE COMMON TAGS  
::CLEAR THE COMMON TAGS ($CHTAG) AREA  
MOV     #$CHTAG,R6      ::FIRST LOCATION TO BE CLEARED  
CLR     (R6)+           ::CLEAR MEMORY LOCATION  
CMP     $SWR,R6        ::DONE?  
BNE     -6              ::LOOP BACK IF NO  
MOV     $STACK,SP      ::SETUP THE STACK POINTER  
  
::INITIALIZE A FEW VECTORS  
MOV     $SCOPE,$IOTVEC  ::IOT VECTOR FOR SCOPE ROUTINE  
MOV     #340,$IOTVEC+2 ::LEVEL 7  
MOV     $ERRR,$EMTVEC   ::EMT VECTOR FOR ERROR ROUTINE  
MOV     #340,$EMTVEC+2 ::LEVEL 7  
MOV     $TRAP,$TRAPVEC  ::TRAP VECTOS FOR TRAP CALLS  
MOV     #340,$TRAPVEC+2 ::LEVEL 7  
MOV     $PIADR,$PIRVEC  ::POWER FAILURE VECTOR  
MOV     #340,$PIRVEC+2  ::LEVEL 7  
MOV     $ENDPT,$SEOPT   ::SETUP END-OF-PROGRAM COUNTER  
CLR     $TIMES          ::INITIALIZE NUMBER OF ITERATIONS  
CLR     $ESCAPE         ::CLEAR THE ESCAPE ON ERROR ADDRESS  
MOV     #1,$SERMAX      ::ALLOW ONE ERROR PER TEST  
  
::INITIALIZE THE "T-BIT" TRAP VECTOR. THEN LOAD LOCATION "$RTRN". IN  
::THE "END-OF-PASS" ($EOP) ROUTINE, WITH A "RTI" OR "RTT".  
MOV     $RTRN,$TBITVEC  ::SET "T" BIT VECTOR TO $RTRN  
MOV     #340,$TBITVEC+2 ::LEVEL 7  
MOV     $RTI,$RTRN      ::SET $RTRN TO A RTI  
MOV     #655,$RESVEC    ::TRY TO DO A RTT  
CLR     -(SP)           ::DUMMY PS  
MOV     #645,-(SP)      ::AND PC  
RTT     ::TRY THE RTT  
MOV     $RTT,$RTRN     ::RTT IS LEGAL--SET $RTRN TO A RTT  
BR      665  
ADD     #10,SP          ::RTT ILLEGAL--CLEAN OFF THE STACK  
MOV     $RESVEC+2,$RESVEC ::RESTORE TRAP CATCHER  
CLR     $TBIT          ::CLEAR "T" BIT SWITCH  
MOV     #,$SLPADR      ::INITIALIZE THE LOOP ADDRESS FOR SCOPE
```

```

004026 012767 004026 175054      MOV      #0,SPERR          ;;SETUP THE ERROR LOG ADDRESS
                                ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND IN
                                ;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
004026 012767 000000 000000      MOV      @ERRVEC,SP        ;;SAVE ERROR VECTOR
004026 012767 000004 000004      MOV      @ERRVEC,SP        ;;SETUP ERROR VECTOR
004026 012767 175064 175064      MOV      @DSWR,SWR        ;;SETUP FOR A HARDWARE SWITCH REGISTER
004026 012767 175064 175064      MOV      @DISP,DISPLAY    ;;AND A HARDWARE DISPLAY REGISTER
004026 012767 175050 175050      CMP      #1,DSWR          ;;TRY TO REFERENCE HARDWARE SWR
004026 012767 001012 001012      BNE      69$              ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
                                ;;AND THE HARDWARE SWR IS NOT = -1
004026 012767 00403 00403      BR       68$              ;;BRANCH IF NO TIMEOUT
004026 012767 004102 004102      67$:    MOV      @68$,SP        ;;SET UP FOR TRAP RETURN
                                RTI
004026 012767 000176 175030      68$:    MOV      @SWREG,SWR     ;;POINT TO SOFTWARE SWR
004026 012767 000174 175024      68$:    MOV      @DISPREG,DISPLAY
004026 012637 000004 000004      69$:    MOV      (SP)+,@ERRVEC  ;;RESTORE ERROR VECTOR
004026 005067 175176 175203      CLR      $PASS            ;;CLEAR PASS COUNT
004026 012767 000200 175203      BITB    @APTSIZE,$ENVM    ;;TEST USER SIZE UNDER APT
004026 001403 001340 174774      BEQ     70$              ;;YES,USE NON-APT SWITCH
004026 012767 001340 174774      MOV     @SSWREG,SWR      ;;NO,USE APT SWITCH REGISTER
004026 005227 177777 177777      .SETTL  TYPE PROGRAM NAME
                                ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
004026 001055 004770 000042      INC     #1                ;;FIRST TIME
004026 022737 004770 000042      BNE    71$                ;;BRANCH IF NO
004026 001451 004230 000042      CMP    @SENDAD,@#42      ;;ACT-11
004026 004401 004230 000042      BEQ    71$                ;;BRANCH IF YES
                                TYPE
                                .72$
                                .SETTL  GET VALUE FOR SOFTWARE SWITCH REGISTER
004026 005737 000042 000042      TST    @#42              ;;ARE WE RUNNING UNDER XXDF ACT?
004026 0126727 175136 000001      BNE    73$                ;;BRANCH IF YES
004026 001406 004204 000042      CMPB   $ENV,#1          ;;ARE WE RUNNING UNDER APT?
004026 026727 174730 000176      BEQ    73$                ;;BRANCH IF YES
004026 001005 004212 000176      CMP    SWR,@SWREG        ;;SOFTWARE SWITCH REG SELECTED?
004026 0104405 004214 000176      BNE    74$                ;;BRANCH IF NO
004026 000403 004216 000176      JTSWR  74$                ;;GET SOFT-SWR SETTINGS
004026 0112767 000001 174706      BR     74$                ;;SET AUTO-MODE INDICATOR
004026 000426 004220 000001 174706      73$:   MOVB   #1,$AUTOB        ;;SET AUTO-MODE INDICATOR
004026 000426 004226 000001 174706      74$:   BR     71$                ;;GET OVER THE ASCIZ
004026 004304 004304 000001 174706      72$:   .ASCIZ 'CRLF.*OFFPA,FP11-A 11.34 FPP DIAGNOSTIC PART 1+CRLF
004026 004304 004304 000001 174706      71$:   LOOP:

```

```

*****
*TEST :      LDFPS, STFPS AND DATA PATHS TEST
*
*THIS IS A TEST OF THE LDFPS (LOAD FLOATING POINT STATUS) AND STFPS
*(STORE FLOATING POINT STATUS) INSTRUCTIONS. A COUNT PATTERN IS GENERATED
*AND RUN THROUGH THE FLOATING POINT STATUS REGISTER.

```



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: COMPLETE CHECK OF FPP INSTRUCTIONS
MOV R0,R5
MOVL R5,R5
BCC R5,R2
: SEE IF MORE THAN 5 ERRORS -- E
: OCCURRED. BR IF YES.
: OTHERWISE
: REPORT ERROR.
004474 000000 004560 CMP #5,0AERFLG
BLO R05
:
MOV #A1,0ASTMP2
MOV R0,0ASTMP3
MOV R1,0ASTMP4
MOV R4,0ASTMP5
A4: ERROR 1
:
A5: BR R2 :CONTINUE TESTING.
:
: SEE IF ANY ERRORS OCCURRED.
: IF NOT GO TO NEXT TEST.
: OTHERWISE SEE IF A SUMMARY
: SHOULD BE TYPED.
004476 000751 A5: TST 0AERFLG
BEQ ADONE
BIT #SW13,0SWR
BEQ R6
BIT #SW7,0SWR
BEQ ADONE
:
A6: :TYPE ERROR SUMMARY.
MOV R2,0ASTMP2
MOV R3,0ASTMP3
MOV #A7,0SERRPC
MOVB #2,0SITEMB
JSR PC,0SERTYPE
BR ADONE
:
AERFLG: .WORD 0
:UNABLE TO DECODE FPP INSTRUCTION. TRAPPED TO 244.
:SAVE PC OF TRAP.
004562 011637 001236 AERR1: MOV (SP),0ASTMP2
CMP (SP)+,(SP)+
IS: ERPOR 10
BR ADONE
:UNABLE TO DECODE INSTRUCTION. TRAPPED TO 10.
: DID TRAP OCCUR OF FPP INSTRUCTION?
004574 021627 004352 AERR2: CMP (SP),#A11+2
BEQ IS
CMP (SP),#A12+2
BEQ IS
JMP #CPTWO
: IF NOT FPP INSTRUCTION THEN
: REPORT SPURIOUS TRAP TO 10.
: OTHERWISE REPORT IN DECIDE ERROR.
004614 011637 001236 IS: MOV (SP),0ASTMP2
CMP (SP)+,(SP)+
2S: ERROR 11
BR ADONE
:TRAP TO 4 HANDLER:
: DID THE TRAP OCCUR ON THE
: LDFPS INSTRUCTION?
: OR THE STFPS INSTRUCTION?
004626 021627 004352 AERR3: CMP (SP),#A11+2
BEQ IS
CMP (SP),#A12+2

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004670 004671 004672 004673 004674 004675 004676 004677 004678 004679 004680 004681 004682 004683 004684 004685 004686 004687 004688 004689 004690 004691 004692 004693 004694 004695 004696 004697 004698 004699 004700 004701 004702 004703 004704 004705 004706 004707 004708 004709 004710 004711 004712 004713 004714 004715 004716 004717 004718 004719 004720 004721 004722 004723 004724 004725 004726 004727 004728 004729 004730 004731 004732 004733 004734 004735 004736 004737 004738 004739 004740 004741 004742 004743 004744 004745 004746 004747 004748 004749 004750 004751 004752 004753 004754

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BEQ 25
JMP 25BCPSPUR ;IF NEITHER THEN REPORT
;UNEXPECTED TRAP TO 4.

1S: MOV (SP), 2#STMP2
CMP (SP)+, (SP)+
15S: ERROR 14
BR ADONE

2S: MOV (SP), 2#STMP2
CMP (SP)+, (SP)+
25S: ERROR 15

ADONE: RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER HAS
;THE USER TYPED CONTROL G?).

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*****
*TEST 2 CFCC TEST
*
*THIS IS A TEST OF THE COPY CONDITION CODES INSTRUCTION, CFCC.
*
*****

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004672 000004
004674 104412
004676 012700 300017
ST2: SCOPE
LPERR
MOV #17,R0 ;SET UP THE LOOP ON ERROR ADDRESS.
;R0 CONTAINS TO TEST PATTERN.

B1: LDFPS R0 ;LOAD THE TEST PATTERN

B2: CFCC ;COPY CONDITION CODES.

004706 013703 177776
004712 042703 177760
004716 020003
004720 001002
B3: SOB R0,B1 ;SEE IF PATTERN TRANSFERED.
BR B0ONE

BERR: STFPS R1 ;WAS FPS MODIFIED BY CFCC?
MOV #B2, 2#STMP2

004736 020001
004740 001006
004742 010337 001240
004746 010037 001242
1S: MOV R3, 2#STMP3
MOV R0, 2#STMP4
ERROR 3
BR B3

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1914
1915 004756
1916 004756 010037 001240
1917 004756 010137 001242
1918 004766 104004
1919 004770 000754
1920
1921 004772
1922 004772 104412
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1937 004774 000004
1938 004776 104413
1939 005000 012737 000760 001244
1940 005006 012737 000202 001250
1941 005014 012737 041352 001252
1942 005022 005000
1943
1944 005024 :70100
1945 005026 012737 005034 001236
1946
1947 005034 170001
1948
1949 005036 170201
1950 005040 005002
1951 005042 020201
1952 005044 001402
1953 005046 004737 005432
1954
1955 005052
1956 005052 104413
1957 005054 012700 147757
1958
1959 005060 :70100
1960 005062 012737 005070 001236
1961 005070 170001
1962
1963 005072 170201
1964 005074 012702 147557
1965 005100 020102
1966 005102 001402
1967 005104 004737 005530
1968
1969 005110

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BERR:  MOV R0,2#STMP3
        MOV R1,2#STMP4
IS:     ERROR 4
        BR B3

BOONE:  RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
          ;SEE IF THE USER HAS EXPRESSED
          ;THE DESIRE TO CHANGE THE SOFTWARE
          ;VIRTUAL CONSOLE SWITCH REGISTER HAS
          ;THE USER TYPED CONTROL G?).

:*****
:*TEST 3 SETF, SETD, SETI AND SETL TEST
:*
:*THIS IS A TEST OF THE SETF, SETD, SETI AND SETL INSTRUCTIONS.
:*EACH INSTRUCTION IS EXECUTED WITH THE FPS CONTAINING
:*ALL ONES AND ALSO WITH THE FPS CLEAR. THE RESULT OF EACH
:* SITUATION IS CHECKED.
:*
:*****
TST3:  SCOPE
        LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
        MOV #760,2#STMP5
C1:    MOV #202,2#STMP7
        MOV #SETF1,2#STMP10
        CLP RD
        LDFPS R0 ;CLEAR THE FPS.
        MOV #C15,2#STMP2
C15:   SETF ;TEST INSTRUCTION.
        STFPS R1 ;GET RESULT.
        CLR R2
        CMP R2,R1 ;DID AN ERROR OCCUR?
        BEQ IS
        JSR PC,2#CERR1
IS:    LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
C2:    MOV #147757,R0
        LDFPS R0 ;PUT 147757 IS FPS
        MOV #C25,2#STMP2
C25:   SETF ;CLEAR FD BIT.
        STFPS R1 ;GET RESULT
        MOV #147557,R2
        CMP R1,R2 ;RESULT CORRECT.
        BEQ IS
        JSR PC,2#CERR2
IS:

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L03

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1970 005110 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
1971 005112 012737 000203 001250 C3: MOV #203,0#STMP7
1972 005120 012737 041360 001252 MOV #SETD1,0#STMF10
1973 005126 012700 147757 MOV #147757,R0
1974
1975 005132 170100 LDFPS R0 ;LOAD 147757 INTO FPS.
1976 005134 012737 005142 001236 MOV #C35,0#STMP2
1977 005142 170011 C35: SETD ;SETD FD BIT.
1978
1979 005144 170201 STFPS R1
1980 005146 012702 147757 MOV #147757,R2
1981 005152 020102 CMP R1,R2 ;RESULT CORRECT?
1982 005154 001402 BEQ IS
1983 005156 004737 005530 JSR PC,0#CERR2
1984
1985 005162 IS:
1986 005162 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
1987 005164 005000 C4: CLR R0 ;CLEAR FPS.
1988 005166 170100 LDFPS R0
1989 005170 012737 005176 001236 MOV #C45,0#STMP2
1990
1991 005176 170011 C45: SETD ;SET FD BIT.
1992
1993 005200 170201 STFPS R1 ;GET RESULT.
1994 005202 012702 000200 MOV #200,R2
1995 005206 020102 CMP R1,R2 ;RESULT CORRECT?
1996 005210 001402 BEQ IS
1997 005212 004737 005432 JSR PC,0#CERR1
1998
1999 005216 IS:
2000 005216 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
2001 005220 012737 000204 001250 C5: MOV #204,0#STMP7
2002 005226 012737 041366 001252 MOV #SETI1,0#STMP10
2003 005234 005000 CLR R0
2004
2005 005236 170100 LDFPS R0 ;CLEAR FPS
2006 005240 012737 005246 001236 MOV #C55,0#STMP2
2007
2008 005246 170002 C55: SETI ;CLEAR FL BIT.
2009
2010 005250 170201 STFPS R1 ;GET RESULT.
2011 005252 005002 CLR R2
2012 005254 020201 CMP R2,R1 ;RESULT CORRECT?
2013 005256 001402 BEQ IS
2014 005260 004737 005432 JSR PC,0#CERR1
2015
2016 005264 IS:
2017 005264 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
2018 005266 012700 147757 C6: MOV #147757,R0 ;PUT 147757 INTO FPS
2019 005272 170100 LDFPS R0
2020 005274 012737 005302 001236 MOV #C65,0#STMP2
2021
2022 005302 170002 C65: SETI ;CLEAR FL BIT.
2023
2024 005304 170201 STFPS R1 ;GET THE RESULT.
2025 005306 012702 147657 MOV #147657,R2

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M03

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2026 005312 020102          CMP      R1,R2          ;RESULT CORRECT?
2027 005314 001402          BEQ      1$
2028 005316 004737 005530          JSR      PC,@#CERR2
2029
2030          1$:
2031 005322 104413          LPERR                    ;SET UP THE LOOP ON ERROR ADDRESS.
2032 005324 012737 000205 001250 07:  MOV      #205,@#STMP7
2033 005332 012737 041374 001252          MOV      #SETL1,@#STMP10
2034 005340 012700 147757          MOV      #147757,R0
2035 005344 170100          LDFPS   R0              ;SET FPS TO 147757.
2036 005346 012737 005354 001236          MOV      #075,@#STMP2
2037
2038 005354 170012          075:  SETL                    ;SET FL BIT.
2039
2040 005356 170201          STFPS   R1              ;GET THE RESULT.
2041 005360 012702 147757          MOV      #147757,R2
2042 005364 020102          CMP      R1,R2          ;RESULT CORRECT?
2043 005366 001402          BEQ      1$
2044 005370 004737 005530          JSR      PC,@#CERR2
2045
2046 005374          1$:
2047 005374 104413          LPERR                    ;SET UP THE LOOP ON ERROR ADDRESS.
2048 00537E 005000          08:  CLR      R0
2049 005400 170100          LDFPS   R0              ;CLEAR FPS.
2050 005402 012737 005410 001236          MOV      #085,@#STMP2
2051
2052 005410 170012          085:  SETL                    ;SET FL BIT.
2053
2054 005412 170201          STFPS   R1
2055 005414 012702 000100          MOV      #100,R2
2056 005420 020102          CMP      R1,R2          ;RESULT CORRECT.
2057 005422 001402          BEQ      1$
2058 005424 004737 005432          JSR      PC,@#CERR1
2059
2060 005430 000522          1$:  BR      CDONE
2061
2062          ;THESE ARE ERROR ANALYSIS ROUTINES:
2063 005432 010103          CERR1: MOV      R1,R3
2064 005434 032703 177477          BIT      #177477,R3      ;ARE ANY OTHER BITS SET?
2065 005440 001401          BEQ      2$
2066 005442 000503          1$:  BR      CERR4
2067
2068 005444 022703 000300          2$:  CMP      #300,R3          ;ARE BOTH FD AND FL SET?
2069 005450 001774          BEQ      1$
2070 005452 032703 000300          BIT      #300,R3          ;ARE THEY BOTH CLEAR?
2071 005456 001771          BEQ      1$
2072
2073 005460 032703 000200          BIT      #200,R3          ;IS FD SET?
2074 005464 001407          BEQ      3$
2075 005466 012737 041360 001254          MOV      #SETD1,@#STMP11
2076 005474 012737 000203 001246          MOV      #203,@#STMP6
2077 005502 000452          BR      CERR3
2078
2079 005504 032703 000100          3$:  BIT      #100,R3          ;IS FL SET
2080 005510 001754          BEQ      1$
2081 005512 012737 041374 001254          MOV      #SETL1,@#STMP11
  
```

N03

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2082 005520 012737 000205 001246      MOV      #205,0#STMP6
2083 005526 000440                BR      CERR3
2084
2085 005530 010103                CERR2:  MOV      R1,R3
2086 005532 005103                COM      R3
2087
2088 005534 032703 177477                BIT      #177477,R3                ;ARE ANY OTHER BITS SET?
2089 005540 001401                BEQ      2$
2090 005542 000443                1$:     BR      CERR4
2091
2092 005544 032703 000300                2$:     BIT      #300,R3                ;ARE BOTH FD AND FL SET?
2093 005550 001774                BEQ      1$
2094 005552 032701 000300                BIT      #300,R1                ;ARE THEY BOTH CLEAR?
2095 005556 001771                BEQ      1$
2096
2097 005560 032701 000200                BIT      #200,R1                ;IS FD CLEAR?
2098 005564 001007                BNE      3$
2099 005566 012737 041352 001254      MOV      #SETF1,0#STMP11
2100 005574 012737 000202 001246      MOV      #202,0#STMP6
2101 005602 000412                BR      CERR3
2102
2103 005604 032701 000100                3$:     BIT      #100,R1
2104 005610 001354                BNE      1$                ;IS FL CLEAR.
2105 005612 012737 041366 001254      MOV      #SETI1,0#STMP11
2106 005620 012737 000204 001246      MOV      #204,0#STMP6
2107 005626 000400                BR      CERR3
2108
2109                ;REPORT THE ERRORS:
2110 CERR3:
2111 005630 010137 001240      MOV      R1,0#STMP3
2112 005634 010237 001242      MOV      R2,0#STMP4
2113 005640 012637 005674      MOV      (SP)+,0#CPC
2114 005644 104012                1$:     ERROR  12
2115 005646 000177 000022      JMP      0#CPC
2116
2117 CERR4:
2118 005652 010137 001240      MOV      R1,0#STMP3
2119 005656 010237 001242      MOV      R2,0#STMP4
2120 005662 012637 005674      MOV      (SP)+,0#CPC
2121 005666 104013                1$:     ERROR  13
2122 005670 000177 000000      JMP      0#CPC
2123
2124 CPC:      .WORD  0
2125
2126 CDONE:
2127 005676 104412      RSETUP                ;GO INITIALIZE THE FPS AND STACK; AND
2128                                ;SEE IF THE USER HAS EXPRESSED
2129                                ;THE DESIRE TO CHANGE THE SOFTWARE
2130                                ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
2131                                ;THE USER TYPED CONTROL G?).
2132
2133
2134                ;*****
2135                ;*TEST 4      ILLEGAL FPP OP CODES AND STST TEST
2136                ;*
2137                ;*THIS IS A TEST OF THE FPP OPERATION CODES:
  
```

170003
170004
170010
170013
170014
170077

THESE ARE ILLLEGAL INSTRUCTIONS AND (WITH INTERRUPTS ENABLED,
SHOULD CAUSE A TRAP TO 244.
ALSO TESTED HERE IS THE INSTRUCTION:
STST R1
WHICH SHOULD PUT THE FEC CODE 2 IN R1, AFTER ANY OF THE ABC.E
OP CODES IS EXECUTED.

005760 005762 005766 005770 005774 005776 006002 006004 006010 006012 006014 006016 006020 006024 006026 006032 006034 006036 006042

```
54:   SCOPE                                ;SET UP THE LOOP ON ERROR ADDRESS.
      LPERR                               ;INITIAL OP CODE.
      MOV   #170003,R5
      MOVR  #DERR2,#ERRVECT
      MOV   #DERR1,#FPVECT

01:   CLR   R0                               ;CLEAR FPS.
      LDSPS R0
      CLR   R2
      MOV   R5,#02                          ;SET UP THE ILLLEGAL INSTRUCTION.
      MOVR  R5,#STMP5
      MOV   #02,#STMP2

02:   WORD  0
03:   CFCC
04:   INC   R2
      INC   R2

      STFPS R1                               ;REPORT FAILURE. DID NOT TRAP.
      MOV   R1,#STMP3

15:   ERROR  16

05:   CMP   #170010,R5                       ;COMPUTE NEXT OP CODE
      BNE   D6
      MOV   #170013,R5
      BR    D1

06:   CMP   #170077,R5
      BNE   D7
      BR    DDONE

07:   INC   R5
      BR    D1

DERR1: CMP   #03,(SP)                       ;DID TRAP OCCUR ON TEST INSTRUCTION?
      BEQ   15
      JMP   #FPSPUR

15:   CMP   (SP)+,(SP)+
      STFPS R1                               ;GET THE FPS AND SEE IF IT IS
      CMP   #10000,R1                       ;SET CORRECTLY.
      BEQ   35
```



```

000000 001240 MOV #100000,2#STMP3
000001 001242 MOV R1,2#STMP4
000002 001244 ERROR 17
28:
000003 006063 MOV #1,R4
000004 006064 STST R4
29:
:GET THE FEC CODE. NOTE THAT
:IF THE DESTINATION MODE IS
:IMPROPERLY DECODED AN ODD
:ADDRESS TRAP TO 4 SHOULD OCCUR.
:WAS FEC CORRECT?
000005 006066 CMP #2,R4
000006 006072 BNE D9
000007 006074 BR D5
000008
000009 006076 006076 012737 006064 001240 09: MOV #08,2#STMP3
000010 006104 010437 001242 MOV R4,2#STMP4
000011 006110 104020 18: ERROR 20
000012 006112 000726 BR D5
000013
000014 006114 022716 006066 DERR2: CMP #08+2,(SP)
000015 006120 001402 BEQ D10
000016 006122 000137 040232 JMP 2#CPSPUR
: DID THE TRAP OCCUR ON THE
: STST INSTRUCTION?
000017
000018 006126 011637 001236 D10: MOV (SP),2#STMP2
000019 006132 022626 CMP (SP)+,(SP)+
000020 006134 104021 18: ERROR 21
000021 006136 000714 BR D5
000022
000023 006140 104412 DDONE: RSETUP
: GO INITIALIZE THE FPS AND STACK: AND
: SEE IF THE USER HAS EXPRESSED
: THE DESIRE TO CHANGE THE SOFTWARE
: VIRTUAL CONSOLE SWITCH REGISTER (HAS
: THE USER TYPED CONTROL G?).
:*****
: *TEST 5 FID, INTERRUPT DISABLE, BIT TEST
: *
: *THIS IS A TEST OF FPS BIT 14 (FID) OR FLOATING INTERRUPT DISABLE.
: *AN ILLEGAL INSTRUCTION IS EXECUTED WITH FID=1. NO INTERRUPT SHOULD
: *OCCUR.
: *
:*****
000024 006142 000004 TESTS: SCOPE
000025 006144 104413 LPERR
000026 006146 012737 006250 000244 MOV #EERR2,2#FPVECT :SET UP THE LOOP ON ERROR ADDRESS.
:SETUP FOR THE INTERRUPT.
000027
000028 006154 012730 040000 E1: MOV #4000,R0
000029 006160 170100 LDFPS R0 :SET FID.
000030 006162 012737 006170 001236 MOV #E3,2#STMP2
000031 006170
000032 006172 170020 E2: .WORD 170020
000033 006172 170000 E3: CFCC :ILLEGAL FPP INSTRUCTION.
000034

```

```

006201 170304 000002 STS R1 :SEE IF ERROR WAS DETECTED.
006206 001010 000002 BR R1 EERR0
006214 000431 000002 BR R4 :SEE IF FEC=2
006216 010137 001240 EERR0: MOV R1,2*STMP3 :REPORT FPS INCORRECTLY SET.
006220 012737 001240 1S: MOV R1,40000,2*STMP4
006230 000422 000002 BR R2 EDONE
006234 010537 001240 EERR1: MOV R5,2*STMP3 :REPORT FEC NOT 2.
006240 010437 001242 1S: MOV R4,2*STMP4
006244 000423 000002 BR R3 EDONE
006250 021627 006172 EERR2: CMP (SP),#E4 :DID THE ILLEGAL INSTRUCTION TRAFF
006254 001402 000002 1S: BEQ 1S
006256 000137 040200 JMP 2*FPSPUR
006262 011637 001236 1S: MOV (SP),2*STMP2
006266 022626 000002 CMP (SP)+,(SP)+
006270 170201 000002 STFS R1
006272 010137 001240 2S: MOV R1,2*STMP3
006276 104024 000002 BR R4 EDONE
006300 104412 000002 RSETUP :GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER (HAS
:THE USER TYPED CONTROL G?).

```

```

*****
*TEST 6 LDD AND STD. WITH SRC AND DST MODE 1. TEST
*
*THIS IS A TEST OF BOTH THE INSTRUCTION:
* LDD (RD),ACD
*AND THE INSTRUCTION:
* STD ACD,(RD)
*MOST OF THE FAILURES ARE ISOLATED TO THE SRC OR DST FLOWS. NOTE
*THAT THE INTEGRITY OF ACD HAS NOT BEEN ASSURED. THIS MEANS THAT
*IN SOME CASES IT WILL BE IMPOSSIBLE TO ISOLATE CERTAIN DATA PATTERN
*FAILURES TO EITHER THE FLOWS OR THIS ACCUMULATOR.
*
*****
*STEP SCOPE

```

006302 000004

E04

MACRO-11 OFFER-2 SDF 11 24 FEB DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:39 PAGE 43
 OFFER-11 01-NOV-76 21:03 TE LDD AND STD. WITH SRC AND DST MODE 1. TEST

006304	0124413			F1:	LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
006304	012737	006356	001236		MOV	#F3,2#STMP2	
006306	005000				CLR	R0	
006314	170100				DFPS	R0	
006316	170011				SETD		
006320	012701	010110			MOV	#FDAT0,R1	:SET UP THE LOAD DATA.
006326	012702	010154			MOV	#FXDAT0,R2	
006332	012703	000010			MOV	#10,R3	
006336	012221			F2:	MOV	(R2)+,(R1)+	
006340	077302				SOB	R3,F2	
006342	012700	010120			MOV	#FDAT14,R0	:SETUP R0 FOR THE LDD (R0),ACC.
006346	012737	007574	000004		MOV	#FERR20,2#ERRVECT	:IF THE SRC FLOWS FAIL THEN :AN ODD ADDRESS MAY OCCUR.
006354	005003				CLR	R3	
006356	172410			F3:	LDD	(R0),ACC	
006360	005203			F4:	INC	R3	
006362	005203				INC	R3	
006364	020027	010120			CMP	R0,#FDAT14	:WAS R0 AFFECTED?
006370	001402				BEQ	F5	
006372	000137	006740			JMP	2#FERR1	
006376	020327	000002		F5:	CMP	R3,R2	:SEE IF THE PC WAS ADVERSELY
006402	001402				BEQ	1\$:AFFECTED DURING THE INSTRUCTION.
006404	000137	007036			JMP	2#FERR2	
006410	012701	010110		1\$:	MOV	#FDAT0,R1	:MAKE SURE THE SOURCE DATA WAS
006414	012702	010154			MOV	#FXDAT0,R2	:NOT AFFECTED.
006420	012703	000010			MOV	#10,R3	
006424	022122			2\$:	CMP	(R1)+,(R2)+	
006426	001402				BEQ	3\$	
006430	000137	006702			JMP	2#FERRO	
006434	077305			3\$:	SOB	R3,2\$	
006436	170201				STFPS	R1	:MAKE SURE THE FPS IS CORRECT.
006440	022701	000200			CMP	#200,R1	
006444	001402				BEQ	F6	
006446	000137	007554			JMP	2#FERR1:	
006452				F6:	LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
006452	104413				MOV	#F10,2#STMP2	
006454	012737	006516	001236				
006462	012703	177777			MOV	#-1,R3	
006466	012704	000010			MOV	#10,R4	
006472	012705	010132			MOV	#FDAT00,R5	:SET UP THE OUTPUT DATA BUFFER.
006476	010325			F7:	MOV	R3,(R5)+	
006500	077402				SOB	R4,F7	
006502	012700	010142			MOV	#FDAT04,R0	:SET UP R0 FOR DST MODE 1 REG C.
006506	012737	007742	000004		MOV	#FERR25,2#ERRVECT	:IF THE DST FLOWS FAIL AN ODD :ADDRESS COULD OCCUR.

006514	005000		CLR	R3	
006516	005203		F10:	STD	R0,(R0)
006518	005203		F11:	INC	R3
006522	005203			INC	R3
006524	020027	010142	JMP	R0,#FDAT04	WAS R0 MODIFIED?
006530	001402		BEQ	F12	
006532	000137	007076	JMP	#FERR3	
006536	020027	000002	F12:	CMP	R3,#2
006542	001402		BEQ	F13	WAS THE PC AFFECTED CORRECTLY?
006544	000167	000320	JMP	FERR4	
006550	012701	010132	F13:	MOV	#FDAT00,R1
006554	012702	010154	MOV	#FXDAT0,R2	
006560	022122		CMP	(R1)+,(R2)+	SEE IF THE DATA WAS OUTPUT
006562	001402		BEQ	F13	TO THE TARGET AREA CORRECTLY.
006564	000137	007174	JMP	#FERR5	
006570	022122		F13:	CMP	(R1)+,(R2)+
006572	001402		BEQ	F14	
006574	000137	007174	JMP	#FERR5	
006600	022122		F14:	CMP	(R1)+,(R2)+
006602	001402		BEQ	F15	
006604	000137	007174	JMP	#FERR5	
006610	022122		F15:	CMP	(R1)+,(R2)+
006612	001402		BEQ	F16	
006614	000137	007174	JMP	#FERR5	
006620	022122		F16:	CMP	(R1)+,(R2)+
006622	001402		BEQ	F17	
006624	000137	007520	JMP	#FERR10	
006630	022122		F17:	CMP	(R1)+,(R2)+
006632	001402		BEQ	F20	
006634	000137	007230	JMP	#FERR6	
006640	022122		F20:	CMP	(R1)+,(R2)+
006642	001402		BEQ	F21	
006644	000167	000514	JMP	FERR7	
006650	022122		F21:	CMP	(R1)+,(R2)+
006652	001402		BEQ	F22	
006654	000137	007520	JMP	#FERR10	
006660	005001		F22:	CLR	R1
006662	170201		STPS	R1	MAKE SURE FPS IS CORRECT.
006664	022701	000200	CMP	#200,R1	
006670	001402		BEQ	F23	
006672	000137	007554	JMP	#FERR11	
006676	000137	010174	F23:	JMP	#FDONE

G04

Address	Operation	Source	Destination	Mode	Comments
001154	MOV	#FXDAT0, @#STMP3			:SOURCE DATA AFFECTED BY THE LOC INSTRUCTION.
001156	MOV	#FXDAT0+12, @#STMP4			
001158	MOV	#FDATIO, @#STMP5			
00115A	MOV	#FDATIO+12, @#STMP6			
001174	15: ERROR	25			
001174	JMP	@#FDCNE			
001242	FERR1: MOV	#FDATIO4, @#STMP4			:FSRC FLOWS FAILURE.
001240	MOV	R0, @#STMP3			
001244	MOV	#762, @#STMP5			
00125C	MOV	#321, @#STMP7			
010110	CMP	#FDATIO, R0			:FSRC MODE 4'
001246	15: BNE	15			
001246	MOV	#324, @#STMP6			
000324	BR	45			
001246	15: CMP	#FDATIO4+10, R0			:FSRC MODE 2'
001246	BNE	25			
001246	MOV	#322, @#STMP6			
000403	BR	45			
010174	25: ERROR	27			
010174	38: JMP	@#FDCNE			
010174	45: ERROR	26			
010174	55: JMP	@#FDCNE			
006360	FERR2: MOV	#F4, R1			:THE PC WAS INCORRECTLY AFFECTED DURING THE INSTRUCTION.
001242	FERR2: MOV	R1, @#STMP4			
000304	SUB	#4, R1			
006303	ASL	R3			
006301	ADD	R3, R1			
001240	MOV	R1, @#STMP3			
010174	15: ERROR	30			
010174	JMP	@#FDCNE			
006520	FERR4: MOV	#F11, R1			
000762	BR	FERR2			
001242	FERR3: MOV	#FDATIO4, @#STMP4			:FAILURE IN THE F0ST FLOWS.
001240	MOV	R0, @#STMP3			
001244	MOV	#527, @#STMP5			
001250	MOV	#641, @#STMP7			
010132	CMP	#FDATIO0, R0			:DST MODE 4'
001246	15: BNE	15			
001246	MOV	#644, @#STMP6			
000412	BR	45			
010152	15: CMP	#FDATIO4+10, R0			:DST MODE 2'
001004	BNE	25			

007151	104033	000648	001246		MOV #42, @STMP6 BR 45	
007152	104033					
007153	104033			28:		
007154	000137	010174		28:	ERROR 32 JMP @FDONE	
007155	104033			48:		
007156	000137	010174		48:	ERROR 31 JMP @FDONE	
007174				FERR5:		: FAILURE OF STO.
007174	010037	001240			MOV R0, @STMP3	
007200	012737	010132	001242		MOV #FDAT06, @STMP4	
007206	012737	010150	001244		MOV #FDAT07, @STMP5	
007214	012737	010154	001246		MOV #FXDAT0, @STMP6	
007222	104033			18:	ERROR 33	
007224	000137	010174			JMP @FDONE	
007230	012701	010144		FERR6:	MOV #FDAT05, R1	: DID (BUT GR7) FAIL IN THE FOST
007234	012702	177777			MOV #-1, R2	: FLOWS?
007240	012703	000003			MOV #3, R3	
007244	020221			18:	CMP R2, (R1)+	
007246	001017				BNE 5\$	
007250	077303				SOB R3, 1\$	
007252	010037	001240				: REPORT FAILURE OF (BUT GR7) IN
007256	012737	000412	001244		MOV #412, @STMP5	: THE FOST FLOWS.
007264	012727	000147	001246		MOV #147, @STMP6	
007272	012737	000145	001250		MOV #145, @STMP7	
007300	104034			28:	ERROR 34	
007302	000137	010174			JMP @FDONE	
007306	012701	010144		58:	MOV #FDAT05, R1	: DID (BUT GR7) FAIL IN THE SRC FLOWS?
007312	012703	000003			MOV #3, R3	
007316	005721			68:	TST (R1)+	
007320	001402				BEQ 7\$	
007322	000137	007520			JMP @FERR10	
007326	077305			78:	SOB R3, 6\$	
007330	010037	001240				: REPORT FAILURE OF (BUT GR7) IN
007334	012737	000207	001244		MOV #207, @STMP5	: THE FSRC FLOWS.
007342	012737	000176	001246		MOV #176, @STMP6	
007350	012737	000174	001250		MOV #174, @STMP7	
007356	104035			108:	ERROR 35	
007360	000137	010174			JMP @FDONE	
007364	012701	010146		FERR7:	MOV #FDAT06, R1	: DID (BUT FC) FAIL IN THE FOST FLOWS?
007370	012702	177777			MOV #-1, R2	
007374	012703	000002			MOV #2, R3	
007400	020221			18:	CMP R2, (R1)+	
007402	001017				BNE 5\$	
007404	077303				SOB R3, 1\$	

007436	010037	001240		MOV	R0,#STMP3	:REPORT FAILURE OF BUT FC IN THE
007437	010037	000707	001244	MOV	#707,#STMP5	:FSRC FLOWS.
007438	010037	000244	001246	MOV	#244,#STMP6	
007439	010037	000245	001250	MOV	#245,#STMP7	
00743A	104036			2S: ERROR	36	
00743B	000137	010174		JMP	#FDONE	
007442	012737	010146		5S: MOV	#FDAT06,R1	:DID BUT FC FAIL IN THE FSRC FLOWS?
007443	012737	000002		MOV	#2,R3	
007444	025721			6S: TST	(R1)+	
007445	001402			BEQ	#75	
007446	000137	007520		JMP	#FERR10	
007447	077305			7S: SOB	R3,65	
007464	010037	001240		MOV	R0,#STMP3	:REPORT FAILURE OF BUT FC IN THE
007470	012737	000441	001244	MOV	#441,#STMP5	:FSRC FLOWS.
007476	012737	000076	001246	MOV	#76,#STMP6	
007504	012737	000077	001250	MOV	#77,#STMP7	
007512	104037			10S: ERROR	37	
007514	000137	010174		JMP	#FDONE	
007520				FERR10:		:REPORT DATA ERROR.
007521	010037	001240		MOV	R0,#STMP3	
007522	012737	010142	001242	MOV	#FDAT04,#STMP4	
007524	012737	010150	001244	MOV	#FDAT07,#STMP5	
007532	012737	010164	001246	MOV	#FXDAT4,#STMP6	
007540	012737			1S: ERROR	40	
007546	104040			JMP	#FDONE	
007550	000137	010174				
007554				FERR11:		:REPORT BAD FPS.
007554	010137	001240		MOV	R1,#STMP3	
007560	012737	000200	001242	MOV	#200,#STMP4	
007566	104041			1S: ERROR	41	
007570	000137	010174		JMP	#FDONE	
007574	012737	040411	001264	FERR20: MOV	#NULL,#STMP15	:THE EXECUTION OF THE LDC
007602	005037	001252		CLR	#STMP10	:CAUSED A TRAP TO 4, BECAUSE
007606	011637	001236		MOV	(SP),#STMP2	:A FSRC FLOW FAILURE RESULTED
007612	012737	010120	001240	MOV	#FDAT14,#STMP3	:IN AN ODD ADDRESS.
007620	012737	000321	001250	MOV	#321,#STMP7	
007626	012737	000762	001244	MOV	#762,#STMP5	
007634	021627	006362		CMP	(SP),#F4+2	:SEE IF FSRC MODE 6 OR 7 WAS
007640	001424			BEQ	FERR21	:EXECUTED.
007642	020027	010116		CMP	R0,#FDAT13	:FSRC MODE 5?
007646	001006			BNE	#25	
007650	012737	000325	001246	MOV	#325,#STMP6	:REPORT FSRC FLOW FAILURE TO
007656	022626			CMP	(SP)+,(SP)+	:MODE 5.
007660	104042			1S: ERROR	42	
007662	000544			BR	FDONE	

007664	020027	010122		23:	CMF	RD, #FDAT15	:FSRC MODE 3'
007670	001402				BEG	35	
007672	000137	040232			JMP	@CPSPUR	
007676				35:			:REPORT FSRC FLOW FAILURE TO
007676	012737	000323	001246		MOV	#323, @STMP6	:MODE 3.
007704	022626				CMF	(SP)+, (SP)+	
007706	104042			45:	ERROR	42	
007710	000531				BR	FDONE	
007712	022626			FERR21:	CMF	(SP)+, (SP)+	:REPORT FSRC FLOW FAILURE TO
							:MODE 6 OR MODE 7.
007714	012737	042156	001264		MOV	#MS16, @STMP15	
007722	012737	000326	001246		MOV	#326, @STMP6	
007730	012737	000327	001252		MOV	#327, @STMP10	
007736	104042			15:	ERROR	42	
007740	000515				BR	FDONE	
007742	012737	040411	001264	FERR25:	MOV	#NULL, @STMP15	:THE EXECUTION OF THE STD INSTRUCTION
007750	005037	001252			CLR	@STMP10	:TRAPPED TO 4, BECAUSE A FAILURE
007754	012737	010142	001240		MOV	#FDAT04, @STMP3	:IN THE FDST FLOWS RESULTED
007762	011637	001236			MOV	(SP), @STMP2	:IN AN ODD ADDRESS.
007766	012737	000527	001244		MOV	#527, @STMP5	
007774	012737	000641	001250		MOV	#641, @STMP7	
010002	021627	006520			CMF	(SP), #F10+2	:FLOW FAILURE TO FDST MODE 6 OR 7'
010006	001424				BEG	FERR26	
010010	020027	010140			CMF	RD, #FDAT03	:DID FDST FLOW FAIL TO MODE 5'
010014	001006				BNE	25	
010016	012737	000645	001246		MOV	#645, @STMP6	:REPORT FLOW FAILURE TO FDST
010024	022626				CMF	(SP)+, (SP)+	:MODE 5.
010026	104043			15:	ERROR	43	
010030	000461				BR	FDONE	
010032	020027	010144		25:	CMF	RD, #FDAT05	:DID FDST FLOW FAIL TO MODE 3'
010036	001402				BEG	35	
010040	000137	040232			JMP	@CPSPUR	
010044				35:			:REPORT FDST FLOW FAILED TO MODE 3.
010044	012737	000643	001246		MOV	#643, @STMP6	
010052	022626				CMF	(SP)+, (SP)+	
010054	104043			45:	ERROR	43	
010056	000446				BR	FDONE	
010060				FERR26:			:REPORT FDST FLOW FAILURE TO MODE
010060	012737	042156	001264		MOV	#MS16, @STMP15	:6 OR MODE 7.
010066	012737	000646	001246		MOV	#646, @STMP6	
010074	012737	000647	001252		MOV	#647, @STMP10	
010102	022626				CMF	(SP)+, (SP)+	
010104	104043			15:	ERROR	43	
010106	000432				BR	FDONE	

2642 010110 177777
2643 010112 177777
2644 010114 177777
2645 010116 177777
2646 010120 177777
2647 010122 177777
2648 010124 177777
2649 010126 177777
2650 010130 177777
2651 010132 177777
2652 010134 177777
2653 010136 177777
2654 010140 177777
2655 010142 177777
2656 010144 177777
2657 010146 177777
2658 010150 177777
2659 010152 177777
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2661 010156 177777
2662 010160 177777
2663 010162 177777
2664 010164 052525
2665 010166 031463
2666 010170 007417
2667 010172 000477
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2670 010174
2671 010174 104412
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2684 010176 000004
2685 010200 104413
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2687 010202
2688 010202 170011
2689 010204 012700 011026
2690 010210 012701 010776
2691 010214 012702 000004
2692 010220 012120
2693 010222 077202
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2695 010224 012700 011026
2696 010230 172510
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FDATE: -1
FDATE1: -1
FDATE2: -1
FDATE3: -1
FDATE4: -1
FDATE5: -1
FDATE6: -1
FDATE7: -1
-1
FDATE0: -1
FDATE1: -1
FDATE2: -1
FDATE3: -1
FDATE4: -1
FDATE5: -1
FDATE6: -1
FDATE7: -1
-1
FXDATE: -1
FXDATE1: -1
FXDATE2: -1
FXDATE3: -1
FXDATE4: 052525
FXDATE5: 031463
FXDATE6: 007417
FXDATE7: 000477

FDONE: RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).

::*****
;*TEST 7 FSRC MODE 0 TEST
;*:
;*THIS IS A TEST OF FSRC MODE ZERO USING THE LDD AND LDF INSTRUCTIONS.
;*:
::*****
*TEST: SCOPE ;SET UP THE LOOP ON ERROR ADDRESS.
LPEAR

I1: SETD ;SET FD.
MOV #IDATIO,RO
MOV #IPATIO,R1
MOV #4,R2
I2: MOV (R1)+,(RO)+ ;SET UP THE INPUT DATA BUFFER.
SOB R2,I2
MOV #IDATIO,PO ;LOAD AC1
LDD (RO),AC1

L04

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2698 010232 012700 011006 MOV #IPAT20,R0 ;LOAD ACC
2700 010236 172410 LDD (R0),ACC
2701 010240 012701 000001 MOV #1,R1 ;IN CASE THE FSRC FLOWS FAR
2702 010244 012737 010576 000004 MOV #IERR0,@ERRVECT ;AN ODD ADDRESS TRAP TO 4 MAY OCCUR.
2703 010252 012737 010266 001236 MOV #13,@STMP2
2704 010260 012737 042636 001240 MOV #MS35,@STMP3
2705 010266 172401 13: LDD AC1,ACC ;TEST INSTRUCTION.
2706 010270 000240 14: NOP
2707 010272 000240 15: NOP
2708
2709 010274 012700 011016 MOV #IDAT00,R0
2710 010300 174010 STD ACC,(R0) ;GET ACC, THE RESULTS.
2711
2712 010302 012700 011016 MOV #IDAT00,R0 ;SEE IF DATA IS CORRECT.
2713 010306 012701 011026 MOV #IDATIO,R1
2714 010312 012702 000004 MOV #4,R2
2715 010316 022021 16: CMP (R0)+,(R1)+
2716 010320 001424 BEQ I105
2717
2718 010322 012700 011022 MOV #IDAT02,R0 ;SEE IF (BUT FD) FAILED.
2719 010326 012702 000002 MOV #2,R2
2720 010332 005720 17: TST (R0)+
2721 010334 001413 BEQ I10
2722
2723 010336 012700 011022 MOV #IDAT02,R0
2724 010342 012702 000002 MOV #2,R2
2725 010346 022720 18: CMP #-1,(R0)+
2726 010352 001402 BEQ 25
2727 010354 000137 010660 JMP @IERR1
2728 010360 077206 25: SOB R2,I5
2729 010362 000401 BR I106
2730 010364 077216 110: SOB R2,I7
2731 010366 000137 010700 1106: JMP @IERR2
2732
2733 010372 077227 1105: SOB R2,I6
2734
2735 ;NOW TEST THE LOAD INSTRUCTION WITH FSRC MODE ZERO AND FD CLEAR.
2736
2737 010374 111:
2738 010374 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
2739
2740 010376 012700 010776 112: MOV #IPAT10,R0
2741 010402 012701 011026 MOV #IDATIO,R1
2742 010406 012702 000004 MOV #4,R2
2743 010412 012021 113: MOV (R0)+,(R1)+
2744 010414 077202 SOB R2,I13
2745
2746 010416 012700 011026 MOV #IDATIO,R0 ;SET UP AC1
2747 010422 172510 LDD (R0),AC1
2748
2749 010424 012700 011006 MOV #IPAT20,R0 ;SET UP ACC
2750 010430 172410 LDD (R0),ACC
2751
2752 010432 012701 000001 MOV #1,R1
2753 010436 012737 010454 001236 MOV #I14,@STMP2

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M04

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2754 010444 012737 042643 001240      MOV      #MS36, @#STMP3
2755 010452 170001                      SETF                      ;CLEAR FC.
2756
2757 010454 172401      I14:    LDF      AC1.ACO      ;TEST INSTRUCTION.
2758 010456 000240      I15:    NOP
2759 010460 000240      I16:    NOP
2760
2761 010462 170200      STFPS   RO                  ;SEE IF FPS IS STILL CLEAR.
2762 010464 022700 000004      CMP      #4, RO
2763 010470 001402      BEQ     I17
2764 010472 000137 010752      JMP     @#IERR3
2765
2766 010476      I17:    ;RESET TO DOUBLE MODE.
2767 010476 170011      SETD
2768
2769 010500 012700 011016      MOV      #IDAT00, RO
2770 010504 174010      STD     ACO, (RO)        ;GET ACO
2771
2772 010506 012737 177777 011032      MOV      #-1, @#IDAT12
2773 010514 012737 177777 011034      MOV      #-1, @#IDAT13
2774 010522 012700 011016      MOV      #IDAT00, RO
2775 010526 012701 011026      MOV      #IDAT10, R1
2776 010532 012702 0000C4      MOV      #4, R2
2777 010536 022021      I20:    CMP      (RO)+, (R1)+    ;SEE IF ACO WAS CORRECT.
2778 010540 001414      BEQ     I23
2779
2780 010542 023737 011022 011002      CMP      @#IDAT02, @#IPAT12    ;DID (BUT FC) FAIL?
2781 010550 001402      BEQ     I22
2782 010552 000137 010660      I21:    JMP      @#IERR1
2783 010556 023737 011024 011004      I22:    CMP      @#IDAT03, @#IPAT13
2784 010564 001372      BNE     I21
2785 010566 000137 010726      JMP      @#IERR4
2786
2787 010572 077217      I23:    SOB     R2, I20
2788
2789 010574 000520      BR      IDONE              ;NO ERRORS.
2790
2791      ;IF AN ODD ADDRESS TRAP OCCURS COME HERE TO ANALYZE THE FSRC FAILURE.
2792 010576 022716 010270      IERRO:  CMP      #I4, (SP)      ;MAKE SURE THE TRAP OCCURRED
2793 010602 001413      BEQ     1$                  ;ON THE INSTRUCTION BEING TESTED.
2794 010604 022716 010272      CMP      #I5, (SP)
2795 010610 001410      BEQ     1$
2796 010612 022716 010456      CMP      #I15, (SP)
2797 010616 001405      BEQ     1$
2798 010620 022716 010460      CMP      #I16, (SP)
2799 010624 001402      BEQ     1$
2800 010626 000137 040232      JMP     @#CPSPUR
2801
2802 010632 011637 001236      1$:    MOV      (SP), @#STMP2      ;REPORT FAILURE.
2803 010636 012737 000627 001240      MOV      #627, @#STMP3
2804 010644 012737 000320 001242      MOV      #320, @#STMP4
2805 010652 022626      CMP      (SP)+, (SP)+
2806 010654 104047      2$:    ERROR  47
2807 010656 000467      BR      IDONE
2808
2809      ;REPORT DATA ERROR.

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2810 010660 IERR1:
2811 010660 012737 011026 001242 MOV #IDATIO, @#STMP4
2812 010666 012737 011016 001244 MOV #IDAT00, @#STMP5
2813 010674 104051 1$: ERROR 51
2814 010676 000457 BR IDONE
2815
2816 ;REPORT FAILURE OF (BUT FD)
2817 010700 012737 000153 001244 IERR2: MOV #153, @#STMP5
2818 010706 012737 000434 001246 MOV #434, @#STMP6
2819 010714 012737 000435 001250 MOV #435, @#STMP7
2820 010722 IERR25:
2821 010722 104050 1$: ERROR 50
2822 010724 000444 BR IDONE
2823 010726 000153 001244 IERR4: MOV #153, @#STMP5
2824 010734 012737 000435 001246 MOV #435, @#STMP6
2825 010742 012737 000434 001250 MOV #434, @#STMP7
2826 010750 000764 BR IERR25
2827
2828 ;REPORT INCORRECT FPS AFTER LOAD INSTRUCTION.
2829 IERR3:
2830 010752 012737 010454 001236 MOV #I14, @#STMP2
2831 010760 010037 001240 MOV R0, @#STMP3
2832 010764 012737 000004 001242 MOV #4, @#STMP4
2833 010772 104041 1$: ERROR 41
2834 010774 000420 BR IDONE
2835
2836
2837 010776 000000 IPAT10: 0
2838 011000 170360 IPAT11: 170360
2839 011002 016161 IPAT12: 016161
2840 011004 052525 IPAT13: 052525
2841
2842 011006 177777 IPAT20: -1
2843 011010 177777 IPAT21: -1
2844 011012 177777 IPAT22: -1
2845 011014 177777 IPAT23: -1
2846
2847 011016 000000 IDAT00: 0
2848 011020 000000 IDAT01: 0
2849 011022 000000 IDAT02: 0
2850 011024 000000 IDAT03: 0
2851
2852 011026 000000 IDATIO: 0
2853 011030 000000 IDATI1: 0
2854 011032 000000 IDATI2: 0
2855 011034 000000 IDATI3: 0
2856
2857 011036 IDONE:
2858 011036 104412 RSETUP
2859
2860 ;GO INITIALIZE THE FPS AND STACK; AND
2861 ;SEE IF THE USER HAS EXPRESSED
2862 ;THE DESIRE TO CHANGE THE SOFTWARE
2863 ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
2864 ;THE USER TYPED CONTROL G?).
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::*****

TEST 10 FDST MODE 0 TEST
THIS IS A TEST OF THE STORE INSTRUCTIONS, STD AND STF, WITH FDST MODE 0.

SCOPE
LPERR :SET UP THE LOOP ON ERROR ADDRESS.
SETO :SET FC
MOV #TPAT10,R0
MOV #TDAT10,R1
MOV #4,R2
T2: MOV (R0)+,(R1)+ :SET UP THE INPUT DATA BUFFER.
SOB R2,T2
MOV #TDAT10,R0 :LOAD ACC
LDC (R0),ACC
MOV #TPAT20,R0 :LOAD AC1
LDC (R0),AC1
MOV #1,R1 :IF THE (BUT FDST) FORK FAILS
MOV #ERR0,#ERRVEC :AN ODD ADDRESS TRAP COULD RESULT.
MOV #T3,#STMP2
MOV #MS35,#STMP3
T3: STD ACC,AC1
T4: NOP
T5: NOP
MOV #TDAT00,R0
STD AC1,(R0) :GET THE DATA.
MOV #TDAT00,R3 :SEE IF THE DATA IS CORRECT.
MOV #TDAT10,R4
MOV #4,R5
T6: CMP (R3)+,(R4)+
BEQ T105
MOV #TDAT02,R3 :DID (BUT FC) FAIL?
MOV #2,R5
T7: TST (R3)+
BEQ T10
JMP #TERR1
T10: SOB R5,T7
JMP #TERR2
T105: SOB R5,T6
:NOW TEST THE STF ACC,AC1 INSTRUCTION.
T11: LPERR :SET UP THE LOOP ON ERROR ADDRESS.
T12: MOV #TPAT10,R0 :SET UP THE INPUT DATA BUFFER.
MOV #TDAT10,R1
MOV #4,R2

Vertical column of hex addresses and values on the left side of the page, including addresses like 000004, 012700, 012701, etc.

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T10:  FDST MODE 0 TEST
T13:  MOV (R0)+(R1)+
      SOB R2,T13
011300 012700 011634 MOV #TDAT10,R0 ;SET JP ACC
011304 174110 LDG (R0),ACC
011308 012700 011614 MOV #TPAT20,R0 ;SET JP AC1
011312 172510 LDG (R0),AC1
011316 012700 000001 MOV #1,R1
011320 012737 001236 MOV #14,#STMP2
011324 042643 001240 MOV #MS36,#STMP3
011328 170001 SETF ;CLEAR FD
011332 174001 T14: STP ACC,AC1
011336 000240 T15: NOP
011340 005000 T16: NOP
011344 170200 CLR R0
011348 022700 CMP #10,R0 ;SEE IF FPS IS CLEAR.
011352 001401 BEQ T17
011356 003521 BR T17
011360 170011 T17: SETD ;SET FD.
011364 012700 011624 MOV #TDAT00,R0
011368 174110 STD AC1,(R0) ;PICK UP AC1.
011372 012737 177777 011640 MOV #-1,#TDAT12
011376 012737 177777 011642 MOV #-1,#TDAT13
011380 012703 011624 MOV #TDAT00,R3
011384 012704 011634 MOV #TDAT10,R4
011388 012705 000004 MOV #4,R5
011392 022324 T20: CMP (R3)+(R4)+ ;WAS THE DATA TRANSFERRED CORRECTLY?
011396 001412 BEG T23
011400 023737 011630 011610 CMP #TDAT02,#TPAT12 ;DID (BUT FD) FAIL.
011404 001401 BEQ T22
011408 000440 BR T22
011412 023737 011632 011612 T21: CMP #TDAT03,#TPAT13
011416 301373 T22: BNE T21
011420 000456 BR T21
011424 077515 T23: SOB R5,T20
011428 000515 BR TCONE

;TRAP HERE THROUGH VECTOR 4 IF AN ODD ADDRESS OCCURS.
TERR0: CMP #T4,(SP) ;MAKE SURE THE TRAP WAS ON
      BEQ 15 ;AN INSTRUCTION BEING TESTED.
      BEQ #T5,(SP)
      BEQ 15
      CMP #T15,(SP)
      BEQ 15
      CMP #T16,SP

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000000	000000	040232		BEG	18	
000000	000000			END		20CPSPUR
000000	000000	000236		18:	MOV	(SP),20STMP2
000000	000000				CFR	(SP),4(SP)+
000000	000000	000240		25:	MOV	#527,20STMP3
000000	000000				MOV	#640,20STMP4
000000	000000				ERROR	121
000000	000000				BR	TDONE
:REPORT DATA FAILURE.						
000000	000000			ERR1:	MOV	#TDAT10,20STMP4
000000	000000				MOV	#TDAT00,20STMP5
000000	000000			18:	ERROR	123
000000	000000				BR	TDONE
:REPORT FAILURE OF (BUT FD).						
000000	000000			TERR2:	MOV	#160,20STMP6
000000	000000				MOV	#161,20STMP7
000000	000000			TERR25:	MOV	#640,20STMP5
000000	000000			18:	ERROR	122
000000	000000				BR	TDONE
000000	000000			TERR4:	MOV	#161,20STMP5
000000	000000				MOV	#160,20STMP7
000000	000000				BR	TERR25
:REPORT INCORRECT FPS AFTER STORE INSTRUCTION.						
000000	000000			ERR3:	MOV	#T15,20STMP2
000000	000000				MOV	R0,20STMP3
000000	000000				MOV	#10,20STMP4
000000	000000			18:	ERROR	41
000000	000000				BR	TDONE
000000	000000			TPAT10:		0
000000	000000			TPAT11:		170360
000000	000000			TPAT12:		016161
000000	000000			TPAT13:		052525
000000	000000			TPAT20:		-1
000000	000000			TPAT21:		-1
000000	000000			TPAT22:		-1
000000	000000			TPAT23:		-1
000000	000000			TDAT00:		0
000000	000000			TDAT01:		0
000000	000000			TDAT02:		0
000000	000000			TDAT03:		0
000000	000000			TDAT10:		0
000000	000000			TDAT11:		0
000000	000000			TDAT12:		C
000000	000000			TDAT13:		C
000000	000000			TDONE:		

* B. STUCK LOWS WILL SHOW AS 1'S IN THE 'AND' PATTERN.
 * IF THE FAILURE IS INTERMITTANT THEN THIS PROCEDURE WILL STILL
 * APPLY!!
 * IF THE FAILURE MOVES FROM ONE BIT TO ANOTHER, OR FROM ONE
 * GROUP OF BITS TO ANOTHER GROUP OF BITS THEN THE FAULT WILL
 * PROBABLY NOT SHOW UP IN THE 'AND' OR THE 'OR' PATTERNS. IN THIS
 * CASE THE 'AND' PATTERN WILL BE ALL 0'S AND THE 'OR' PATTERN
 * WILL BE ALL 1'S. WHEN THIS OCCURS SOME OTHER METHOD OF REPAIR MUST
 * BE FOUND (SUCH AS INSPECTION OF EACH INDIVIDUAL ERROR REPORT
 * RATHER THAN USING THE SUMMARY).
 *
 * MAP THE FOLLOWING NOTATION ONTO EACH BIT POSITION IN THE 'AND'
 * AND THE 'OR' PATTERNS WHICH ARE TYPED IN THE ERROR SUMMARY.
 *
 * A15,A14,...A1,A0 B15,B14,...B1,B0 C15,C14,...C1,C0 D15,D14,...D1,D0
 *
 * IN THIS NOTATION A15 THROUGH A0 IS THE FIRST OF THE FOUR 16 BIT
 * OCTAL NUMBERS TYPED, B15 THROUGH B0 IS THE SECOND, ETC.
 *
 * THIS TABLE SHOWS THE CORRESPONDING AM2901 CHIP ('E' NUMBER)
 * WHICH IS RESPONSIBLE FOR EACH BIT POSITION USING THE ABOVE
 * NOTATION. NOTE THAT ECO'S TO THE HARDWARE MIGHT MAKE THIS
 * TABLE OBSOLETE IF IT IS NOT UP DATED. NOTE ALSO THAT THERE ARE
 * FOUR BITS FOR EACH AM2901 CHIP:

BITS	AM2901 CHIP NUMBER
A15,A14,A13,A12	E61
A11,A10,A9,A8	E62
A7,A6,A5,A4	E90
A3,A2,A1,A0	E81
B15,B14,B13,B12	E86
B11,B10,B9,B8	E85
B7,B6,B5,B4	E83
B3,B2,B1,B0	E88
C15,C14,C13,C12	E79
C11,C10,C9,C8	E94
C7,C6,C5,C4	E89
C3,C2,C1,C0	E87
D15,D14,D13,D12	E78
D11,D10,D9,D8	E77
D7,D6,D5,D4	E92
D3,D2,D1,D0	E80

* NOW FIVE IMPORTANT CASES WHICH WILL ARRISE WHEN A FAULTY
 * AM2901 IS PRESENT CAN BE DESCRIBED:

- * 1.) IF ONLY ONE BIT OF THE 64 BITS IS INCORRECT THE CHIP INDICATED
 IN THE ABOVE TABLE IS MOST PROBABLY AT FAULT. BUT IF THAT
 CHIP IS REPLACED AND THE ERROR PERSISTS THEN SUPPOSE THAT
 BIT IS, LN WHERE 'L' IS A, B, C OR D

ANDN IS 15, 14, OR 0
 THEN IN GENERAL ANY OF THE FOUR CHIPS RESPONSIBLE FOR
 AN, BN, CN OR DN COULD BE AT FAULT, WITH LN BEING MOST PROBABLE
 FOR EXAMPLE IF BIT C12 IS FAULTY, THEN CHIP E79
 IS THE MOST PROBABLE SOURCE OF THE ERROR. IF REPAIRING
 THAT CHIP DOES NOT REMOVE THE FAULT THEN TRY EACH OF THE
 CHIPS ASSOCIATED WITH BITS A12, B12 AND D12 SHOULD BE TRIED
 WITH EQUAL PROBABILITY OF THE FAULT BEING
 IN ANY ONE OF THESE OTHER THREE CHIPS, TRY CHIPS E61, E66 AND E73.

*2.)

IF THERE ARE FOUR CONSECUTIVE BITS IN ERROR, FOLLOWING THE
 PATTERN:
 LN, LN+1, LN+2 AND LN+3 WHERE 'L' IS A, B, C
 OR D,
 AND N=0,4,8 OR 12

THEN THE ABOVE TABLE SHOULD DIRECTLY IDENTIFY THE FAILING CHIP.

*3.)

IF FOUR BITS ARE DROPPED WHICH FIT THE PATTERN:
 AN, BN, CN AND DN WHERE N=15, 14, OR 0
 THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH EACH OF
 THE BITS AN, BN, CN AND DN COULD BE AT FAULT WITH
 EQUAL PROBABILITY.

*4.)

IF 16 BITS ARE IN ERROR, FITTING THE PATTERN:
 AN, AN+1, AN+2, AN+3 WHERE N=0,4,8 OR 12
 BN, BN+1, BN+2, BN+3
 CN, CN+1, CN+2, CN+3
 AND
 DN, DN+1, DN+2, DN+3

THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH THESE BITS COULD BE AT FAULT WITH EQUAL PROBABILITY.

*5.)

IF THE FAILING BIT PATTERNS DISPLAYED IN THE 'AND' AND THE 'OR'
 DATA TYPED IN THE SUMMARY DOES NOT CONFORM EXPLICITELY TO
 ANY OF THE ABOVE PATTERNS, THEN THE TROUBLE SHOOTER MUST
 INTUITIVELY TRY TO FIND WHICH OF THE ABOVE CASES (1 THROUGH 4)
 IS A 'BEST FIT' OF THE SYMPTOMS.

*ST11: SCOPE

011646	000004
011650	170011
011652	012737
011650	012737
011666	012700
011672	012701
011676	104413
011700	004737
011704	012703
011710	
011710	172410
011712	174000
011714	172400
011716	174011
011720	004737

001244
001236

```

:TEST ACCUMULATOR 0 WITH FLOATING ONE
MOV #NUM0,00$TMP5
MOV #G1,00$TMP2
MOV #GPAT00,R0
MOV #GDAT00,R1
LPERR
JSR PC,00$GSETUP
MOV #102,R3
:SET UP THE LOOP ON ERROR ADDRESS.
:LOAD TEST PATTERN.

G1:
LDD (R0),AC0
STD AC0,AC0
LDD AC0,AC0
STD AC0,(R1)
JSR PC,00$GCMP
:STORE THE TEST PATTERN.
:COMPARE THE DATA READ WITH
:THAT WHICH WAS WRITTEN.
:SET FD.

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01:1724		TST	2#GFLAG1		
01:1724		BNE	G2		
		COM	2#GFLAG1		
		SEC	G3		
		CLC			
	35:	ROL	6(R0)		:GENERATE THE NEXT TEST PATTERN.
	36:	ROL	4(R0)		
		ROL	2(R0)		
		ROL	(R0)		
01:1737	013664	JSR	PC,2#GRESET		:RESET DEFAULT PATTERN IN OUTPUT :BUFFER.
01:1766	077330	S0B	R3,G1		
01:1766	004737	JSR	PC,2#GSUM		:TYPE ERROR SUMMARY.
					:TEST ACCUMULATOR 0 WITH FLOATING ZERO
01:1774	012737	042210	001244		
01:2002	012737	012032	001236		
01:2010	012700	014143			
01:2014	012701	014170			
01:2020	104413				
01:2022	004737	013606			
01:2026	012703	00C102			
01:2032					
01:2032	172410				
01:2034	174000				
01:2036	172400				
01:2040	174011				
01:2042	004737	013704			
01:2046	005737	014124			
01:2052	001004				
01:2054	005137	014124			
01:2060	000241				
01:2062	000401				
01:2064	000261				
01:2066	006160	000006			
01:2072	006160	000004			
01:2076	006160	000002			
01:2102	006110				
01:2104	004737	013664			
01:2110	077330				
01:2112	004737	014022			
					:TEST ACCUMULATOR 1 WITH FLOATING ONE
01:2116	012737	042216	001244		
01:2124	012737	012154	001236		
01:2132	012700	014130			
01:2136	012701	014170			
01:2142	104413				
01:2144	004737	013606			
01:2150	012703	00C102			
01:2154					
01:2156	172410				
01:2158	174000				

```

012100 012100 000000 013704 LDD AC0,AC0 ;STORE THE TEST PATTERN.
012101 012101 000000 STD AC0,(R1)
012102 012102 000000 JSR PC,#GCMPC ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.
012170 005737 014124 TST #GFLAG1
012174 001004 014124 BNE G10
012176 005137 014124 COM #GFLAG1
012202 000261 SEC
012204 000401 BR G11
012206 000241 G10: CLC
012210 006160 000006 G11: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
012214 006160 000004 ROL 4(R0)
012220 006160 000002 ROL 2(R0)
012224 006110 ROL (R0)
012226 004737 013664 JSR PC,#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
012232 077330 SOB R3,G7
012234 004737 014022 JSR PC,#GSUM ;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 1 WITH FLOATING ZERO
012240 012737 042216 001244 MOV #MNUM1,#STMP5
012246 012737 012276 001236 MOV #G12,#STMP2
012254 012700 014140 MOV #GPAT00,R0
012260 012701 014170 MOV #GDAT00,R1
012264 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
012266 004737 013606 JSR PC,#GSETUP ;LOAD TEST PATTERN.
012272 012703 000102 MOV #102,R3
G12: LDD (R0),AC0
012276 172410 STD AC0,AC1
012300 174001 LDD AC1,AC0 ;STORE THE TEST PATTERN.
012302 172401 STD AC0,(R1)
012304 174011 JSR PC,#GCMPC ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.
012306 004737 013704
012312 005737 014124 TST #GFLAG1
012316 001004 014124 BNE G13
012320 005137 014124 COM #GFLAG1
012324 000241 CLC
012326 000401 BR G14
012330 000261 G13: SEC
012332 006160 000006 G14: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
012336 006160 000004 ROL 4(R0)
012342 006160 000002 ROL 2(R0)
012346 006110 ROL (R0)
012350 004737 013664 JSR PC,#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
012354 077330 SOB R3,G12
012356 004737 014022 JSR PC,#GSUM ;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 2 WITH FLOATING ONE
012362 012737 042223 001244 MOV #MNUM2,#STMP5
012370 012737 012420 001236 MOV #G15,#STMP2
012376 012700 014130 MOV #GPAT00,R0
012402 012701 014170 MOV #GDAT00,R1
012406 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
012410 004737 013606 JSR PC,#GSETUP ;LOAD TEST PATTERN.

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3304	012703	000102			MOV	R3,G15		
3305	012704	000103			LDD	(R0),AC0		
3306	012705	172410			STD	AC0,AC2		
3307	012706	174002			LDD	AC2,AC0		
3308	012707	174002			STD	AC0,(R1)		:STORE THE TEST PATTERN.
3309	012708	004737	013704		JSR	PC,2*GCOMP		:COMPARE THE DATA READ WITH :THAT WHICH WAS WRITTEN.
3310	012709	005737	014124		TST	2*GFLAG1		
3311	012710	001004			BNE	G16		
3312	012711	005137	014124		COM	2*GFLAG1		
3313	012712	000261			SEC			
3314	012713	000401			BR	G17		
3315	012714	000241			G16:	CLC		
3316	012715	006160	000006		G17:	ROL	6(R0)	:GENERATE THE NEXT TEST PATTERN.
3317	012716	006160	000004		ROL	4(R0)		
3318	012717	006160	000002		ROL	2(R0)		
3319	012718	006110			ROL	(R0)		
3320	012719	004737	013664		JSR	PC,2*GRESET		:RESET DEFAULT PATTERN IN OUTPUT :BUFFER.
3321	012720	004737	014022		S0B	R3,G15		
3322	01250C	004737	014022		JSR	PC,2*GSUM		:TYPE ERROR SUMMARY.
3323					:TEST ACCUMULATOR 2 WITH FLOATING ZERO			
3324	012504	012737	042223	001244	MOV	#NUM2,2*STMP5		
3325	012512	012737	012542	001236	MOV	#G20,2*STMP2		
3326	012520	012700	014140		MOV	#PAT10,R0		
3327	012524	012701	014170		MOV	#DAT00,R1		
3328	012530	104413			LPERR			:SET UP THE LOOP ON ERROR ADDRESS.
3329	012532	004737	013606		JSR	PC,2*GSETUP		:LOAD TEST PATTERN.
3330	012536	012703	000102		MOV	R3,G20		
3331	012542	172410			G20:	LDD	(R0),AC0	
3332	012544	174002			STD	AC0,AC2		
3333	012546	172402			LDD	AC2,AC0		:STORE THE TEST PATTERN.
3334	012550	174011			STD	AC0,(R1)		
3335	012552	004737	013704		JSR	PC,2*GCOMP		:COMPARE THE DATA READ WITH :THAT WHICH WAS WRITTEN.
3336	012556	005737	014124		TST	2*GFLAG1		
3337	012562	001004			BNE	G21		
3338	012564	005137	014124		COM	2*GFLAG1		
3339	012570	000241			CLC			
3340	012572	000401			BR	G22		
3341	012574	000261			G21:	SEC		
3342	012576	006160	000006		G22:	ROL	6(R0)	:GENERATE THE NEXT TEST PATTERN.
3343	012582	006160	000004		ROL	4(R0)		
3344	012606	006160	000002		ROL	2(R0)		
3345	012612	006110			ROL	(R0)		
3346	012614	004737	013664		JSR	PC,2*GRESET		:RESET DEFAULT PATTERN IN OUTPUT :BUFFER.
3347	012620	077330			S0B	R3,G20		
3348	012622	004737	014022		JSR	PC,2*GSUM		:TYPE ERROR SUMMARY.
3349					:TEST ACCUMULATOR 3 WITH FLOATING ONE			
3350	012626	012737	042230	001244	MOV	#NUM3,2*STMP5		
3351	012634	012737	012664	001236	MOV	#G23,2*STMP2		

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3370	012642	012700	014130		MOV	#GPAT00,R0	
3371	012646	012701	014170		MOV	#GDAT00,R1	
3372	012652	104413			LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
3373	012654	004737	013606		JSR	PC,#GSETUP	;LOAD TEST PATTERN.
3374	012660	012703	000102		MOV	#102,R3	
3375	012664			G23:			
3376	012664	172410			LDD	(R0),AC0	
3377	012666	174003			STD	AC0,AC3	
3378	012670	172403			LDD	AC3,AC0	;STORE THE TEST PATTERN.
3379	012672	174011			STD	AC0,(R1)	
3380	012674	004737	013704		JSR	PC,#GCMP	;COMPARE THE DATA READ WITH ;THAT WHICH WAS WRITTEN.
3381							
3382	012700	005737	014124		TST	#GFLAG1	
3383	012704	001004			BNE	G24	
3384	012706	005137	014124		COM	#GFLAG1	
3385	012712	000261			SEC		
3386	012714	000401			BR	G25	
3387	012716	000241		G24:	CLC		
3388	012720	006160	000006	G25:	ROL	6(R0)	;GENERATE THE NEXT TEST PATTERN.
3389	012724	006160	000004		ROL	4(R0)	
3390	012730	006160	000002		ROL	2(R0)	
3391	012734	006110			ROL	(R0)	
3392	012736	004737	013664		JSR	PC,#GRESET	;RESET DEFAULT PATTERN IN OUTPUT ;BUFFER.
3393							
3394	012742	077330			S0B	R3,G23	
3395	012744	004737	014022		JSR	PC,#GSUM	;TYPE ERROR SUMMARY.
3396							
3397							
3398	012750	012737	042230	001244	;TEST ACCUMULATOR 3 WITH FLOATING ZERO		
3399	012756	012737	013006	001236	MOV	#MNUM3,#STMP5	
3400	012764	012700	014140		MOV	#G26,#STMP2	
3401	012770	012701	014170		MOV	#GPAT10,R0	
3402	012774	104413			MOV	#GDAT00,R1	
3403	012776	004737	013606		LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
3404	013002	012703	000102		JSR	PC,#GSETUP	;LOAD TEST PATTERN.
3405	013006				MOV	#102,R3	
3406	013006	172410		G26:			
3407	013010	174003			LDD	(R0),AC0	
3408	013012	172403			STD	AC0,AC3	
3409	013014	174011			LDD	AC3,AC0	;STORE THE TEST PATTERN.
3410	013016	004737	013704		STD	AC0,(R1)	
3411					JSR	PC,#GCMP	;COMPARE THE DATA READ WITH ;THAT WHICH WAS WRITTEN.
3412	013022	005737	014124		TST	#GFLAG1	
3413	013026	001004			BNE	G27	
3414	013030	005137	014124		COM	#GFLAG1	
3415	013034	000241			CLC		
3416	013036	000401			BR	G30	
3417	013040	000261		G27:	SEC		
3418	013042	006160	000006	G30:	ROL	6(R0)	;GENERATE THE NEXT TEST PATTERN.
3419	013046	006160	000004		ROL	4(R0)	
3420	013052	006160	000002		ROL	2(R0)	
3421	013056	006110			ROL	(R0)	
3422	013060	004737	013664		JSR	PC,#GRESET	;RESET DEFAULT PATTERN IN OUTPUT ;BUFFER.
3423							
3424	013064	077330			S0B	R3,G26	
3425	013066	004737	014022		JSR	PC,#GSUM	;TYPE ERROR SUMMARY.

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 DEEPA.F11 01-NOV-76 21:03 T11 ACCUMULATORS DATA PATTERNS TEST

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3482 013324 004737 013664 JSR PC, @#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
3483 ;BUFFER.
3484 013330 077330 SOB R3, G34
3485 013332 004737 014022 JSR PC, @#GSUM ;TYPE ERROR SUMMARY.
3486
3487 ;TEST ACCUMULATOR 5 WITH FLOATING ONE
3488 013336 012737 042245 001244 MOV #MNUM5, @#STMP5
3489 013344 012737 013374 001236 MOV #G37, @#STMP2
3490 013352 012700 014130 MOV #GPAT00, R0
3491 013356 012701 014170 MOV #GDAT00, R1
3492 013362 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
3493 013364 004737 013606 JSR PC, @#GSETUP ;LOAD TEST PATTERN.
3494 013370 012703 000102 MOV #102, R3
3495 013374
3496 013374 172410 LDD (R0), ACO
3497 013376 174005 STD ACO, ACS
3498 013400 172405 LDD ACS, ACO ;STORE THE TEST PATTERN.
3499 013402 174011 STD ACO, (R1)
3500 013404 004737 013704 JSR PC, @#GCMP ;COMPARE THE DATA READ WITH
3501 ;THAT WHICH WAS WRITTEN.
3502 013410 005737 014124 TST @#GFLAG1
3503 013414 001004 BNE G40
3504 013416 005137 014124 COM @#GFLAG1
3505 013422 000261 SEC
3506 013424 000401 BR G41
3507 013426 000241 G40: CLC
3508 013430 006160 000006 G41: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
3509 013434 006160 000304 ROL 4(R0)
3510 013440 006160 000002 ROL 2(R0)
3511 013444 006110 ROL (R0)
3512 013446 004737 013664 JSR PC, @#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
3513 ;BUFFER.
3514 013452 077330 SOB R3, G37
3515 013454 004737 014022 JSR PC, @#GSUM ;TYPE ERROR SUMMARY.
3516
3517 ;TEST ACCUMULATOR 5 WITH FLOATING ZERO
3518 013460 012737 042245 001244 MOV #MNUM5, @#STMP5
3519 013466 012737 013516 001236 MOV #G42, @#STMP2
3520 013474 012700 014140 MOV #GPAT10, R0
3521 013500 012701 014170 MOV #GDAT00, P1
3522 013504 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
3523 013506 004737 013606 JSR PC, @#GSETUP ;LOAD TEST PATTERN.
3524 013512 012703 000102 MOV #102, R3
3525 013516
3526 013516 172410 LDD (R0), ACO
3527 013520 174005 STD ACO, ACS
3528 013522 172405 LDD ACS, ACO ;STORE THE TEST PATTERN.
3529 013524 174011 STD ACO, (R1)
3530 013526 004737 013704 JSR PC, @#GCMP ;COMPARE THE DATA READ WITH
3531 ;THAT WHICH WAS WRITTEN.
3532 013532 005737 014124 TST @#GFLAG1
3533 013536 001004 BNE G43
3534 013540 005137 014124 COM @#GFLAG1
3535 013544 000241 CLC
3536 013546 000401 BR G44
3537 013550 000261 G43: SEC
  
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 CFFPAA.P11 01-NOV-76 21:03 T11 ACCUMULATORS DATA PATTERNS TEST

3538	013552	006163	000006	344:	ROL	6(R0)	:GENERATE THE NEXT TEST PATTERN.
3539	013556	006160	000004		ROL	4(R0)	
3540	013562	006160	000002		ROL	2(R0)	
3541	013566	006110			ROL	(R0)	
3542	013570	004737	013664		JSR	PC,@#GRESET	:RESET DEFAULT PATTERN IN OUTPUT :BUFFER.
3543							
3544	013574	077330			SOB	R3,042	
3545	013576	004737	014022		JSR	PC,@#GSUM	:TYPE ERROR SUMMARY.
3546							
3547							
3548	013602	000137	014202		JMP	@#GDONE	
3549							
3550							:USE THIS ROUTINE TO INITIALIZE ALL THE DATA BUFFERS.
3551	013606	012705	014124	GSETUP:	MOV	#GFLAG1,R5	
3552	013612	012704	000026		MOV	#26,R4	
3553	013616	005025		1\$:	CLR	(R5)+	
3554	013620	077402			SOB	R4,1\$	
3555							
3556	013622	012705	014140		MOV	#GPAT10,R5	
3557	013626	012704	000010		MOV	#10,R4	
3558	013632	005125		2\$:	COM	(R5)+	
3559	013634	077402			SOB	R4,2\$	
3560							
3561	013636	020067	000266	GS1:	CMP	R0,GPAT00	
3562	013642	001401			BEQ	3\$	
3563	013644	000207			RTS	PC	
3564							
3565	013646	012705	014170	3\$:	MOV	#GDAT00,R5	
3566	013652	012704	000004		MOV	#4,R4	
3567	013656	005125		4\$:	COM	(R5)+	
3568	013660	077402			SOB	R4,4\$	
3569	013662	000207			RTS	PC	
3570							
3571	013664	012705	014170	GRESET:	MOV	#GDAT00,R5	
3572	013670	012704	000004		MOV	#4,R4	
3573	013674	005025		1\$:	CLR	(R5)+	
3574	013676	077402			SOB	R4,1\$	
3575	013700	000137	013636		JMP	@#GS1	
3576							
3577							:SEE IF THE DATA WRITTEN MATCHES THE DATA READ.
3578	013704	012705	014170	GCMP:	MOV	#GDAT00,R5	
3579	013710	012704	000004		MOV	#4,R4	
3580	013714	010002			MOV	R0,R2	
3581	013716	022225		1\$:	CMP	(R2)+,(R5)+	
3582	013720	001402			BEQ	2\$	
3583	013722	000137	013732		JMP	@#GERR1	
3584	013726	077405		2\$:	SOB	R4,1\$	
3585	013730	000207			RTS	PC	
3586							
3587							:COME HERE TO REPORT AND RECORD ERRORS.
3588	013732	012637	014200	GERR1:	MOV	(SP)+,@#GADR	:SAVE THE RETURN ADDRESS.
3589	013736	010003			MOV	R0,R3	:COMPUTE 'OR' OF BAD DATA.
3590	013740	012705	014160		MOV	#G0R0,R5	
3591	013744	012704	000004		MOV	#4,R4	
3592	013750	052325		1\$:	BIS	(R3)+,(R5)+	
3593	013752	077402			SOB	R4,1\$	

014200 000000
014201 000000
014202 000000
014203 000000
014204 000000
014205 000000
014206 104413
014207 000000
014208 000000
014209 000000
014210 005037 014734
014211 012700 014736
014212 012701 015056
014213 012703 000024
014214 012120
014215 077302
014216 004767 000422
014217 170011
014218 012700 014736
014219 172410
014220 174001
014221 012700 014746
014222 172410
014223 174002
014224 012700 014756
014225 172410
014226 174003
014227 012700 014766
014228 172410
014229 174004
014230 012700 014776
014231 172410
014232 174005

SCB2: 0
SCB3: 0
SDAT00: 0
SDAT01: 0
SDAT02: 0
SDAT03: 0
GADR: 0
SCONE: RSETUP

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER HAS
:THE USER TYPED CONTROL G?..

::*****
:*TEST 12 FPP ACCUMULATORS DUAL ADDRESS TEST
*
:*THIS TEST PERFORMS A DUAL ADDRESSING TEST ON THE FLOATING ACCUMULATORS.
:*NOTE THAT ACCUMULATOR ZERO IS USED TO ACCESS ALL THE OTHERS.
*

*ST12: SCOPE :SET UP THE LOOP ON ERROR ADDRESS.
LPERR
*
H1: CLR #HFLAG :INITIALIZE THE LOAD BUFFER DATA.
MOV #HAIW,RO
MOV #HOAT1,R1
MOV #24,R3
H2: MOV (R1)+,(RO)+
SOB R3,H2
*
JSR PC,HCLR :CLEAR THE OUTPUT DATA BUFFER.
*
H3: SETD
:LOAD ACCUMULATOR 1
MOV #HAIW,RO
LDD (RO),ACO
STD ACO,AC1
:LOAD ACCUMULATOR 2
MOV #HA2W,RO
LDD (RO),ACC
STD ACO,AC2
:LOAD ACCUMULATOR 3
MOV #HA3W,RO
LDD (RO),ACO
STD ACO,AC3
:LOAD ACCUMULATOR 4
MOV #HA4W,RO
LDD (RO),ACO
STD ACO,AC4
:LOAD ACCUMULATOR 5
MOV #HA5W,RO

014400	012700	014766			
014404	012702	000004			
014408	010001				
014412	005121				
014416	172410				
014420	174004				
014424	004737	014546			
014428	004737	014524			
014432	077210				
014436					
014440					
014444					
014448					
014452					
014456					
014460					

```

DC (R0),AC0
STD ACC,AC5
14: JSR PC,20HSTD ;GO READ ALL ACCUMULATORS BACK.
JSR PC,20HCMP ;SEE IF DATA IS CORRECT.
;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 1
;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK
;THE DATA.
MOV #04,R2
MOV R0,R1
15: COM (R1)+
LDD (R0),AC0
STD ACC,AC1
JSR PC,20HSTD ;READ ALL THE ACCUMULATORS BACK.
JSR PC,20HCMP ;CHECK THE DATA.
SOB R2,H5
;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 2
;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK
;THE DATA.
MOV #04,R2
MOV R0,R1
16: COM (R1)+
LDD (R0),AC0
STD ACC,AC2
JSR PC,20HSTD ;READ ALL THE ACCUMULATORS BACK.
JSR PC,20HCMP ;CHECK THE DATA.
SOB R2,H6
;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 3
;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK
;THE DATA.
MOV #04,R2
MOV R0,R1
17: COM (R1)+
LDD (R0),AC0
STD ACC,AC3
JSR PC,20HSTD ;READ ALL THE ACCUMULATORS BACK.
JSR PC,20HCMP ;CHECK THE DATA.
SOB R2,H7
;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 4
;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK
;THE DATA.
MOV #04,R2
MOV R0,R1
18: COM (R1)+
LDD (R0),AC0
STD ACC,AC4
JSR PC,20HSTD ;READ ALL THE ACCUMULATORS BACK.
SOB R2,H8

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E06

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014624 005737 014734 JSR PC, @HCOMP ;CHECK THE DATA.
014625 000137 014624 SOB R2, H10

;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 5
;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK
;THE DATA.
014626 004737 014736 MOV #H5W, R0
014627 000204 000204 MOV #4, R2
014628 000137 000137 MOV R0, R1
014629 000137 000137 H11: COM (R1)+
014630 000137 000137 LDD (R0), ACD
014631 000137 000137 STD ACD, AC5
014632 004737 014736 JSR PC, @HSTD ;READ ALL THE ACCUMULATORS BACK.
014633 000137 014624 JSR PC, @HCOMP ;CHECK THE DATA.
014634 000137 000137 SOB R2, H11

014635 005737 014734 TST @HFLAG
014636 001402 001402 BEQ H12
014637 000137 015126 JMP @HDONE

014638 005137 014734 H12: COM @HFLAG
014639 000137 014240 JMP @H3

;STORE ALL ACCUMULATORS IN THE OUTPUT BUFFERS.
;CLEAR ALL OUTPUT BUFFERS.
014640 004737 014662 HSTD: JSR PC, @HCLR
014641 000137 015006 ;STORE ACCUMULATOR 1
014642 012704 015006 MOV #HA1R, R4
014643 017240 017240 LDD AC1, ACD
014644 017401 017401 STD ACD, (R4)
014645 017401 017401 ;STORE ACCUMULATOR 2
014646 012704 015016 MOV #HA2R, R4
014647 017240 017240 LDD AC2, ACD
014648 017401 017401 STD ACD, (R4)
014649 012704 015026 ;STORE ACCUMULATOR 3
014650 017240 017240 MOV #HA3R, R4
014651 017403 017403 LDD AC3, ACD
014652 017401 017401 STD ACD, (R4)
014653 012704 015036 ;STORE ACCUMULATOR 4
014654 017240 017240 MOV #HA4R, R4
014655 017404 017404 LDD AC4, ACD
014656 017401 017401 STD ACD, (R4)
014657 012704 015046 ;STORE ACCUMULATOR 5
014658 017240 017240 MOV #HA5R, R4
014659 017405 017405 LDD AC5, ACD
014660 017404 017404 STD ACD, (R4)
014661 000207 000207 RTS PC

;COMPARE DATA LOADED WITH DATA READ.
014662 012637 014732 HCOMP: MOV (SP)+, @HADR ;SAVE RETURN ADDRESS.
014663 012703 014736 MOV #HA1W, R3
014664 012704 015006 MOV #HA1R, R4
014665 012705 000204 MOV #24, R5
014666 022324 022324 HCOMP1: CMP (R3)+, (R4)+
014667 001402 001402 BEQ HCOMP2
014668 000137 014700 JMP @HERROR
  
```

F06

3- FPP DIAGNOSTIC PART 1 MACY11 27,1006) 01-NOV-76 21:09 PAGE 70
T12 FPP ACCUMULATORS DUAL ADDRESS TEST

000000	000000	000000	000000	000000	000000	000000	HCMPI: SOB R5, HCMPI JMP @HADR
000000	000000	000000	000000	000000	000000	000000	:CLEAR THE DATA OUTPUT BUFFER.
000000	000000	000000	000000	000000	000000	000000	HCLR: MOV @HA1R, R4 MOV @R4, R5 HCLR1: CLR (R4)+ SOB R5, HCLR1 RTS PC
000000	000000	000000	000000	000000	000000	000000	:REPORT ERROR.
000000	000000	000000	000000	000000	000000	000000	ERROR: MOV @HA1W, R3 MOV @STMP2, R4 MOV @I2, R5 15: MOV R3, (R4)+ ADD @I0, R3 SOB R5, 15 28: ERROR 46 JMP @HDONE
000000	000000	000000	000000	000000	000000	000000	HADR: 0 HFLAG: 0
000000	000000	000000	000000	000000	000000	000000	HA1W: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA2W: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA3W: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA4W: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA5W: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA1R: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA2R: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA3R: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA4R: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HA5R: .WORD 0,0,0,0
000000	000000	000000	000000	000000	000000	000000	HDAT1: .WORD 73567,73567,73567,73567
000000	000000	000000	000000	000000	000000	000000	HDAT2: .WORD 63146,63146,63146,63146
000000	000000	000000	000000	000000	000000	000000	HDAT3: .WORD 10421,10421,10421,10421
000000	000000	000000	000000	000000	000000	000000	HDAT4: .WORD 31463,31463,31463,31463
000000	000000	000000	000000	000000	000000	000000	HDAT5: .WORD 42104,42104,42104,42104

H06

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 T13 FSRC MODE 0 WITH ILLEGAL ACCUMULATOR 'EE'

015642				MOV	#SPAT10,R0	:LOAD ACC
				LDD	(R0),ACC	
000244				MOV	#SERR4,@FPVECT	
000004				MOV	#1,R0	
				MOV	#SERR5,@ERRVECT	
				CLR	R3	
	S8:			LDD	ACC,ACC	
	S9:			JFCC		
				INC	R3	
	S10:			INC	R3	
015652				MOV	#SDATOC,R1	
				STD	ACC,(R1)	:NO TRAP! GET ACC.
015652				MOV	#SDATOC,R1	:WAS ACC MODIFIED.
015642				MOV	#SPAT10,R2	
000004				MOV	#4,R3	
	S11:			CMP	(R1)+,(R2)+	
				BEQ	S12	
015514				JMP	@SERR6	
	S12:			SOB	R3,S11	
015540				JMP	@SERR7	
						:TRAPPED TO 244.
015170				SERR0:	CMP (SP),#S3	:PC OF TRAP CORRECT?
				BEQ	15	
040200				JMP	@FPSPUR	
015236				15:	MOV #S7,@SADR	
001236				SERR10:	MOV (SP),@STMP2	
				CMP	(SP)+,(SP)+	
				CLR	R4	
				STFPS	R4	:IS FPS CORRECT?
100200				CMP	#100200,R4	
				BNE	SERR15	
				CLR	R4	
				STST	R4	:IS FEC CORRECT?
000002				CMP	#2,R4	
				BNE	SERR20	
000216				JMP	@SADR	
015274				SERR4:	CMP (SP),#S9	
				BEQ	15	
040200				JMP	@FPSPUR	
015662				15:	MOV #SDONE,@SADR	
				BR	SERR10	
100200				:REPORT	FPS FAILURE:	
001240				SERR15:	MOV #100200,@STMP4	
				MOV	R4,@STMP3	
				15:	ERROR 117	
000154				JMP	@SADR	

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REFORM FEC BAD:
SERR20: MOV #S2,#STMP4
MOV #S4,#STMP3
IS: ERROR 120
JMP @SADR

:ACC WAS MODIFIED. (BUT FSRC) FORM FAILED.

SERR2: MOV #S2,#STMP2
IS: ERROR 112
BR SDONE
SERP6: MOV #S8,#STMP2
IS: ERROR 114
BR SDONE

SERR3: MOV #S2,#STMP2
IS: ERROR 111
BR SDONE
SERR7: MOV #S8,#STMP2
IS: ERROR 113
BR SDONE

:FAILURE OF (BUT FSRC) CAUSED AN ODD ADDRESS TRAP TO 4.
SERR1: CMP (SP),#S3 :DID TRAP OCCUR ON TESTED INSTRUCTION?
BEQ IS
CMP (SP),#S4
BEQ IS
JMP @CPSPUR

IS: MOV (SP),#STMP2
CMP (SP)+,(SP)+
SERR5: ERROR 115
BR SDONE

SERR5: CMP (SP),#S8 :DID TRAP OCCUR ON TEST INSTRUCTION?
BEQ IS
CMP (SP),#S9
BEQ IS
JMP @CPSPUR

IS: MOV (SP),#STMP2
CMP (SP)+,(SP)+
SERR5: ERROR 116
BR SDONE

SADR: 0
-1
SPAT10: 10421
SPAT11: 21042
SPAT12: 31463
SPAT13: 42104

SDAT00: 0
SDAT01: 0
SDAT02: 0

4042 015660 000000
4043 015662
4044 015662 104412
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4058 015664 000004
4059 015666 104413
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4061 015670
4062 015670 170011
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4064 015672 012700 016146
4065 015676 172410
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4067 015700 012700 016126
4068 015704 005003
4069 015706 012737 015776 000004
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4071 015714 172420
4072 015716 005203
4073 015720 005203
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4075 015722 012701 016136
4076 015726 174011
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4078 015730 020027 016116
4079 015734 001001
4080 015736 000442
4081
4082 015740 012702 016126
4083 015744 012703 016136
4084 015750 012704 000004
4085 015754 022223
4086 015756 001401
4087 015760 000443
4088 015762 077404
4089
4090 015764 022700 016136
4091 015770 001401
4092 015772 000424
4093
4094 015774 000470
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4096
4097

SDAT03: 0
SDONE: RSET JF
:GO INITIALIZE THE FPS AND STACK: AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER HAS
:THE USER TYPED CONTROL G?).

*TEST 14 FSRC MODE 2 TEST
*
* THIS IS A TEST OF FSRC MODE 2, ALTO
* INCREMENT MODE.
*

TEST14: SCOPE LPERR :SET JP THE LOOP ON ERROR ADDRESS.
J1: SETD :SET DOUBLE MODE
MOV #JDAT0,R0
LDD (R0),AC0 :LOAD AC0
MOV #JDAT10,R0
CLR R3
MOV #JERR0,#ERRVECT
J2: LDD (R0)+,AC0 :TEST INSTRUCTION
J3: INC R3
J4: INC R3
MOV #JDAT00,R1
STD AC0,(R1) :PICK UP RESULTS
CMP R0,#JBUFO :WAS AN AUTO
BNE IS :DECREMENT EXECUTED?
BR JERR1
IS: MOV #JDAT10,R2 :IS DATA CORRECT?
MOV #JDAT00,R3
MOV #4,R4
J5: CMP (R2)+,(R3)+
BEQ J6
BR JERR2
J6: SOB R4,J5
CMP #JDAT10+10,R0 :WAS RD INCREM.
BEQ J7 :BY 10 (OCTAL)
BR JERR1
J7: BR JDONE
;IF A TRAP THROUGH 4 OCCURS COME HERE

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4098 015776 021627 015716 JERR0: CMP (SP),#J3 :SEE IF THE TRAP
4099 016002 001405 BEQ J10 :OCCURRED ON THE
4100 016004 021627 015720 CMP (SP),#J4 :TESTED INSTRUCTION
4101 016010 001402 BEQ J10
4102 016012 000137 040232 JMP @#CPSPUR
4103
4104 016016 012737 000762 001240 J10: MOV #762,@#STMP3 :REPORT FSRC FLOW
4105 016024 012737 000322 001242 MOV #322,@#STMP4 :FAILURE
4106 016032 011637 001236 MOV (SP),@#STMP2
4107 016036 022626 CMP (SP)+,(SP)+
4108 016040 104052 1$: ERROR 52
4109 016042 000445 BR JDONE
4110
4111 016044 JERR1: :REPORT, RD NOT
4112 016044 012737 015714 001236 MOV #J2,@#STMP2 :CORRECTLY AFFECTED
4113 016052 010037 001242 MOV RD,@#STMP3
4114 016056 012737 016136 001242 MOV #JDATIC+10,@#STMP4
4115 016064 104053 1$: ERROR 53
4116 016066 000433 BR JDONE
4117
4118 ;REPORT DATA FAILURE
4119
4120 JERR2:
4121 016070 MOV #J2,@#STMP2
4122 016070 012737 015714 001236 MOV #JDATIC,@#STMP3
4123 016076 012737 016126 001240 MOV #JDATIC,@#STMP3
4124 016104 012737 016136 001242 MOV #JDATIC,@#STMP4
4125 016112 104054 1$: ERROR 54
4126 016114 000420 BR JDONE
4127
4128 JBUF0: .WORD 010421
4129 016120 021042 JBUF1: 021042
4130 016122 042104 JBUF2: 042104
4131 016124 031463 JBUF3: 031463
4132
4133 JDATIC: 052525
4134 016130 114631 JDATIC1: 114631
4135 016132 063146 JDATIC2: 063146
4136 016134 073567 JDATIC3: 073567
4137
4138 JDATIC0: 0
4139 016140 000000 JDATIC1: 0
4140 016142 000000 JDATIC2: 0
4141 016144 000000 JDATIC3: 0
4142
4143 JDATIC: -1
4144 016146 177777 JDATIC1: -1
4145 016150 177777 JDATIC2: -1
4146 016152 177777 JDATIC3: -1
4147 016154 177777
4148
4149 JDONE:
4150 016156 104412 RSETUP :GO INITIALIZE THE FPS AND STACK; AND
4151 :SEE IF THE USER HAS EXPRESSED
4152 :THE DESIRE TO CHANGE THE SOFTWARE
4153 :VIRTUAL CONSOLE SWITCH REGISTER (HAS
4154 :THE USER TYPED CONTROL G?).
    
```

41:05 41:10 41:15 41:20 41:25 41:30 41:35 41:40 41:45 41:50 41:55 42:00 42:05 42:10 42:15 42:20 42:25 42:30 42:35 42:40 42:45 42:50 42:55 43:00 43:05 43:10 43:15 43:20 43:25 43:30 43:35 43:40 43:45 43:50 43:55 44:00 44:05 44:10 44:15 44:20 44:25 44:30 44:35 44:40 44:45 44:50 44:55

```

*****
*TEST 15      FSRC MODE 4 TEST
*
* THIS IS A TEST OF FSRC MODE 4, AUTO
* DECREMENT MODE.
*
*****
*ST15:  SCOPE
         LPERR                                ;SET UP THE LOOP ON ERROR ADDRESS.

K1:
         SETD                                 ;SET DOUBLE MODE

         MOV      #KPAT0,R0
         LDD      (R0),ACC                    ;LOAD A DEFAULT
                                             ;PATTERN INTO ACC

         MOV      #KBUF0,R0
         CLR      R3
         MOV      #KERR0,@#ERRVECT

K2:
         LDD      -(R0),ACC                    ;TEST INSTRUCTION
K3:
         INC      R3
K4:
         INC      R3

         MOV      #KDAT00,R1
         STD      ACC,(R1)                   ;PICK UP THE RESULT

         CMP      R0,#KBUF0+10              ;WAS AN AUTO
         BNE      1$                         ;INCREMENT EXECUTED
         BR       KERR1

1$:
         MOV      #KDAT10,R2                  ;IS DATA CORRECT?
         MOV      #KDAT00,R3
         MOV      #4,R4

K5:
         CMP      (R2)+,(R3)+
         BEQ      K6
         BR       KERR2

K6:
         SOB      R4,K5

         CMP      #KBUF0-10,R0               ;WAS R0 DECREMENTED
         BEQ      K7                         ;PROPERLY?
         BR       KERR1

K7:
         BR       KDONE

;TRAP TO HERE ON AN ODD ADDRESS ERROR

KERR0:  CMP      (SP),#K3                    ;SEE IF THE ERROR
         BEQ      K10                         ;OCCURRED AT THE
         CMP      (SP),#K4                    ;INSTRUCTION TESTED.
         BEQ      K10
         JMP      @#CPSPUR

K10:
         MOV      #762,@#$TMP3                ;REPORT FAILURE IN
  
```

016160 000004
016162 104413
016164 170011
016166 012700 016440
016172 172410
016174 012700 016420
016200 005003
016202 012737 016272 000004
016210 172440
016212 005203
016214 005203
016216 012701 016430
016222 174011
016224 020027 016430
016230 001001
016232 000441
016234 012702 016410
016240 012703 016430
016244 012704 000034
016250 022223
016252 001401
016254 000442
016256 077404
016260 022700 016410
016264 001401
016266 000423
016270 000457
016272 021627 016212
016276 001405
016300 021627 016214
016304 001402
016306 000137 040232
016312 012737 000762 001240

```

4210 016320 012737 000324 001242      MOV      #324,0#STMP4      :FSRC FLOWS
4211 016326 011637 001236      MOV      (SP),0#STMP2
4212 016332 104055      :$:      ERROR          55
4213 016324 000445      BR       KDONE
4214
4215 016336      KERR1:      :REPORT, RD
4216 016336 012737 016210 001236      MOV      #K2,0#STMP2      :INCORRECTLY AFFECTED.
4217 016344 010037 001240      MOV      RD,0#STMP3
4218 016350 012737 016410 001242      MOV      #KDAT10,0#STMP4
4219 016356 104056      :$:      ERROR          56
4220 016360 000433      BR       KDONE
4221
4222      ;REPORT DATA FAILURE
4223
4224      KERR2:
4225 016362 012737 016210 001236      MOV      #K2,0#STMP2
4226 016370 012737 016410 001240      MOV      #KDAT10,0#STMP3
4227 016376 012737 016430 001242      MOV      #KDAT00,0#STMP4
4228 016404 104057      :$:      ERROR          57
4229 016406 000420      BR       KDONE
4230
4231 016410 052525      KDAT10: .WORD      052525
4232 016412 114631      KDAT11:          114631
4233 016414 063140      KDAT12:          063140
4234 016416 073567      KDAT13:          073567
4235
4236 016420 010421      KBUF0:          010421
4237 016422 031463      KBUF1:          031463
4238 016424 042104      KBUF2:          042104
4239 016426 021042      KBUF3:          021042
4240
4241 016430 000000      KDAT00:          0
4242 016432 000000      KDAT01:          0
4243 016434 000000      KDAT02:          0
4244 016436 000000      KDAT03:          0
4245
4246 016440 177777      KPAT0:          -1
4247 016442 177777      KPAT1:          -1
4248 016444 177777      KPAT2:          -1
4249 016446 177777      DPAT3:          -1
4250
4251 016450      KDONE:
4252 016450 104412      RSETUP          ;GO INITIALIZE THE FPS AND STACK; AND
4253      ;SEE IF THE USER HAS EXPRESSED
4254      ;THE DESIRE TO CHANGE THE SOFTWARE
4255      ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
4256      ;THE USER TYPED CONTROL G?).
4257
4258
4259      ;*****
4260      ;*TEST 16      FSRC MODE 2, WITH FD=0, TEST
4261      ;*
4262      ;* THIS IS A TEST OF FSRC MODE 2 WITH
4263      ;* FD=0. (AUTO INCREMENT)
4264      ;*
4265      ;*****

```

```

4266 016452 000004          TST16: SCOPE
4267 016454 104413          LPERR
                                ;SET UP THE LOOP ON ERROR ADDRESS.
4268
4269 016456
4270 016456 170011          L1:
                                SETD
                                ;SET DOUBLE MODE
4271
4272 016460 012700 016726          MOV    #LPATIO,RO
4273 016464 172410          LOD    (RO),AC0
                                ;LOAD AC0
4274
4275 016466 012700 016750          MOV    #LDATIO,RO
                                ;SET UP THE INPUT
4276 016472 012701 016736          MOV    #LPAT20,R1
                                ;DATA
4277 016476 012702 000004          MOV    #4,R2
4278
4279 016502 012120          1$:  MOV    (R1)+,(RO)+
4280 016504 077202          SOB
                                R2,1$
4281
4282 016506 012700 016750          MOV    #LDATIO,RO
4283 016512 005003          CLR
                                R3
4284 016514 170001          SETF
                                ;CLEAR FD.
4285
4286 016516 172420          L2:  LDF    (RO)+,AC0
4287 016520 005203          L3:  INC
                                R3
4288
4289 016522
4290 016522 170011          L4:
                                SETD
                                ;SET FD
4291
4292 016524 012701 016762          MOV    #LDATIO,R1
4293 016530 174011          STD
                                AC0,(R1)
                                ;PICK UP RESULTS
4294
4295 016532 020027 016754          CMP    RO,#LDAT12
                                ;WAS RO INCREMENTED
4296 016536 001401          BEQ
                                1$
                                ;CORRECTLY BY 4
4297 016540 000421          BR
                                LERR1
4298
4299 016542 012737 177777 016754 1$:  MOV    #-1,#LDAT12
4300 016550 012737 177777 016756          MOV    #-1,#LDAT13
4301 016556 012702 016750          MOV    #LDATIO,R2
                                ;IS DATA CORRECT
4302 016562 012703 016762          MOV    #LDATIO,R3
4303 016566 012704 000004          MOV    #4,R4
4304
4305 016572 022223          L5:  CMP    (R2)+,(R3)+
4306 016574 001401          BEQ
                                L6
4307 016576 000427          BR
                                LERR2
4308 016600 077404          L6:  SOB
                                R4,L5
4309
4310 016602 000473          BR
                                LDONE
4311
4312 016604          LERR1:
                                ;REPORT FAILURE
4313 016604 012737 016516 001236          MOV    #L2,#STMP2
                                ;RO NOT INCREMENTED
4314 016612 010037 001240          MOV    RO,#STMP3
                                ;BY 4
4315 016616 012737 016754 001242          MOV    #LDAT12,#STMP4
4316 016624 104060          1$:  ERROR
4317 016626 000461          BR
                                60
                                LDONE
4318
4319 016630          LERR3:
                                ;REPORT DATA FAILURE.
4320 016630 012737 016516 001236          MOV    #L2,#STMP2
4321 016636 012737 016750 001240          MOV    #LDATIO,#STMP3
  
```

016762 001242
016766 001270
016768 001270
016770 001270
016772 001270
016774 001270
016776 001270
016778 001270
016780 001270
016782 001270
016784 001270
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016800 001270
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016804 001270
016806 001270
016808 001270
016810 001270
016812 001270
016814 001270
016816 001270
016818 001270
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016824 001270
016826 001270
016828 001270
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016832 001270
016834 001270
016836 001270
016838 001270
016840 001270
016842 001270
016844 001270
016846 001270
016848 001270
016850 001270
016852 001270
016854 001270
016856 001270
016858 001270
016860 001270
016862 001270
016864 001270
016866 001270
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016976 001270
016978 001270
016980 001270
016982 001270
016984 001270
016986 001270
016988 001270
016990 001270
016992 001270
016994 001270
016996 001270
016998 001270
017000 001270

016762 001242
016766 001270
016768 001270
000004
016516 001236
016750 001240
016764 001242

15: MOV #LDAT00,2#STMP4
ERROR 61
BR LDONE
LERR2: MOV #LPAT20,R2 :DID (BU? FD,
MOV #LDAT00,R3 :FAIL.
MOV #4,R4
15: CMP (R2)+,(R3)+
BNE LERR3
SOB R4,15
MOV #L2,2#STMP2
MOV #LDAT10,2#STMP3
MOV #LDAT01,2#STMP4
25: ERROR 62
BR LDONE

LPAT10: .WORD -1
LPAT11: -1
LPAT12: -1
LPAT13: -1

LPAT20: 052525
LPAT21: 114631
LPAT22: 063142
LPAT23: 073567

.WORD 000001
LDAT10: 0
LDAT11: 0
LDAT12: 0
LDAT13: 0
.WORD 000001
LDAT00: 0
LDAT01: 0
LDAT02: 0
LDAT03: 0

LDONE: RSETUP

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER (HAS
:THE USER TYPED CONTROL G?).

*TEST !7 FSRC MODE 2 WITH GR7, IMMEDIATE MODE, TEST
*
* THIS IS A TEST OF FSRC MODE 2
* USING GR7 (THE PC). THIS IS IMMEDIATE
* MODE.
*

016774 000004
016776 170011
016778

ST17: SCOPE
M1: SETC

017000	012700	017000			MOV	#PAT10, R0	
017004	174010				LDD	(R0), ACC	: LOAD BACKGROUND : PATTERN INTO ACC.
017006	005004	017006	000004		CLR	R4	
017008	012700				MOV	#MERR3, #ERRVECT	
017016	172120	000000		M15:	LDD	#0, ACC	: TEST INSTRUCTION
017020	001702				. = .2		
017024	005204			M2:	WORD	5204	
017028	005204			M3:	INC	R4	: NOTE THAT
	005204			M4:	INC	R4	: 005204=INC R4
017030	020427	000000			CMR	R4, #3	: SEE IF THE PC
017034	001401				BEQ	#5	: WAS INCREMENTED
017036	000443				BR	MERRC	: BY 2 DURING THE : INSTRUCTION. IF : NOT THEN A BAD : CONSTANT WAS GENERATED
017040	012700	017012		M5:	MOV	#DAT00, R0	
017044	174010				STD	ACC, (R0)	: GET THE DATA
017046	012700	017312			MOV	#DAT00, R0	
017052	022720	005204			CMR	#5204, (R0)+	: IS THE DATA CORRECT?
017056	001401				BEQ	#5	
017060	000451				BR	MERR1	
017062	012701	000003		M5:	MOV	#3, R1	
017066	005720			M6:	TST	(R0)+	
017070	001002				BNE	#7	
017072	077103				SQB	R1, M6	
017074	000512				BR	MDONE	
017076	012700	017312		M7:	MOV	#DAT00, R0	: DID (BUT GRM) FAIL?
017102	012701	000004			MOV	#4, R1	
017106	022720	005204		M8:	CMR	#5204, (R0)+	
017112	001401				BEQ	#9	
017114	000433				BR	MERR1	
017116	077105			M9:	SQB	R1, M8	
017120				MERR2:			: REPORT FAILURE : OF (BUT GR7)
017120	012737	017016	001236		MOV	#M15, #STMP2	
017126	012737	017302	001240		MOV	#PAT20, #STMP3	
017134	012737	017312	001242		MOV	#DAT00, #STMP4	
017142	104063			M15:	ERROR	#3	
017144	000466				BR	MDONE	
017146	012705	017022		MERR0:	MOV	#M2, R5	: REPORT FAILURE
017152	010537	001242			MOV	R5, #STMP4	: PC INCREMENTED
017156	162704	000003			SQB	#3, R4	
017162	006304				ASL	R4	
017164	160435				SQB	R4, R5	
017166	010537	001240			MOV	R5, #STMP3	
017172	012737	017016	001236		MOV	#M15, #STMP2	
017200	104064			M15:	ERROR	#4	
017202	000447				BR	MDONE	


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+1700 017200 000000 000000 000000
+1701 017201 000000 000000 000000
+1702 017202 000000 000000 000000
+1703 017203 000000 000000 000000
+1704 017204 000000 000000 000000
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+1707 017207 000000 000000 000000
+1708 017208 000000 000000 000000
+1709 017209 000000 000000 000000
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+1750 017250 000000 000000 000000

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```

ERR1:  MOV    #M15, @STMP2      ;REPORT DATA
        MOV    #MDAT00, @STMP3 ;FAILURE
        MOV    #MPAT20, @STMP4
15:    ERROR  66
        BR     MDONE
;TRAP TO HERE THROUGH 4.
ERR3:  BIT    #1, (SP)         ;SEE IF THE
        BNE   15              ;TRAP TO 4 OCCURRED
        JMP   @PCSPUR        ;BECAUSE OF AN
                                ;ODD ADDRESS
                                ;IF YES REPORT
                                ;BAD CONSTANT
                                ;GENERATED
15:    MOV    (SP), @STMP3
        MOV    #M2, @STMP4
        MOV    #M15, @STMP2
25:    CMP    (SP)+, (SP)+
        ERROR  65
        BR     MDONE

MPAT10:  -1
MPAT11:  -1
MPAT12:  -1
MPAT13:  -1

MPAT20:  5204
MPAT21:  5204
MPAT22:  5204
MPAT23:  5204

MDAT00:  0
MDAT01:  0
MDAT02:  0
MDAT03:  0

MDONE:  RSETJP                ;GO INITIALIZE THE FPS AND STACK; AND
                                ;SEE IF THE USER HAS EXPRESSED
                                ;THE DESIRE TO CHANGE THE SOFTWARE.
                                ;VIRTUAL CONSOLE SWITCH REGISTER HAS
                                ;THE USER TYPED CONTROL G?).

;*****
;*TEST 20      FSRC MODE 3 TEST
;*
;* THIS IS A TEST OF FSRC MODE 3. AUTO INCREMENT
;* DEFERRED
;*
;*****
+1724 017224 000004 000004 000004  ST20:  SCOPE
+1725 017225 000000 000000 000000
+1726 017226 170C11 170C11 170C11  NI:    SETD                ;SET FD MODE
+1727 017227 000000 000000 000000
+1728 017228 012700 020010 020010  MOV    #NPAT10, R0
+1729 017229 172410 172410 172410  LDD    (R0), ACC        ;LOAD ACC WITH A DEFAULT

```

E07

017350
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017350      017350      017776      MOV      #NPAT20,R0      :PATTERN
017351      017351      017776      CLR      R3
017352      017352      000004      MOV      #NERR0,2#EPRVECT
017353      017353      000000      LDD     2(R0)+,AC0
017354      017354      000000      INC     R3
017355      017355      000000      INC     R3
017360      012701      017756      MOV      #NDAT00,R1
017361      017361      000000      STD     AC0,(R1)      :GET THE DATA
017366      020027      020000      CMP     R0,#NPAT20+2   :WAS R0 INCREMENTED
017367      001437      000000      BEQ     N12            :BY 2?
017374      020027      020006      N5:     CMP     R0,#NPAT20+10 :FSRC MODE 2?
017400      001001      000000      BNE     N6
017402      000506      000000      BR      NERR1
017404      020027      017756      N6:     CMP     R0,#NPAT20-10 :FSRC MODE 4?
017410      001001      000520      000000      BNE     N7
017412      000520      000000      BR      NERR2
017414      020027      017776      N7:     CMP     R0,#NPAT20
017420      001023      000000      BNE     N8
017422      012702      017756      MOV     #NDAT00,R2      :FSRC MODE 3?
017426      012703      000004      MOV     #4,R3
017432      022227      177777      N8:     CMP     (R2)+,#-1
017436      001002      000000      BNE     N9
017440      077304      000000      SOB    R3,N8
017442      000510      000000      BR      NERR3
017444      012702      017756      N9:     MOV     #NDAT00,R2      :FSRC MODE 1
017450      012703      017776      MOV     #NPAT20,R3
017454      012704      000004      MOV     #4,R4
017460      022223      000000      N10:    CMP     (R2)+,(R3)+
017462      001002      000000      BNE     N11
017464      077403      000000      SOB    R4,N10
017466      000502      000000      BR      NERR4
017470      000505      000000      N11:    BR      NERR5
017472      012702      017756      N12:    MOV     #NDAT00,R2      :DATA CORRECT?
017476      012703      020020      MOV     #NDAT10,R3
017502      012704      000004      MOV     #4,R4
017506      022223      000000      N13:    CMP     (R2)+,(R3)+
017510      001002      000000      BNE     N14
017512      077403      000000      SOB    R4,N13
017514      000545      000000      BR      NDONE
017516      000504      000000      N14:    BR      NERR6
  
```

:IF AN ODD ADDRESS TRAP OCCURS COME HERE
 :TO SEE IF THE FAILURE WAS IN THE FSRC

020000 000000
020001 000000
020002 000000
020003 000000
020004 000000
020005 000000
020006 000000
020007 000000
020008 000000
020009 000000
020010 000000
020011 000000
020012 000000
020013 000000
020014 000000
020015 000000
020016 000000
020017 000000
020018 000000
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020100 000000
020101 000000
020102 000000

NPAT20: .WORD NDAT10
NPAT21: 070707 NDAT11
NPAT22: 070707 NDAT12
NPAT23: 070707 NDAT13

.WORD 1
.WORD -1
.WORD -1
.WORD -1
.WORD -1

NDAT10: .WORD 010421
NDAT11: 021042
NDAT12: 031463
NDAT13: 042104

NDONE: RSETLP

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER AND
:THE USER TYPED CONTROL G.

*TEST 21 FSRC MODE 5 TEST
*
* THIS IS A TEST OF FSRC MODE 5. AUTO DECREMENT
* DEFERRED.
*

†ST21: SCOPE

01: SETD ;SET FD MODE
MOV #OPAT10,R0 ;LOAD ACD WITH A
LDD (R0),ACD ;DEFAULT PATTERN.
MOV #OPAT21,R0
CLR R3
MOV #ERR0,ERRVEC ;IF A FAILURE
;OCCURS IN THE FSRC
;FLOWS AN ODD ADDR.
;TRAP TO 4 MAY OCCUR.
02: LDD 2-(R0),ACD ;TEST INSTRUCTION
03: INC R3
04: INC R3
MOV #ODAT00,R1 ;GET THE DATA
STD ACD,(R1)
CMP R0,#OPAT20 ;WAS R0 DECREMENTED
BEQ 012 ;BY 2?
05: CMP R0,#OPAT21+10 ;FSRC MODE 2

```

+698B 020110 001001 BNE 06
+698C 020111 000508 BR 0ERR1
+698D 020112 020027 02047E 06: CMP R0,#OPAT21-10 :FSRC MODE 4?
+698E 020113 001001 BNE 07
+698F 020114 000517 BR 0ERR2
+6990 020122 020027 020502 07: CMP R0,#OPAT21
+6991 020123 012702 020464 MOV #ODAT01,R2 :FSRC MODE 0?
+6992 020124 012703 000004 08: MOV #4,R3
+6993 020125 022227 177777 CMP (R2)+,#-1
+6994 020126 001002 BNE 09
+6995 020127 077304 SOB R3,06
+6996 020128 000510 BR 0ERR3
+6997 020150 012702 020462 09: MOV #ODAT00,R2 :FSRC MODE 1?
+6998 020151 012703 020502 MOV #OPAT21,R3
+6999 020152 012704 000004 MOV #4,R4
+699A 020153 022223 010: CMP (R2)+,(R3)+
+699B 020154 001002 BNE 011
+699C 020155 077403 SOB R4,010
+699D 020156 000502 BR 0ERR4
+699E 020174 000505 011: BR 0ERR5
+699F 020176 012702 020462 012: MOV #ODAT00,R2 :DATA CORRECT?
+69A0 020202 012703 020524 MOV #ODAT10,R3
+69A1 020206 012704 000004 MOV #4,R4
+69A2 020212 022223 013: CMP (R2)+,(R3)+
+69A3 020214 001002 BNE 014
+69A4 020216 077403 SOB R4,013
+69A5 020220 000545 BR 0DONE
+69A6 020222 000504 014: BR 0ERR6
+69A7 :IF AN ODD ADDRESS TRAP OCCURS COME
+69A8 :HERE TO SEE IF THE FAILURE WAS IN THE
+69A9 :FSRC FLOWS:
+69AA 020224 022716 020064 0ERR0: CMP #04,(SP) :FSRC MODE 6 OR 7?
+69AB 020230 001412 BEQ 0ERR10
+69AC 020232 022716 020062 CMP #03,(SP)
+69AD 020236 001402 BEQ 1$
+69AE 020240 000137 040232 JMP #CPSPUR
+69AF 020244 020027 020504 1$: CMP R0,#OPAT21+2 :FSRC MODE 3?
+69B0 020250 001425 BEQ 0ERR1
+69B1 020252 000137 040232 JMP #CPSPUR
+69B2 020256 0ERR10: :WENT TO FSRC
+69B3 020256 011637 001236 MOV (SP),#STMP2 :MODE 6 OR 7
+69B4 020262 022626 CMP (SP)+,(SP)+
+69B5 020264 104074 1$: ERROR 74
+69B6 020266 000522 BR 0DONE
+69B7 020270 011637 001240 0ERR11: MOV (SP),#STMP3 :WENT TO FSRC MODE

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4738	020322	012737	000322	001246	OERR1:	MOV	#322,2#STMP6	:FSRC MODE2
4739	020323	012737	000327	001242	OERR2:	MOV	#627,2#STMP4	
4740	020324	012737	000325	001250		MOV	#325,2#STMP7	
4741	020326	012737	020060	001236		MOV	#02,2#STMP2	
4742	020354	104076			IS:	ERROR	76	
4743	020356	000466				BR	ODONE	
4744	020360	012737	000324	001246	OERR2:	MOV	#324,2#STMP6	:FSRC MODE 4
4745	020366	000761				BR	OERR20	
4746	020370	012737	000320	001246	OERR3:	MOV	#320,2#STMP6	:FSRC MODE 0
4747	020376	000755				BR	OERR20	
4748	020400	012737	000321	001246	OERR4:	MOV	#321,2#STMP6	:FSRC MODE 1
4749	020406	000751				BR	OERR20	
4750	020410	010037	001240		OERR5:	MOV	RC,2#STMP3	:RC NOT DECREMENTED
4751	020414	012737	020500	001242		MOV	#OPAT20,2#STMP4	:PROPERLY
4752	020422	012737	020064	001236		MOV	#04,2#STMP2	
4753	020430	104077			IS:	ERROR	77	
4754	020432	000440				BR	ODONE	
4755	020434				OERR6:			:DATA FAILURE
4756	020434	012737	020060	001236		MOV	#02,2#STMP2	
4757	020442	012737	020462	001240		MOV	#ODAT00,2#STMP3	
4758	020450	012737	020524	001242		MOV	#ODAT10,2#STMP4	
4759	020456	104100			IS:	ERROR	100	
4760	020460	000425				BR	ODONE	
4761	020462	000000			ODAT00:	.WORD	0	
4762	020464	000000			ODAT01:		0	
4763	020466	000000			ODAT02:		0	
4764	020470	000000			ODAT03:		0	
4765	020472	052525	052525	052525		.WORD	52525,52525,52525	
4766	020500	020524			CPAT20:	.WORD	ODAT10	
4767	020502	070707			OPAT21:	070707		
4768	020504	070707			OPAT22:	070707		
4769	020506	070707			OPAT23:	070707		
4770	020510	070707			OPAT24:	070707		
4771	020512	000001				.WORD	1	
4772	020514	177777			OPAT10:	.WORD	-1	
4773	020516	177777			OPAT11:		-1	
4774	020520	177777			OPAT12:		-1	
4775	020522	177777			OPAT13:		-1	
4776	020524	073567			ODAT10:	.WORD	73567	
4777	020526	004210			ODAT11:		004210	
4778	020530	114631			ODAT12:		114631	
4779	020532	125252			ODAT13:		125252	
4780	020534				ODONE:			

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020534 10-412

020536 000004
020540
020540 170011
020542 012700 021160
020546 172410
020550 012737 020656 000004
020556 012700 020727
020562 172460 000241
020564
020566 012701 021200
020572 174011
020574 012703 000004
020600 012702 021170
020604 012701 021200
020610 022221
020612 001007
020614 077303
020616 022700 020727
020622 001401
020624 000512
020626 000137 021210

020632 012701 021200
020636 012703 000004
020642 022721 177777
020646 001401
020650 000512
020652 077305
020654 000523

020656 021627 020564
020662 001411
020664 021627 020566
020670 001402
020672 000137 040232

020676 012737 000327 001246

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RSETUP
;GO INITIALIZE THE FPS AND STACK AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (AS
;THE USER TYPED CONTROL G?).

;*****
;*TEST 22 FSRC MODE 6 TEST
;*
;* THIS IS A TEST OF FSRC MODE 6, INDEX MODE
;*
;*****
*ST22: SCOPE
P1: SETD ;SET FD MODE
MOV #PPAT10,R0
LDD (R0),ACD ;LOAD A DEFAULT PATTERN
;INTO ACD
MOV #PERRO,#ERRVECT ;IF THE (BUT FSRC) FCRG
;FAILS AN ODD ADDRESS TRAP
;COULD OCCUR.
MOV #PDAT10-241,R0 ;COULD OCCUR.
P2: LDD 241(R0),ACD
P3=P2+2
P4: MOV #PDAT00,R1
STD ACD,(R1) ;GET THE DATA
MOV #4,R3
MOV #PDAT10,R2
MOV #PDAT00,R1
P5: CMP (R2)+,(R1)+ ;CHECK THE DATA
BNE P6
SOB R3,P5
CMP #PDAT10-241,R0 ;R0 CORRECT?
BEQ 1$
BR PERR21
1$: JMP #PDONE
P6: MOV #PDAT00,R1
MOV #4,R3
P7: CMP #-1,(R1)+ ;WAS IT FSRC MODE 0?
BEQ P8
BR PERR1
P8: SOB R3,P7
BR PERR2
;TRAP TO HERE ON AN ODD ADDRESS
PERR0: CMP (SP),#P3
BEQ PERR11
CMP (SP),#P4 ;WAS IT FSRC MODE 7?
BEQ PERR10
JMP #CPSPUR
PERR10: MOV #327,#STMP6
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4826	020704	000443			BR	PERR17	
4827	020706	022700	020727		PERR11: CMP	#PDATIO-241,RO	;WAS IT FSRC MODE 1
4828	020712	001004			BNE	PERR12	
4829	020714	012737	000321	001246	MOV	#321,@#STMP6	
4830	020722	000434			BR	PERR17	
4831	020724	022700	020737		PERR12: CMP	#PDATIO-241+10,RO	;WAS IT FSRC MODE 2
4832	020730	001004			BNE	PERR13	
4833	020732	012737	000322	001246	MOV	#322,@#STMP6	
4834	020740	000425			BR	PERR17	
4835	020742	022700	020731		PERR13: CMP	#PDATIO-241+2,RO	;WAS IT FSRC MODE 3
4836	020746	001004			BNE	PERR14	
4837	020750	012737	000323	001246	MOV	#323,@#STMP6	
4838	020756	000416			BR	PERR17	
4839	020760	022700	020717		PERR14: CMP	#PDATIO-241-10,RO	;WAS IT FSRC MODE 4
4840	020764	001004			BNE	PERR15	
4841	020766	012737	000324	001246	MOV	#324,@#STMP6	
4842	020774	000407			BR	PERR17	
4843	020776	022700	020725		PERR15: CMP	#PDATIO-241-2,RO	;WAS IT FSRC MODE 5
4844	021002	001401			BEQ	PERR16	
4845	021004	000416			BR	PERR20	
4846	021006	012737	000325	001246	PERR16: MOV	#325,@#STMP6	
4847							
4848	021014	012737	000627	001244	PERR17: MOV	#627,@#STMP5	;REPORT FSRC
4849	021022	012737	000326	001250	MOV	#326,@#STMP7	;FLOWS FAILURE.
4850	021030	011637	001236		MOV	(SP),@#STMP2	
4851	021034	022626			CMP	(SP)+,(SP)+	
4852	021036	104101			1\$: ERROR	101	
4853	021040	000463			BR	PDONE	
4854							
4855	021042	011637	001236		PERR20: MOV	(SP),@#STMP2	;REPORT RD AFFECTED
4856	021046	022626			CMP	(SP)+,(SP)+	
4857	021050	000403			BR	PERR22	
4858	021052	012737	020562	001236	PERR21: MOV	#P2,@#STMP2	
4859	021060				PERR22:		
4860	021060	010037	001240		MOV	RO,@#STMP3	
4861	021064	012737	020727	001242	MOV	#PDATIO-241,@#STMP4	
4862	021072	104102			1\$: ERROR	102	
4863	021074	000445			BR	PDONE	
4864							
4865	021076				PERR1:		;DATA FAILURE.
4866	021076	012737	020562	001236	MOV	#P2,@#STMP2	
4867	021104	012737	021170	001240	MOV	#PDATIO,@#STMP3	
4868	021112	012737	021200	001242	MOV	#PDATIO,@#STMP4	
4869	021120	104104			1\$: ERROR	104	
4870	021122	000432			BR	PDONE	
4871							
4872	021124				PERR2:		;FSRC FAILURE TO
4873	021124	012737	020562	001236	MOV	#P2,@#STMP2	;MODE 0
4874	021132	012737	000627	001244	MOV	#627,@#STMP5	
4875	021140	012737	000326	001250	MOV	#326,@#STMP7	
4876	021146	012737	000320	001246	MOV	#320,@#STMP6	
4877	021154	104103			1\$: ERROR	103	
4878	021156	000414			BR	PDONE	
4879							
4880	021160	177777			PPATIO: .WORD	-1	
4881	021162	177777			PPATIO: .WORD	-1	


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4882 021164 177777 PPAT12: -1
4883 021166 177777 PPAT13: -1
4884
4885 021170 010421 PDAT10: .WORD 010421
4886 021172 031463 PDAT11: 031463
4887 021174 052525 PDAT12: 052525
4888 021176 073567 PDAT13: 073567
4889
4890 021200 000000 PDAT00: .WORD 0
4891 021202 000000 PDAT01: 0
4892 021204 000000 PDAT02: 0
4893 021206 000000 PDAT03: 0
4894
4895 021210 PDONE:
4896 021210 104412 RSETUP ;GO INITIALIZE THE FPS AND STACK: RAC
4897 ;SEE IF THE USER HAS EXPRESSED
4898 ;THE DESIRE TO CHANGE THE SOFTWARE
4899 ;VIRTUAL CONSOLE SWITCH REGISTER HAS
4900 ;THE USER TYPED CONTROL G?
4901
4902
4903 ;*****
4904 ;*TEST 23 FSRC MODE 7 TEST
4905 ;*
4906 ;* THIS IS A TEST OF FSRC MODE 7. INDEX
4907 ;* DEFERRED MODE.
4908 ;*
4909 ;*****
4910 021212 000004 *ST23: SCOPE
4911
4912 021214 Q1:
4913 021214 170011 SETD
4914
4915 021216 012700 021650 MOV #QPAT10,RO
4916 021222 172410 LDD (RO),AC0 ;LOAD A DEFAULT
4917 ;PATTERN INTO AC0
4918 021224 012737 021356 000004 MOV #QERR0,Q#ERRVECT ;IF THE (BUT FSRC)
4919 ;FORK FAILS AN
4920 ;ODD ADR TRAP COULD
4921 ;OCCUR
4922 021232 012700 021417 MOV #QPAT20-241,RO
4923
4924 021236 172470 000241 Q2: LDD Q241(RO),AC0
4925 021240 021240 Q3=Q2+2
4926
4927 021242 012701 021670 Q4: MOV #QDAT00,R1
4928 021246 174011 STD AC0,(R1) ;GET THE DATA
4929
4930 021250 012703 000004 MOV #4,R3
4931 021254 012704 021670 MOV #QDAT00,R4
4932 021260 012705 021700 MOV #QDAT10,R5
4933 021264 022425 Q5: CMP (R4)+,(R5)+ ;CHECK THE DATA
4934 021266 001007 BNE Q6
4935 021270 077303 SOB R3,Q5
4936
4937 021272 022700 021417 CMP #QPAT20-241,RO ;CHECK RO.

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4938	021276	001401			BEG	1\$	
4939	021300	003514			BR	QERR21	
4940	021302	000137	021710	1\$:	JMP	Q#QDONE	
4941							
4942	021306	012701	021670	06:	MOV	#QDAT00,R1	
4943	021312	012703	000004		MOV	#4,R3	
4944	021316	022721	177777	07:	CMP	#-1,(R1)+	;WAS IT FSRC MODE 0?
4945	021322	001002			BNE	Q8	
4946	021324	077304			SOB	R3,Q7	
4947	021326	000513			BR	QERR2	
4948							
4949	021330	012701	021660	08:	MOV	#QPAT20,R1	
4950	021334	012702	021670		MOV	#QDAT00,R2	
4951	021340	012703	000004		MOV	#4,R3	
4952	021344	022122		09:	CMP	(R1)+,(R2)+	;WAS IT FSRC 6
4953	021346	001401			BEG	Q10	;OR DATA FAILURE
4954	021350	000524			BR	QERR1	
4955	021352	077304		Q10:	SOB	R3,Q9	
4956	021354	000504			BR	QERR3	
4957							
4958							;TRAP TO HERE ON AN ODD ADR FAILURE
4959							
4960	021356	021627	020564	QERR0:	CMP	(SP),#P3	
4961	021362	000137	040232		JMP	Q#CPSPUR	
4962							
4963	021366	022700	021417	QERR11:	CMP	#QPAT20-241,RO	;WAS IT FSRC
4964	021372	001004			BNE	QERR12	;MODE 1?
4965	021374	012737	000321 001246		MOV	#321,Q#STMP6	
4966	021402	000434			BR	QERR17	
4967	021404	022700	021427	QERR12:	CMP	#QPAT20-241+10,RO	;WAS IT FSRC
4968	021410	001004			BNE	QERR13	;MODE 2?
4969	021412	012737	000322 001246		MOV	#322,Q#STMP6	
4970	021420	000425			BR	QERR17	
4971	021422	022700	021421	QERR13:	CMP	#QPAT20-241+2,RO	;WAS IT FSRC
4972	021426	001004			BNE	QERR14	;MODE 3?
4973	021430	012737	000323 001246		MOV	#323,Q#STMP6	
4974	021436	000416			BR	QERR17	
4975	021440	022700	021407	QERR14:	CMP	#QPAT20-241-10,RO	;WAS IT FSRC
4976	021444	001004			BNE	QERR15	;MODE 4
4977	021446	012737	000324 001246		MOV	#324,Q#STMP6	
4978	021454	000407			BR	QERR17	
4979							
4980	021456	022700	021415	QERR15:	CMP	#QPAT20-241-2,RO	;WAS IT FSRC
4981	021462	001401			BEG	QERR16	;MODE 5
4982	021464	000416			BR	QERR20	
4983							
4984	021466	012737	000325 001246	QERR16:	MOV	#325,Q#STMP6	
4985							
4986	021474	012737	000627 001244	QERR17:	MOV	#627,Q#STMP5	;REPORT FSRC FAILURE
4987	021502	012737	000327 001250		MOV	#327,Q#STMP7	
4988	021510	011637	001236		MOV	(SP),Q#STMP2	
4989	021514	022626			CMP	(SP)+,(SP)+	
4990	021516	104105		1\$:	ERROR	105	
4991	021520	000473			BR	QDONE	
4992							
4993	021522	011637	001236	QERR20:	MOV	(SP),Q#STMP2	;REPORT RO AFFECTED.

N07

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4994 021526 022626          CMP      (SP)+,(SP)+
4995 021530 000403          BR       QERR22
4996 021532 012737 021236 001236 QERR21: MOV      #Q2,Q#STMP2
4997 021540          QERP22:
4998 021540 010037 001240          MOV      RD,Q#STMP3
4999 021544 012737 021417 001242          MOV      #QPAT20-241,Q#STMP4
5000 021552 104106          IS:     ERROR 106
5001 021554 000455          BR       QDONE
5002
5003 021556 012737 000320 001246 QERR2:  MOV      #320,Q#STMP6      ;WENT TO FSRC
5004 021564 000403          BR       QERR4          ;MODE 0
5005 021566 012737 000326 001246 QERR3:  MOV      #326,Q#STMP6      ;WENT TO FSRC
5006          ;MODE 6
5007 021574 012737 000627 001244 QERR4:  MOV      #627,Q#STMP5
5008 021602 012737 000327 001250          MOV      #327,Q#STMP7
5009 021610 012737 021236 001236          MOV      #Q2,Q#STMP2
5010 021616 104107          IS:     ERROR 107
5011 021620 000433          BR       QDONE
5012
5013 021622          QERR1:          ;DATA FAILURE
5014 021622 012737 021236 001236          MOV      #Q2,Q#STMP2
5015 021630 012737 021700 001240          MOV      #QDAT10,Q#STMP3
5016 021636 012737 021670 001242          MOV      #QDAT00,Q#STMP4
5017 021644 104110          IS:     ERROR 110
5018 021646 000420          BR       QDONE
5019
5020 021650 177777          QPAT10: .WORD  -1
5021 021652 177777          QPAT11:          -1
5022 021654 177777          QPAT12:          -1
5023 021656 177777          QPAT13:          -1
5024
5025 021660 021700          QPAT20: .WORD  QDAT10
5026 021662 052525          QPAT21:          52525
5027 021664 052525          QPAT22:          52525
5028 021666 052525          QPAT23:          52525
5029
5030 021670 000000          QDAT00: .WORD   0
5031 021672 000000          QDAT01:          0
5032 021674 000000          QDAT02:          0
5033 021676 000000          QDAT03:          0
5034
5035 021700 073567          QDAT10: .WORD  073567
5036 021702 052525          QDAT11: .WORD  052525
5037 021704 031463          QDAT12: .WORD  031463
5038 021706 010421          QDAT13: .WORD  010421
5039
5040 021710          QDONE:
5041 021710 104412          RSETUP          ;GO INITIALIZE THE FPS AND STACK; AND
5042          ;SEE IF THE USER HAS EXPRESSED
5043          ;THE DESIRE TO CHANGE THE SOFTWARE
5044          ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
5045          ;THE USER TYPED CONTROL G?).
5046
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:*****
:TEST 24          (BUT EZBT Y8),(BUT ENBT) AND (BUT FIUV) TEST
:*
```

T24 (BUT EZBT YB) (BUT ENBT) AND (BUT FIV) TEST

```

* THIS IS A TEST OF THE (BUT EZBT YB) FORK, THE
* (BUT ENBT) FORK AND (BUT FIV) FORK IN THE
* LOAD INSTRUCTION FLOWS.
* EACH OF THE PATTERNS:
*   0
*   +NUM
*   -NUM
*   -0
* IS LOADED TWICE, ONCE WITH AC=0 THEN
* WITH AC=0. AFTER EACH LOAD THE FPS IS
* CHECK TO INSURE THAT CONTROL WAS PASSED
* THROUGH WITH THE FORKS PROPERLY.

```

```

U24:  SCOPE
      CLR      @BFLAG
      MOV      @UPAT00,R0      ;SET UP AC=0 DATA.
      MOV      #4,R1
      MOV      #-1,(R0)+
      SOB     R1,R0

      MOV      #033,@UTMP1
      MOV      #023,@UTMP2
      MOV      @UERR0,@FPVECT ;IN CASE (BUT FIV FAILS)

U1:   LPERR
      MOV      @200,R0      ;SET UP THE LOOP ON ERROR ADDRESS.
      LDFPS   R0
      MOV      @UPAT00,R0      ;LOAD ACC
      LDD     (R0),AC0
      MOV      @UTMP1,@UR0M1
      MOV      #001,@UR0M2
      MOV      #254,@UR0M3

      MOV      @UPAT10,R0      ;LOAD C INTO ACC
      LDD     (R0),AC0
      MOV      R0,@STMP10
      MOV      #U2,@STMP2

      MOV      #204,R4
      STEPS   R5      ;SEE IF FPS IS CORRECT

      CMP     R4,R5
      BEQ    U3
      JMP     @UERR1

U3:   LPERR
      MOV      @200,R0      ;SET UP THE LOOP ON ERROR ADDRESS.
      LDFPS   R0
      MOV      @UPAT00,R0      ;LOAD ACC
      LDD     (R0),AC0
      MOV      @UTMP2,@UR0M1
      MOV      #003,@UR0M2
      MOV      #054,@UR0M3

```

021736	012737	000033	023014
021744	012737	000023	023016
021752	012737	022472	000244
021760	104413		
021768	012700	000200	
021776	170100		
021784	012700	022742	
021792	172410		
021800	013737	023014	023020
022004	012737	000001	023022
022012	012737	000254	023024
022020	012700	022752	
022024	172410		
022026	010037	001252	
022032	012737	022024	001236
022040	012704	000204	
022044	170205		
022046	020405		
022050	001402		
022052	000137	022516	
022056			
022058	104413		
022060	012700	000200	
022064	170100		
022066	012700	022742	
022072	172410		
022074	013737	023016	023020
022102	012737	000003	023022
022110	012737	000054	023024

Address	Hex	Hex	Label	Instruction	Comments
022116	012700	022762		MOV #UPAT20,RO	:LOAD A POSITIVE NUMBER INTO ACC
022122	172410		4:	LDD (RO),ACC	
022124	010037	001252		MOV RO,#STMP10	
022126	012737	022122	001236	MOV #U4,#STMP2	
022136	012704	000200		MOV #200,R4	:FPS CORRECT
022146	170205			STFPS R5	
02214E	020405			CMR R4,R5	
022150	001402			BEG J5	
022152	000137	022602		JMP #UERR2	
022154	104413		J5:	LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
022156	012700	000200		MOV #200,RO	
022158	170100			LDFPS RO	
022164	012700	022742		MOV #UPAT00,RO	:LOAD ACC
022170	172410			LDD (RO),ACC	
022172	013737	023016	023020	MOV #UTMP2,#UROM1	
022200	012737	000403	023022	MOV #403,#UROM2	
022206	012737	000256	023024	MOV #056,#UROM3	
022214	012700	022772		MOV #UPAT30,RC	:LOAD A NEGATIVE NUMBER INTO ACC
022220	172410		U6:	LDD (RO),ACC	
022222	010037	001252		MOV RO,#STMP10	
022224	012737	022220	001236	MOV #U6,#STMP2	
022234	012704	000210		MOV #210,R4	:FPS CORRECT
022240	170205			STFPS R5	
022242	020405			CMR R4,R5	
022244	001402			BEG J7	
022246	000137	022602		JMP #UERR2	
022252	104413		J7:	LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
022254	012700	000200		MOV #200,RO	
022260	170100			LDFPS RO	
022262	012700	022742		MOV #UPAT00,RO	:LOAD ACC
022266	172410			LDD (RO),ACC	
022270	013737	023014	023020	MOV #UTMP1,#UROM1	
022276	012737	000401	023022	MOV #401,#UROM2	
022304	012737	000256	023024	MOV #256,#UROM3	
022312	012700	023002		MOV #UPAT40,RO	:LOAD -0 INTO ACC
022316	172410		U10:	LDD (RO),ACC	
022320	000240		L11:	NOP	:TRAP FROM HERE IF
022322	010037	001252		MOV RO,#STMP10	
022324	012737	022316	001236	MOV #U10,#STMP2	:(BUT FIUV) FAILS!
022334	012704	000214		MOV #214,R4	:SEE IF FPS IS CORRECT.
022340	170205			STFPS R5	
022342	020405			CMR R4,R5	
022344	001402			BEG U12	
022346	000137	022516		JMP #UERR1	
022352	005737	023012	U12:	TST #UFLAG	:SEE IF ALL THE PATTERNS
022356	001021			BNE U14	:HAVE BEEN TEST WITH
					:BOTH AC NOT EQUAL TO C AND AC=0
022360	012700	022742		MOV #UPAT00,RC	:IF NOT GO BACK AND
022364	012701	000004		MOV #4,R1	:CHECK THEM WITH AC=0
022370	005020		U13:	CLP (R1)+	

F08

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023026 000004
023030
023030 104413
023032 012700 000200
023036 170100
023040 012700 023562
023044 172410
023046 012737 023060 001236
023054 012700 023562
023060 172010
023064 170205
023064 170011
023066 012700 023562
023072 174010
023074 012701 023562
023100 012702 000004
023104 022021
023106 001405

023110 004737 023530
023114 104133
023116 000137 023602
023122 077210
023124 022705 000204
023130 001410

023132 012737 000204 001242
023140 010537 001240
023144 104137
023146 000137 023602
023152
023152 104413
023154 012700 000200
023160 170100
023162 012700 023562
023166 172410
023170 012737 023206 001236
023176 005000
023200 170100
023202 012700 023562
023206 172010
023210 170205
023212 170011
023214 012700 023562
023220 174010
023222 012701 023562
023226 012702 000004
  
```

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*****
*TEST 25 ADDF,ADD,SUBF AND SUBC WITH FSPC=AC=0 TEST*
*****

*
* THIS IS A TEST OF ADD AND SUB WITH FSPC=AC=0
*
*****

*ST25: SCOPE
*
LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #200,RC
LDFPS RO :SET DOUBLE MODE
MOV #WPAT00,RO :LOAD ACD=0
LDD (RO),ACD
MOV #W2,#STMP2
MOV #WPAT00,RO
W2: ADD (RO),ACD :TEST INSTRUCTION.
STFPS RS :GET FPS
SETD :SET DOUBLE MODE
MOV #WPAT00,RO
STD ACD,(RO) :GET THE RESULT
MOV #WPAT00,R1
MOV #4,R2
W3: CMP (R0)+,(R1)+ :IS RESULT CORRECT
BEQ W4 :NO

JSR PC,#WSETUP
W4: ERROR 133
JMP #WDONE
W5: SOB R2,W3
CMP #204,RS :IS FPS CORRECT
BEQ W5 :NO

MOV #204,#STMP4
MOV RS,#STMP3
W6: ERROR 137
JMP #WDONE

LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #200,RO
LDFPS RO :SET DOUBLE MODE
MOV #WPAT00,RO :LOAD ACD=0
LDD (RO),ACD
MOV #W6,#STMP2
CLR RO :GO TO FLOATING MODE
LDFPS RO
MOV #WPAT00,RO
W6: ADDF (RO),ACD :TEST INSTRUCTION
STFPS RS :GET FPS
SETD :RESET TO DOUBLE MODE
MOV #WPAT00,RO
STD ACD,(RO) :GET THE RESULT
MOV #WPAT00,R1
MOV #4,R2
  
```


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24 FEB DIAGNOSTIC PART 1 MAC111 27 (1006) 01-NOV-76 21:09 PAGE 37
 *25 ADDF.ADDO.SUBF AND SUBC WITH FSRC=AC=C TEST

023300	001407	000004		W7:	CMP	(R0)+,(R1)+	: WAS THE RESULT
023301	001407	000004		BEG	W10	: NO. REPORT FAILURE.	
023302	001407	000004		18:	ERROR	134	
023303	001407	000004		BR	WDONE		
023304	001407	000004		W10:	SOB	R2,W1	
023305	001407	000004		CMP	#4,R5	: WAS FPS CORRECT	
023306	001407	000004		BEG	W11	: INCORRECT FPS.	
023307	001407	000004	001242	MOV	#4,2#STMP4		
023308	001407	000004		MOV	R5,2#STMP3		
023309	001407	000004		18:	ERROR	140	
023310	001407	000004		BR	WDONE		
023311	001407	000004		W11:	LPERR	: SET UP THE LOOP ON ERROR ADDRESS.	
023312	001407	000004		MOV	#200,R0		
023313	001407	000004		LDFPS	R0	: SET DOUBLE MODE	
023314	001407	000004		MOV	#WPAT00,R0	: LOAD ACO=0	
023315	001407	000004		LDD	(R0),ACO		
023316	001407	000004		MOV	#W12,2#STMP2		
023317	001407	000004	001236	MOV	#WPAT00,R0		
023318	001407	000004		W12:	SUBD	(R0),ACO	
023319	001407	000004		STFPS	R5	: TEST INSTRUCTION	
023320	001407	000004		SETD		: GET FPS	
023321	001407	000004		MOV	#WPAT00,R0	: SET DOUBLE MODE	
023322	001407	000004		STD	ACO,(R0)		
023323	001407	000004		MOV	#WPAT00,R1	: GET THE RESULT	
023324	001407	000004		W13:	CMP	(R0)+,(R1)+	
023325	001407	000004		BEG	W14	: IS RESULT CORRECT?	
023326	001407	000004				: NO.	
023327	001407	000004		18:	JSR	PC,2#WSETUP	
023328	001407	000004		ERROR	135		
023329	001407	000004		BR	WDONE		
023330	001407	000004		W14:	SOB	R2,W13	
023331	001407	000004		CMP	#204,R5	: IS FPS CORRECT?	
023332	001407	000004		BEG	W15	: NO.	
023333	001407	000004		MOV	#204,2#STMP4		
023334	001407	000004	001242	MOV	R5,2#STMP3		
023335	001407	000004		18:	ERROR	141	
023336	001407	000004		BR	WDONE		
023337	001407	000004		W15:	LPERR	: SET UP THE LOOP ON ERROR ADDRESS.	
023338	001407	000004		MOV	#200,R0		
023339	001407	000004		LDFPS	R0	: SET DOUBLE MODE	
023340	001407	000004		MOV	#WPAT00,R0	: LOAD ACO=C	
023341	001407	000004		LDD	(R0),ACO		
023342	001407	000004		MOV	#W16,2#STMP2		
023343	001407	000004	001236	CLR	R0		
023344	001407	000004		LDFPS	R0	: ENTER FLOATING MODE.	
023345	001407	000004		W16:	MOV	#WPAT00,R0	
023346	001407	000004		SUBF	(R0),ACO	: TEST INSTRUCTION.	
023347	001407	000004		STFPS	R5	: GET FPS	
023348	001407	000004		SETD		: RESET TO DOUBLE MODE	
023349	001407	000004		MOV	#WPAT00,R0	: GET THE RESULT.	
023350	001407	000004		STD	ACO,(R0)		

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023556 MOV #WPAT00,R1
023557 MOV #4,R2
023558 CMP (R0)+,R1 :IS RESULT CORRECT?
023559 BEQ W20 :NO.
023560 JSR PC,@WSETUP
023561 ERROR 136
023562 BR WDONE
023563 SOB R2,W17
023564 CMP #4,R5 :IS FPS CORRECT?
023565 BEQ WDONE :NO.
023566 MOV #4,@STMP4
023567 MOV R5,@STMP3
023568 ERROR 142
023569 BR WDONE
:SET UP FOR ERROR CALL
023570 WSETUP: MOV #WPAT00,@STMP3
023571 MOV #WPAT00,@STMP4
023572 MOV #WPAT00,@STMP5
023573 MOV #WPAT00,@STMP6
023574 RTS PC
023575 WPAT00: .WORD 0
023576 WPAT01: 0
023577 WPAT02: 0
023578 WPAT03: 0
023579 WDATA0: .WORD 0
023580 WDATA1: 0
023581 WDATA2: 0
023582 WDATA3: 0
023583 WDONE: RSETUP :GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER HAS
:THE USER TYPED CONTROL G??.
:*****
:*TEST 26 ADD AND SUB WITH FSRC=0
:*
:* THIS IS A TEST OF ADD AND SUB WITH FSRC=0.
:*
:*****
*ST26: SCOPE
X1: LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #200,R0
LDFPS R0 :SET DOUBLE MODE
MOV #XPAT00,R0 :SET ACC TO POSITIVE
MOV R0,@XTMP :NUMBER #0

```

Address	Hex	Dec	Label	Instruction	Comments
460	023642	001236		LDD (R0),AC0	
461	023643	012700		MOV #X2,#STMP2	
462	023644	172010	X2:	MOV #XPAT10,R0	:FSRC=0
463	023645	172010		ADDC (R0),AC0	:TEST INSTRUCTION
464	023646	172010		STFPS R5	
465	023646	172010		SETD	
466	023650	012700	024336	MOV #XDAT00,R0	:GET RESULT
467	023654	174010		STD AC0,(R0)	
468	023656	012701	024346	MOV #XPAT00,R1	
469	023662	012702	000304	MOV #4,R2	
470	023666	022021	X3:	CMP (R0)+,(R1)+	:IS RESULT CORRECT?
471	023670	001401		BEQ X4	
472	023672	000534		BR XERR1	
473	023674	077204	X4:	SOB R2,X3	
474	023676	012704	000200	MOV #200,R4	
475	023702	020405		MOV R4,R5	:IS FPS CORRECT?
476	023704	001402		BEQ X5	
477	023706	000137	024304	JMP @XERR2	
478	023712		X5:		
479	023712	104413		LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
480	023714	012700	000200	MOV #200,R0	
481	023720	170100		RDFPS R0	:SET DOUBLE MODE
482	023722	012703	024366	MOV #XPAT20,R0	:SET ACC TO
483	023726	010037	024334	MOV R0,@XTMP	:NEGATIVE NUMBER
484	023732	172410		LDD (R0),AC0	
485	023734	012737	023746	MOV #X6,#STMP2	
486	023742	012700	024356	MOV #XPAT10,R0	:FSRC=0
487	023746	172010	X6:	ADDC (R0),AC0	:TEST INSTRUCTION
488	023750	170205		STFPS R5	
489	023752	170011		SETD	
490	023754	012700	024336	MOV #XDAT00,R0	:GET RESULT
491	023760	174010		STD AC0,(R0)	
492	023762	012701	024366	MOV #XPAT20,R1	
493	023766	012702	000304	MOV #4,R2	
494	023772	022021	X7:	CMP (R0)+,(R1)+	:IS RESULT CORRECT?
495	023774	001401		BEQ X10	
496	023776	000511		BR XERR1	
497	024000	077204	X10:	SOB R2,X7	
498	024002	012704	000210	MOV #210,R4	
499	024006	020405		CMP R4,R5	:IS FPS CORRECT?
500	024010	001401		BEQ X11	
501	024012	000534		BR XERR2	
502	024014		X11:		
503	024014	104413		LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
504	024016	012700	000200	MOV #200,R0	
505	024022	170100		RDFPS R0	:SET DOUBLE MODE
506	024024	012700	024346	MOV #XPAT00,R0	:SET ACC TO NON-ZERO
507	024030	010037	024334	MOV R0,@XTMP	:POSITIVE NUMBER
508	024034	172410		LDD (R0),AC0	
509	024036	012737	024050	MOV #X12,#STMP2	
510	024044	012700	024356	MOV #XPAT10,R0	:FSRC=0
511	024050	173010	X12:	SUBD (R0),AC0	:TEST INSTRUCTION
512	024052	170205		STFPS R5	
513	024054	170011		SETD	
514	024056	012700	024336	MOV #XDAT00,R0	:GET RESULT
515	024062	174010		STD AC0,(R0)	

JOB

Address	Instruction	Comments
000000	MOV #XPAT00,R1	
000004	MOV #4,R2	
000008	CMP (R0)+,(R1)+	:IS RESULT CORRECT?
000012	BEG X14	
000016	BR XERR3	
000020	SOB R2,X13	
000024	MOV #200,R4	:IS FPS CORRECT?
000028	CMP R4,R5	
000032	BEG X15	
000036	BR XERR4	
000040	LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
000044	MOV #200,R0	
000048	LDFPS R0	:SET DOUBLE MODE
000052	MOV #XPAT20,R0	:SET ACC=A NEGATIVE
000056	MOV R0,@#XTMP	:NUMBER
000060	LDD (R0),AC0	
000064	MOV #X16,@#STMP2	
000068	MOV #XPAT10,R0	:FSRC=0
000072	SUBD (R0),AC0	:TEST INSTRUCTION.
000076	STFPS R5	
000080	SETD	
000084	MOV #XDAT00,R0	:GET RESULT
000088	STD AC0,(R0)	
000092	MOV #XPAT20,R1	
000096	MOV #4,R2	
000100	CMP (R0)+,(R1)+	:IS RESULT CORRECT?
000104	BEG X20	
000108	BR XERR3	
000112	SOB R2,X17	
000116	MOV #210,R4	:IS FPS CORRECT?
000120	CMP R4,R5	
000124	BEG X21	
000128	BR XERR4	
000132	BR XDONE	
000136		:REPORT DATA ERRORS
000140	XERR1: MOV #XPAT10,@#STMP3	
000144	MOV @#XTMP,@#STMP4	
000148	MOV #XDAT00,@#STMP5	
000152	1\$: ERROR 143	
000156	BR XDONE	
000160	XERR3: MOV #XPAT10,@#STMP3	
000164	MOV @#XTMP,@#STMP4	
000168	MOV #XDAT00,@#STMP5	
000172	MOV @#XTMP,@#STMP6	
000176	1\$: ERROR 144	
000180	BR XDONE	
000184		:REPORT FPS ERRORS
000188	XERR2: MOV R5,@#STMP3	
000192	MOV R4,@#STMP4	
000196	1\$: ERROR 145	

K08

024316 000427
024320 000000
024324 010537
024328 010437
024330 104146
024332 000421
024334 000000
024336 000000
024340 000000
024342 000000
024344 000000

024346 010421
024350 021042
024352 031463
024354 042104

024356 000000
024360 000000
024362 000000
024364 000000
024366 104210
024370 114631
024372 125252
024374 135673

024376
024378 104412

BR XDONE
XERR4: MOV R5,2*STMP3
MOV R4,2*STMP4
IS: ERROR 146
BR XDONE
XTMP: .WORD 0
XDAT00: .WORD 0
XDAT01: .WORD 0
XDAT02: .WORD 0
XCAT03: .WORD 0

XPAT00: .WORD 010421
XPAT01: .WORD 021042
XPAT02: .WORD 031463
XPAT03: .WORD 042104

XPAT10: .WORD 0
XPAT11: .WORD 0
XPAT12: .WORD 0
XPAT13: .WORD 0
XPAT20: .WORD 104210
XPAT21: .WORD 114631
XPAT22: .WORD 125252
XPAT23: .WORD 135673

XDONE: RSETJP
:GO INITIALIZE THE FPS AND STACK: AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER (HAS
:THE USER TYPED CONTROL G?).

*TEST 27 SUBD WITH AC=0 TEST
*
* THIS IS A TEST OF SUBD WITH AC=0. BOTH POSITIVE
* AND NEGATIVE FSRC'S ARE TRIED.
*

†ST27: SCOPE
CLR @YFLAG
MOV @YPAT00,@YTMP1 ;P
MOV @YPAT10,@YTMP2 ;N
MOV @210,@YTMP3
Y1: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV @200,RO
LDFPS RO ;SET DOUBLE MODE
MOV @YPAT20,RO ;SET ACC=0
LDD (RO),AC0
MOV @YTMP1,RO
Y2: SUBD (RO),AC0 ;TEST INSTRUCTION
STFPS RS
SETC

5610	024460	012700	024742	MOV	#YDAT00,R0	:GET RESULT
5611	024464	174010		STD	ACD,(R0)	
5612	024466	012702	000004	MOV	#4,R2	
5613	024472	013701	024736	MOV	@#YTMP2,R1	:CHECK RESULT.
5614	024476	022021		Y3: CMP	(R0)+,(R1)+	
5615	024500	001026		BNE	Y6	
5616	024502	077203		SOB	R2,Y3	
5617	024504	023705	024740	Y4: CMP	@#YTMP3,R5	:FPS CORRECT?
5618	024510	001401		BEQ	Y4	
5619	024512	000475		BR	YERR3	
5620	024514	005737	024732	Y4: TST	@#YFLAG	:FINISHED TEST?
5621	024520	001015		BNE	Y5	
5622	024522	012737	177777 024732	MOV	#-1,@#YFLAG	
5623	024530	012737	024762 024734	MOV	#YPAT10,@#YTMP1	
5624	024536	012737	024752 024736	MOV	#YPAT00,@#YTMP2	
5625	024544	012737	000200 024740	MOV	#200,@#YTMP3	
5626	024552	000726		BR	Y1	
5627	024554	000512		Y5: BR	YDONE	
5628	024556	012702	000004	Y6: MOV	#4,R2	
5629	024562	012700	024734	MOV	#YTMP1,R0	:DID XOR OF SIGN BIT
5630	024566	012701	024742	MOV	#YDAT00,R1	:FAIL?
5631	024572	022021		Y7: CMP	(R0)+,(R1)+	
5632	024574	001002		BNE	YERR1	
5633	024576	077203		SOB	R2,Y7	
5634	024600	000421		BR	YERR2	
5635	024602			YERR1:		:DATA FAILURE
5636	024602	012737	024452 001236	MOV	#Y2,@#STMP2	
5637	024610	013737	024734 001240	MOV	@#YTMP1,@#STMP3	
5638	024616	012737	024772 001242	MOV	#YPAT20,@#STMP4	
5639	024624	012737	024742 001244	MOV	#YDAT00,@#STMP5	
5640	024632	013737	024736 001246	MOV	@#YTMP2,@#STMP6	
5641	024640	104147		1\$: ERROR	147	
5642	024642	000457		BR	YDONE	
5643	024644			YERR2:		:XOR OF SIGN BIT
5644	024644	012737	024452 001236	MOV	#Y2,@#STMP2	:FAILED
5645	024652	013737	024734 001240	MOV	@#YTMP1,@#STMP3	
5646	024660	012737	024772 001242	MOV	#YPAT20,@#STMP4	
5647	024666	012737	024742 001244	MOV	#YDAT00,@#STMP5	
5648	024674	013737	024736 001246	MOV	@#YTMP2,@#STMP6	
5649	024702	104150		1\$: ERROR	150	
5650	024704	000436		BR	YDONE	
5651	024706			YERR3:		:FPS WRONG.
5652	024706	012737	024452 001236	MOV	#Y2,@#STMP2	
5653	024714	010537	001240	MOV	R5,@#STMP3	
5654	024720	013737	024740 001242	MOV	@#YTMP3,@#STMP4	
5655	024726	104151		1\$: ERROR	151	
5656	024730	000424		BR	YDONE	
5657						
5658	024732	000000		YFLAG:	.WORD 0	
5659	024734	000000		YTMP1:	0	
5660	024736	000000		YTMP2:	0	
5661	024740	000000		YTMP3:	0	
5662						
5663	024742	000000		YDAT00:	.WORD 0	
5664	024744	000000		YDAT01:	0	
5665	024746	000000		YDAT02:	0	

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5666 024750 000000 YDAT03: 0
5667
5668 024752 063146 YPAT00: 063146
5669 024754 052525 YPAT01: 052525
5670 024756 042104 YPAT02: 042104
5671 024760 167356 YPAT03: 167356
5672
5673 024762 163146 YPAT10: 163146
5674 024764 052525 YPAT11: 052525
5675 024766 042104 YPAT12: 042104
5676 024770 167356 YPAT13: 167356
5677
5678 024772 000000 YPAT20: 0
5679 024774 000000 YPAT21: 0
5680 024776 000000 YPAT22: 0
5681 025000 000000 YPAT23: 0
5682

```

```

YDONE:
RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).

```

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*****
;TEST 30 ADD WITH AC=0 TEST

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; THIS IS A TEST OF ADD WITH AC=0. BOTH
; POSITIVE AND NEGATIVE FSRC'S ARE TRIED.
;

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5698 025004 000004
5699 025006 005067 000224
5700 025012 012737 025254 025240
5701 025020 012737 000200 025242
5702 025026
5703 025026 104413
5704 025030 012700 000200
5705 025034 170100
5706 025036 012700 025274
5707 025042 172410
5708 025044 013700 025240
5709 025050 172010
5710 025052 170205
5711 025054 170011
5712 025056 012700 025244
5713 025062 174010
5714 025064 012702 000004
5715 025070 013701 025240
5716 025074 022021
5717 025076 001401
5718 025100 000423
5719 025102 077204
5720 025104 023705 025242
5721 025110 001401

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```

TST30: SCOPE
CLR ZFLAG
MOV #ZPAT00, @#ZTMP1 ;P
MOV #200, @#ZTMP2
Z1: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #200, R0
LDFPS R0 ;SET DOUBLE MODE
MOV #ZPAT20, R0 ;SET ACO=0
LDD (R0), ACO
MOV @#ZTMP1, R0
Z2: ADD (R0), ACO ;TEST INSTRUCTION
STFPS R5
SETD
MOV #ZDAT00, R0 ;GET RESULT
STD ACO, (R0)
MOV #4, R2
MOV @#ZTMP1, R1 ;RESULT CORRECT?
Z3: CMP (R0)+, (R1)+
BEQ Z4
BR ZERR1
Z4: SOB R2, Z3 ;FPS CORRECT?
CMP @#ZTMP2, R5
BEQ Z5

```

```

5722 025112 000437          BR      ZERR2
5723 025114 005737 025236  Z5:  TST  @ZFLAG ;FINISHED TEST?
5724 025120 001012          BNE   Z6
5725 025122 012737 177777 025236  MOV  #-1,@ZFLAG
5726 025130 012737 025264 025240  MOV  @ZPAT10,@ZTMP1
5727 025136 012737 000210 025242  MOV  @Z10,@ZTMP2
5728 025144 000730          BR    Z1
5729 025146 000456          BR    ZDONE
5730 025150          Z6:  ZERR1: ;DATA FAILURE
5731 025150 012737 025050 001236  MOV  @Z2,@ZTMP2
5732 025156 013737 025240 001240  MOV  @ZTMP1,@ZTMP3
5733 025164 012737 025274 001242  MOV  @ZPAT20,@ZTMP4
5734 025172 012737 025244 001244  MOV  @ZDAT00,@ZTMP5
5735 025200 013737 025240 001246  MOV  @ZTMP1,@ZTMP6
5736 025206 104152          I$:  ERROR 152
5737 025210 000435          BR    ZDONE
5738 025212          ZERR2:
5739 025212 012737 025050 001236  MOV  @Z2,@ZTMP2
5740 025220 010537 001240  MOV  R5,@ZTMP3
5741 025224 013737 025242 001242  MOV  @ZTMP2,@ZTMP4
5742 025232 104153          I$:  ERROR 153
5743 025234 000423          BR    ZDONE
5744
5745 025236 000000          ZFLAG: .WORD 0
5746 025240 000000          ZTMP1: 0
5747 025242 000000          ZTMP2: 0
5748
5749 025244 000000          ZDAT00: .WORD 0
5750 025246 000000          ZDAT01: 0
5751 025250 000000          ZDAT02: 0
5752 025252 000000          ZDAT03: 0
5753
5754 025254 031463          ZPAT00: 031463
5755 025256 010421          ZPAT01: 010421
5756 025260 146314          ZPAT02: 146314
5757 025262 156735          ZPAT03: 156735
5758
5759 025264 156735          ZPAT10: 156735
5760 025266 167356          ZPAT11: 167356
5761 025270 135673          ZPAT12: 135673
5762 025272 146314          ZPAT13: 146314
5763
5764 025274 000000          ZPAT20: 0
5765 025276 000000          ZPAT21: 0
5766 025300 000000          ZPAT22: 0
5767 025302 000000          ZPAT23: 0
5768
5769 025304          ZDONE:
5770 025304 104412          RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
5771 ;SEE IF THE USER HAS EXPRESSED
5772 ;THE DESIRE TO CHANGE THE SOFTWARE
5773 ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
5774 ;THE USER TYPED CONTROL G?).
5775
5776
5777

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::*****

F09

Address	Instruction	Comment
026464	LDD (R0),ACC	:ACC
026466	MOV #CCP2,R0	
026468	ADD (R0),ACC	:TEST INSTRUCTION
026470	STFPS R5	:SET FPS
026472	MOV #CCDAT0,R0	:GET THE RESULT
026474	STD ACC,(R0)	
026476	MOV #CCP2,R1	:IS IT CORRECT
026478	MOV #4,R2	
026480	CMP (R0)+,(R1)+	
026482	BEQ CC6	
026484	MOV #CCDAT0,R0	:DID A BAD
026486	MOV #CCP0,R1	:CONSTANT (NOT 57)
026488	MOV #4,R2	:GET GENERATED
026490	CMP (R0)+,(R1)+	:FOR THE ALIGNMENT
026492	BEQ CC5	:FLOWS?
026494	JMP #CCER1	:DATA ERROR.D
026496	SOB R2,CC4	
026498	JMP #CCER2	:BAD CONSTANT.D
026500	SOB R2,CC3	
026502	CMP #4,R5	:FPS CORRECT?
026504	BEQ CC7	
026506	JMP #CCER0	:BAD FPS.
026508		:EXPONENT DIFFERENCE=56=70 (OCT) FD=1
026510	LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
026512	MOV #3200,R4	:SET FIV,FIV. AND FD
026514	LDFPS R4	
026516	MOV #CC8,#STMP2	
026518	MOV #CCP0,R0	:SET ACC OPERAND
026520	LDD (R0),ACC	
026522	MOV #CCP1,R0	:FSRC
026524	ADD (R0),ACC	:TEST INSTRUCTION
026526	STFPS R5	:GET FPS
026528	MOV #CCDAT0,R0	:GET THE RESULT
026530	STD ACC,(R0)	
026532	MOV #CCP7,R1	:IS IT CORRECT
026534	MOV #4,R2	
026536	CMP (R0)+,(R1)+	
026538	BEQ CC12	
026540	MOV #CCDAT0,R0	:DID A BAD
026542	MOV #CCP1,R1	:CONSTANT (NOT 57)
026544	MOV #4,R2	:GET GENERATED
026546	CMP (R0)+,(R1)+	:FOR THE ALIGNMENT
026548	BEQ CC11	:FLOWS?
026550	JMP #CCER3	:DATA ERROR.D
026552	SOB R2,CC10	
026554	JMP #CCER4	:BAD CONSTANT.D
026556	SOB R2,CC9	
026558	CMP #4,R5	:FPS CORRECT?
026560	BEQ CC13	
026562	JMP #CCER0	:BAD FPS.
026564		:EXPONENT DIFFERENCE=25=31 (OCT) FD=0
026566	LPERR	:SET UP THE LOOP ON ERROR ADDRESS.
026568	MOV #CC14,#STMP2	
026570	MOV #CCP0,R0	:SET UP ACC OPERAND.

Address	Op/Label	Op/Label	Op/Label	Comments
027000	LDCC	(R0), ACC		
027004	MOV	#3000, R4		:SET FIV, FIV. CLEAR FO.
	LDFPS	R4		
027008	MOV	#CCP6, R0		:FSRC
0014:	ADD	(R0), ACC		:TEST INSTRUCTION
	STFPS	R5		
	SETD			:REENTER DOUBLE MOVE
027012	MOV	#CCDAT0, R0		:GET THE RESULT
	STD	ACC, (R0)		
027016	MOV	#CCP6, R1		:IS THE RESULT CORRECT?
000002	MOV	#2, R2		
0015:	CMP	(R0)+, (R1)+		
	BEQ	CC18		
027020	MOV	#CCDAT0, R0		:WAS A BAD CONSTANT
	MOV	#CCP3, R1		:USED (NOT 25) IN
000002	MOV	#2, R2		:THE ALIGN FLOWS?
0016:	CMP	(R0)+, (R1)+		
	BEQ	CC17		
027024	JMP	#CCCR5		:DATA ERROR F
0017:	SOB	R2, CC16		
	JMP	#CCCR6		:BAD CONSTANT F
0018:	SOB	R2, CC15		
	CMP	R4, R5		
	BEQ	CC19		
027028	JMP	#CCCR90		:BAD FPS.
				:EXPONENT DIFFERENCE=24=30 (OCT) FD=C
0019:	LPERR			:SET UP THE LOOP ON ERROR ADDRESS.
026732	MOV	#CC20, #STMP2		
026734	MOV	#CCP3, R0		:SET UP ACC OPERAND.
026736	LDD	(R0), ACC		
026738	MOV	#3000, R4		:SET FIV, FIV. CLEAR FO.
02673A	LDFPS	R4		
02673C	MOV	#CCP5, R0		:FSRC
02673E	ADD	(R0), ACC		:TEST INSTRUCTION
026740	STFPS	R5		
026742	SETD			:REENTER DOUBLE MOVE
026744	MOV	#CCDAT0, R0		:GET THE RESULT
026746	STD	ACC, (R0)		
026748	MOV	#CCP10, R1		:IS THE RESULT CORRECT?
02674A	MOV	#2, R2		
0021:	CMP	(R0)+, (R1)+		
	BEQ	CC24		
02674E	MOV	#CCDAT0, R0		:WAS A BAD CONSTANT
026750	MOV	#CCP5, R1		:USED (NOT 25) IN
026752	MOV	#2, R2		:THE ALIGN FLOWS?
0022:	CMP	(R0)+, (R1)+		
	BEQ	CC23		
026754	JMP	#CCCR7		:DATA ERROR F
0023:	SOB	R2, CC22		
	JMP	#CCCR8		:BAD CONSTANT F
0024:	SOB	R2, CC21		
	CMP	R4, R5		
	BEQ	CC25		
026758	JMP	#CCCR90		:BAD FPS.
				:EXPONENT DIFFERENCE=1 FD=1

H09

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0025: LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 :SET FIV,FIV, AND FD
LDFPS R4
MOV #CC26,@STMP2
MOV #CCP0,R0 :SET ACC OPERAND
LDD (R0),ACC :FSRC
MOV #CCP3,R0 :TEST INSTRUCTION
ADDD (R0),ACC :GET FPS
STFPS R5 :GET THE RESULT
MOV #CCDAT0,R0
STD ACC,(R0)
MOV #CCP11,R1 :IS IT CORRECT
MOV #4,R2
CMP (R0)+,(R1)+
BEQ CC30
MOV #CCDAT0,R0 :DID A BAD
MOV #CCP3,R1 :CONSTANT (NOT 57)
MOV #4,R2 :GET GENERATED
CMP (R0)+,(R1)+ :FOR THE ALIGNMENT
BEQ CC29 :FLOWS?
JMP @CCER10 :DATA ERROR.D
SOB R2,CC28 :BAD CONSTANT.D
JMP @CCER11
SOB R2,CC27 :FPS CORRECT?
CMP R4,R5
BEQ CC31
JMP @CCER0 :BAD FPS.
:EXPONENT DIFFERENCE=100=144 (OCT) FD=1
0031: LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 :SET FIV,FIV, AND FD
LDFPS R4
MOV #CC32,@STMP2
MOV #CCP0,R0 :SET ACC OPERAND
LDD (R0),ACC :FSRC
MOV #CCP4,R0 :TEST INSTRUCTION
ADDD (R0),ACC :GET FPS
STFPS R5 :GET THE RESULT
MOV #CCDAT0,R0
STD ACC,(R0)
MOV #CCP4,R1 :IS IT CORRECT
MOV #4,R2
CMP (R0)+,(R1)+
BEQ CC36
MOV #CCDAT0,R0 :DID A BAD
MOV #CCP4,R1 :CONSTANT (NOT 57)
MOV #4,R2 :GET GENERATED
CMP (R0)+,(R1)+ :FOR THE ALIGNMENT
BEQ CC35 :FLOWS?
JMP @CCER12 :DATA ERROR.D
SOB R2,CC34 :BAD CONSTANT.D
JMP @CCER13
SOB R2,CC33 :FPS CORRECT?
CMP R4,R5
BEQ CC37

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0000	000000	000000	000000	000000	000000	JMP	00CCERO	:BAC FRS.
0001	000000	000000	000000	000000	000000	JMP	00CCDONE	
0002	000000	000000	000000	000000	000000	MOV	R4,0#STMP4	:FPS ERROR C
0003	000000	000000	000000	000000	000000	MOV	R5,0#STMP3	
0004	000000	000000	000000	000000	000000	ERROR	154	
0005	000000	000000	000000	000000	000000	JMP	00CCDONE	
0006	000000	000000	000000	000000	000000	MOV	R4,0#STMP4	:FPS ERROR F
0007	000000	000000	000000	000000	000000	MOV	R5,0#STMP3	
0008	000000	000000	000000	000000	000000	ERROR	165	
0009	000000	000000	000000	000000	000000	JMP	00CCDONE	
0010	000000	000000	000000	000000	000000	MOV	00CP2,0#STMP3	:DATA ERROR C
0011	000000	000000	000000	000000	000000	MOV	00CP2,0#STMP6	
0012	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0013	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0014	000000	000000	000000	000000	000000	ERROR	166	
0015	000000	000000	000000	000000	000000	JMP	00CCDONE	
0016	000000	000000	000000	000000	000000	MOV	00CP2,0#STMP3	:CONSTANT BAC D(B)
0017	000000	000000	000000	000000	000000	MOV	00CP2,0#STMP6	
0018	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0019	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0020	000000	000000	000000	000000	000000	ERROR	172	
0021	000000	000000	000000	000000	000000	JMP	00CCDONE	
0022	000000	000000	000000	000000	000000	MOV	00CP1,0#STMP3	
0023	000000	000000	000000	000000	000000	MOV	00CP7,0#STMP6	
0024	000000	000000	000000	000000	000000	BR	CCERS0	
0025	000000	000000	000000	000000	000000	MOV	00CP1,0#STMP3	:CONSTANT BAD D(G)
0026	000000	000000	000000	000000	000000	MOV	00CP7,0#STMP6	
0027	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0028	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0029	000000	000000	000000	000000	000000	ERROR	173	
0030	000000	000000	000000	000000	000000	JMP	00CCDONE	
0031	000000	000000	000000	000000	000000	MOV	00CP6,0#STMP3	:DATA ERROR F
0032	000000	000000	000000	000000	000000	MOV	00CP6,0#STMP6	
0033	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0034	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0035	000000	000000	000000	000000	000000	ERROR	170	
0036	000000	000000	000000	000000	000000	BR	CCDONE	
0037	000000	000000	000000	000000	000000	MOV	00CP6,0#STMP3	:CONSTANT BAC F(B)
0038	000000	000000	000000	000000	000000	MOV	00CP6,0#STMP6	
0039	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0040	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0041	000000	000000	000000	000000	000000	ERROR	174	
0042	000000	000000	000000	000000	000000	BR	CCDONE	
0043	000000	000000	000000	000000	000000	MOV	00CP5,0#STMP3	:DATA ERROR F
0044	000000	000000	000000	000000	000000	MOV	00CP10,0#STMP6	
0045	000000	000000	000000	000000	000000	BR	CCERS5	
0046	000000	000000	000000	000000	000000	MOV	00CP5,0#STMP3	:CONSTANT BAD F(G)
0047	000000	000000	000000	000000	000000	MOV	00CP10,0#STMP6	
0048	000000	000000	000000	000000	000000	MOV	00CDAT0,0#STMP5	
0049	000000	000000	000000	000000	000000	MOV	00CP0,0#STMP4	
0050	000000	000000	000000	000000	000000	ERROR	175	
0051	000000	000000	000000	000000	000000	BR	CCDONE	
0052	000000	000000	000000	000000	000000	MOV	00CP3,0#STMP3	:DATA ERROR D
0053	000000	000000	000000	000000	000000	MOV	00CP11,0#STMP6	
0054	000000	000000	000000	000000	000000	BR	CCERS0	
0055	000000	000000	000000	000000	000000	MOV	00CP3,0#STMP3	:CONSTANT BAD D(G)

027728	012737	030034	001246	MOV	#CCP11,2#STMP6	
027729	000670			BR	CCER44	
027730	012737	030034	001240	CCER12: MOV	#CCP4,2#STMP5	:DATA ERROR D
027736	012737	030034	001246	MOV	#CCP4,2#STMP6	
027744	000614			BR	CCER50	
027746	012737	030034	001240	CCER13: MOV	#CCP4,2#STMP3	:CONSTANT BAD D'B.
027754	012737	030034	001246	MOV	#CCP4,2#STMP6	
027762	000624			BR	CCER22	
027764	000000			CCDATA:		
027766	000000					
027770	000000					
027772	000000					
027774	000200			CCP0:	200	:E(AC)=1
027776	000000					
030000	000000					
030002	000000					
030004	016200			CCP1:	16200	:E(FSRC)=E(AC)+56=57 =71(OCT)
030006	000000					
030010	000000					
030012	000000					
030014	016400			CCP2:	16400	:E(FSRC)=E(AC)+57=58 =72(OCT)
030016	000000					
030020	000000					
030022	000000					
030024	000400			CCP3:	400	:E(FSRC)=E(AC)+1=2
030026	000000					
030030	000000					
030032	000000					
030034	031200			CCP4:	31200	:E(FSRC)=E(AC)+100=101=145(OCT)
030036	000000					
030040	000000					
030042	000000					
030044	006200			CCP5:	6200	:E(FSRC)=E(AC)+24=25=31(OCT)
030046	000000					
030050	000000					
030052	000000					
030054	006400			CCP6:	6400	:E(FSRC)=E(AC)+25=26=32(OCT)
030056	000000					
030060	000000					
030062	000000					
030064	016200			CCP7:	16200	:CCP1 RES
030066	000000					
030070	000000					
030072	000001					
030074	006200			CCP10:	6200	:CCP5 RES
030076	000001					
030100	000000					
030102	000000					
030104	000500			CCP11:	500	:CCP3 RES
030106	000000					
030110	000000					
030112	000000					
030114	000200			CCP12:	200	:BAD CONSTANT :RES CCP2,CCP4
030116	000000					
030120	000000					
030122	000000					

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CCDONE: RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAE
;THE USER TYPED CONTROL G?).

*TEST 33 ADDF AND ADDD WITH E(AC) GREATER THAN E(FSRC) TEST
*
*THIS IS A TEST OF THE ADDD AND ADDF
*INSTRUCTIONS AND THE ALIGN FSRC ALGORITHM
*FLOWS. FIRST THE CONSTANT USED IS CHECKED.
*THEN SIMPLE AND WORST CASE ALIGNMENT
*SITUATIONS ARE TRIED. NOTE E(AC)
*IS GREATER THAN E(FSRC).
*

ST33: SCOPE
EXPONENT DIFFERENCE=57=71 (OCT) FD=1

BB1: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIV FIV, AND FD
LDFPS R4
MOV #BBE0,2#FPVECT ;SET UP FOR ERROR
MOV #BB2,2#STMP2 ;IN CASE THE OVER\
;UNDER FLOWS FAIL.
MOV #BBPAT2,R0 ;SET ACD OPERAND.
LDD (R0),ACD
MOV #BBPAT1,R0 ;FSRC
ADD (R0),ACD ;TEST INSTRUCTION
STFPS R5
BB2: MOV #BBDAT0,R0 ;GET THE RESULT
STD ACD,(R0)
BB3: MOV #BBPAT2,R1 ;RESULT CORRECT?
MOV #4,R2
BB4: CMP (R0)+,(R1)+
BEQ BB5
BB5: JMP 2#BBE01 ;DATA ERROR D
SOB R2,BB4 ;WAS FPS CORRECT?

CMP R4,R5
BEQ BB6
JMP 2#BBE0 ;FPS ERROR
EXPONENT DIFFERENCE=56=70 (OCT) FD=1

BB6: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIV.FIV, AND FD
LDFPS R4
MOV #BB7,2#STMP2
MOV #BBPAT4,R0 ;SET ACD OPERAND
LDD (R0),ACD
MOV #BBPAT1,R0 ;FSRC
ADD (R0),ACD ;TEST INSTRUCTION
STFPS R5 ;GET FPS

6338	030266	012700	031300		MOV	#BBDAT0,R0	:GET THE RESULT
6339	030272	174010			STD	AC0,(R0)	
6340	030274	012701	031410		MOV	#BBP10,R1	:IS IT CORRECT
6341	030300	012702	000004		MOV	#4,R2	
6342	030304	022021		BB10:	CMP	(R0)+,(R1)+	
6343	030306	001415			BEG	BB13	
6344	030310	012700	031300		MOV	#BBDAT0,R0	:DID A BAD
6345	030314	012701	031350		MOV	#BBPAT4,R1	:CONSTANT (NOT 57)
6346	030320	012702	000004		MOV	#4,R2	:GET GENERATED
6347	030324	022021		BB11:	CMP	(R0)+,(R1)+	:FOR THE ALIGNMENT
6348	030326	001402			BEG	BB12	:FLOWS?
6349	030330	000137	031064		JMP	J#BBER2	:DATA ERROR.D
6350	030334	077205		BB12:	SOB	R2,BB11	
6351	030336	000137	031102		JMP	J#BBER3	:BAD CONSTANT.D
6352	030342	077220		BB13:	SOB	R2,BB10	
6353	030344	020405			CMP	R4,R5	:FPS CORRECT?
6354	030346	001402			BEQ	BB14	
6355	030350	000137	030766		JMP	J#BBER0	:BAD FPS.
6356							:EXPONENT DIFFERENCE=25=31 (OCT) FD=0
6357	030354			BB14:			
6358	030354	104413			LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
6359	030356	012737	030404	001236	MOV	#BB15,J#STMP2	
6360	030364	012700	031310		MOV	#BBPAT0,R0	:SET UP AC0 OPERAND
6361	030370	172410			LDD	(R0),AC0	
6362	030372	012704	003000		MOV	#3000,R4	:SET FIV AND FIV
6363							:CLEAR FD
6364	030376	170104			LDFPS	R4	
6365	030400	012700	031320		MOV	#BBPAT1,R0	:FSRC
6366	030404	172010		BB15:	ADDF	(R0),AC0	:TEST INSTRUCTION
6367	030406	170205			STFPS	R5	
6368	030410	170011			SETD		:REENTERED DOUBLE MODE.
6369	030412	012700	031300		MOV	#BBDAT0,R0	:GET THE RESULT
6370	030416	174010			STD	AC0,(R0)	
6371	030420	012701	031310		MOV	#BBPAT0,R1	:IS THE RESULT
6372	030424	012702	000002		MOV	#2,R2	:CORRECT?
6373	030430	022021		BB16:	CMP	(R0)+,(R1)+	
6374	030432	001402			BEQ	BB17	
6375	030434	000137	031136		JMP	J#BBER4	:DATA ERROR F
6376	030440	077205		BB17:	SOB	R2,BB16	
6377	030442	020405			CMP	R4,R5	:IS FPS CORRECT?
6378	030444	001402			BEQ	BB20	
6379	030446	000137	031006		JMP	J#BBER10	:FPS ERROR.
6380							:EXPONENT DIFFERENCE=24=30 (OCT)
6381	030452			BB20:			
6382	030452	104413			LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
6383	030454	012737	030502	001236	MOV	#BB21,J#STMP2	
6384	030462	012700	031340		MOV	#BBPAT3,R0	:SET UP AC0 OPERAND.
6385	030466	172410			LDD	(R0),AC0	
6386	030470	012704	003000		MOV	#3000,R4	:SET FIU.FIV. CLEAR FD.
6387	030474	170104			LDFPS	R4	
6388	030476	012700	031320		MOV	#BBPAT1,R0	:FSRC
6389	030502	172010		BB21:	ADDF	(R0),AC0	:TEST INSTRUCTION
6390	030504	170205			STFPS	R5	
6391	030506	170011			SETD		:REENTER DOUBLE MODE
6392	030510	012700	031300		MOV	#BBDAT0,R0	:GET THE RESULT
6393	030514	174010			STD	AC0,(R0)	

M09

MAINDEC-11-DEFFA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 116
 DEFFA.F11 01-NOV-76 21:03 T33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST

6394	030516	012701	031400		MOV	#BBP7,R1		; IS THE RESULT CORRECT?
6395	030522	012702	000002		MOV	#2,R2		
6396	030526	022021		BB22:	CMP	(R0)+,(R1)+		
6397	030530	001415			BEQ	BB25		
6398	030532	012700	031300		MOV	#BBDAT0,R0		; WAS A BAD CONSTANT
6399	030536	012701	031340		MOV	#BBPAT3,R1		; USED (NOT 25) IN
6400	030542	012702	000002		MOV	#2,R2		; THE ALLIGN FLOWS?
6401	030546	022021		BB23:	CMP	(R0)+,(R1)+		
6402	030550	001402			BEQ	BB24		
6403	030552	000137	031172		JMP	J#BBERS5		; DATA ERROR F
6404	030556	077205		BB24:	SOB	R2, BB23		
6405	030560	000137	031210		JMP	J#BBER6		; BAD CONSTANT F
6406	030564	077220		BB25:	SOB	R2, BB22		
6407	030566	020405			CMP	R4,R5		
6408	030570	001402			BEQ	BB26		
6409	030572	000137	031006		JMP	J#BBER10		; BAD FPS.
6410								; EXPONENT DIFFERENCE=1
6411	030576			BB26:	LPERR			; SET UP THE LOOP ON ERROR ADDRESS.
6412	030576	104413			MOV	#BB27,J#STMP2		
6413	030600	012737	030626	001236	MOV	#3200,R4		
6414	030606	012704	003200		LDFPS	R4		; SET UP ACO OPERAND
6415	030612	170104			MOV	#BBPAT5,R0		
6416	030614	012700	031360		LDD	(R0),ACO		
6417	030620	172410			MOV	#BBPAT1,R0		; FSRC
6418	030622	012700	031320		ADD	(R0),ACO		; TEST INSTRUCTION
6419	030626	172010		BB27:	STFPS	R5		
6420	030630	170205			MOV	#BBDAT0,R0		; GET THE RESULT.
6421	030632	012700	031300		STD	ACO,(R0)		
6422	030636	174010			MOV	#BBP11,R1		; IS IT CORRECT?
6423	030640	012701	031420		MOV	#4,R2		
6424	030644	012702	000004		CMP	(R0)+,(R1)+		
6425	030650	022021		BB30:	BEQ	BB31		
6426	030652	001402			JMP	J#BBER7		; DATA ERROR D
6427	030654	000137	031244		SOB	R2, BB30		
6428	030660	077205		BB31:	CMP	R4,R5		; IS FPS CORRECT
6429	030662	020405			BEQ	BB32		
6430	030664	001402			JMP	J#BBER0		
6431	030666	000137	030766					; EXPONENT DIFFERENCE=100=144 (OCT)
6432				BB32:	LPERR			; SET UP THE LOOP ON ERROR ADDRESS.
6433	030672				MOV	#BB33,J#STMP2		
6434	030672	104413			MOV	#3200,R4		
6435	030674	012737	030722	001236	LDFPS	R4		; SET FIV, FIV AND FD
6436	030702	012704	003200		MOV	#BBPAT6,R0		; SET UP ACO OPERAND.
6437	030706	170104			LDD	(R0),ACO		
6438	030710	012700	031370		MOV	#BBPAT1,R0		; FSRC
6439	030714	172410			ADD	(R0),ACO		; TEST INSTRUCTION
6440	030716	012700	031320		STFPS	R5		
6441	030722	172010		BB33:	MOV	#BBDAT0,R0		; GET THE RESULT
6442	030724	170205			STD	ACO,(R0)		
6443	030726	012700	031300		MOV	#BBPAT6,R1		; IS IT CORRECT
6444	030732	174010			MOV	#4,R2		
6445	030734	012701	031370		CMP	(R0)+,(R1)+		
6446	030740	012702	000004		BEQ	BB35		
6447	030744	022021		BB34:	JMP	J#BBER8		; DATA ERROR D
6448	030746	001402						
6449	030750	000137	031262					

6450	030754	077205			BB35:	S0B	R2, BB34		
6451	030756	020405				OMP	R4, R5	: IS FPS CORRECT	
6452	030760	001002				BNE	BBERO		
6453	030762	000167	000442			JMP	BBDONE		
6454	030766	010437	001242		BBERO:	MOV	R4, @#STMP4	: FPS ERROR D	
6455	030772	010537	001240			MOV	R5, @#STMP3		
6456	030776	104164			1\$:	ERROR	164		
6457	031000	104412				RSETUP		: GO INITIALIZE THE FPS AND STACK; AND	
6458								: SEE IF THE USER HAS EXPRESSED	
6459								: THE DESIRE TO CHANGE THE SOFTWARE	
6460								: VIRTUAL CONSOLE SWITCH REGISTER (HAS	
6461								: THE USER TYPED CONTROL G?).	
6462	031002	000137	031430			JMP	@#BBDONE		
6463	031006	010437	001242		BBER10:	MOV	R4, @#STMP4	: FPS ERROR F	
6464	031012	010537	001240			MOV	R5, @#STMP3		
6465	031016	104165			1\$:	ERROR	165		
6466	031020	104412				RSETUP		: GO INITIALIZE THE FPS AND STACK; AND	
6467								: SEE IF THE USER HAS EXPRESSED	
6468								: THE DESIRE TO CHANGE THE SOFTWARE	
6469								: VIRTUAL CONSOLE SWITCH REGISTER (HAS	
6470								: THE USER TYPED CONTROL G?).	
6471	031022	000137	031430			JMP	@#BBDONE		
6472	031026	012737	031330	001242	BBER1:	MOV	@#BBPAT2, @#STMP4	: DATA ERROR D	
6473	031034	012737	031330	001246		MOV	@#BBPAT2, @#STMP6		
6474	031042	012737	031320	001240	BBER11:	MOV	@#BBPAT1, @#STMP3		
6475	031050	012737	031300	001244		MOV	@#BBDAT0, @#STMP5		
6476	031056	104166			1\$:	ERROR	166		
6477	031060	000137	031430			JMP	@#BBDONE		
6478	031064	012737	031350	001242	BBER2:	MOV	@#BBPAT4, @#STMP4		
6479	031072	012737	031410	001246		MOV	@#BBP10, @#STMP6		
6480	031100	000760				BR	BBER11		
6481	031102	012737	031350	001242	BBER3:	MOV	@#BBPAT4, @#STMP4	: BAD CONSTANT D	
6482	031110	012737	031410	001246		MOV	@#BBP10, @#STMP6		
6483	031116	012737	031320	001240		MOV	@#BBPAT1, @#STMP3		
6484	031124	012737	031300	001244		MOV	@#BBDAT0, @#STMP5		
6485	031132	104167			1\$:	ERROR	167		
6486	031134	000535				BR	BBDONE		
6487	031136	012737	031310	001242	BBER4:	MOV	@#BBPAT0, @#STMP4	: DATA ERROR F	
6488	031144	012737	031310	001246		MOV	@#BBPAT0, @#STMP6		
6489	031152	012737	031320	001240	BBER40:	MOV	@#BBPAT1, @#STMP3		
6490	031160	012737	031300	001244		MOV	@#BBDAT0, @#STMP5		
6491	031166	104170			1\$:	ERROR	170		
6492	031170	000517				BR	BBDONE		
6493	031172	012737	031340	001242	BBER5:	MOV	@#BBPAT3, @#STMP4		
6494	031200	012737	031400	001246		MOV	@#BBP7, @#STMP6		
6495	031206	000761				BR	BBER40		
6496	031210	012737	031340	001242	BBER6:	MOV	@#BBPAT3, @#STMP4	: CONSTANT ERROR F	
6497	031216	012737	031400	001246		MOV	@#BBP7, @#STMP6		
6498	031224	012737	031320	001240		MOV	@#BBPAT1, @#STMP3		
6499	031232	012737	031300	001244		MOV	@#BBDAT0, @#STMP5		
6500	031240	104171			1\$:	ERROR	171		
6501	031242	000472				BR	BBDONE		
6502	031244	012737	031360	001242	BBER7:	MOV	@#BBPAT5, @#STMP4		
6503	031252	012737	031320	001246		MOV	@#BBPAT11, @#STMP6		
6504	031260	000670				BR	BBER11		
6505	031262	012737	031370	001242	BBER8:	MOV	@#BBPAT6, @#STMP4		

***WITH NEGATIVE OPERANDS. EVERY COMBINATION OF
**OPERAND SIGNS IS TRIED.

ST24: SCOPE
BOTH OPERANDS NEGATIVE

DD1: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIC, FIV, AND FC
LDFPS R4

DD2: MOV #DD2, #STMP2 ;SET ACC OPERAND
MOV #DDP1, R0 ;SET ACC OPERAND
LDD (R0), ACC ;ESRC
MOV #DDP1, R0 ;TEST INSTRUCTION
ADDD (R0), ACC ;GET FPS
STFPS R5 ;GET THE RESULT

DD3: MOV #DDDAT0, R0 ;IS IT CORRECT
STD ACC, (R0)
MOV #DDP9, R1
MOV #4, R2
CMP (R0)+, (R1)+
BEQ DD6 ;DID A ADD-SUB
MOV #DDDAT0, R0 ;FLOW A FAILURE
MOV #DDP4, R1
MOV #4, R2
CMP (R0)+, (R1)+

DD4: BEQ DD5 ;216, 442, 503
JMP #DDDER1 ;DATA ERROR.D
SOB R2, DD4 ;FLOW FAILURE.E
JMP #DDDER2

DD5: SOB R2, DD3 ;FPS CORRECT?
BIS #10, R4 ;BAD, FPS
CMP R4, R5
BEQ DD7
JMP #DDDER0

DD6: ;AC POS FSRC NEG AC=-FSRC

DD7: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIC, FIV, AND FC
LDFPS R4

DD8: MOV #DD8, #STMP2 ;SET ACC OPERAND
MOV #DDP2, R0 ;SET ACC OPERAND
LDD (R0), ACC ;FSPC
MOV #DDP1, R0 ;TEST INSTRUCTION
ADDD (R0), ACC ;GET FPS
STFPS R5 ;GET THE RESULT

DD9: MOV #DDDAT0, R0 ;IS IT CORRECT
STD ACC, (R0)
MOV #DDP0, R1
MOV #4, R2
CMP (R0)+, (R1)+
BEQ DD11 ;FLOW FAILURE
JMP #DDDER3

DD10: SOB R2, DD10 ;FPS CORRECT?
BIS #4, R4
CMP R4, R5

031432 000004
031433 104413
031434 012704 003200
031435 170104
031436 012737 031464 001236
031437 012700 033324
031438 172410
031439 012700 033314
031440 172010
031441 170205
031442 012700 033274
031443 174010
031444 012701 033414
031445 012702 000004
031446 022021
031447 001415
031448 012700 033274
031449 012701 033344
031450 012702 000004
031451 022021
031452 001402
031453 000137 032524
031454 077205
031455 000137 032562
031456 077220
031457 052704 000010
031458 020405
031459 001402
031460 000137 032506
031562 104413
031563 012704 003200
031564 170104
031565 012737 031612 001236
031566 012700 033324
031567 172410
031568 012700 033314
031569 172010
031570 170205
031571 012700 033274
031572 174010
031573 012701 033304
031574 012702 000004
031575 022021
031576 001402
031577 000137 032620
031578 077205
031579 052704 000004
031580 020405

34 ADD WITH NEGATIVE OPERANDS TEST

Address	Instruction	Comments
032506	BEG DD12 JMP @DDERO	:BAD FPS
032506	:AC NEG FSRC POS	AC=FSRC
032506	:SET UP THE LOOP ON ERROR ADDRESS.	
032506	:SET FIV, FIV AND FD	
032506	LPERR	
032506	MOV #3200,R4	
032506	LDFPS R4	
032506	MOV @DD13,@STMP2	
032506	MOV @DDP1,R0	:SET ACC OPERAND
032506	LDD (R0),ACC	
032506	MOV @DDP2,R0	:FSRC
032506	ADD (R0),ACC	:TEST INSTRUCTION
032506	STFPS R5	:GET FPS
032506	MOV @DDDAT0,R0	:GET THE RESULT
032506	STD ACC,(R0)	
032506	MOV @DDP8,R1	:IS IT CORRECT
032506	MOV #4,R2	
032506	DD14: CMP (R0)+,(R1)+	
032506	BEG DD15	
032506	JMP @DDER4	:FLOW FAILURE 216,440,121
032506	SOB R2,DD14	
032506	BIS #4,R4	
032506	CMP R4,R5	:EPS CORRECT?
032506	BEG DD16	
032506	JMP @DDERO	:BAD FPS
032506	:AC POS FSRC NEG	AC=FSRC
032506	:SET UP THE LOOP ON ERROR ADDRESS.	
032506	:SET FIV, FIV AND FD	
032506	LPERR	
032506	MOV #3200,R4	
032506	LDFPS R4	
032506	MOV @DD17,@STMP2	
032506	MOV @DDP3,R0	:SET ACC OPERAND
032506	LDD (R0),ACC	
032506	MOV @DDP6,R0	:ESPC
032506	ADD (R0),ACC	:TEST INSTRUCTION
032506	STFPS R5	:GET FPS
032506	MOV @DDDAT0,R0	:GET THE RESULT
032506	STD ACC,(R0)	
032506	MOV @DDP7,R1	:IS IT CORRECT
032506	MOV #4,R2	
032506	DD18: CMP (R0)+,(R1)+	
032506	BEG DD21	
032506	MOV @DDDAT0,R0	:FLOWS FAILURE
032506	MOV @DDP8,R1	:216,440,101
032506	MOV #4,R2	:GET GENERATED
032506	DD19: CMP (R0)+,(R1)+	
032506	BEG DD20	
032506	JMP @DDER5	:DATA ERROR.
032506	SOB R2,DD19	
032506	JMP @DDER6	
032506	DD21: SOB R2,DD18	
032506	CMP R4,R5	:EPS CORRECT?
032506	BEG DD22	
032506	JMP @DDERO	:BAD FPS
032506	:AC NEG FSRC POS	AC=FSRC
032506	:SET UP THE LOOP ON ERROR ADDRESS.	
032506	:SET FIV, FIV AND FD	
032506	LPERR	
032506	MOV #3200,R4	
032506	LDFPS R4	
032506	MOV @DD17,@STMP2	
032506	MOV @DDP3,R0	:SET ACC OPERAND
032506	LDD (R0),ACC	
032506	MOV @DDP6,R0	:ESPC
032506	ADD (R0),ACC	:TEST INSTRUCTION
032506	STFPS R5	:GET FPS
032506	MOV @DDDAT0,R0	:GET THE RESULT
032506	STD ACC,(R0)	
032506	MOV @DDP7,R1	:IS IT CORRECT
032506	MOV #4,R2	
032506	DD18: CMP (R0)+,(R1)+	
032506	BEG DD21	
032506	MOV @DDDAT0,R0	:FLOWS FAILURE
032506	MOV @DDP8,R1	:216,440,101
032506	MOV #4,R2	:GET GENERATED
032506	DD19: CMP (R0)+,(R1)+	
032506	BEG DD20	
032506	JMP @DDER5	:DATA ERROR.
032506	SOB R2,DD19	
032506	JMP @DDER6	
032506	DD21: SOB R2,DD18	
032506	CMP R4,R5	:EPS CORRECT?
032506	BEG DD22	
032506	JMP @DDERO	:BAD FPS
032506	:AC NEG FSRC POS	AC=FSRC

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:SET UP THE LOOP ON ERROR ADDRESS.  
:SET F10,F11, AND F2  
:SET ACC OPERAND  
:FSPC  
:TEST INSTRUCTION  
:GET FPS  
:GET THE RESULT  
:IS IT CORRECT?  
:FLO,S FAILURE  
:CONSTANT NOT 57,  
:216,042,101  
:DATA ERROR.  
:FPS CORRECT  
:BAD FPS  
:ACC POS  
:ACC FRSRC/  
:SET UP THE LOOP ON ERROR ADDRESS.  
:SET F10,F11,AND F2  
:SET ACC OPERAND  
:FSPC  
:TEST INSTRUCTION  
:GET FPS  
:GET THE RESULT  
:IS IT CORRECT  
:ADD-SUB  
:FLOWAS FAILURE  
:CON 216 M440 NOT 141  
:GET GENERATED  
:FOR THE ALLIGNMENT  
:FLOWAS?  
:DATA ERROR, D  
:FPS CORRECT?
```

F10

Address	Instruction	Comments
033506	JMP 2#DDERO	:BAD FPS
033506	ADD NEG FSRC POS FSRC AC	
033506	033506	
033220	LDPER	:SET UP THE LOOP ON ERROR ADDRESS.
033220	MOV #3200,R4	:SET F10, F1V, AND FS
033220	LDFPS R4	
033224	MOV #DD37,2#STMP2	
033354	MOV #DDP5,RC	:SET ACC OPERAND
033354	LDD (R0),ACC	
033324	MOV #DDP4,RC	:FSPC
033324	ADD (R0),ACC	:TEST INSTRUCTION
033324	STFPS R5	:GET FPS
033274	MOV #DDATO,RC	:GET THE RESULT
033274	STD ACC,(R0)	
033274	MOV #DDP8,R1	:IS IT CORRECT
033274	MOV #4,R2	
0338:	CMP (R0)+,(R1)+	
033274	BEG DD41	
033274	MOV #DDATO,RC	:ADD SUB
033274	MOV #DDP7,R1	:FLOWS FAILURES
033274	MOV #4,R2	:GET 216,042,141
033274	CMP (R0)+,(R1)+	:FOR THE ALLIGNMENT
033274	BEG DD40	:FLOWS?
033200	JMP 2#DDER11	:DATA ERROR. D
033236	SOB R2,DD39	
033236	JMP 2#DDER12	:BAD CONSTANT.D
033236	SOB R2,DD39	
033236	BIS #10,R4	
033236	CMP R4,R5	:FPS CORRECT?
033236	BEG DD42	
033236	JMP 2#DDERO	:BAD FPS
033236	JMP 2#DDCONE	
033236	DDERO: MOV R4,2#STMP4	:FPS ERROR
033236	MOV R5,2#STMP3	
033236	1\$:	
033236	ERROR 164	
033236	JMP 2#DDDONE	
033236	DDER1:	
033236	MOV #DDP1,2#STMP3	
033236	MOV #DDP1,2#STMP4	
033236	MOV #DDATO,2#STMP5	
033236	MOV #DDP9,2#STMP6	
033236	1\$:	
033236	ERROR 165	
033236	JMP 2#DDDONE	
033236	DDER2:	
033236	MOV #DDP1,2#STMP3	
033236	MOV #DDP1,2#STMP4	
033236	MOV #DDATO,2#STMP5	
033236	MOV #DDP9,2#STMP6	
033236	1\$:	
033236	ERROR 176	
033236	JMP 2#DDCONE	
033236	DDER3:	
033236	MOV #DDP1,2#STMP3	
033236	MOV #DDP2,2#STMP4	
033236	MOV #DDATO,2#STMP5	
033236	MOV #DDP0,2#STMP6	
033236	1\$:	
033236	ERRCR 177	

032652	000137	033424		JMP	0#000000
032654	012737	033324	001240	DDER4:	MOV #DDP2,0#STMP5
032656	012737	033324	001242		MOV #DDP1,0#STMP4
032658	012737	033274	001244		MOV #DDDAT0,0#STMP5
032660	012737	033304	001246		MOV #DDP0,0#STMP6
032700	104200			IS:	ERROR 200
032706	000137	033424			JMP 0#000000
032710	012737	033364	001240	DDER5:	MOV #DDP6,0#STMP3
032714	012737	033334	001242		MOV #DDP3,0#STMP4
032722	012737	033274	001244		MOV #DDDAT0,0#STMP5
032730	012737	033374	001246		MOV #DDP7,0#STMP6
032736	104165			IS:	ERROR 165
032744	000137	033424			JMP 0#000000
032752	012737	033364	001240	DDER6:	MOV #DDP6,0#STMP3
032760	012737	033334	001242		MOV #DDP3,0#STMP4
032766	012737	033274	001244		MOV #DDDAT0,0#STMP5
032774	012737	033374	001246		MOV #DDP7,0#STMP6
033002	104201			IS:	ERROR 201
033004	000137	033424			JMP 0#000000
033010	012737	033334	001240	DDER7:	MOV #DDP3,0#STMP3
033016	012737	033364	001242		MOV #DDP6,0#STMP4
033024	012737	033274	001244		MOV #DDDAT0,0#STMP5
033032	012737	033374	001246		MOV #DDP7,0#STMP6
033040	104165			IS:	ERROR 165
033042	000137	033424			JMP 0#000000
033046	012737	033334	001240	DDER8:	MOV #DDP3,0#STMP3
033054	012737	033364	001242		MOV #DDP6,0#STMP4
033062	012737	033274	001244		MOV #DDDAT0,0#STMP5
033070	012737	033374	001246		MOV #DDP7,0#STMP6
033076	104202			IS:	ERROR 202
033100	000137	033424			JMP 0#000000
033104	012737	033354	001240	DDER9:	MOV #DDP5,0#STMP3
033112	012737	033344	001242		MOV #DDP4,0#STMP4
033120	012737	033274	001244		MOV #DDDAT0,0#STMP5
033126	012737	033404	001246		MOV #DDP8,0#STMP6
033134	104165			IS:	ERROR 165
033136	000137	033424			JMP 0#000000
033142	012737	033354	001240	DDER10:	MOV #DDP5,0#STMP3
033150	012737	033344	001242		MOV #DDP4,0#STMP4
033156	012737	033274	001244		MOV #DDDAT0,0#STMP5
033164	012737	033404	001246		MOV #DDP8,0#STMP6
033172	104203			IS:	ERROR 203
033174	000137	033424			JMP 0#000000
033200	012737	033344	001240	DDER11:	MOV #DDP4,0#STMP3
033206	012737	033354	001242		MOV #DDP5,0#STMP4
033214	012737	033274	001244		MOV #DDDAT0,0#STMP5
033222	012737	033404	001246		MOV #DDP8,0#STMP6
033230	104165			IS:	ERROR 165

* THIS IS A TEST OF THE SUBD INSTRUCTION.
 * BOTH A POSITIVE AND A NEGATIVE NUMBER
 * SUBTRACTED FROM IT SELF
 *

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003200 000000
003201 104413
003202 012704 003200
003203 170104
003204 012737 003606 001236
003205 012700 034160
003206 172410
003207 012700 034160
003208 173010
003209 012700 034116
003210 174010
003211 012701 034126
003212 012702 000004
003213 022021
003214 001415
003215 012700 024116
003216 012701 034150
003217 012702 000004
003218 022021
003219 001402
003220 000137 033726
003221 077205
003222 000137 033764
003223 077220
003224 052704 000004
003225 020405
003226 001402
003227 000137 033710
003228 104413
003229 012704 003200
003230 170104
003231 012737 003606 001236
003232 012700 034160
003233 172410
003234 012700 034160
003235 173010
003236 012700 034116
003237 174010
003238 012701 034126
003239 012702 000004
003240 022021
003241 001415
003242 012700 034116
003243 012701 034170
003244 012702 000004
  
```

```

SCOPE
USE POSITIVE OPERANDS
EE1:
LPERF #3200,R4           :SET UP THE LOOP ON ERROR ADDRESS.
LDFPS R4                :SET F10, F1V, AND FC
MOV #EE2,@STMP2
MOV #EEP1,R0            :SET ACC OPERAND
LDD (R0),ACC
MOV #EEP1,R0            :FSPC
SUBD (R0),ACC          :TEST INSTRUCTION
STFPS R5                :GET FPS
MOV #EEDATO,R0        :GET THE RESULT
STD ACC,(R0)
MOV #EEO,R1            :IS IT CORRECT?
MOV #4,R2
CMP (R0)+,(R1)+
BEQ EE6
MOV #EEDATO,R0        :DID A BAD
MOV #EEO,R1            :CONSTANT (NOT 5?)
MOV #4,R2              :GET GENERATED
CMP (R0)+,(R1)+       :FOR THE ALIGNMENT
BEQ EE5                :FLOWS?
JMP @EEEP1             :DATA ERROR.D
SOB R2,EE4
JMP @EEER2
SOB R2,EE3             :BAD CONSTANT.D
BIS #4,R4
CMP R4,R5              :FPS CORRECT?
BEQ EE7
JMP @EEEO              :BAD FPS
:USE NEGATIVE OPERANDS
EE7:
LPERF #3200,R4           :SET UP THE LOOP ON ERROR ADDRESS.
LDFPS R4                :SET F10, F1V, AND FC
MOV #EE8,@STMP2
MOV #EEP3,R0            :SET ACC OPERAND
LDD (R0),ACC
MOV #EEP3,R0            :FSPC
SJBD (R0),ACC          :TEST INSTRUCTION
STFPS R5                :GET FPS
MOV #EEDATO,R0        :GET THE RESULT
STD ACC,(R0)
MOV #EEO,R1            :IS IT CORRECT?
MOV #4,R2
CMP (R0)+,(R1)+
BEQ EE12
MOV #EEDATO,R0        :DID A BAD
MOV #EEP4,R1           :CONSTANT (NOT 5?)
MOV #4,R2              :GET GENERATED
  
```

6990	034060	000137	034200		EE10:	OMP	(R0)+,(R1)+		:FOR THE ALLIGNMENT
6991	034060	012737	034160	001240		BEG	EE11		:FLOWS?
6992	034066	012737	034160	001242		JMP	2#EEER3		:DATA ERROR.C
6993	034074	012737	034116	001244	EE11:	SOB	R2,EE10		
6994	034102	012737	034126	001246		JMP	2#EEER4		:BAD CONSTANT.D
6995	034110	104207			EE12:	SOB	R2,EE9		
6996	034112	000137	034200			BIS	#4,R4		
6997	034116	000000				OMP	R4,R5		:FPS CORRECT?
6998	034120	000000				BEG	EE13		
6999	034122	000000			EE13:	JMP	2#EEER0		:BAD FPS.
7000	034124	000000			EEER0:	JMP	2#EEDONE		
7001	034126	000000				MOV	R4,2#STMP4		:BAD FPS
7002	034130	000000				MOV	R5,2#STMP3		
7003	034132	000000			1\$:	ERROR	205		
7004	034134	000000				JMP	2#EEDONE		
7005	034136	000000			EEER1:	MOV	#EEP1,2#STMP3		
7006	034140	000200				MOV	#EEP1,2#STMP4		
7007	034142	000000				MOV	#EEDATO,2#STMP5		
7008	034144	000000				MOV	#EEO,2#STMP6		
7009	034146	000000			1\$:	ERROR	206		
						JMP	2#EEDONE		
					EEER2:	MOV	#EEP1,2#STMP3		
						MOV	#EEP1,2#STMP4		
						MOV	#EEDATO,2#STMP5		
						MOV	#EEO,2#STMP6		
					1\$:	ERROR	207		
						JMP	2#EEDONE		
					EEER3:	MOV	#EEP3,2#STMP3		
						MOV	#EEP3,2#STMP4		
						MOV	#EEDATO,2#STMP5		
						MOV	#EEO,2#STMP6		
					1\$:	ERROR	206		
						JMP	2#EEDONE		
					EEER4:	MOV	#EEP3,2#STMP3		
						MOV	#EEP3,2#STMP4		
						MOV	#EEDATO,2#STMP5		
						MOV	#EEO,2#STMP6		
					1\$:	ERROR	207		
						JMP	2#EEDONE		
					EEDATO:	0			
						0			
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					EEP1:	200			
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K10

T35 SUBC TEST

034153 000400
034154 000000
034155 000000
034156 000000
034160 100200
034162 000000
034164 000000
034166 000000
034170 100400
034172 000000
034174 000000
034176 000000
034200 10412

EEP2: 400
0
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EEP3: 100200
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EEP4: 100400
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EEDONE:
RSETUP

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER HAS
:THE USER TYPED CONTROL G?).

:TEST 36 NORMALIZE ALGORITHM TEST

:* THIS IS A TEST OF THE NORMALIZE
:* FLOW ALGORITHM. TWO PATTERNS ARE USED,
:* FIRST THE MINIMUM SITUATION REQUIRING ONE
:* LEFT SHIFT AND THEN THE MAXIMUM SITUATION
:* REQUIRING 56 SHIFTS.
:*

ST36: SCOPE
:USE DATA PATTERNS THAT REQUIRE ONLY ONE LEFT SHIFT TO NORMALIZE
FF1:

034202 000004
034204 104413
034206 012704 003200
034212 170104
034214 01273 034234 001236
034222 01270 034526
034226 172410
034230 012700 034536
034234 172010
034236 170205
034240 012700 034476
034244 174010
034246 012701 034546
034252 012702 000004
034256 022021
034260 001401
034262 000466
034264 077204
034266 020405
034270 001401
034272 000435

:SET UP THE LOOP ON ERROR ADDRESS.
:SET F10, F1V, AND FD
#3200,R4
R4
#FF2,2#STMP2
#FF2,R0 :SET ACO OPERAND
(R0),ACO
#FF3,R0 :FSPC
(R0),ACO :TEST INSTRUCTION
R5 :GET FPS
#FFDAT0,R0 :GET THE RESULT
ACO,(R0)
#FF4,R1 :IS IT CORRECT
#4,R2
(R0)+,(R1)+
FF4: BR FFER2 :BAD DATA
SOB R2,FF3
CMP R4,R5 :FPS CORRECT?
BEQ FF5
BR FFER0 :BAD FPS

:USE DATA PATTERNS WHICH REQUIRE 56 LEFT SHIFTS TO NORMALIZE
:THE RESULT
FF5:

034274
034274 104413
034276 012704 003200

LPERR :SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 :SET F10, F1V, AND FD

7086	034324	012700	001236	LDFPS	R4	
7087	034326	012700	001236	MOV	#FF6,2#STMP2	
7088	034328	012700		MOV	#FFP0,R0	:SET ACC OPERAND
7089	03432A	172410		LD	(R0),ACC	
7090	03432C	012700	034516	MOV	#FF7,R0	:FSRC
7091	03432E	172010		ADD	(R0),ACC	:TEST INSTRUCTION
7092	034330	170205		STFPS	R5	:GET FPS
7093	034332	012700	034476	MOV	#FFDAT0,R0	:GET THE RESULT
7094	034334	174010		STD	ACC,(R0)	
7095	034336	012701	034546	MOV	#FFP4,R1	:IS IT CORRECT
7096	034338	012702	000004	MOV	#4,R2	
7097	03433A	022021		CMP	(R0)+,(R1)+	
7098	03433C	001401		BEG	FF10	
7099	03433E	000413		BR	FFER1	:BATA
7100	034340	077204		SOB	R2,FF7	
7101	034342	020405		CMP	R4,R5	:FPS CORRECT
7102	034344	001401		BEG	FF11	
7103	034346	000401		BR	FFER0	:BAD FPS
7104	034348	000474		BR	FFDONE	
7105	034366	010537	001240	FFER0:	MOV R5,2#STMP3	
7106	034372	010437	001242		MOV R4,2#STMP4	
7107	034376	104164		1\$:	ERROR 164	
7108	034400	000466		BR	FFDONE	
7109	034402			FFER1:		
7110	034402	012737	034516	001240	MOV	#FFP1,2#STMP3
7111	034410	012737	034506	001242	MOV	#FFP0,2#STMP4
7112	034416	012737	034476	001244	MOV	#FFDAT0,2#STMP5
7113	034424	012737	034546	001246	MOV	#FFP4,2#STMP6
7114	034432	104210		1\$:	ERROR 210	
7115	034434	000137	034556		JMP	2#FFDONE
7116	034440			FFER2:		
7117	034440	012737	034536	001240	MOV	#FFP3,2#STMP3
7118	034446	012737	034526	001242	MOV	#FFP2,2#STMP4
7119	034454	012737	034476	001244	MOV	#FFDAT0,2#STMP5
7120	034462	012737	034546	001246	MOV	#FFP4,2#STMP6
7121	034470	104210		1\$:	ERROR 210	
7122	034472	000137	034556		JMP	2#FFDONE
7123	034476	000000		FFDAT0:	0	
7124	034500	000000			0	
7125	034502	000000			0	
7126	034504	000000			0	
7127	034506	016000		FFP0:	16000	
7128	034510	000000			0	
7129	034512	000000			0	
7130	034514	000001			1	
7131	034516	116000		FFP1:	116000	
7132	034520	000000			0	
7133	034522	000000			0	
7134	034524	000000			0	
7135	034526	000500		FFP2:	500	

M10

```

7122 034530 000000          0
7123 034532 000000          0
7124 034534 000000          0
7125 034536 100400      FFP3:
7126 034540 000000          0
7127 034542 000000          0
7128 034544 000000          0
7129 034546 000200      FFP4:          :FFP4=FFP0+FFP1
7130 034550 000000          :          =FFP3+FFP4
7131 034552 000000          0
7132 034554 000000          0
7133
7134 034556      FFDONE:
7135
7136
7137 034556      TST37:
7138
7139
7140
7141      .SBTTL  END OF PASS ROUTINE
7142
7143      ;*****
7144      ;*INCREMENT THE PASS NUMBER ($PASS)
7145      ;*INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
7146      ;*IF SW12=1 INHIBIT TRACE TRAP
7147      ;*IF THERES A MONITOR GO TO IT
7148      ;*IF THERE ISN'T JUMP TO LOOP
7149
7150      $EOP:
7151      034556 000004          SCOPE
7152      034560 005067 144316  CLR          $TSTNM          ;;ZERO THE TEST NUMBER
7153      034564 005067 144512  CLR          $TIMES          ;;ZERO THE NUMBER OF ITERATIONS
7154      034570 005267 144530  INC          $PASS          ;;INCREMENT THE PASS NUMBER
7155      034574 042767 100000 144522 BIC          #100000,$PASS ;;DON'T ALLOW A NEG. NUMBER
7156      034602 005327          DEC          (PC)+          ;;LOOP?
7157      034604 000001      $EOPCT: .WORD 1
7158      034606 003074          BGT          $DOAGN          ;;YES
7159      034610 012737          MOV          (PC)+,2(PC)+ ;;RESTORE COUNTER
7160      034612 000001      $ENDCT: .WORD 1
7161      034614 034604          $EOPCT
7162      034616 104401 034624  TYPE          65$          ;;TYPE ASCIZ STRING
7163      034622 000407          BR          64$          ;;GET OVER THE ASCIZ
7164      ;;65$: .ASCIZ <12><15>/END PASS #/
7165
7166      034642 016746 144456      64$: MOV          $PASS,-(SP)          ;;SAVE $PASS FOR TYPEOUT
7167          ;;TYPE PASS NUMBER IN OCTAL
7168      034646 104403          TYPOS          ;;GO TYPE--OCTAL ASCII
7169      034650          006          .BYTE 6          ;;TYPE 6 DIGITS
7170      034651          000          .BYTE 0          ;;SUPPRESS LEADING ZEROS
7171      034652 104401 034660  TYPE          67$          ;;TYPE ASCIZ STRING
7172      034656 000421          BR          66$          ;;GET OVER THE ASCIZ
7173      ;;67$: .ASCIZ / TOTAL ERRORS SINCE LAST REPORT /
7174      034722          66$:
7175      034722 016746 144164      MOV          $ERTTL,-(SP)          ;;SAVE $ERTTL FOR TYPEOUT
7176          ;;TOTAL NUMBER OF ERRORS IN OCTAL
7177      034726 104403          TYPOS          ;;GO TYPE--OCTAL ASCII
  
```

```

7178 034730 006 .BYTE 6 ;;TYPE 6 DIGITS
7179 034731 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
7180 034732 104401 001313 TYPE .SCLF ;;TYPE CARRIAGE RETURN, LINE FEED
7181 034736 005067 144150 CLR $EPTTL ;;CLEAR ERROR TOTAL
7182 034742 013700 000042 $GET42: MOV $M42,RO ;;GET MONITOR ADDRESS
7183 034746 001414 BEQ $DOAGN ;;BRANCH IF NO MONITOR
7184 034750 005046 CLR -(SP) ;;INSURE THE "T" BIT IS CLEAR
7185 034752 012746 034760 MOV #$CLR.T,-(SP) ;;SETUP FOR AN RTI OR RTT
7186 034756 000426 BR $RTRN ;;GO DO AN RTI OR RTT TO LOAD THE PSW
;;WITH A CLEARED "T" BIT
7188 034760 $CLR.T:
7189 034760 013700 000042 MOV $M42,RO ;;INSURE RO CONTAINS THE MONITORS
7190 034764 001405 BEQ $DOAGN ;;RETURN ADDRESS
7191 034766 000095 RESET ;;CLEAR THE WORLD
7192 034770 004710 $ENDAD: JSR PC,(RO) ;;GO TO MONITOR
7193 034772 000240 NOP ;;SAVE ROOM
7194 034774 000240 NOP ;;FOR
7195 034776 000240 NOP ;;ACT11
7196 035000 $DOAGN:
7197 035000 104400 TRAP ;;PUSH OLD PSW AND PC ON STACK
7198 035002 042716 000020 BIC #20,(SP) ;;CLEAR THE "T" BIT
7199 035006 032777 010000 144124 BIT #BIT12,$SWR ;;RUN WITH TRACE TRAP?
7200 035014 001005 BNE 1$ ;;BR IF NO
7201 035016 005167 000020 COM $TBIT ;;IS IT TIME FOR TRACE TRAP
7202 035022 100402 BMI 1$ ;;BR IF NO
7203 035024 052716 000020 BIS #20,(SP) ;;SET TRACE TRAP
7204 035030 012746 035036 1$: MOV #$LOOP,-(SP) ;;JUMP TO START OF TEST
7205 035034 000002 $RTRN: RTI ;;RETURN--THIS IS CHANGED TO
;;AN "RTT" IF "RTT" IS A LEGAL
;;INSTRUCTION
7208 035036 $LOOP:
7209 035036 000137 JMP $M(PC)+ ;;RETURN
7210 035040 004304 $RTNAD: .WORD LOOP
7211 035042 000000 $TBIT: .WORD 0 ;;"T" BIT STATE INDICATOR
7212 035044 377 377 000 $ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
7213 035050 .EVEN
7214
7215 .SBTTL SCOPE HANDLER ROUTINE
7216
7217 ;;*****
7218 ;;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
7219 ;;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
7220 ;;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
7221 ;;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
7222 ;;*SW14=1 LOOP ON TEST
7223 ;;*SW11=1 INHIBIT ITERATIONS
7224 ;;*SW09=1 LOOP ON ERROR
7225 ;;*SW08=1 LOOP ON TEST IN SWR<7:0>
7226 ;;*CALL
7227 ;;* SCOPE ;;SCOPE=IOT
7228
7229 035050 $SCOPE:
7230 035050 104406 CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
7231 035052 032777 040000 144060 1$: BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
7232 035060 001114 BNE $OVER ;;YES IF SW14=1
7233 ;*****START OF CODE FOR THE XOR TESTER*****

```

SCOPE HANDLER ROUTINE

```

7232 035362 000416          SYSTR: BR          65          :: IF RUNNING ON THE "XOR" TESTER...
7233 0113746 000004          MOV          20ERRVEC - SF: :: THIS INSTRUCTION IS A "NOOP"
7234 0113747 000004          MOV          20ERRVEC          :: SAVE THE CONTENTS OF THE ERROR VECTOR
7235 0057437 144004          TST          20177060          :: SET FOR TIMEOUT
7236 0113747 000004          MOV          (SP)+, 20ERRVEC    :: TIME OUT ON "XOR"
7237 0113747 000004          BR          55VLAD           :: RESTORE THE ERROR VECTOR
7238 0226226 000004          CMP          (SP)+, (SP)+      :: GO TO THE NEXT TEST
7239 0113747 000004          MOV          (SP)+, 20ERRVEC    :: CLEAR THE STACK AFTER A "TIME OUT"
7240 000423          BR          75              :: RESTORE THE ERROR VECTOR
7241 032777 000400 144012          65::##### BEND OF CODE FOR THE XOR TESTER##### :: LOOP ON THE PRESENT TEST
7242 001404          BIT          %BIT08, 20SWR      :: LOOP ON SPEC. TEST
7243 0113747 144004 143744          BEQ          25              :: BR IF NO
7244 0113747 143737          CMPB        20SWR, 20STNM      :: ON THE RIGHT TEST? SWR(7:0)
7245 105767          BEQ          50VER           :: BR IF YES
7246 001404          TSTB       20SERFLG          :: HAS AN ERROR OCCURRED?
7247 126767 143743 143727          BEQ          35              :: BR IF NO
7248 101015          CMPB        20SERMAX, 20SERFLG :: MAX. ERRORS FOR THIS TEST OCCURRED?
7249 032777 001000 143754          BHI          35              :: BR IF NO
7250 001404          BIT          %BIT09, 20SWR      :: LOOP ON ERROR?
7251 016767 143716 143712          BEQ          45              :: BR IF NO
7252 000446          MOV          20SLPERR, 20SLPADR :: SET LOOP ADDRESS TO LAST SCOPE
7253 105067 143701          BR          45              :: ZERO THE ERROR FLAG
7254 005067 144074          CLRB       20SERFLG          :: CLEAR THE NUMBER OF ITERATIONS TO MAKE
7255 000415          CLR          20STIMES          :: ESCAPE TO THE NEXT TEST
7256 032777 004000 143722          BR          15              :: INHIBIT ITERATIONS?
7257 001011          BIT          %BIT11, 20SWR      :: BR IF YES
7258 005767 144100          BNE          15              :: IF FIRST PASS OF PROGRAM
7259 001406          TST          20SPASS          :: INHIBIT ITERATIONS
7260 005267          BEQ          15              :: INCREMENT ITERATION COUNT
7261 026767 144044 143644          INC          20SICNT          :: CHECK THE NUMBER OF ITERATIONS MADE
7262 002024          CMP          20STIMES, 20SICNT :: BR IF MORE ITERATION REQUIRED
7263 012767 000001 143634          BGE          15              :: REINITIALIZE THE ITERATION COUNTER
7264 016767 000052 144024          MOV          20SMXCNT, 20STIMES :: SET NUMBER OF ITERATIONS TO DO
7265 105267 143620          INCB       20STNM           :: COUNT TEST NUMBERS
7266 011667 143614 144032          MOV          20STNM, 20STSTN  :: SET TEST NUMBER IN APT MAILBOX
7267 011667 143612          MOV          (SP), 20SLPADR    :: SAVE SCOPE LOOP ADDRESS
7268 011667 143610          MOV          (SP), 20SLPERR    :: SAVE ERROR LOOP ADDRESS
7269 005067 144000          CLR          20ESCAPE         :: CLEAR THE ESCAPE FROM ERROR ADDRESS
7270 112767 000001 143603          MOV          20I, 20SERMAX     :: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
7271 016777 143622          $OVER: MOV          20STNM, 20DISPLAY :: DISPLAY TEST NUMBER
7272 016716 143562          MOV          20SLPADR, (SP)   :: FUDGE RETURN ADDRESS
7273 000002          RTI                          :: FIXES PS
7274 000001          $MXCNT: !                    :: MAX. NUMBER OF ITERATIONS

```

.SBTTL ERROR HANDLER ROUTINE

```

*****
* THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
* SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
* AND GO TO ERTYPE ON ERROR
* THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
* SW15=1      HALT ON ERROR
* SW13=1      INHIBIT ERROR TYPEGUTS
* SW10=1      BELL ON ERROR

```

ERROR HANDLER ROUTINE

;;SNOB=1 LOOP ON ERROR
;;CALL
;; ERROR N ;;ERROR=ENT AND N=ERROR ITEM NUMBER

ERROR:

1S: CKSWR ;; TEST FOR CHANGE IN SOFT-SWR
INCB SERFLG ;; SET THE ERROR FLAG
BEQ 7S ;; DON'T LET THE FLAG GO TO ZERO
MOV \$SYSTEM,DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
BIT \$BIT10,\$SWR ;; BELL ON ERROR
BEQ 1S ;; NO - SKIP
TYPE \$BELL ;; RING BELL
INC \$ERTTL ;; COUNT THE NUMBER OF ERRORS
MOV (\$SP),SERAPC ;; GET ADDRESS OF ERROR INSTRUCTION
SUB \$2,SERAPC
MOVB \$SERAPC,\$ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
BIT \$BIT13,\$SWR ;; SKII TYPEOUT IF SET
BNE 20S ;; SKIP TYPEOUTS
JSR PC,ERTYPE ;; GO TO USER ERROR ROUTINE
TYPE \$SCLF
20S: CMPB \$APTENV,\$ENV ;; RUNNING IN APT MODE
BNE 2S ;; NO SKIP APT ERROR REPORT
MOVB \$ITEMB,\$IS ;; SET ITEM NUMBER AS ERROR NUMBER
JSR PC,\$ATY4 ;; REPORT FATAL ERROR TO APT
21S: .BYTE 0
.BYTE 0
22S: BR 22S ;; APT ERROR LOOP
2S: TST \$SWR ;; HALT ON ERROR
BPL 3S ;; SKIP IF CONTINUE
HALT ;; HALT ON ERROR!
CKSWR ;; TEST FOR CHANGE IN SOFT-SWR
BIT \$BIT09,\$SWR ;; LOOP ON ERROR SWITCH SET?
BEQ 4S ;; BR IF NO
MOV \$LERR,\$(SP) ;; FUDGE RETURN FOR LOOPING
TST \$ESCAPE ;; CHECK FOR AN ESCAPE ADDRESS
BEQ 5S ;; BR IF NONE
MOV \$ESCAPE,\$(SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE
5S: CMP \$SENDAD,\$#42 ;; ACT-11 AUTO-ACCEPT?
BNE 6S ;; BRANCH IF NO
HALT ;; YES
6S: BIT \$BIT09,\$SWR
BNE ERM10
MOV (\$SP),\$SREG0 ;; SEE IF ERROR #377
ADD #-2,\$SREG0
CMPB \$377,\$SREG0
BNE ERM10
ADD \$2,\$(SP)
ERM10: RTI

.SBTTL SAVE AND RESTORE R0-R5 ROUTINES

;;*****

Vertical column of numbers and characters on the left side of the page, likely a list of addresses or identifiers.

Vertical column of numbers and characters on the left side of the page, likely representing memory addresses or line numbers.

Main body of assembly code listing instructions such as STYPE, MOV, CMPB, BITB, JSR, and their associated parameters and addresses.

Comments and annotations on the right side of the page, explaining the logic of the assembly instructions, such as 'IS THERE A TERMINAL?' and 'REPLACE TAB WITH SPACE'.


```

    77:00 037122 011646 000004 000002 $RDCHR: MOV    (SP),-(SP)    ;; PUSH DOWN THE PC
    77:01 037124 016666 000004 000002 MOV    4(SP),2(SP)    ;; SAVE THE PS
    77:02 037132 105777 142006 15:   TSTB   2$TKS        ;; WAIT FOR
    77:03 037136 100375 142002 000004 SPL     1$           ;; A CHARACTER
    77:04 037140 117766 177600 000004 MOVB   2$TKB,4(SP)    ;; READ THE TTY
    77:05 037146 042766 000004 BIC    #'C<17>,4(SP) ;; GET RID OF JUNK IF AN
    77:06 037154 026627 000004 000023 CMP    4(SP),#23     ;; IS IT A CONTROL-'S'
    77:07 037162 001013 141754 2$:   BNE    3$           ;; BRANCH IF NC
    77:08 037164 105777 141754 2$:   TSTB   2$TKS        ;; WAIT FOR A CHARACTER
    77:09 037170 100375 141754 2$:   BP     2$           ;; LOOP UNTIL ITS THERE
    77:10 037172 117746 177600 000004 MOVB   2$TKB,-(SP)   ;; GET CHARACTER
    77:11 037176 042716 177600 BIC    #'C17,.(SP)  ;; MAKE IT 7-BIT ASCII
    77:12 037202 022627 000021 CMP    (SP)+,#21     ;; IS IT A CONTROL-'0'
    77:13 037206 001366 141754 2$:   BNE    2$           ;; IF NOT DISCARD IT
    77:14 037210 000750 141754 1$:   BR     1$           ;; YES, RESUME
    77:15 037212 026627 000004 000140 3$:   CMP    4(SP),#140   ;; IS IT UPPER CASE?
    77:16 037220 002407 000004 000175 BLT    4$           ;; BRANCH IF YES
    77:17 037222 026627 000004 000175 CMP    4(SP),#175   ;; IS IT A SPECIAL CHAR?
    77:18 037230 003003 000004 000175 BGT    4$           ;; BRANCH IF YES
    77:19 037232 042766 000040 000004 BIC    #'0,4(SP)    ;; MAKE IT UPPER CASE
    77:20 037240 000002 4$:   RTI                    ;; GO BACK TO USER
    77:21 037242 052536 005015 000 $CNTLU: .ASCIZ /'U<15><12/ ;; CONTROL "U"
    77:22 037247 136 006507 000012 $CNTLG: .ASCIZ /'G<15><12/ ;; CONTROL "G"
    77:23 037254 005015 053523 020122 $MSWR:  .ASCIZ <15><12>/SWR =
    77:24 037262 020075 000004 053505 $MNEW:  .ASCIZ / NEW =
    77:25 037265 040 047040 053505
    77:26 037272 036440 000040
    
```

.SBTTL TRAP DECODER

```

    ;;*****
    ;;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
    ;;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
    ;;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
    ;;*GO TO THAT ROUTINE.
    
```

```

    77:27 037276 010046 000002 $TRAP: MOV    R0,-(SP)    ;; SAVE R0
    77:28 037300 016600 000002 MOV    2(SP),R0      ;; GET TRAP ADDRESS
    77:29 037304 005740 000002 TST    -(R0)        ;; BACKUP BY 2
    77:30 037306 111000 000002 MOVB   (R0),R0      ;; GET RIGHT BYTE OF TRAP
    77:31 037310 006300 000002 ASL    R0           ;; POSITION FOR INDEXING
    77:32 037312 016000 037332 MOV    $TRPAD(R0),R0 ;; INDEX TO TABLE
    77:33 037316 000200 000002 RTS     R0          ;; GO TO ROUTINE
    
```

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

    77:34 037320 011646 000004 000002 $TRAP2: MOV    (SP),-(SP)  ;; MOVE THE PC DOWN
    77:35 037322 016666 000004 000002 MOV    4(SP),2(SP)    ;; MOVE THE PSW DOWN
    77:36 037330 000002 000002 RTI                    ;; RESTORE THE PSW
    
```

.SBTTL TRAP TABLE

:*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:*BY THE "TRAP" INSTRUCTION.

ROUTINE	STARTING ADDRESS	ROUTINE NAME	DESCRIPTION
TRAP0	037332	WORD	TTY TYPEOUT ROUTINE
TRAP1	037334	TYPE	TTY TYPEOUT ROUTINE
TRAP2	037336	TYPOC	TYPE OCTAL NUMBER (WITH LEADING ZEROS)
TRAP3	037340	TYPOS	TYPE OCTAL NUMBER (NO LEADING ZEROS)
TRAP4	037342	TYPON	TYPE OCTAL NUMBER (AS PER LAST CALL)
TRAP5	037344	GTSWR	GET SOFT-SWR SETTING
TRAP6	037346	CKSWR	TEST FOR CHANGE IN SOFT-SWR
TRAP7	037350	RDCHR	TTY TYPEIN CHARACTER ROUTINE
TRAP10	037352	SAVREG	SAVE R0-R5 ROUTINE
TRAP11	037354	RESREG	RESTORE R0-R5 ROUTINE
TRAP12	037356	RSET	ROUTINE TO RESET STACK AND FPS
TRAP13	037360	LPER	ROUTINE TO SET LOOP ON ERROR ADDRESS

.SBTTL POWER DOWN AND UP ROUTINES

POWER DOWN ROUTINE

\$PWRDN:	MOV	#SILLUP, @PWRVEC	;; SET FOR FAST UP
	MOV	#340, @PWRVEC+2	;; PRIO:7
	MOV	R0, -(SP)	;; PUSH R0 ON STACK
	MOV	R1, -(SP)	;; PUSH R1 ON STACK
	MOV	R2, -(SP)	;; PUSH R2 ON STACK
	MOV	R3, -(SP)	;; PUSH R3 ON STACK
	MOV	R4, -(SP)	;; PUSH R4 ON STACK
	MOV	R5, -(SP)	;; PUSH R5 ON STACK
	MOV	@SWR, -(SP)	;; PUSH @SWR ON STACK
	MOV	SP, \$SAVR6	;; SAVE SP
	MOV	@PWRUP, @PWRVEC	;; SET UP VECTOR
	HALT		
	BR	.-2	;; HANG UP

POWER UP ROUTINE

\$PWRUP:	MOV	#SILLUP, @PWRVEC	;; SET FOR FAST DOWN
	MOV	\$SAVR6, SP	;; GET SP
	CLR	\$SAVR6	;; WAIT LOOP FOR THE TTY
IS:	INC	\$SAVR6	;; WAIT FOR THE INC
	BNE	IS	;; OF WORD
	MOV	(SP)+, @SWR	;; POP STACK INTO @SWR
	MOV	(SP)+, R5	;; POP STACK INTO R5
	MOV	(SP)+, R4	;; POP STACK INTO R4
	MOV	(SP)+, R3	;; POP STACK INTO R3
	MOV	(SP)+, R2	;; POP STACK INTO R2
	MOV	(SP)+, R1	;; POP STACK INTO R1
	MOV	(SP)+, R0	;; POP STACK INTO R0
	MOV	@PWRDN, @PWRVEC	;; SET UP THE POWER DOWN VECTOR
	MOV	#340, @PWRVEC+2	;; PRIO:7

7744 037332 037320
7745 037334 035662
7746 037336 036170
7747 037340 036144
7748 037342 036204
7749 037344 036710
7750 037346 036640
7751 037350 037122
7752 037352 035566
7753 037354 035624
7754 037356 040274
7755 037360 040266
7756 000000
7757 000000
7758 000000
7759 000000
7760 000000
7761 000000
7762 000000
7763 000000
7764 037362 012737 037540 000024
7765 037370 012737 000340 000026
7766 037376 010046
7767 037400 010146
7768 037402 010246
7769 037404 010346
7770 037406 010446
7771 037410 010546
7772 037412 017746 141522
7773 037416 010667 000122
7774 037422 012737 037434 000024
7775 037430 000000
7776 037432 000776
7777 000000
7778 000000
7779 000000
7780 037434 012737 037540 000024
7781 037442 016706 000076
7782 037446 005067 000072
7783 037452 005267 000066
7784 037456 001375
7785 037460 012677 141454
7786 037464 012605
7787 037466 012604
7788 037470 012603
7789 037472 012602
7790 037474 012601
7791 037476 012600
7792 037500 012737 037362 000024
7793 037506 012737 000340 000026

```

7794 037514 104401      SPWRMG: .WORD POWERM      ::REPORT THE POWER FAILURE
7795 037516 040344      MOV      (PC)+,SP        ::POWER FAIL MESSAGE POINTER
7796 037520 012716      SPWRAD: .WORD START      ::RESTART AT START
7797 037522 003606      BIC      #20,2(SP)       ::RESTART ADDRESS
7798 037524 042766      CLR      $TBIT           ::CLEAR "T" BIT
7799 037532 005067      RTI      $TBIT          ::CLEAR THE "T" BIT FLAG
7800 037536 000002      $TLUP: HALT             ::THE POWER UP SEQUENCE WAS STARTED
7801 037540 000000      BR      .-2             ::BEFORE THE POWER DOWN WAS COMPLETE
7802 037542 000776      $SAVR6: 0               ::PUT THE SP HERE
7803 037544 000000

```

.SBTTL ERROR TYPE OUT ROUTINE

```

*****
*****
*THIS ROUTINE IS CALLED TO TYPE AN ERROR MESSAGE WHICH IS INCLUDED
*IN THE ERROR MESSAGE DATA TABLE. IT IS CALLED BY THE $ERROR ROUTINE
*OR BY FIRST SETTING $ITEMB EQUAL TO THE ERROR TABLE ITEM TO BE PRINTED
*OUT AND THEN EXECUTING A:
*                               JSR      PC,ERTYPE
*

```

```

7815 037546 104401      ERTYPE: TYPE          ;TYPE A CRLF
7816 037550 001313      .WORD      $CRLF
7817 037552 113737      MOV      #0,$STSTNM,0,$STMPO
7818 037560 042737      BIC      #177400,0,$STMPO
7819 037566 013737      MOV      #0,$ERRPC,0,$STMP1
7820 037574 010046      MOV      RO,-(SP)      ;GET PC OF CALL
7821                                     ;SAVE RO
7822 037576 113700      MOV      #0,$ITEMB,RO  ;GET THE ITEM NUMBER.
7823 037602 042700      BIC      #177400,RO
7824 037606 001005      BNE      1$
7826 037610 013746      MOV      #0,$ERRPC,-(SP) ;IF ZERO THEN JUST
7827 037614 104402      TYPOC      ;PRINT THE PC
7828 037616 000137      JMP      #ERTS
7829
7830 037622 022700      1$: CMP      #377,RO
7831 037626 001005      BNE      20$
7832 037630 016600      MOV      4(SP),RO
7833 037634 011000      MOV      (RO),RO
7834 037636 062700      ADD      #400,RO
7835 037642 005300      20$: DEC      RO
7836 037644 006300      ASL      RO
7837 037646 006300      ASL      RO
7838 037650 006300      ASL      RO
7839 037652 062700      ADD      #ERRTB,RO
7840
7841 037656 012037      MOV      (RO)+,0,$2$    ;PICK UP THE ADDRESS
7842 037662 001404      BEQ      3$            ;OF THE EM. ERROR MESSAGE
7843 037664 104401      TYPE
7844 037666 000000      2$: .WORD      0
7845 037670 104401      TYPE
7846 037672 001313      .WORD      $CRLF
7847
7848 037674 012037      3$: MOV      (RO)+,0,$4$ ;GET THE DH.DATA HEADER
7849 037700 001404      BEQ      5$

```


7906	040036	122710	005004	10\$:	CMPB	#4, (R0)	:FORMAT FOUR?
7907	040042	001004			BNE	11\$	
7908							
7909	040044	013146			MOV	2(R1)+, -(SP)	:FORMAT FOUR SO TYPE
7910	040046	104403			TYPOS		:AN OCTAL NUMBER
7911	040050	016			.BYTE	16	:SUPPRESSING LEADING ZERGES.
7912	040051	000			.BYTE	0	
7913	040052	000435			BR	ERT2	
7914							
7915	040054	122710	000005	11\$:	CMPB	#5, (R0)	:FORMAT FIVE?
7916	040060	001005			BNE	13\$	
7917							
7918	040062	012137	040070		MOV	(R1)+, 2#12\$:FORMAT FIVE SO TYPE AN
7919	040066	104401			TYPE		:ASCIZ STRING.
7920	040070	000000		12\$:	.WORD	0	
7921	040072	000427			BR	ERT3	
7922							
7923	040074	122710	000011	13\$:	CMPB	#11, (R0)	:FORMAT ELEVEN?
7924	040100	001005			BNE	15\$	
7925							
7926	040102	013137	040110		MOV	2(R1)+, 2#14\$:FORMAT ELEVEN SO PICK
7927	040106	104401			TYPE		:A POINTER TO AN ASCIZ
7928	040110	000000		14\$:	.WORD	0	:STRING.
7929	040112	000417			BR	ERT3	
7930							
7931	040114	122710	000012	15\$:	CMPB	#12, (R0)	:FORMAT TWELVE?
7932	040120	001011			BNE	17\$	
7933							
7934	040122	013102			MOV	2(R1)+, R2	:FORMAT TWELVE SO TYPE
7935	040124	012703	000006		MOV	#6, R3	:TYPE SIX OCTAL NUMBERS
7936	040130	012246		16\$:	MOV	(R2)+, -(SP)	
7937	040132	104402			TYPOS		
7938	040134	104401			TYPE		
7939	040136	040414			.WORD	SPACE	
7940	040140	077305			SQB	R3, 16\$	
7941	040142	000401			BR	ERT2	
7942							
7943	040144	000000		17\$:	HALT		:UNDEFINED FORMAT FOR DATA????
7944							
7945	040146	104401		ERT2:	TYPE		:PRINT A TAB AFTER TYPING
7946	040150	040412			.WORD	\$TAB	:AN DATA TABLE ENTRY
7947							:OF ALL FORMATS EXCEPT
7948							:ASCIZ, FORMATS 5 OR 11
7949							
7950	040152	005200		ERT3:	INC	R0	:POINT TO THE NEXT FORMAT
7951	040154	005711			TST	(R1)	:END OF DATA TABLE.
7952	040156	001401			BEQ	ERT4	
7953	040160	000663			BR	ERT1	
7954							
7955	040162	104401		ERT4:	TYPE		:DONE.
7956	040164	001313			.WORD	\$CRLF	
7957	040166	012603			MOV	(SP)+, R3	:RESTORE R1, R2 AND R3
7958	040170	012602			MOV	(SP)+, R2	
7959	040172	012601			MOV	(SP)+, R1	
7960	040174	012600		ERT5:	MOV	(SP)+, R0	:RESTORE R0.
7961	040176	000207			RTS	PC	:AND RETURN.

.SBTTL FPP SPURIOUS TRAP TO 244 HANDLER

THIS ROUTINE HANDLES UNEXPECTED TRAPS TO THE FPP TRAP VECTOR AT 244.
THE LAST FPP INSTRUCTION EXECUTED AND ITS ADDRESS HAS BEEN RECORDED
ALONG WITH THE FEC, FPS AND PC OF TRAP ARE REPORTED.

FPSR: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
CMP (SP)+, (SP)+ ;RESTORE SP.
STFPS R0 ;GET FPS
MOV R0, @STMP3
STST R0 ;GET FEC
MOV R0, @STMP4
S: ERROR 211
RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
SEE IF THE USER HAS EXPRESSED
THE DESIRE TO CHANGE THE SOFTWARE
VIRTUAL CONSOLE SWITCH REGISTER HAS
THE USER TYPED CONTROL G.

001236
001240
001242
00137 034556

.SBTTL CPU SPURIOUS TRAP TO 4 HANDLER

THIS ROUTINE REPORTS UNEXPECTED CPU TRAPS TO VECTOR 4.

CPSR: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
CMP (SP)+, (SP)+
S: ERROR 212
RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
SEE IF THE USER HAS EXPRESSED
THE DESIRE TO CHANGE THE SOFTWARE
VIRTUAL CONSOLE SWITCH REGISTER HAS
THE USER TYPED CONTROL G.

001236
00137 034556

.SBTTL CPU SPURIOUS TRAP TO 10 HANDLER

THIS ROUTINE REPORTS UNEXPECTED CPU TRAPS TO VECTOR 10.

CP*40: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
CMP (SP)+, (SP)+
S: ERROR 213
RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
SEE IF THE USER HAS EXPRESSED
THE DESIRE TO CHANGE THE SOFTWARE
VIRTUAL CONSOLE SWITCH REGISTER HAS
THE USER TYPED CONTROL G.

001236
00137 034556

.SBTTL SET LOOP ON ERROR ADDRESS ROUTINE


```
PER: MOV (SP),0#SLPERR
RTI
```

.SBTTL FLAG RESET AND CONSOLE TEST ROUTINE

*THIS ROUTINE WILL BE CALLED AT THE END OF EACH TEST TO
*RESET THE STACK, CLEAR THE FPS AND SEE IF THE USER HAS TYPED
*CONTROL G ON THE TERMINAL. IF THE USER HAS TYPED CONTROL G AND
*THERE IS NO PHYSICAL CONSOLE SWITCH REGISTER THEN THE CONTENTS
*OF THE SOFTWARE SWITCH REGISTER WILL BE TYPED IN OCTAL ON THE
*TELETYPE AND THE USER CAN MODIFY IT.

```
RSET: CMP 0#SWR,0177570 :SEE IF THERE IS A PHYSICAL
:CONSOLE SWITCH REGISTER.
BNE IS :BRANCH IF NO.
CHKSWR :OTHERWISE TYPE THE CONTENTS
:OF THE PROGRAM VIRTUAL SWITCH REGISTER
:AND GIVE THE USER A CHANCE TO
:MODIFY IT.
```

```
IS: MOV 0#FSPUR,0#FVTECT :SAVE RETURN ADDRESS.
MOV 0#CSPUR,0#ERRVTECT :RESET THE STACK POINTER.
MOV 0#PTWC,0#ID :CLEAR THE FPS.
MOV (SP),R0
MOV 0#STACK,SP
CLR R4
LDFPS R4
JMP (R0) :RETURN.
```

.NLIST BEX

:SPECIAL MESSAGES:

```
POWERM: .ASCIZ <CRLF>'POWER FAILURE. PROGRAM RESTARTING.'<CRLF>
NULL: .BYTE 0
STAB: .ASCIZ <TAB>
SPACE: .ASCIZ
LFIEX1: .ASCIZ <CRLF>'PC OF LAST FPP INSTRUCTION EXECUTED: '<TAB>
LFIEX2: .ASCIZ <CRLF>'LAST FPP INSTRUCTION EXECUTED: '<TAB>
FPSMS: .ASCIZ <CRLF>'FLOATING POINT STATUS REGISTER:
FECHS: .ASCIZ <CRLF>'FEC:
STHE: .ASCIZ 'THE '
NOOP1: .ASCIZ <TAB>' INSTRUCTION FAILED.'<CRLF>
NOOP15: .ASCII 'EITHER A BAD CONSTANT WAS GENERATED OR '
:MICROPROGRAM FLOW WENT '
NOOP2: .ASCIZ <CRLF>'FROM STATE '
NOOP3: .ASCIZ 'TO STATE '
NOOP4: .ASCIZ <CRLF>'INSTEAD OF '
```

050200 053517 051105 047440 052123 040517 035103 051516 051105 047522 020115 052123 052123 047111

... ..

... ..

... ..

... ..

... ..

... ..

... ..

... ..

... ..

... ..

... ..

```

(ASCII) *ACS=
(SET)
(CLEAR)
(LOADED DATA:
(READ DATA:
(EXPECTED DATA:
(DATA IN (RD) FSR0:
(DATA IN ACC:
(GOT RESULT:
(EXPECTED RESULT:

```

ERROR MESSAGES:

```

EM10=EM5
EM11:
EM12:
EM13:
EM14:
EM15:
EM16:
EM17:
EM18:
EM19:
EM20:
EM21:
EM22:
EM23=EM22
EM24=EM22
EM25:
EM26:
EM27=EM26
EM28:
EM31:

```

LDFFS AND STFFS TEST FAILED.
LDFFS AND STFFS TEST ERROR SUMMARY.
CFCC TRANSFERRED BAD DATA TO THE CPU.
CFCC MODIFIED THE FPS REGISTER.
UNEXPECTED FPP TRAP TO 244.
UNEXPECTED CPU TRAP TO 4.
UNEXPECTED CPU TRAP TO 10.
UNABLE TO DECODE FPP INSTRUCTION. TRAPPED TO 10.
LDFFS RD FAILED IN THE FSR0 FLOWS.
TRAPPED TO 4.
CRLF) DID NOT GO FROM STATE 400 TO 670.
STFFS R1 FAILED IN THE FOST FLOWS.
TRAPPED TO 4.
CRLF) DID NOT GO FROM STATE 634 TO 710.
AN ILLEGAL FPP INSTRUCTION DID NOT TRAP.
AN ILLEGAL FPP INSTRUCTION
*CRLF) TRAPPED TO 244, BUT FAILED TO SET *
*THE FPS CORRECTLY. (CRLF) EITHER A BAD CONSTANT *
WAS GENERATED OR THE ALU LOGICAL OR FUNCTION FAILED.
AN ILLEGAL FPP INSTRUCTION
*TRAPPED TO 244, BUT A SUBSEQUENT *
STST (CRLF)
FAILED TO PICK UP THE CORRECT FEC CODE = 2.
STST R4 FAILED IN THE DESTINATION FLOWS.
TRAPPED TO 4. (CRLF)
DID NOT GO FROM STATE 636 TO 710.
AN ILLEGAL FPP INSTRUCTION.
WITH INTERRUPTS DISABLED.
SOURCE LOCATIONS MODIFIED BY LDD.
CRLF) A DATO WAS PERFORMED INSTEAD OF A DATI.
LDD (RD) ACC FAILED. (CRLF)
RD WAS MODIFIED.
*THE PC WAS BAD AFTER *
AN FPP INSTRUCTION.
STD ACC, (RD) FAILED. (CRLF)
RD WAS MODIFIED.

TIME	TIME	TIME	EM#	MESSAGE
000000	000000	000000	EM33:	.ASCII *STD ACC, (R0) FAILED. <CRLF>
000000	000000	000000	EM33:	.ASCIZ *OUTPUT BAD.*
000000	000000	000000	EM34:	.ASCII *STD ACC, (R0) FAILED IN THE FDST FLOWS.*
000000	000000	000000	EM34:	.ASCIZ <CRLF> *THE (BUT GR7) FORK FAILED.*
000000	000000	000000	EM35:	.ASCII *LDD (R0) ACC FAILED IN THE FSRC FLOWS.*
000000	000000	000000	EM35:	.ASCIZ <CRLF> *THE (BUT GR7) FORK FAILED.*
000000	000000	000000	EM36:	.ASCII *STD ACC, (R0) FAILED IN THE FDST FLOWS.*
000000	000000	000000	EM36:	.ASCIZ <CRLF> *THE (BUT FD) FORK FAILED.*
000000	000000	000000	EM37:	.ASCII *LDD (R0) ACC FAILED IN THE FSRC FLOWS.*
000000	000000	000000	EM37:	.ASCIZ <CRLF> *THE (BUT FD) FORK FAILED.*
000000	000000	000000	EM40:	.ASCII *LDD (R0) ACC OR THE STD ACC, (R0) FAILED.*
000000	000000	000000	EM40:	.ASCIZ <CRLF> *BAD DATA WAS DETECTED AFTER A SEQUENCE OF THE TWO INSTRUCTIONS.*
000000	000000	000000	EM41:	.ASCIZ *FPS BAD AFTER EXECUTION OF: *
000000	000000	000000	EM42:	.ASCII *LDD (R0) ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM42:	.ASCIZ *THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.*
000000	000000	000000	EM43:	.ASCII *STD ACC, (R0) FAILED IN THE FDST FLOWS. <CRLF>
000000	000000	000000	EM43:	.ASCIZ *THE (BUT FDST) FORK FAILED. TRAPPED TO 4.*
000000	000000	000000	EM44:	.ASCIZ *FPP ACCUMULATORS DATA TEST FAILED.*
000000	000000	000000	EM45-EM44	.ASCIZ *FPP ACCUMULATORS DUAL ADDRESSING TEST FAILED.*
000000	000000	000000	EM46-EM44	.ASCIZ *FPP ACCUMULATORS DUAL ADDRESSING TEST FAILED.*
000000	000000	000000	EM47:	.ASCII *LD AC1, ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM47:	.ASCIZ *THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.*
000000	000000	000000	EM50:	.ASCII *LD AC1, ACC FAILED IN THE FSRC FLOWS.*
000000	000000	000000	EM50:	.ASCIZ *THE (BUT FD) FORK FAILED.*
000000	000000	000000	EM51:	.ASCIZ *LD AC1, ACC TRANSFERRED BAD DATA.*
000000	000000	000000	EM52:	.ASCII *LDD (R0)+, ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM52:	.ASCIZ *THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.*
000000	000000	000000	EM53:	.ASCII *LDD (R0)+, ACC FAILED IN THE FSRC FLOWS.*
000000	000000	000000	EM53:	.ASCIZ <CRLF> *R0 WAS BAD. <CRLF>
000000	000000	000000	EM53:	.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR <CRLF>
000000	000000	000000	EM53:	.ASCIZ *DID NOT GO FROM STATE 627 TO 322. \
000000	000000	000000	EM54:	.ASCIZ *LDD (R0)+, ACC TRANSFERRED BAD DATA. <CRLF>
000000	000000	000000	EM55:	.ASCII *LDD -(R0) ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM55:	.ASCIZ *THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.*
000000	000000	000000	EM56:	.ASCII *LDD -(R0) ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM56:	.ASCIZ <CRLF> *R0 WAS BAD. <CRLF>
000000	000000	000000	EM56:	.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR <CRLF>
000000	000000	000000	EM56:	.ASCIZ *DID NOT GO FROM STATE 627 TO 324. \
000000	000000	000000	EM57:	.ASCIZ *LDD -(R0) ACC TRANSFERRED BAD DATA. <CRLF>
000000	000000	000000	EM60:	.ASCII *LDF (R0)+, ACC FAILED IN THE FSRC FLOWS. <CRLF>
000000	000000	000000	EM60:	.ASCIZ <CRLF> *R0 WAS BAD. <CRLF>
000000	000000	000000	EM60:	.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR <CRLF>
000000	000000	000000	EM60:	.ASCIZ *DID NOT GO FROM STATE 627 TO 322. \
000000	000000	000000	EM61:	.ASCIZ *LDF (R0)+, ACC TRANSFERRED BAD DATA. <CRLF>

00000000	00000000	00000000	EM62:	.ASCII	'LD (R0)+,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000		.ASCII	'WENT FROM STATE 441 TO 077.'
00000000	00000000	00000000		.ASCII	'INSTEAD OF FROM 441 TO 076.'
00000000	00000000	00000000	EM63:	.ASCII	'LDC #NUM,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT GR7) FORK FAILED.'
00000000	00000000	00000000		.ASCII	'WENT FROM STATE 207 TO 174.'
00000000	00000000	00000000		.ASCII	'INSTEAD OF FROM 207 TO 176.'
00000000	00000000	00000000	EM64:	.ASCII	'LDD #NUM,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'A BAC CONSTANT WAS USED WHEN THE PC WAS INCREMENTED.'
00000000	00000000	00000000	EM65=EM64		
00000000	00000000	00000000	EM66:	.ASCII	LDD #NUM,ACD TRANSFERRED BAD DATA./
00000000	00000000	00000000	EM67:	.ASCII	'LDD 2(R0)+,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000		.ASCII	<CRLF>'WENT FROM STATE 627 TO EITHER 326 OR 325.'
00000000	00000000	00000000		.ASCII	<CRLF>'INSTEAD OF FROM 627 TO 323.'
00000000	00000000	00000000	EM70:	.ASCII	'LDD 2(R0)+,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000	EM71:	.ASCII	'LDD 2(R0)+,ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000	EM72:	.ASCII	'LDD 2(R0)+,ACD<CRLF>
00000000	00000000	00000000		.ASCII	'FAILED TO INCREMENT PC BY 2.'
00000000	00000000	00000000	EM73:	.ASCII	'LDD 2(R0)+,ACD LOADED BAD DATA.'
00000000	00000000	00000000	EM74:	.ASCII	'LDD 2-(R0),ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000		.ASCII	<CRLF>'WENT FROM STATE 627 TO EITHER 326 OR 325.'
00000000	00000000	00000000		.ASCII	<CRLF>'INSTEAD OF FROM 627 TO 325.'
00000000	00000000	00000000	EM75:	.ASCII	'LDD 2-(R0),ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000	EM76:	.ASCII	'LDD 2-(R0),ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000	EM77:	.ASCII	'LDD 2-(R0),ACD<CRLF>
00000000	00000000	00000000		.ASCII	'FAILED TO DECREMENT PC BY 2.'
00000000	00000000	00000000	EM100:	.ASCII	'LDD 2-(R0),ACD LOADED BAD DATA.'
00000000	00000000	00000000	EM101:	.ASCII	'LDD NUM(R0),ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000	EM102:	.ASCII	'LDD NUM(R0),ACD<CRLF>
00000000	00000000	00000000		.ASCII	'FAILED TO AFFECT PC BY 2.'
00000000	00000000	00000000	EM103:	.ASCII	'LDD NUM(R0),ACD FAILED IN THE FSRC FLOWS.'
00000000	00000000	00000000		.ASCII	'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000	EM104:	.ASCII	'LDD NUM(R0),ACD LOADED BAD DATA.'

00000000	00000000	00000000	00000000	EM105:	.ASCII	'LDD ANUM(RO),ACD FAILED IN THE FSRG FLOWS.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'THE (BUT FSRG) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000	00000000	EM106:	.ASCII	'LDD ANUM(RO),ACD<CRLF>'
00000000	00000000	00000000	00000000		.ASCIZ	'FAILED TO AFFECT RO BY 2.'
00000000	00000000	00000000	00000000	EM107:	.ASCII	'LDD ANUM(RO),ACD FAILED IN THE FSRG FLOWS.'
00000000	00000000	00000000	00000000		.ASCIZ	'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000	00000000	EM110:	.ASCIZ	'LDD ANUM(RO),ACD LOADED BAD DATA.'
00000000	00000000	00000000	00000000	EM111:	.ASCII	'LDD AC7,ACD FAILED TO TRAP TO 244.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'AC7 IS AN ILLEGAL ACCUMULATOR.'
00000000	00000000	00000000	00000000	EM112=EM111		
00000000	00000000	00000000	00000000	EM113:	.ASCII	'LDD AC6,ACD FAILED TO TRAP TO 244.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'AC6 IS AN ILLEGAL ACCUMULATOR.'
00000000	00000000	00000000	00000000	EM114=EM113		
00000000	00000000	00000000	00000000	EM115=EM111		
00000000	00000000	00000000	00000000	EM116=EM113		
00000000	00000000	00000000	00000000	EM117:	.ASCII	'USE OF AN ILLEGAL ACCUMULATOR WITH FSRG MODE ZERO.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'TRAPPED BUT FAILED TO SET FPS CORRECTLY.'
00000000	00000000	00000000	00000000	EM120:	.ASCII	'USE OF AN ILLEGAL ACCUMULATOR WITH FSRG MODE ZERO.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'TRAPPED BUT FAILED TO SET FPS CORRECTLY.'
00000000	00000000	00000000	00000000	EM121:	.ASCII	'ST ACD,AC1 FAILED IN THE FOST FLOWS.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'THE (BUT FOST) FORK FAILED. TRAPPED TO 4.'
00000000	00000000	00000000	00000000	EM122:	.ASCII	'ST ACD,AC1 FAILED IN THE FOST FLOWS.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'THE (BUT FD) FORK FAILED.'
00000000	00000000	00000000	00000000	EM123:	.ASCIZ	'ST ACD,AC1 TRANSFERRED BAD DATA.'
00000000	00000000	00000000	00000000	EM124:	.ASCII	'FPS BAD AFTER LDD (RO),ACD.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'THE (BUT EZBT Y8) FORK FAILED.'
00000000	00000000	00000000	00000000	EM125:	.ASCII	'FPS BAD AFTER LDD (RO),ACD.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'THE (BUT ENBT) FORK FAILED.'
00000000	00000000	00000000	00000000	EM126:	.ASCII	'LDD (RO),ACD TRAPPED TO 244.'
00000000	00000000	00000000	00000000		.ASCII	'FSRC= -0 AND FIUV= 0.'<CRLF>
00000000	00000000	00000000	00000000		.ASCII	'THE (BUT FIUV) FORK FAILED.'
00000000	00000000	00000000	00000000		.ASCII	<CRLF>'WENT FROM STATE 256 TO 354.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'INSTEAD OF FROM 256 TO 254.'
00000000	00000000	00000000	00000000	EM127:	.ASCII	'LDD (RO),ACD FAILED TO TRAP TO 244.'
00000000	00000000	00000000	00000000		.ASCII	'FSRC= -0, FIUV= 1.'
00000000	00000000	00000000	00000000		.ASCII	<CRLF>'THE (BUT FIUV) FORK FAILED.'<CRLF>
00000000	00000000	00000000	00000000		.ASCII	'WENT FROM STATE 256 TO 254.'
00000000	00000000	00000000	00000000		.ASCIZ	<CRLF>'INSTEAD OF FROM 256 THE 354.'
00000000	00000000	00000000	00000000	EM130:	.ASCII	'LDD (RO),ACD TRAPPED TO 244.'
00000000	00000000	00000000	00000000		.ASCII	'FSRC= -0, FIUV= 1.'<CRLF>
00000000	00000000	00000000	00000000		.ASCIZ	'BUT FEC WAS BAD.'
00000000	00000000	00000000	00000000	EM131:	.ASCIZ	'LDCDF (RO),ACD LOADED BAD DATA.'
00000000	00000000	00000000	00000000	EM132:	.ASCIZ	'LDCDF (RO),ACD LOADED BAD DATA.'
00000000	00000000	00000000	00000000	EM133:	.ASCIZ	'LDCDF (RO),ACD LOADED BAD DATA.'

020104
024040
020104
024040

101 042104 020104
042101 043104 024040
123 041125 020104
043102 024040

.ASCIZ ADDD (RO),ACD WITH (RO)=ACD=C
EM134:
.ASCIZ /ADDF (RO),ACD WITH (RO)=ACD=C
EM135:
.ASCIZ SUBC (RO),ACD WITH (RO)=ACD=C
EM136:
.ASCIZ SUBF (RO),ACD WITH (RO)=ACD=C

EM137-EM139
EM140-EM142
EM141-EM143
EM143-EM145
EM143:

020104
024040

101 042104 020104
042102 024040

.ASCIZ /ADDD (RO),ACD WITH (RO)=0
EM144:
.ASCIZ /SUBD (RO),ACD WITH (RO)=0
EM145-EM147
EM146-EM148
EM147:
.ASCIZ /SUBC (RO),ACD WITH ACD=C
EM150-EM152
EM151-EM153
EM152:

EM145-EM147
EM146-EM148
EM147:

020104
024040

123 041125 020104
041127 024040

.ASCIZ /ADDD (RO),ACD WITH ACD=C /
EM153-EM155
EM154:
.ASCII /AN OVERFLOW ERROR OCCURRED ON ADD* <CRLF>
.ASCII /CAUSING A TRAP TO 244.
.ASCII <CRLF>*(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER UNDER FAILED.*
.ASCIZ <CRLF>*SHOULD HAVE GONE FROM STATE 420 TO 131.*
EM155:
.ASCII /AN UNDERFLOW ERROR OCCURRED ON ADD* <CRLF>
.ASCII /CAUSING A TRAP TO 244.
.ASCII <CRLF>*(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER UNDER FAILED.*
.ASCIZ <CRLF>*SHOULD HAVE GONE FROM STATE 420 TO 131.*
EM156:
.ASCII /ADDD (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..
.ASCII <CRLF>*THE (BUT FD) FORK FAILED. WENT
.ASCII \FROM STATE 665 TO 113.\<CRLF>
.ASCIZ \INSTEAD OF FROM 665 TO 313.\<CRLF>\WITH FT SET.\

020104
024040

101 042104 020104
042101 020116 053117
042103 052501 044623
200 041050 052125
051600 047910 046125

.ASCII /AN UNDERFLOW ERROR OCCURRED ON ADD* <CRLF>
.ASCII /CAUSING A TRAP TO 244.
.ASCII <CRLF>*(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER UNDER FAILED.*
.ASCIZ <CRLF>*SHOULD HAVE GONE FROM STATE 420 TO 131.*
EM155:
.ASCII /AN UNDERFLOW ERROR OCCURRED ON ADD* <CRLF>
.ASCII /CAUSING A TRAP TO 244.
.ASCII <CRLF>*(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER UNDER FAILED.*
.ASCIZ <CRLF>*SHOULD HAVE GONE FROM STATE 420 TO 131.*
EM156:
.ASCII /ADDD (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..
.ASCII <CRLF>*THE (BUT FD) FORK FAILED. WENT
.ASCII \FROM STATE 665 TO 113.\<CRLF>
.ASCIZ \INSTEAD OF FROM 665 TO 313.\<CRLF>\WITH FT CLEAR.\

042101
042103
047910

042101 042104 024040
052200 042510 024040
106 047922 020115
047111 052123 040505

.ASCII /ADDD (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..
.ASCII <CRLF>*THE (BUT FD) FORK FAILED. WENT
.ASCII \FROM STATE 665 TO 313.\<CRLF>
.ASCIZ \INSTEAD OF FROM 665 TO 113.\<CRLF>\WITH FT SET.\

042101
042103
047910

101 042104 020104
200 044124 020105
051106 046517 051440
111 051516 042524

.ASCII /ADDD (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..
.ASCII <CRLF>*THE (BUT FD) FORK FAILED. WENT
.ASCII \FROM STATE 665 TO 313.\<CRLF>
.ASCIZ \INSTEAD OF FROM 665 TO 113.\<CRLF>\WITH FT CLEAR.\

042101
042103
047910

042101 042104 024040
124 042510 043040
111 020116 044124

.ASCII /ADDD (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..
.ASCII <CRLF>*THE (BUT FD) FORK FAILED. WENT
.ASCII \FROM STATE 665 TO 313.\<CRLF>
.ASCIZ \INSTEAD OF FROM 665 TO 113.\<CRLF>\WITH FT CLEAR.\

042101
042103
047910

101 042104 020106
044124 020105 047504
047111 052040 042510

.ASCII /ADDF (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..<CRLF>
.ASCII /THE DOUBLE CONSTANT WAS USED INSTEAD OF THE FLOATING CONSTANT* <CRLF>
.ASCIZ /IN THE ROUND ALGORITHM.*
EM161:
.ASCII /ADDF (RO),ACD FAILED IN THE ROUND\TRUNK FLOWS..<CRLF>
.ASCII /THE DOUBLE CONSTANT WAS USED INSTEAD OF THE FLOATING CONSTANT* <CRLF>
.ASCIZ /IN THE ROUND ALGORITHM.*
EM162:
.ASCIZ /ADDD (RO),ACD PRODUCED A BAD RESULT./

042101
042103
047910

042101 042104 024040
101 042104 020106

.ASCII /ADDF (RO),ACD PRODUCED A BAD RESULT./
EM163:
.ASCIZ /ADDF (RO),ACD PRODUCED A BAD RESULT./

042101
042103
047910

042101 042104 020106

.ASCIZ /ADDF (RO),ACD PRODUCED A BAD RESULT./

042101
042103
047910

101 042104 020106

.ASCIZ /ADDF (RO),ACD PRODUCED A BAD RESULT./

042101
042103
047910

101 042104 020106

.ASCIZ /ADDF (RO),ACD PRODUCED A BAD RESULT./

060000	044124	020106	050106	EM164:	.ASCIZ	\THE FPS WAS BAD AFTER ADDC (RO),ACC.
060001	124	042510	043040	EM165:	.ASCIZ	\THE FPS WAS BAD AFTER ADDF (RO),ACC.
060002	042101	042104	024040	EM166:	.ASCII	\ADDC (RO),ACC PRODUCED A BAD RESULT. \<<CRLF>
060003	120	047522	040502		.ASCIZ	\PROBABLE ERROR IN THE ALIGN FLOWS.
060004	042101	042104	024040	EM167:	.ASCII	\ADDC (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060005	106	047514	020127		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 111, TO 014.
060006	200	020101	040502		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 57 DEC) \
060007	040527	020123	051525		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060008	101	042104	020106	EM170:	.ASCII	\ADDF (RO),ACC PRODUCED A BAD RESULT. \<<CRLF>
060009	051120	041117	041101		.ASCIZ	\PROBABLE ERROR IN THE ALIGN FLOWS.
060010	101	042104	020106	EM171:	.ASCII	\ADDF (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060011	046106	053517	042040		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 111, TO 014.
060012	040600	041040	042101		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 25 DEC) \
060013	127	051501	052440		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060014	042101	042104	024040	EM172:	.ASCII	\ADDC (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060015	106	047514	020127		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 111, TO 015.
060016	200	020101	040502		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 57 DEC) \
060017	040527	020123	051525		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060018	101	042104	020104	EM173:	.ASCII	\ADDC (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060019	046106	053517	042040		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 215.
060020	040600	041040	042101		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 57 DEC) \
060021	127	051501	052440		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060022	042101	043104	024040	EM174:	.ASCII	\ADDF (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060023	106	047514	020127		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 015.
060024	200	020101	040502		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 25 DEC) \
060025	040527	020123	051525		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060026	101	042104	020106	EM175:	.ASCII	\ADDF (RO),ACC FAILED IN THE ALIGN FLOWS. \<<CRLF>
060027	046106	053517	042040		.ASCII	\FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 215.
060028	040600	041040	042101		.ASCII	\<<CRLF>\A BAD CONSTANT (NOT 57 DEC) \
060029	127	051501	052440		.ASCIZ	\WAS USED IN THE ALIGN ALGORITHM.
060030	042101	042104	024040	EM176:	.ASCII	\ADDC (RO),ACC FAILED IN THE ADD-SUB FLOWS. \<<CRLF>
060031	104	042111	047040		.ASCIZ	\DID NOT TAKE THE PATH: STATE 216, TO 442, TO 500. \
060032	101	042104	020104	EM177:	.ASCII	\ADDC (RO),ACC FAILED IN THE ADD-SUB FLOWS. \<<CRLF>
060033	044504	020104	047516		.ASCIZ	\DID NOT TAKE THE PATH: STATE 216, TO 042, TO 121. \
060034	042101	042104	024040	EM200:	.ASCII	\ADDC (RO),ACC FAILED IN THE ADD-SUB FLOWS. \<<CRLF>
060035	104	042111	047040		.ASCIZ	\DID NOT TAKE THE PATH: STATE 216, TO 440, TO 121. \
060036	101	042104	020104	EM201:	.ASCII	\ADDC (RO),ACC FAILED IN THE ADD-SUB FLOWS. \<<CRLF>
060037	044504	020104	047516		.ASCIZ	\DID NOT TAKE THE PATH: STATE 216, TO 440, TO 101. \
060038	042101	042104	024040	EM202:	.ASCII	\ADDC (RO),ACC FAILED IN THE ADD-SUB FLOWS. \<<CRLF>
060039	104	042111	047040		.ASCIZ	\DID NOT TAKE THE PATH: STATE 216, TO 042, TO 101. \
060040				EM203:		


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042104 020104 .ASCII 'ADD (RO),ACD FAILED IN THE ACC-SUB FLOWS.'(CR,F.
042104 047516 .ASCIZ 'DID NOT TAKE THE PATH: STATE 216, TO 440, TO 141.
EM204:
042104 024040 .ASCII 'ADD (RO),ACD FAILED IN THE ACC-SUB FLOWS.'(CR,F.
042111 047540 .ASCIZ 'DID NOT TAKE THE PATH: STATE 216, TO 042, TO 141.
EM205:
042510 042040 .ASCIZ 'THE FPS WAS BAD AFTER SUBD (RO),ACD.'
EM206:
052523 042102 024040 .ASCIZ 'SUBD (RO),ACD PRODUCED A BAD RESULT.'
041125 020104 EM207: .ASCII 'SUBD (RO),ACD PRODUCED A BAD RESULT.'
044124 020105 .ASCIZ '(CR,F)'THE XOR OF THE SIGN BIT FAILED IN STATE 024.'
042104 020104 EM210: .ASCIZ 'ADD (RO),ACD FAILED IN THE NORMALIZE FLOWS.'
EM211=EM5
EM212=EM6
EM213=EM7

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:DATA HEADERS

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063250 020040 042524 052123 DH1: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
063310 053411 047522 042524 .ASCIZ (TAB)'WROTE.'(TAB)'READ.'(TAB)'EXPECTED.'
063340 020040 042524 052123 DH2: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
063400 047001 020104 040502 .ASCIZ 'AND BAD DATA.'(TAB)'OR BAD DATA.'
063433 040 052040 051505 DH3: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
063473 001 042522 042101 .ASCIZ (TAB)'READ PSW.'(TAB)'EXPECTED PSW.'
063524 020040 042524 052123 DH4: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
063564 053411 047522 042524 .ASCIZ (TAB)'WROTE FPS.'(TAB)'FPS AFTER CFCC.'
063620 020040 042524 052123 DH5: .ASCIZ ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF TRAP.'
DH6=DH5
DH7=DH5
DH10=DH5
DH11=DH5
DH12=0
DH13=0
DH14=DH5
DH15=DH5
063660 020040 042524 052123 DH16: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
063720 047411 020120 047503 .ASCIZ (TAB)'OP CODE.' FPS.'
063740 020040 042524 052123 DH17: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
064000 043411 052117 043040 .ASCIZ (TAB)'GOT FPS.'(TAB)'EXPECTED FPS.'
064030 020040 042524 052123 DH20: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF TRAP.'
064067 011 041520 047440 .ASCIZ (TAB)'PC OF STST.'(TAB)'READ FEC.'
DH21=DH5
064116 040506 046111 042105 DH22: .ASCIZ 'FAILED TO CORRECTLY SET FPS.'
064153 006 044501 042514 DH23: .ASCII 'FAILED TO CORRECTLY SET FEC TO 000002.'(CR,F)
064222 020040 042524 052123 .ASCIZ ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
064262 050011 020103 043117 .ASCIZ (TAB)'PC OF STST.'(TAB)'READ FEC.'
064311 124 040522 050120 DH24: .ASCII 'TRAPPED TO 244. FLOW WENT FROM STATE 554 TO STATE 430.'
064377 200 047111 052123 .ASCIZ (CR,F)'INSTEAD OF FROM STATE 554 TO STATE 432.'
064450 020040 042524 052123 DH25: .ASCIZ ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'(TAB)
064512 020040 042524 052123 DH26: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
064552 043411 052117 051040 .ASCIZ (TAB)'GOT RO.'(TAB)'EXPECTED RO.'
DH27=DH26
DH30=0
DH31=DH26
DH32=DH26
064600 020040 042524 052123 DH33: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'

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064540	051011 064600 064600 064600 064600 000000	020050	052050	.ASCIZ	'(TAB)'RO (TARGET LOCATIONS FOR CLPUT).'
				DM34=DM33 DM35=DM33 DM36=DM33 DM37=DM33 DM40=DM33 DM41=0	
064703 064742	04C 051011 064703 000000	05204C 020060	051505 052050	DM42: .ASCII .ASCIZ	' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF TRAP.' '(TAB)'RO (TARGET LOCATIONS FOR OUTPUT).'
				DM43=DM42 DM44=0	
065005 065024	105 020040 065005	051122 042524	051117 052123	DM45: .ASCIZ DM46: .ASCIZ	'ERROR SUMMARY.' ' TEST.'(TAB)'CALL AT PC.'
				DM47=DM45	
065050 065110	020040 053411 065050 063620 064512	042524 052111	052123 020110	DM50: .ASCII .ASCIZ	' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.' '(TAB)'WITH FD.'
				DM51=DM50 DM52=DM5 DM53=DM26	
065122	020040 063620 064512 065122 064512 065122 065122 065122	042524	052123	DM54: .ASCIZ	' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
				DM55=DM5 DM56=DM26 DM57=DM54 DM60=DM26 DM61=DM54 DM62=DM54 DM63=DM54	
065163 065231 065232 065272	122 200 020040 043411 065122 063620 063620 064450 064512 065122 063620 063620 064450 064512 065122 063620 064512 064450 065122 063620 064512 064450 065122 064450	051505 042524 052117	046125 052123 050040	DM65: .ASCII .ASCII DM64: .ASCII .ASCIZ	'RESULTING IN AN ODD ADDRESS TRAP TO 4.' '(CRLF)' ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.' '(TAB)'GOT PC.'(TAB)'EXPECTED PC.'
				DM66=DM54 DM67=DM5 DM70=DM5 DM71=DM25 DM72=DM26 DM73=DM54 DM74=DM5 DM75=DM5 DM76=DM25 DM77=DM26 DM100=DM54 DM101=DM5 DM102=DM26 DM103=DM25 DM104=DM54 DM105=DM5 DM106=DM26 DM107=DM25 DM110=DM54 DM111=DM25	
065320 065354 065425 065476	044124 047503 200 020040 064450	020105 052116 047111 042524	041050 047522 052123 052123	DM112: .ASCII .ASCII .ASCII .ASCIZ	'THE (BUT FSRC) FORK FAILED.'(CRLF) 'CONTROL WENT FROM STATE 762 TO STATE 627.' '(CRLF)'INSTEAD OF FROM STATE 762 TO STATE 637.'(CRLF) ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
				DM113=DM25	

M12

065537	065320			DH114=DH112	
065640	041600	042510	024040	DH115: .ASCII	'THE (BUT FSRC) FORK FAILED RESULTING IN AN ODD ADDRESS TRAP TO -.'
065713	:11	047117	051124	.ASCII	<CR LF>'CONTROL WENT FROM STATE 762 TO STATE 627.'<CR LF>
065763	040	051516	042524	.ASCII	'INSTEAD OF FROM STATE 762 TO STATE 627.'<CR LF>
	065537	052040	051505	.ASCIZ	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF TRAP.'
	063740			DH116=DH115	
066023	040	052040	051505	DH117=DH17	
056063	011	047507	020124	DH120: .ASCII	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
	063620			.ASCIZ	<TAB>'GOT FEC.'<TAB>'EXPECTED FEC.'
	065050			DH121=DH5	
	065050			DH122=DH50	
	063740			DH123=DH50	
	063740			DH124=DH17	
	063620			DH125=DH17	
	065122			DH126=DH5	
	066023			DH127=DH54	
	065122			DH130=DH120	
	065122			DH131=DH54	
	065122			DH132=DH54	
066113	106	044501	042514	DH133: .ASCII	'FAILED TO PRODUCE THE CORRECT RESULTS.'<CR LF>
066162	020040	042524	052123	.ASCIZ	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
	066113			DH134=DH133	
	066113			DH135=DH133	
	066113			DH136=DH133	
066223	120	047522	052504	DH137: .ASCII	'PRODUCED THE CORRECT RESULT BUT FAILED TO SET THE FPS CORRECTLY.'
066323	040	052040	051505	.ASCII	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
066363	011	047507	020124	.ASCIZ	<TAB>'GOT FPS.'<TAB>'EXPECTED FPS.'
	066223			DH140=DH137	
	066223			DH141=DH137	
	066223			DH142=DH137	
	066113			DH143=DH133	
	066113			DH144=DH133	
	066223			DH145=DH137	
	066223			DH146=DH137	
	065122			DH147=DH54	
066413	130	051117	047440	DH150: .ASCII	'XOR OF SIGN BIT FAILED.'<CR LF>
066443	040	052040	051505	.ASCIZ	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
	066223			DH151=DH137	
	066113			DH152=DH133	
	066223			DH153=DH137	
066504	020040	042524	052123	DH154: .ASCIZ	'TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF TRAP.'
	066504			DH155=DH154	
	065122			DH156=DH54	
	065122			DH157=DH54	
	065122			DH160=DH54	
	065122			DH161=DH54	
	065122			DH162=DH54	
	065122			DH163=DH54	
	063740			DH164=DH17	
	063740			DH165=DH17	
	065122			DH166=DH54	
	065122			DH167=DH54	
	065122			DH170=DH54	
	065122			DH171=DH54	
	065122			DH172=DH54	
	065122			DH173=DH54	

065122				DH174=DH54
065122				DH175=DH54
065122				DH176=DH54
065122				DH177=DH54
065122				DH200=DH54
065122				DH201=DH54
065122				DH202=DH54
065122				DH203=DH54
065122				DH204=DH54
063740				DH205=DH17
065122				DH206=DH54
065122				DH207=DH54
065122				DH210=DH54
066544	020040	042524	052123	DH211: .ASCIZ ' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF TRAP.'<TAB>'FEC.'
063620				DH212=DH5
063620				DH213=DH5

;DATA FORMATS:

066611	004	000	005	DF1: .BYTE	4,0,5,0,5,0,0,0
066621	004	000	005	DF2: .BYTE	4,0,5,4,5,0,5,0
066631	004	000	005	DF3: .BYTE	4,0,5,0,5,0,5,0
066641	066631	000	005	DF4=DF3	
	004			DF5: .BYTE	4,0,5,0,5,0,5,11,5,0,5,0
	066641			DF6=DF5	
	066641			DF7=DF5	
	066641			DF10=DF5	
	066641			DF11=DF5	
066655	005	011	005	DF12: .BYTE	5,11,5,5,5,4,5,4,5,5,4,5,4,5,11,5,11,5,5,4,0,5,0,5,0,0
066707	005	011	005	DF13: .BYTE	5,11,5,5,5,4,0,5,0,5,0,0
	066641			DF14=DF6	
	066641			DF15=DF6	
066723	004	000	005	DF16: .BYTE	4,0,5,0,5,0,0
	066631			DF17=DF3	
066732	004	000	005	DF20: .BYTE	4,0,5,0,5,0,5,0
066742	004	000	005	DF21: .BYTE	4,0,5,0
066746	005	005	004	DF22: .BYTE	5,5,4,0,5,0,5,0,5,0
066760	004	000	005	DF23: .BYTE	4,0,5,0,5,0,5,0
066770	005	004	000	DF24: .BYTE	5,4,0,5,0,5,0
066777	004	000	005	DF25: .BYTE	4,0,5,0,5,0,5,0,5,0,5,0,5,0,5,0,5,0,5,0
067023	004	000	005	DF26: .BYTE	4,0,5,0,5,0,0,0,5,5,5,5,4,5,4,5,5,5,5,4,5,4
067050	004	000	005	DF27: .BYTE	4,0,5,0,5,0,0,0,5,5,5,5,4,5,4,5,5
067070	005	011	005	DF30: .BYTE	5,11,5,5,5,4,0,5,0,5,0,0
	067023			DF31=DF26	
	067050			DF32=DF27	
067104	004	000	005	DF33: .BYTE	4,0,5,0,5,0,5,5,5,0,5,0,5,12,5,5,5,0,5,0,5,12
067122	004	000	005	DF34: .BYTE	4,0,5,0,5,0,5,5,5,5,4,5,4,5,5,5,5,4,5,4
	067132			DF35=DF34	
	067132			DF36=DF34	
	067132			DF37=DF34	
067156	004	000	005	DF40: .BYTE	4,0,5,0,5,0,5,5,5,0,5,0,5,3,5,5,5,0,5,0,5,3
067204	011	005	005	DF41: .BYTE	11,5,5,5,4,0,5,0,5,0,5,0
067220	004	000	005	DF42: .BYTE	4,0,5,0,5,0,5,5,5,4,5,4,11,4,5,5,5,5,4,5,4
	067220			DF43=DF42	
067246	005	011	005	DF44: .BYTE	5,11,5,5,5,4,0,5,0,5,5,5,5,3,5,5,5,5,3

B13

RESET AND CONSOLE TEST ROUTINE

Address	Hex	Dec	Op	Instruction	Value
067400	0000	000	005	DF47: .BYTE	5, 5, 5, 5, 5, 4, 0, 5, 0, 5, 4, 5, 5, 5, 5, 5, 3, 5, 5, 5, 5, 5, 3
067404	0000	000	005	DF48: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067408	0000	000	005	DF49: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
06740C	0000	000	005	DF50: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067410	0000	000	005	DF51: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067414	0000	000	005	DF52: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067418	0000	000	005	DF53: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
06741C	0000	000	005	DF54: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067420	0000	000	005	DF55=DF47	
067424	0000	000	005	DF56=DF47	
067428	0000	000	005	DF57=DF54	
067430	0000	000	005	DF60=DF53	
067434	0000	000	005	DF61=DF54	
067438	0000	000	005	DF62=DF54	
06743C	0000	000	005	DF63=DF54	
067440	0000	000	005	DF64: .BYTE	4, 0, 5, 0, 5, 0, 0
067444	0000	000	005	DF65=DF64	
067448	0000	000	005	DF66=DF54	
067450	0000	000	005	DF67=DF21	
067454	0000	000	005	DF70: .BYTE	4, 0, 5, 0, 5, 5, 5, 5, 5, 4, 5, 4, 5, 5, 5, 5, 5, 4, 5, 4
067458	0000	000	005	DF71=DF70	
067460	0000	000	005	DF72: .BYTE	4, 0, 5, 0, 5, 0, 0
067464	0000	000	005	DF73=DF54	
067468	0000	000	005	DF74=DF21	
067470	0000	000	005	DF75=DF70	
067474	0000	000	005	DF76=DF70	
067478	0000	000	005	DF77=DF72	
067480	0000	000	005	DF100=DF54	
067484	0000	000	005	DF101=DF70	
067488	0000	000	005	DF102=DF72	
067490	0000	000	005	DF103=DF70	
067494	0000	000	005	DF104=DF54	
067498	0000	000	005	DF105=DF70	
067500	0000	000	005	DF106=DF72	
067504	0000	000	005	DF107=DF70	
067508	0000	000	005	DF110=DF54	
067510	0000	000	005	DF111: .BYTE	4, 0, 5, 0
067514	0000	000	005	DF112=DF111	
067518	0000	000	005	DF113=DF111	
067520	0000	000	005	DF114=DF111	
067524	0000	000	005	DF115=DF111	
067528	0000	000	005	DF116=DF111	
067530	0000	000	005	DF117=DF3	
067534	0000	000	005	DF120=DF3	
067538	0000	000	005	DF121=DF47	
067540	0000	000	005	DF122=DF50	
067544	0000	000	005	DF123=DF51	
067548	0000	000	005	DF124: .BYTE	4, 0, 5, 0, 5, 0, 0, 5, 5, 5, 5, 4, 5, 4, 5, 5, 5, 5, 4, 5, 4, 5, 5, 5, 3
067550	0000	000	005	DF125=DF124	
067554	0000	000	005	DF126=DF111	
067558	0000	000	005	DF127=DF111	
067560	0000	000	005	DF130=DF3	
067564	0000	000	005	DF131: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067568	0000	000	005	DF132=DF131	
067570	0000	000	005	DF133: .BYTE	4, 0, 5, 0, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3, 5, 5, 5, 3
067574	0000	000	005	DF134=DF133	

067615	000	005	DF135=DF133 DF136=DF133 DF137: .BYTE 4.0.5.0.5.0.5.0
067625	000	005	DF140=DF137 DF141=DF137 DF142=DF137 DF143=DF133 DF144=DF133 DF145=DF137 DF146=DF137 DF147=DF133 DF150=DF133 DF151=DF137 DF152=DF133 DF153=DF137 DF154: .BYTE 4.0.5.0
067631	000	005	DF155=DF154 DF156=DF133 DF157=DF133 DF160=DF133 DF161: .BYTE 4.0.5.0.5.5.5.2.5.5.5.2.5.5.5.2.5.5.5.2
067655	004	005	DF162=DF133 DF163=DF161 DF164=DF3 DF165=DF3 DF166=DF133 DF167=DF133 DF170=DF161 DF171=DF161 DF172=DF133 DF173=DF133 DF174=DF161 DF175=DF161 DF176=DF133 DF177=DF133 DF200=DF133 DF201=DF133 DF202=DF133 DF203=DF133 DF204=DF133 DF205=DF3 DF206=DF133 DF207=DF133 DF210=DF133 DF211: .BYTE 4.0.5.0.5.0
067655	000	005	DF212=DF211 DF213=DF211

067664 .EVEN
:DATA TABLES:

067664	001232	001234	040412	DT1: .WORD	\$TMP0,\$TMP1,\$TAB,\$TMP2,\$TAB,\$TMP3
067700	001242	001244	000000	.WORD	\$TMP4,\$TMP5,0
067706	001232	001234	040412	DT2: .WORD	\$TMP0,\$TMP1,\$TAB,AEFLG,\$TAB,\$TMP2,\$TAB,\$TMP3,0
067730	001232	001234	040412	DT3: .WORD	\$TMP0,\$TMP1,\$TAB,\$TMP2,\$TAB,\$TMP3
067744	040412	001242	000000	.WORD	\$TAB,\$TMP4,0

070000	000000	000000	000000	DT14=DT3	.WORD	STMP0,STMP1,STAB,STMP2,LFIE/1,STMP21,LFIE/2
070001	000000	000000	000000	DT5=DT6	.WORD	STMP20,FPMS,STMP3,FECHS,STMP4,0
070002	000000	000000	000000	DT6=DT6	.WORD	STMP0,STMP1,STAB,STMP2,LFIE/1,STMP21,LFIE/2,STMP20,C
070003	000000	000000	000000	DT7=DT6		
070004	000000	000000	000000	DT10=DT6		
070005	000000	000000	000000	DT11=DT6		
070006	000000	000000	000000	DT12=DT6	.WORD	STHE,STMP10,NOOP1,NOOP15,NOOP2,STMP5
070007	000000	000000	000000		.WORD	NOOP3,STMP6,NOOP4,NOOP2,STMP5,NOOP3,STMP7,NOOP5,STMP11
070008	000000	000000	000000		.WORD	NOOP6,STMP10,NOOP7,NOOP10,STMP0,STMP1,STAB,STMP2
070009	000000	000000	000000		.WORD	STAB,STMP3,STMP4,0
070010	000000	000000	000000	DT13=DT6	.WORD	STHE,STMP10,NOOP1,NOOP11,NOOP10,STMP0,STMP1,STAB
070011	000000	000000	000000		.WORD	STMP2,STAB,STMP3,STMP4,0
070012	000000	000000	000000	DT14=DT6		
070013	000000	000000	000000	DT15=DT6		
070014	000000	000000	000000	DT16=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP5,STMP3,0
070015	000000	000000	000000	DT17=DT3		
070016	000000	000000	000000	DT20=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070017	000000	000000	000000		.WORD	STAB,STMP4,0
070018	000000	000000	000000	DT21=DT6	.WORD	STMP0,STMP1,STAB,STMP2,C
070019	000000	000000	000000	DT22=DT6	.WORD	DH3,SCRLF
070020	000000	000000	000000		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070021	000000	000000	000000		.WORD	STAB,STMP4,0
070022	000000	000000	000000	DT23=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070023	000000	000000	000000		.WORD	STAB,STMP4,0
070024	000000	000000	000000	DT24=DT6	.WORD	ILLMS
070025	000000	000000	000000		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070026	000000	000000	000000		.WORD	STAB,STMP4,0
070027	000000	000000	000000	DT25=DT6	.WORD	STMP0,STMP1,STAB,SCRLF,MS1,MS3,STMP3,MS4,STMP4,SCRLF
070028	000000	000000	000000		.WORD	STMP4,SCRLF,MS2,MS3,STMP5,MS4,STMP6,SCRLF,STMP5,C
070029	000000	000000	000000	DT26=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3,STMP4,SCRLF
070030	000000	000000	000000		.WORD	MS6,SCRLF,MS7,STMP5,MS10,STMP6,SCRLF
070031	000000	000000	000000		.WORD	MS11,SCRLF,MS7,STMP5,MS10,STMP7,0
070032	000000	000000	000000	DT27=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070033	000000	000000	000000		.WORD	STMP4,SCRLF,MS12,SCRLF,MS7,STMP5,MS10,STMP7,SCRLF,MS13,C
070034	000000	000000	000000	DT30=DT6	.WORD	MS15,STMP20,SCRLF,MS14,SCRLF
070035	000000	000000	000000		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070036	000000	000000	000000		.WORD	STMP4,0
070037	000000	000000	000000	DT31=DT26		
070038	000000	000000	000000	DT32=DT27		
070039	000000	000000	000000	DT33=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070040	000000	000000	000000		.WORD	SCRLF,MS1,MS3,STMP4,MS4,STMP5,SCRLF,STMP6,SCRLF
070041	000000	000000	000000		.WORD	MS2,MS3,STMP4,MS4,STMP5,SCRLF,STMP4,0
070042	000000	000000	000000	DT34=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070043	000000	000000	000000		.WORD	SCRLF,MS6,SCRLF,MS7,STMP5,MS10,STMP6,SCRLF
070044	000000	000000	000000		.WORD	MS11,SCRLF,MS7,STMP5,MS10,STMP7,0
070045	000000	000000	000000	DT35=DT34		
070046	000000	000000	000000	DT36=DT34		
070047	000000	000000	000000	DT37=DT34		
070048	000000	000000	000000	DT40=DT33		
070049	000000	000000	000000	DT41=DT6	.WORD	STMP20,SCRLF,DH3,SCRLF
070050	000000	000000	000000		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070051	000000	000000	000000		.WORD	STAB,STMP4,0
070052	000000	000000	000000	DT42=DT6	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3
070053	000000	000000	000000		.WORD	SCRLF,MS6,SCRLF,MS7,STMP5,MS10,STMP6,STMP15,STMP10
070054	000000	000000	000000		.WORD	SCRLF,MS11,SCRLF,MS7,STMP5,MS10,STMP7,C

071313	001234	040412	DT43=DT42	
071314	001234	042314	DT44: .WORD	MS17, STMP5, SCRLF, MS20, SCRLF, STMP0, STMP1, STAB, STMP2
071313	001234	042314	DT45: .WORD	SCRLF, MS1, MS21, SCRLF, STMP3, SCRLF, MS2, MS21, SCRLF, STMP4, MS21, SCRLF, STMP5, MS21, SCRLF, STMP6, SCRLF, MS22, MS21, SCRLF, STMP3, SCRLF
071313	001234	042314	DT46: .WORD	MS23, MS21, SCRLF, STMP4, 0
071313	001234	042314	DT46: .WORD	STMP0, STMP1, SCRLF, MS25, MS30, STMP2, MS31, STMP3
071313	001234	042314	DT46: .WORD	MS32, STMP4, MS33, STMP5, MS34, STMP6, SCRLF, MS26
071313	001234	042314	DT46: .WORD	MS30, STMP7, MS31, STMP10
071313	001234	042314	DT46: .WORD	MS32, STMP11, MS33, STMP12, MS34, STMP13, 0
071313	001234	042026	DT47: .WORD	STMP0, STMP1, STAB, STMP2, SCRLF, MS12, MS7, STMP3, MS10
071313	001234	042026	DT47: .WORD	STMP4, SCRLF, MS13, 0
071313	001234	040412	DT50=DT34	
071313	042525	042564	DT51: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
071313	042525	042564	DT51: .WORD	SCRLF, MS25, MS27, STMP4, SCRLF, MS26, MS27, STMP5, 0
071313	001234	040412	DT52=DT47	
071313	000000	040412	DT53: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
071313	001234	040412	DT53: .WORD	STMP4, 0
071313	041566	042314	DT54: .WORD	STMP0, STMP1, STAB, STMP2, SCRLF, MS1, MS21, SCRLF, STMP3
071313	041566	042314	DT54: .WORD	SCRLF, MS2, MS21, SCRLF, STMP3, 0
071320			DT55=DT47	
071320			DT56=DT53	
071320			DT57=DT54	
071320			DT60=DT53	
071320			DT61=DT54	
071320			DT62=DT54	
071320			DT63=DT54	
071356	001234	040412	DT64: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
071356	000000	040412	DT64: .WORD	STMP4, 0
071356			DT65=DT64	
071320			DT66=DT54	
070210			DT67=DT21	
071376	001234	040412	DT70: .WORD	STMP0, STMP1, STAB, STMP2, SCRLF, MS6, SCRLF, MS7, STMP5
071420	001246	001313	DT70: .WORD	MS10, STMP6, SCRLF, MS11, SCRLF, MS7, STMP5, MS10, STMP7, 0
071444	001234	040412	DT71=DT70	
071320			DT72: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3, STMP4, 0
070210			DT73=DT54	
071376			DT74=DT21	
071376			DT75=DT70	
071376			DT76=DT70	
071444			DT77=DT72	
071320			DT100=DT54	
071376			DT101=DT70	
071376			DT102=DT71	
071376			DT103=DT70	
071320			DT104=DT54	
071376			DT105=DT70	
071444			DT106=DT72	
071376			DT107=DT70	
071320			DT110=DT54	
071464	001234	040412	DT111: .WORD	STMP0, STMP1, STAB, STMP2, 0
071464			DT112=DT111	
071464			DT113=DT111	
071464			DT114=DT111	
071464			DT115=DT111	
071464			DT116=DT111	

07117	001234	040412	DT117=DT3	
07118			DT118=DT3	
07119			DT119=DT3	
07120			DT120=DT47	
07121			DT121=DT44	
07122			DT122=DT51	
07123			DT123=DT51	
07124			DT124=DT51	
07125			DT125=DT124	
07126			DT126=DT111	
07127			DT127=DT111	
07128			DT128=DT3	
07129			DT129=DT3	
07130			DT130=DT3	
07131			DT131=DT131	
07132			DT132=DT131	
07133			DT133=DT131	
07134			DT134=DT133	
07135			DT135=DT133	
07136			DT136=DT133	
07137			DT137=DT137	
07138			DT138=DT137	
07139			DT139=DT137	
07140			DT140=DT137	
07141			DT141=DT137	
07142			DT142=DT137	
07143			DT143=DT133	
07144			DT144=DT133	
07145			DT145=DT137	
07146			DT146=DT137	
07147			DT147=DT133	
07148			DT148=DT133	
07149			DT149=DT133	
07150			DT150=DT133	
07151			DT151=DT137	
07152			DT152=DT133	
07153			DT153=DT137	
07154			DT154=DT154	
07155			DT155=DT154	
07156			DT156=DT133	
07157			DT157=DT133	
07158			DT158=DT133	
07159			DT159=DT133	
07160			DT160=DT133	
07161			DT161=DT133	
07162			DT162=DT133	
07163			DT163=DT133	
07164			DT164=DT3	
07165			DT165=DT3	
07166			DT166=DT133	
07167			DT167=DT133	
07168			DT168=DT133	
07169			DT169=DT133	
07170			DT170=DT133	
07171			DT171=DT133	
07172			DT172=DT133	
07173			DT173=DT133	
07174			DT174=DT133	
07175			DT175=DT133	
07176			DT176=DT133	
07177			DT177=DT133	
07200			DT200=DT133	
07201			DT201=DT133	

STMP0, STMP1, STAB, STMP2, STAB, STMP3, STMP4, SCRLF
 MS6, SCRLF, MS7, STMP5, MS10, STMP6, SCRLF
 MS11, SCRLF, MS7, STMP5, MS10, STMP7, SCRLF, MS37, SCRLF, STMP10, C

 STMP0, STMP1, STAB, STMP2, SCRLF, MS37, SCRLF, STMP3
 SCRLF, MS40, SCRLF, STMP4, SCRLF, MS415, SCRLF, STMP5, C

 STMP0, STMP1, STAB, STMP2, SCRLF, MS41, SCRLF, STMP3
 SCRLF, MS42, SCRLF, STMP4, SCRLF, MS43, SCRLF, STMP5
 SCRLF, MS44, SCRLF, STMP6, D

 STMP0, STMP1, STAB, STMP2, STMP10, STAB, STMP11, C

G13

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FLAG RESET AND CONSOLE TEST ROUTINE

001234
001235
001236
001237
001238
001239
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001242
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001244
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001250

DT202=DT133
DT203=DT133
DT204=DT133
DT205=DT133
DT206=DT133
DT207=DT133
DT210=DT133
DT211: .WORD
DT212: .WORD
DT213=DT212

001233
001234

001234 040412
001234 040412

STMP0,STMP1,STAB,STMP2,STAB,STMP3,C
STMP0,STMP1,STAB,STMP2,C

00000:

:12345 .ENC

SYMBOL TABLE
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ROOM1 0000000
ROOM2 0000000
ROOM3 0000000
ROOM4 0000000
ROOM5 0000000
ROOM6 0000000
ROOM7 0000000
ROOM8 0000000
ROOM9 0000000
RDEVC1 0000000
RDEVM1 0000000
RDEVM2 004670
RENV 0000000
RENV1 0000000
RENV2 004560
RENV3 004560
RENV4 004574
RENV5 004626
RFATA1 0000000
RFADR1 0000000
RFADR2 0000000
RFADR3 0000000
RFADR4 0000000
RFAMS1 0000000
RFAMS2 0000000
RFAMS3 0000000
RFAMS4 0000000
RMSGAD 0000000
RMSG1G 0000000
RMSGTY 0000000
RMTYP1 0000000
RMTYP2 0000000
RMTYP3 0000000
RMTYP4 0000000
RPASS 0000000
RPRIOR 0000000
RPTCSU 000040
RPTENV 000001
RPTSIZ 000200
RPTSPO 000100
RSWRE3 000000
RTESTN 000000
RUNIT 000000
RUSWR 000000
RVECT1 000000
RVECT2 000000

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RCS
R1
R2
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R100

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RBCATC 031300
RBDONE 031430
RBER0 030766
RBER1 031026
RBER10 031006
RBER11 031072
RBER2 031067
RBER3 031102
RBER4 031136
RBER4C 031172
RBER5 031210
RBER6 031244
RBER7 031262
RBER8 031310
RBPATO 031320
RBPAT1 031330
RBPAT2 031340
RBPAT3 031350
RBPAT4 031360
RBPAT5 031370
RBPAT6 031410
RBP10 031420
RBP11 031490
RBP7 030130
R81 030304
R810 030324
R811 030324
R812 030342
R813 030354
R814 030404
R815 030430
R816 030440
R817 030166
R82 030502
R820 030526
R821 030546
R822 030556
R823 030564
R824 030576
R825 030626
R826 030172
R827
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R1000 030766
R1001 030766
R1002 030766
R1003 030766
R1004 030766
R1005 030766
R1006 030766
R1007 030766
R1008 030766
R1009 030766
R1010 030766
R1011 030766
R1012 030766
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SYMBOL TABLE

013130	IDATI1	011030	JDATI6	016132	LDAT00	016752
013162	IDATI2	011032	JDATI3	016134	LDAT01	016754
013164	IDATI3	011034	JDAT00	016136	LDAT02	016756
013252	IDAT00	011016	JDAT01	016146	LDAT03	016772
013304	IDAT01	011020	JDAT02	016140	LDONE	016772
013306	IDAT02	011022	JDAT03	016142	LDI	041242
013374	IDAT03	011024	JDAT1	016144	LDJ	041272
012032	IDONE	011036	JDAT2	016150	LERR1	016604
013426	IERR0	010576	JDAT3	016152	LERR2	016656
013430	IERR1	010660	JDONE	016154	LERR3	016630
013516	IERR2	010700	JERR0	016156	LF	= 000012
013550	IERR25	010722	JERR1	015776	LFLEX1	040417
013552	IERR3	010752	JERR2	016044	LFLEX2	040467
012064	IERR4	010726	J1	016070	LFPS1	041227
012066	ILLMS	041506	J10	015670	LOOP	004304
012154	ILL1	041402	J2	016016	LPAT10	016726
014732	ILL2	041445	J3	015714	LPAT11	016730
015006	IOTVEC=	000020	J4	015716	LPAT12	016732
014736	IPAT10	010776	J5	015720	LPAT13	016734
015016	IPAT11	011000	J6	015754	LPAT20	016736
014746	IPAT12	011002	J7	015762	LPAT21	016740
015026	IPAT13	011004	KBUFO	015774	LPAT22	016742
014756	IPAT20	011006	KBUF1	016420	LPAT23	016744
015036	IPAT21	011010	KBUF2	016422	LPERR =	104413
014766	IPAT22	011012	KBUF3	016424	L1	016456
015046	IPAT23	011014	KDATI0	016426	L2	016516
014776	I1	010202	KDATI1	016410	L3	016520
014662	I10	010364	KDATI2	016412	L4	016522
014672	I105	010372	KDATI3	016414	L5	016572
013606	I106	010366	KDAT00	016416	L6	016600
014022	I11	010374	KDAT01	016430	MDAT00	017312
014654	I12	010376	KDAT02	016432	MDAT01	017314
015056	I13	010412	KDAT03	016434	MDAT02	017316
015066	I14	010454	KDONE	016436	MDAT03	017320
015076	I15	010456	KERR0	016450	MDONE	017322
015106	I16	010460	KERR1	016272	MERR0	017146
015116	I17	010476	KERR2	016336	MERR1	017204
015126	I2	010220	KERR3	016362	MERR2	017120
014700	I20	010536	KPAT0	016440	MERR3	017232
014734	I21	010552	KPAT1	016442	MNUMB=	000213
014546	I22	010556	KPAT2	016444	MNUM0	042210
000011	I23	010572	K1	016164	MNUM1	042216
014210	I3	010266	K10	016312	MNUM2	042223
014452	I4	010270	K2	016210	MNUM3	042230
014504	I5	010272	K3	016212	MNUM4	042237
014536	I6	010316	K4	016214	MNUM5	042245
014230	I7	010332	K5	016250	MPAT10	017272
014240	JBUFO	016116	K6	016256	MPAT11	017274
014312	JBUF1	016120	K7	016270	MPAT12	017276
014334	JBUF2	016122	LDATIC	016270	MPAT13	017300
014366	JBUF3	016124	LDATI1	016750	MPAT20	017302
014420	JDATI0	016126	LDATI2	016752	MPAT21	017304
011026	JDATI1	016130	LDATI3	016754	MPAT22	017306

MPAT23	017310	NDAT01	017760	QDAT32	020466	PERR16	021006	QERR21	021532
M51	041554	NDAT02	017762	QDAT03	020470	PERR17	021014	QERR22	021540
M510	041720	NDAT03	017764	QDONE	020534	PEPR2	021124	QERR3	021566
M511	041743	QDONE	020030	QERR0	020224	PERR20	021042	QERR4	021574
M512	041770	NERR0	017520	QERR1	020324	PERR21	021052	QPAT10	021650
M513	042026	NERR1	017620	QERR10	020256	PERR22	021060	QPAT11	021652
M514	042043	NERR10	017552	QERR11	020270	PIRG =	177772	QPAT12	021654
M515	042131	NERR11	017564	QERR2	020360	PIPQVE =	000240	QPAT13	021656
M516	042156	NERR2	017654	QERR20	020332	POWERM	040344	QPAT20	021660
M517	042163	NERR20	017626	QERR3	020370	PPAT10	021160	QPAT21	021662
M52	041566	NERR3	017664	QERR4	020400	PPAT11	021162	QPAT22	021664
M520	042253	NERR4	017674	QERR5	020410	PPAT12	021164	QPAT23	021666
M521	042314	NERR5	017704	QERR6	020434	PPAT13	021166	Q1	021214
M522	042353	NERR6	017730	OPAT10	020514	PROGNUM =	000001	Q10	021352
M523	042403	NOOP1	040613	OPAT11	020516	PRO =	000000	Q2	021236
M524	042432	NOOP10	041072	OPAT12	020520	PR1 =	000040	Q3 =	021240
M525	042525	NOOP11	041163	OPAT13	020522	PR2 =	000100	Q4	021242
M526	042547	NOOP15	040642	OPAT20	020500	PR3 =	000140	Q5	021264
M527	042564	NOOP2	040740	OPAT21	020502	PR4 =	000200	Q6	021306
M53	041573	NOOP3	040755	OPAT22	020504	PR5 =	000240	Q7	021316
M530	042573	NOOP4	040767	OPAT23	020506	PR6 =	000300	Q8	021330
M531	042602	NOOP5	041004	OPAT24	020510	PR7 =	000340	Q9	021344
M532	042611	NOOP6	041032	Q1	020034	PS =	177776	RDCHR =	104407
M533	042620	NOOP7	041052	Q10	020164	PSW =	177776	RESREG =	104411
M534	042627	NPAT10	020010	Q11	020174	PWRVEC =	000024	RESVEC =	000010
M535	042636	NPAT11	020012	Q12	020176	P1	020540	RSETUP =	104412
M536	042643	NPAT12	020014	Q13	020212	P2	020562	RE =	%000006
M537	042652	NPAT13	020016	Q14	020222	P3 =	020564	R7 =	%000007
M54	041622	NPAT20	017776	Q2	020060	P4	020566	SADR	015636
M540	042670	NPAT21	020000	Q3	020062	P5	020610	SAVREG =	104410
M541	042724	NPAT22	020002	Q4	020064	P6	020632	SDAT00	015652
M5415	042704	NPAT23	020004	Q5	020102	P7	020642	SDAT01	015654
M542	042750	NULL	040411	Q6	020112	P8	020652	SDAT02	015656
M543	042766	N1	017326	Q7	020122	QDAT10	021700	SDAT03	015660
M544	043003	N10	017460	Q8	020136	QDAT11	021702	SDONE	015662
M55	041634	N11	017470	Q9	020150	QDAT12	021704	SERR0	015342
M56	041676	N12	017472	PDAT10	021170	QDAT13	021706	SERR1	015552
M57	041714	N13	017506	PDAT11	021172	QDAT00	021670	SERR10	015362
M1	016776	N14	017516	PDAT12	021174	QDAT01	021672	SERR15	015442
M15	017016	N2	017352	PDAT13	021176	QDAT02	021674	SERR2	015502
M2	017022	N3	017354	PDAT00	021200	QDAT03	021676	SERR20	015462
M3	017024	N4	017356	PDAT01	021202	QDONE	021710	SERR3	015526
M4	017026	N5	017374	PDAT02	021204	QERR0	021356	SERR4	015420
M5	017062	N6	017404	PDAT03	021206	QERR1	021622	SERR5	015604
M6	017066	N7	017414	PDONE	021210	QERR11	021366	SERR6	015514
M7	017076	N8	017432	PERR0	020656	QERR12	021404	SERR7	015540
M8	017106	N9	017444	PERR1	021076	QERR13	021422	SETD1	041360
M9	017116	ODAT10	020524	PERR10	020676	QERR14	021440	SETF1	041352
NDAT10	020020	ODAT11	020526	PERR11	020706	QERR15	021456	SETI1	041366
NDAT11	020022	ODAT12	020530	PERR12	020724	QERR16	021466	SETL1	041374
NDAT12	020024	ODAT13	020532	PERR13	020742	QERR17	021474	SPACE	040414
NDAT13	020026	ODAT00	020462	PERR14	020760	QERR2	021556	SPAT10	015642
NDAT00	017756	QDAT01	020464	PERR15	020776	QERR20	021522	SPAT11	015644

SPAT12	015646	TDATI2	011640	TST5	006142	UPAT33	023000	XDAT01	024341
SPAT13	015650	TDATI3	011642	TST6	006302	UPAT40	023002	XDAT02	024342
STACK	= 001100	TDAT00	011624	TST7	010176	UPAT41	023004	XDAT03	024344
START	003606	TDAT01	011626	TYPE	= 104401	UPAT42	023006	YDONE	024376
STFS1	041303	TDAT02	011630	TYPOC	= 104402	UPAT43	023010	XERR1	024222
STKLMT	= 177774	TDAT03	011632	TYPON	= 104404	UROM1	023020	XERR2	024304
STST1	041433	TDONE	011644	TYPOS	= 104403	UROM2	023022	XERR3	024250
ST1	041316	TERR0	011412	T1	= 011042	UROM3	023024	XERR4	024320
SW*2	041333	TERR1	011474	T10	011204	UTMP1	023014	XPAT00	024346
SW*3	001140	TERR2	011514	T105	011212	UTMP2	023016	XPAT01	024350
SW*REG	000176	TERR25	011530	T11	011214	U0	021730	XPAT02	024352
SW*0	= 000001	TERR3	011560	T12	011216	U1	021760	XPAT03	024354
SW*0C	= 000001	TERR4	011542	T13	011232	U10	022316	XPAT10	024356
SW*01	= 000002	TKVEC	= 000060	T14	011274	U11	022320	XPAT12	024362
SW*02	= 000004	TPAT10	011604	T15	011276	U12	022352	XPAT13	024364
SW*03	= 000010	TPAT11	011606	T16	011300	U13	022370	XPAT20	024366
SW*04	= 000020	TPAT12	011610	T17	011316	U14	022422	XPAT21	024370
SW*05	= 000040	TPAT13	011612	T2	011062	U15	022452	XPAT22	024372
SW*06	= 000100	TPAT20	011614	T20	011356	U16	022454	XPAT23	024374
SW*07	= 000200	TPAT21	011616	T21	011372	U2	022024	XTMP	024334
SW*08	= 000400	TPAT22	011620	T22	011374	U3	022056	X1	023606
SW*09	= 001000	TPAT23	011622	T23	011406	U4	022122	X10	024000
SW1	= 000002	TPVEC	= 000064	T3	011130	U5	022154	X11	024014
SW1C	= 002000	TRAPVE	= 000034	T4	011132	U6	022220	X12	024050
SW11	= 004000	TRTVEC	= 000014	T5	011134	U7	022252	X13	024074
SW12	= 010000	TST1	004304	T6	011160	WDAPO0	023572	X14	024102
SW13	= 020000	TST1C	011040	T7	011174	WDAT01	023574	X15	024116
SW14	= 040000	TST11	011646	UDONE	023026	WDAT02	023576	X16	024152
SW15	= 100000	TST12	014204	UERR0	022472	WDAT03	023600	X17	024176
SW2	= 000004	TST13	015130	UERR1	022516	WDONE	023602	X2	023642
SW3	= 000010	TST14	015664	UERR10	022524	WPAT00	023562	X20	024204
SW4	= 000020	TST15	016160	UERR11	022566	WPAT01	023564	X21	024220
SW5	= 000040	TST16	016452	UERR2	022602	WPAT02	023566	X3	023656
SW6	= 000100	TST17	016774	UERR20	022610	WPAT03	023570	X4	023674
SW*7	= 000200	TST2	004672	UERR21	022652	WSETUP	023530	X5	023712
SW8	= 000400	TST20	017324	UERR3	022666	W1	023030	X6	023746
SW9	= 001000	TST21	020032	UERR4	022716	W10	023242	X7	023772
SW1	015132	TST22	020536	UFLAG	023012	W11	023270	YDAT00	024742
SW10	015300	TST23	021212	UPAT00	022742	W12	023320	YDAT01	024744
SW11	015324	TST24	021712	UPAT01	022744	W13	023344	YDAT02	024746
SW12	015334	TST25	023026	UPAT02	022746	W14	023360	YDAT03	024750
SW2	015166	TST26	023604	UPAT03	022750	W15	023406	YDONE	025002
SW3	015170	TST27	024400	UPAT10	022752	W16	023442	YERR1	024602
SW4	015174	TST3	004774	UPAT11	022754	W17	023466	YERR2	024644
SW5	015220	TST30	025004	UPAT12	022756	W2	023060	YERR3	024706
SW6	015230	TST31	025306	UPAT13	022760	W20	023502	YFLAG	024732
SW7	015236	TST32	026340	UPAT20	022762	W3	023104	YPAT00	024752
SW8	015272	TST33	030126	UPAT21	022764	W4	023122	YPAT01	024754
SW9	015274	TST34	031432	UPAT22	022766	W5	023152	YPAT02	024756
*AB	= 000011	TST35	033426	UPAT23	022770	W6	023206	YPAT03	024760
*BITVE	= 000014	TST36	034202	UPAT30	022772	W7	023232	YPAT10	024762
*DAT1C	011634	TST37	034556	UPAT31	022774	XAPT11	024360	YPAT11	024764
*DAT11	011636	TST4	005700	UPAT32	022776	XDAT00	024336	YPAT12	024766

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```

SFATAL 001320
SFFLG 036636
SFILLC 001156
SFILLS 001155
SLOADR 001120
SGODAT 001124
SGET42 034742
SGTSMR 036710
SHO = 000003
SHIBTS 003572
SHCNT = 000003
SHIBTS 000004
SILLJP 037540
SINTAG 001135
SITEMB 001114
SLF 001314
SLFLG 036635
SLOOP 035036
SLPADR 001106
SLPERR 001110
SMADR1 001350
SMADR2 001354
SMADR3 001360
SMADR4 001364
SMAIL 001316
SMAMS1 001346
SMAMS2 001352
SMAMS3 001356
SMAMS4 001362
SMADR 003574
SMFLG 036534
SMNEW 037265
SMMSGAD 001332
SMMSGLG 001334
SMMSGTY 001316
SMSMR 037254
SMTYP1 001347
SMTYP2 001353
SMTYP3 001357
SMTYP4 001363
SMXCNT 035326
SNUL 001154
SNWTST = 000001
SOCNT 036366
SOMODE 036370
SOVER 035312
SPASS 001324
SPASTM 003600
SPWRAC 037522

```

```

SEYABL 001336
SEYENC 001442
SFATAL 001320
SFFLG 036636
SFILLC 001156
SFILLS 001155
SLOADR 001120
SGODAT 001124
SGET42 034742
SGTSMR 036710
SHO = 000003
SHIBTS 003572
SHCNT = 000003
SHIBTS 000004
SILLJP 037540
SINTAG 001135
SITEMB 001114
SLF 001314
SLFLG 036635
SLOOP 035036
SLPADR 001106
SLPERR 001110
SMADR1 001350
SMADR2 001354
SMADR3 001360
SMADR4 001364
SMAIL 001316
SMAMS1 001346
SMAMS2 001352
SMAMS3 001356
SMAMS4 001362
SMADR 003574
SMFLG 036534
SMNEW 037265
SMMSGAD 001332
SMMSGLG 001334
SMMSGTY 001316
SMSMR 037254
SMTYP1 001347
SMTYP2 001353
SMTYP3 001357
SMTYP4 001363
SMXCNT 035326
SNUL 001154
SNWTST = 000001
SOCNT 036366
SOMODE 036370
SOVER 035312
SPASS 001324
SPASTM 003600
SPWRAC 037522

```

```

SPLADN 037322
SPLRMS 037522
SPWRUP 037434
$JLES 001312
$ROCHR 037122
$RDSZ = 000901
$REGAD 001160
$REGO 001162
$REG1 001164
$REG10 001202
$REG11 001204
$REG12 001206
$REG13 001210
$REG14 001212
$REG15 001214
$REG16 001216
$REG17 001220
$REG2 001166
$REG20 001222
$REG21 001224
$REG22 001226
$REG23 001230
$REG3 001170
$REG4 001172
$REG5 001174
$REG6 001176
$REG7 001200
$RESRE 035624
$RTNAD 035040
$RTRN 035034
$SAVRE 035566
$SAVR6 037544
$SCOPE 035050
$SETUP = 000137
$STUP = 177777
$SVLAD 035256
$SVPC = 003572
$SWR = 177400
$SWREG 001340
$SWRMK = 000000
$SWRMS = 000200
$TAB 040412
$TABIT 035042
$TERM = 000030
$TESTN 001322
$THE 040606
$TIMES 001302
$TKB 001146
$TKC 001144
$TMPO 001232

```

```

SYN = 000037
STPB = 001152
STPFLG 001150
STPS 001150
STRAP 037276
STRAP2 037320
STRP = 000014
STRPAD 037332
STSTM 003576
STSTMN 001102
STYPE 035662
STYPEC 036074
STYPEX 036142
STYPOC 036170
STYPON 036204
STYPOS 036144
SUNIT 001330
SUNITM 003602
SUSMR 001342
SVECT1 001366
SVECT2 001370
$XTSTR 035062
$SCET4 = 000001
$OFILL 036367
SY = 071760
.LPER 040266
.PSET 040274
.SY = 003572

```

```

DETECTED: C
GENERATED: C

```

SYMBOL TABLE 3- FPP DIAGNOSTIC PART 1 MACY11 27:0061 01-NOV-78 21:09 PAGE 172

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