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.REMS

IDENTIFICATION

PRODUCT CODE: AC-8484F-MC  
PRODUCT NAME: CZDHKFO DM11 MODEM CONTROL MULTIPLEXER DIAGNOSTIC  
DATE : 18-JUN-1985  
MAINTAINER: NAC SOFTWARE ENGINEERING  
AUTHOR: G. BAISLEY

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## 1.0 ABSTRACT

THIS PROGRAM IS A TEST OF THE MODEM CONTROL MULTIPLEXER USED WITH THE DH11 OPTION

THE PROGRAM IS DIVIDED INTO FUNCTIONAL TEST GROUPS AS FOLLOWS:

GROUP 0: ALL LINE SCANNER AND LINE MULTIPLEXER FUNCTIONS ARE TESTED. NO TEST CONNECTOR IS NEEDED...

GROUP 1: A SINGLE LINE IS TESTED USING THE MODEM CABLE AND A H315 TEST CONNECTOR

GROUP 2: CONNECT-DISCONNECT TEST FOR 103A MODEMS

GROUP 3: CONNECT DISCONNECT TEST FOR 202C MODEMS

## 2.0 REQUIREMENTS

### 2.1 EQUIPMENT

PDP-11 COMPUTER WITH AT LEAST 8K OF MEMORY WITH OR WITHOUT HARDWARE SWITCH REGISTER ASR-33 TELETYPE OR EQUIVALENT MODEM CONTROL MODULES

#### 2.1.1 FOR 16 LINE SCANNER TEST

NO ADDITIONAL HARDWARE IS NEEDED. PROGRAM HAS BEEN MODIFIED TO RUN WITHOUT H861 TEST CONNECTOR.

#### 2.1.2 FOR SINGLE LINE CABLE TEST

4 CABLES TO CONNECT TO THE DISTRIBUTION PANEL H315 TEST CONNECTOR

#### 2.1.3 FOR ON LINE TESTS

4 CABLES TO CONNECT TO THE DISTRIBUTION PANEL  
2 BELL 103A MODEMS (FOR 103A TEST)  
2 BELL 202C MODEMS (FOR 202C TEST)

## 3.0 LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING BINARY TAPES IS TO BE USED.

## 4.0 STARTING PROCEDURE

### 4.1 STARTING ADDRESS

THE STARTING ADDRESS FOR ALL TESTS IS 000200.

RESTART ADDRESS FOR ALL TESTS IS 000200

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4.2 OPERATOR AND/OR PROGRAM ACTION

4.2.1 INITIAL PROGRAM START

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IF PROGRAM IS BEING RUN WITH THE "XOR" MODULE TESTER  
LOCATION 1030(8) MUST BE MODIFIED TO CONTAIN A 240(8)  
'NOP" TO ACTIVATE THAT CODE AFFECTING THE "XOR" TESTER.

\*\*\*\*\*  
NOTE  
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SOFTWARE SWITCH REGISTER IS DEFINED AS LOC. 176  
(REFER TO SECTION 5.1.2 FOR DYNAMIC LOADING INSTRUCTIONS)

4.2.1.1 LOAD ADDRESS 000200  
SET SW00 = 1  
PRESS START  
\*\*\*SOFTWARE SWITCH REGISTER IS LOC. 176

4.2.1.2 PROGRAM WILL TYPE  
"DH11-MODEM CONTROL DIAGNOSTIC "(ONCE ONLY)  
\*\*\*NOTE: IF USING SOFTWARE SWITCH REGISTER THE FOLLOWING  
WILL BE TYPED BEFORE TITLE:  
SWR=XXXXXX NEW= (REFER TO SECTION 5.1.2 FOR OPTIONS)

4.2.1.3 PROGRAM WILL TYPE (WITH SW00 = 1)  
VECTOR ADDRESS-" AND WILL WAIT FOR AN INPUT  
FROM THE TELETYPE KEYBOARD.

4.2.1.4 TYPE A THREE DIGIT NUMBER (OCTAL) WHICH IS THE  
ADDRESS THAT THE MODEM CONTROL WILL INTERRUPT TO, FOLLOWED BY  
<RETURN>. IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL  
TYPE "?" AND THEN REPEAT 4.2.1.3.

NOTE: IF THE ADDRESS ENTERED IS ACCEPTIBLE TO THE PROGRAM,  
BUT IS NOT THE INTERRUPT VECTOR ADDRESS OF THE MODEM CONTROL  
UNDER TEST, A HALT WILL OCCUR AT THAT ADDRESS+2, WHEN  
THE MODEM CONTROL INTERRUPTS.

TO RECOVER, PERFORM 4.2.2.1.

4.2.1.5 THE PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-" AND WAIT FOR  
AN INPUT FROM THE TELETYPE KEYBOARD.

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4.2.1.6 TYPE A 6 DIGIT (OCTAL NUMBER) WHICH IS THE ADDRESS OF THE MODEM CONTROL'S CONTROL REGISTER FOLLOWED BY <RETURN>. IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL TYPE "?" AND THEN REPEAT 4.2.1.6.

NOTE: IF THE ADDRESS ENTERED IS ACCEPTIBLE TO THE PROGRAM BUT IS A NON-EXISTANT REGISTER, A BUS ERROR TRAP WILL OCCUR WHEN THE PROGRAM ADDRESSES THE REGISTER, AND THE PROGRAM WILL HALT AT LOCATION 6.

TO RECOVER, PERFORM 4.2.2.1.

4.2.1.7 THE PROGRAM WILL TYPE "LINE SELECTION PARAMETER-" AND WAIT FOR INPUT FROM THE TTY KEYBOARD.

4.2.1.8 TYPE AN OCTAL NUMBER TO SPECIFY THE LINES TO BE TESTED USING THE FOLLOWING ENCODING SCHEME:

BIT00 = 1      TEST LINE 00  
BIT01 = 1      TEST LINE 01  
BIT02 = 0      DO NOT TEST LINE 2

"

"

BIT15 = 1      TEST LINE 15

EG:      TYPING 377(8) SELECTS LINES 00 THRU 07  
         TYPING 17777(8) SELECTS ALL 16 LINES

IF THE NO. TYPED IS NOT ACCEPTABLE, THE PROGRAM TYPES A "?" AND ASKS FOR THE LINE SELECT PARAMETER AGAIN.

4.2.1.9 THE PROGRAM WILL TYPE "TEST-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD.

4.2.1.10      TYPE A THREE DIGIT OCTAL NUMBER CORRESPONDING TO THE NUMBER OF THE TEST TO BE RUN FOLLOWED BY <RETURN>. IF AN INCORRECT TEST NUMBER IS TYPED THE PROGRAM WILL TYPE "?" AND THEN REPEAT 4.2.1.7 THE AVAILABLE TESTS TOGETHER WITH THE NUMBER TO BE TYPED ARE GIVEN BELOW.

TEST GROUP 0:  
OFF LINE TESTS -FIRST TEST=0  
TEST GROUP 1:  
OFF LINE TESTS USING DC11 TEST CONNECTOR AND MODEM CABLE-FIRST TEST=100  
TEST GROUP 2:  
CONNECT/DISCONNECT TEST FOR BELL 103A MODEMS-FIRST TEST=200  
TEST GROUP 3:  
CONNECT/DISCONNECT TEST FOR BELL 202C MODEMS-FIRST TEST=300

4.2.1.11      THE PROGRAM WILL ENTER THE SELECTED TEST GROUP.

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4.2.2 PROGRAM RESTART

4.2.2.1 WITH SW00=1

LOAD ADDRESS 200  
SET SW00=1 BEFORE PRESSING START.  
\*\*\*SOFTWARE SWITCH REGISTER IS LOC 176\*\*\*  
PRESS START

PROGRAM WILL PERFORM AS DESCRIBED IN 4.2.1.3 TO 4.2.1.10.

4.2.2.2 WITH SW00=0

LOAD ADDRESS 200  
\*\*\*SOFTWARE SWITCH REGISTER IS LOC. 176  
PRESS START

PROGRAM WILL PERFORM AS DESCRIBED IN 4.2.1.7 TO 4.2.1.10

5.0 OPERATING PROCEDURE

5.1 TEST GROUP 0 16 LINE SCANNER TEST

5.1.1 TEST INITIALIZATION

NONE REQUIRED, PROGRAM TYPES "16 LINE SCANNER TEST"  
AND BEGINS TEST EXECUTION.

5.1.2 OPERATIONAL SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G <+G>; THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE 'NEW=' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:

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- A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0 7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED)  
IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
- B) IF A CONTROL U <+U> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

SW15=1, HALT ON ERROR  
SW14=1, LOOP ON CURRENT TEST  
SW13=1, SUPPRESS ERROR TYPEOUT  
SW11=1, SUPPRESS ITERATIONS  
SW10=1, ESCAPE TO NEXT TEST ON ERROR  
SW09=1, FREEZE DATA

### 5.1.3 PROGRAM AND/OR OPERATOR ACTION

5.1.3.1 WITH ALL SWITCHES DOWN, THE PROGRAM WILL RUN ALL TESTS IN THE SELECTED GROUP, SEQUENTIALLY. EACH TEST IS REPEATED A FIXED NUMBER OF TIMES (SEE LISTING FOR DETAILS), EXCEPT FOR TO WHICH IS EXECUTED ONCE ONLY AFTER START OF TEST. WHEN ALL TESTS HAVE BEEN COMPLETED, THE PROGRAM WILL ISSUE A "RESET", RING THE TELETYPE BELL, AND RESTART AT THE FIRST TEST OF THE SELECTED GROUP.

IF AN ERROR OCCURS, THE PROGRAM WILL TYPE AN APPROPRIATE ERROR MESSAGE AND CONTINUE TESTING.

5.1.3.2 WITH SW15=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT A HALT WILL OCCUR AFTER ERROR TYPEOUT.  
NOTE: IF USING THE SOFTWARE SWITCH REGISTER AND AN EROR HALT OCCURS. THE SOFTWARE SWITCH REGISTER CAN BE CHANGED BY PRESSING CONTINUE THE PROGRAM WILL RESPOND WITH THE FOLLOWING:  
SWR=XXXXXX NEW=

5.1.3.3 WITH SW13=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT NO ERROR TYPEOUT WILL OCCUR. THE PC OF THE TEST THAT FAILED WILL BE DISPLAYED IN THE COMPUTER DATA LIGHTS.

5.1.3.4 THIS PROGRAM WILL NO LONGER TRACE TRAP WITH THIS RELEASE

5.1.3.5 WITH SW10=1, PROGRAM ACTION WILL BE AS IN 5.1.3.1 EXCEPT THAT AFTER AN ERROR HAS OCCURED, THE PROGRAM WILL IMMEDIATELY START THE NEXT TEST IN SEQUENCE.

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5.2 TEST GROUP 1 SINGLE LINE CABLE TEST

5.2.1 TEST INITIALIZATION

THE PROGRAM WILL TYPE "SINGLE LINE CABLE TEST  
LINE NUMBER-" AND WILL WAIT FOR AN INPUT FROM  
THE TELETYPE KEYBOARD.

TYPE A 2 DIGIT OCTAL NUMBER BETWEEN 0 AND 17, CORRESPONDING  
TO THE NUMBER OF THE LINE TO BE TESTED, FOLLOWED BY  
<RETURN>. THE PROGRAM WILL THEN BEGIN TEST EXECUTION.  
IF THE TELETYPE INPUT IS INCORRECT, THE PROGRAM  
WILL TYPE "?" AND REPEAT THE MESSAGE.

5.2.2 OPERATIONAL SWITCH SETTINGS

SAME AS 5.1.2

5.2.3 PROGRAM AND/OR OPERATOR ACTION

SAME AS 5.1.3

5.3 TEST GROUP 2 BELL 103A MODEM CONNECT-DISCONNECT TEST

5.3.1 TEST INITIALIZATION

THE PROGRAM WILL TYPE "103A CONNECT-DISCONNECT TEST  
ORIGINATE LINE-" AND WAIT FOR AN INPUT FROM THE TELETYPE  
KEYBOARD.

TYPE THE NUMBER OF THE LINE THAT WILL ORIGINATE THE  
CALL (0-17 OCTAL) FOLLOWED BY RETURN.

THE PROGRAM WILL TYPE "ANSWER LINE-" AND WILL WAIT  
FOR AN INPUT FROM THE TELETYPE KEYBOARD.

TYPE THE NUMBER OF THE LINE THAT WILL ANSWER THE CALL  
(0-17 OCTAL) FOLLOWED BY <RETURN>.

THE PROGRAM WILL TYPE "DIAL ANSWERING DATA SET"  
AND WILL WAIT FOR THE ORIGINATE AND ANSWERING MODEMS  
TO GENERATE INTERRUPTS.

5.3.2 OPERATOR ACTION TO MAKE TELEPHONE CONNECTION

AFTER THE MESSAGE "DIAL ANSWERING DATA SET" IS TYPED  
THE OPERATOR HAS APPROXIMATELY 5 MINUTES TO ESTABLISH  
A CONNECTION BETWEEN THE 2 DATA SETS.

5.3.2.1 PLACE ANSWERING DATA SET IN "AUTO ANSWER" MODE

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5.3.2.2 PLACE ORIGINATING DATA SET IN "TALK" MODE

5.3.2.3 DIAL DIAL ANSWERING DATA SET FROM ORIGINATING DATA SET

5.3.2.4 LISTEN FOR TONE IN HANDSET OF ORIGINATING DATA SET.

WHEN TONE IS HEARD, PRESS "DATA" BUTTON ON ORIGINATING DATA SET.

"DATA" LIGHT SHOULD ILLUMINATE

5.3.2.5 "DATA" LIGHT ON ANSWERING DATA SET SHOULD BE LIT.

5.3.2.6 THE PROGRAM WILL NOW WAIT FOR INTERRUPTS FROM THE MODEM CONTROL.

5.3.2.7 IF THE CONNECTION HAS BEEN PROPERLY ESTABLISHED, THE PROGRAM WILL TYPE "TYPE TTY KEY TO DISCONNECT".

WHEN TTY KEY IS STRUCK, THE PROGRAM WILL BEGIN THE DISCONNECT SEQUENCE.

5.3.2.8 WHEN THE DISCONNECT SEQUENCE HAS BEEN COMPLETED THE PROGRAM WILL TYPE "103A TEST COMPLETE", AND WILL REQUEST THE OPERATOR TO SELECT NEW LINES.

5.3.3 PROGRAM ACTION IN CASE OF ERROR

5.3.3.1 RING ON INCORRECT LINE

IF THE PROGRAM DETECTS A RING SIGNAL ON AN INCORRECT LINE, OR IF ANY OTHER TRANSITION BESIDES RING IS DETECTED BEFORE RING, THE PROGRAM WILL TYPE A FATAL ERROR MESSAGE AND REQUEST THE OPERATOR TO RESELECT LINES AND REDIAL.

5.3.3.2 OTHER ERRORS

IF ANY ERRORS OCCUR AFTER THE FIRST RING HAS BEEN DETECTED, THE PROGRAM WILL TYPE AN APPROPRIATE ERROR MESSAGE AND CONTINUE TESTING TO COMPLETION.

THE ONLY EXCEPTION TO THIS IS IF AN INTERRUPT OCCURS ON A LINE NOT SELECTED, IN WHICH CASE A FATAL ERROR WILL BE REPORTED, AND THE PROGRAM WILL PROCEED AS DESCRIBED IN 5.3.3.1



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5.3.4 OPERATION SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G <+G>; THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE ''NEW='' HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
  - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED) IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
  - B) IF A CONTROL U <+U> IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

SW15=1, HALT ON ERROR  
SW13=1, SUPPRESS ERROR TYPEOUT

5.3.5 DATA SET MODE SWITCHING

AFTER THE PROGRAM HAS TYPED THE MESSAGE DESCRIBED IN 5.3.2.7, BUT BEFORE TTY KEY IS STRUCK, THE OPERATOR MAY SWITCH EITHER DATA SET FROM THE MODE THAT IT IS IN TO ANOTHER MODE. ALL TRANSITIONS DETECTED AT THIS TIME WILL BE REPORTED.

NOTE: THE ORIGINATE DATA SET MUST BE RETURNED TO "TALK" MODE AND THE ANSWERING DATA SET TO "AUTO ANSWER" BEFORE DISCONNECT IS STARTED TO PREVENT ERRORS FROM BEING DETECTED THAT ARE CAUSED BY THE FACT THAT THE MODEM IS IN THE INCORRECT STATE.

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5.4 TEST GROUP 3 BELL 202C MODEM CONNECT DISCONNECT TEST

5.4.1 TEST INITIALIZATION

SAME AS 5.3.1 EXCEPT PROGRAM WILL TYPE "202C CONNECT DISCONNECT TEST".

5.4.2 OPERATOR ACTION TO MAKE TELEPHONE CONNECTION

SAME AS 5.3.2 EXCEPT AT END OF TEST, PROGRAM WILL TYPE "202C TEST COMPLETE".

5.4.3 PROGRAM ACTION IN CASE OF ERRORS

SAME AS 5.3.3

5.4.4 OPERATIONAL SWITCH SETTINGS

SAME AS 5.3.4

5.4.5 DATA SET MODE SWITCHING

SAME AS 5.3.5

5.5 TEST RESELECTION

TO ESCAPE FROM THE TEST IN PROGRESS, AND SELECT A NEW TEST, TYPE <CONTROL C>.

THE PROGRAM WILL STOP EXECUTION OF THE TEST IN PROGRESS AND THEN TYPE "TEST-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD.

PROCEED AS DESCRIBED IN 4.2.1.8

5.5 ADDRESS CHANGE

TO CHANGE THE VECTOR AND REGISTER ADDRESS OF THE MODEM CONTROL UNDER TEST, TYPE <CONTROL V>. THE PROGRAM WILL STOP EXECUTION OF THE TEST IN PROGRESS AND PROCEED AS DESCRIBED IN SECTION 4.2.1, EXCEPT THAT "MODEM CONTROL DIAGNOSTIC" WILL NOT BE TYPED.

5.6 LINE NUMBER CHANGE

TO CHANGE THE LINE NUMBER(S) UNDER TEST, TYPE <CONTROL L>. THE PROGRAM WILL SUSPEND THE TEST IN PROGRESS AND RETURN TO THE INITIALIZATION STAGE OF THE SELECTED TEST.

WHEN THE LINE NUMBER(S) HAS BEEN CHANGED, THE PROGRAM WILL RESTART THE SELECTED TEST USING THE NEW LINE NUMBER(S).

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## 5.7 POWER FAILURE

IF A POWER FAIL TRAP OCCURS DURING TEST EXECUTION  
THE PROGRAM WILL SAVE THE GENERAL REGISTERS OF THE  
PROCESSOR AND HALT.

WHEN POWER UP OCCURS, THE PROGRAM WILL TYPE  
"POWER FAILURE-CURRENT TEST WILL BE RESTARTED".

THE PROGRAM WILL THEN RUSUME TEST EXECUTION.

NOTE: IF A TEST IS NOT IN PROGRESS, I.E., IF THE PROGRAM  
IS WAITING FOR AN INPUT FROM THE TELETYPE KEYBOARD,  
THE ERROR MESSAGE WILL BE "POWER FAILURE".  
THE PROGRAM WILL THEN REQUEST THE OPERATOR TO SELECT A TEST.

NOTE: IF MACHINE HAS A SOLID-STATE SWITCH REGISTER, THEN THE CONTENTS WILL BE  
LOST ON A POWER FAIL AND THEREFORE WILL HAVE TO BE RELOADED.

## 6.0 ERRORS

### 6.1 NORMAL OPERATION

IF AN ERROR OCCURS WITH ALL SWITCHES DOWN, THE PROGRAM  
WILL TYPE AN APPROPRIATE ERROR MESSAGE AND THEN  
RESUME TESTING.

THERE ARE SEVERAL ERROR MESSAGE FORMATS, AND  
THE PARTICULAR MESSAGE TYPED DEPENDS UPON THE  
TEST IN PROGRESS.

#### 6.1.1 ERROR MESSAGES

##### 6.1.1.1 UNIQUE ERROR

ONLY PC OF FAILING TEST IS OUTPUT TO TELEPRINTER

AN EXAMPLE OF THIS TYPE OF ERROR IS:

1. AN INTERRUPT OCCURED AT THE WRONG PRIORITY
2. A REGISTER BIT WAS NOT CLEARED BY RESET

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6.1.1.2 TRANSITION DETECTION ERROR

THIS ERROR WILL OCCUR IN ONE OF THE ON-LINE TESTS  
IF AN EXPECTED INTERRUPT DOES NOT OCCUR, OR IF  
AN UNEXPECTED INTERRUPT DOES OCCUR, ON THE LINES  
UNDER TEST.

FORMAT FOR ERROR TYPEOUT IS

XXXXXX TRANSITION ERROR  
EXP REC LINE  
AA BB CC

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE  
AA=EXPECTED INTERRUPT FLAGS (CORRESPONDS TO 4 MSB OF CONTROL REGISTER)  
BB=RECEIVED INTERRUPT FLAGS (AS ABOVE)  
CC=LINE ON WHICH ERROR OCCURED

## 6.1.1.3 SINGLE LINE STATUS ERROR

THIS ERROR WILL OCCUR IN ANY TEST, OFF LINE OR ON-LINE  
WHEN THE EXPECTED AND RECEIVED LINE STATUS ARE NOT  
THE SAME.

FORMAT FOR SINGLE LINE STATUS ERROR IS

XXXX LINE ERROR  
EXP REC LINE  
AAA BBB CC

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE  
AAA=EXPECTED LINE STATUS AT TIME OF ERROR  
BBB=RECEIVED LINE STATUS AT TIME OF ERROR  
CC=LINE ON WHICH ERROR OCCURED

## 6.1.1.4 FATAL TRANSITION ERROR

THIS ERROR WILL OCCUR IN AN ON-LINE TEST IF AN INTERRUPT  
OCCURS ON A LINE NOT SELECTED FOR TESTING.

FORMAT FOR FATAL ERROR TYPEOUT IS

XXXXXX FATAL ERROR  
CSTAT LSTAT  
AAAAA BBB

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE  
AAAAA=RECEIVED CONTROL STATUS ON LINE THAT INTERRUPTED  
BBB=RECEIVED LINE STATUS ON LINE THAT INTERRUPTED

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6.1.1.4 CONTROL STATUS ERROR

THIS ERROR WILL OCCUR IN A TEST THAT PRIMARILY INVOLVES THE LINE SCANNER

FORMAT FOR CONTROL STATUS ERROR IS

XXXXXX STATUS ERROR  
EXP REC  
AAAAAA BBBB

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE  
AAAAAA=EXPECTED CONTROL STATUS AT TIME OF ERROR  
BBBBBB=RECEIVED(ACTUAL) CONTROL STATUS AT TIME OF ERROR

6.1.1.5 LINE STATUS ERROR

THIS ERROR WILL OCCUR IN THOSE OFF LINE TESTS THAT SET ONE LINE TO A PARTICULAR STATE, AND THEN CHECK ALL OTHER LINES

FORMAT FOR LINE STATUS ERROR IS

XXXX LINE ERROR  
EXP REC LINE SEL  
AAA DDD CC DD

WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE  
AAA=EXPECTED LINE STATUS AT TIME OF ERROR  
BBB=RECEIVED LINE STATUS AT TIME OF ERROR  
CC=LINE ON WHICH ERROR OCCURED  
DD=THE LINE ON WHICH THE PROGRAM WAS OPERATING

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6.1.1.6 TIME OUT ERROR

THIS ERROR WILL OCCUR IF THE LINE UNDER TEST DOES NOT INTERRUPT WITHIN A GIVEN TIME FRAME.

FORMAT FOR THIS ERROR IS

XXXXXX TIME OUT WAITING FOR INTERRUPT  
LN CSR LSR  
AAA BBBBBB CCCCC

WHERE XXXXXX=PC+2 OF ERROR CALL  
AAA=FAILING LINE NUMBER  
BBBBBB=CONTROL STATUS REGISTER  
CCCCC=LINE STATUS REGISTER

6.1.2 REPEATED ERRORS

IF THE SAME ERROR OCCURS REPEATEDLY IN A GIVEN TEST ONLY THE DATA RELATING TO THAT ERROR WILL BE TYPED IF THE ERROR OCCURS IN THE SAME TEST ON THE SAME PASS

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6.2 SCOPE LOOPS

NOTE: SCOPE LOOPING APPLIES ONLY TO TEST GROUPS 0 AND 1

6.2.1 AFTER ERROR HALT

TO LOOP ON A GIVEN TEST AFTER AN ERROR HALT,  
SET SW15=0 TO RUN WITHOUT STOPPING  
SET SW14=1 TO LOOP ON CURRENT TEST  
SET SW13=1 TO SUPPRESS ERROR TYPEOUT  
SET SW10=0 (IF IT IS 1)  
SET SW09=1 TO LOOP ON SAME DATA (IF REQUIRED)

\*\*\*IF USING SOFTWARE SWITCH REGISTER AND YOU WANT TO CHANGE  
THE SWITCH SETTING TYPE A <+G> BEFORE CONTINUING.  
PRESS CONTINUE

THE PROGRAM WILL LOOP ON THE SAME TEST.

6.2.2 FROM PROGRAM START

6.2.2.1 PROCEED AS DESCRIBED IN 4.2.1.1 TO 4.2.1.4

6.2.2.2 WHEN THE PROGRAM TYPES "TEST-", SET SW14=1 TO LOOP  
ON THE TEST THAT WILL BE SELECTED.

6.2.2.3 TYPE IN THE NUMBER OF THE TEST THAT IS TO BE LOOPE  
ON (SEE LISTING FOR TEST NUMBER REFERENCE DESIGNATIONS)

6.2.2.4 THE PROGRAM WILL LOOP ON THE SELECTED TEST UNTIL  
SW14=0.

6.2.3 AFTER <CONTROL>

SAME AS 6.2.2.2 TO 6.2.2.4

7.0 RESTRICTIONS

7.1 STARTING

7.1.1 FOR 16 LINE SCANNER TEST

NO TEST CONNECTOR IS NEEDED TO RUN THIS TEST....

7.1.2 FOR SINGLE LINE CABLE TEST

H315 TEST CONNECTOR MUST BE INSTALLED ON MODEM CABLE

7.1.3 FOR ON LINE TESTS

NONE

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7.2 OPERATING

NONE.

7.3 WHEN ON ACT 11 OR "XOR"  
PROGRAM WILL DEFAULT TO 16 LINE SCANNER TEST

7.4 DEFAULT PARAMETERS (INCLUDING ACT-11 & "XOR")

VECTORS

-----  
DHMVEC: 300 (AUTOMATICALLY GENERATED  
DHMLVL: 302 BY PROGRAM WHEN UNDER ACT-11 OR "XOR")  
ADDRESSES

-----  
DHMCSR: 170500  
DHMLSR: 170502

NOTE: SW00(RESELECT ADDRESSES AND VECTORS BECOMES  
INOOPERATIVE UNDER ACT-11 OR "XOR").



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8.0 EXECUTION TIME

8.1 16 LINE SCANNER TEST

THE TIME FOR 2 PASSES OF THE 16 LINE SCANNER TEST IS APPROXIMATELY 1.5 MINUTES.

8.2 SINGLE LINE CABLE TEST

THE TIME FOR 12 PASSES OF THE SINGLE LINE CABLE TEST IS APPROXIMATELY 1 MINUTE.

8.3 103A MODEM CONNECT-DISCONNECT TEST

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE ANSWERING DATA SET FIRST DETECTS A RING SIGNAL TO THE TIME THAT THE PROGRAM TYPES "SET SW01=1 TO DISCONNECT".

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE PROGRAM TYPES THE ABOVE MESSAGE UNTIL THE TIME THAT THE PROGRAM TYPES "103A TEST COMPLETE".

8.4 202C MODEM CONNECT-DISCONNECT TEST

APPROXIMATELY 1.5 MINUTES WILL ELAPSE BETWEEN THE TIME THAT THE ANSWERING DATA SET DETECTS THE FIRST RING SIGNAL TO THE TIME THAT THE PROGRAM TYPES "SET SW01=1 TO DISCONNECT".

APPROXIMATELY 30 SECONDS WILL ELAPSE BETWEEN THE TIME THAT THE PROGRAM TYPES THE ABOVE MESSAGE UNTIL THE PROGRAM TYPES "202C TEST COMPLETE".

9. PROGRAM DESCRIPTION

THIS PROGRAM CONSISTS OF A SERIES OF TEST GROUPS LINKED BY A SET OF COMMON SERVICE ROUTINES AND A KEYBOARD MONITOR.

WHEN INITIALLY LOADED AND STARTED ...SW00 MUST BE SET =1, THE PROGRAM WILL BEGIN A DIALOG WITH THE OPERATOR TO INPUT THE PARAMETERS REQUIRED BY THE PROGRAM.

WHEN ALL INFORMATION HAS BEEN INPUTTED, THE PROGRAM WILL REQUEST THE OPERATOR TO SELECT A TEST BY TYPING THE NUMBER OF THE TEST TO BE RUN. WHEN A CORRECT TEST NUMBER IS RECEIVED, THE PROGRAM WILL BEGIN EXECUTION OF THE SELECTED TEST.

AT ANY TIME DURING TEST EXECUTION, THE OPERATOR MAY CHANGE A TEST PARAMETER BY ENTERING THE APPROPRIATE COMMAND VIA THE TELETYPE KEYBOARD.

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9. CONT'D

IF AN OFF LINE TEST HAS BEEN SELECTED, THAT TEST WILL  
BE REPEATED UNTIL THE OPERATOR INTERVENES.

IF AN ON LINE TEST HAS BEEN SELECTED, THE OPERATOR IS  
REQUIRED TO TAKE ACTION EACH TIME THE TEST IS COMPLETED.

AT THE END OF EVERY OFF LINE TEST PASS, THE PROGRAM  
WILL RING THE TELETYPE BELL.

AT THE END OF AN ON LINE TEST, A TEST COMPLETE MESSAGE WILL BE  
TYPED.

10. LISTING

11. MODIFICATION HISTORY:

10-JULY-84 KEN RAUHALA  
ADDED DELAY FOR PDP-11/44 WITH CACHE ON.  
THE "MUXS2" MACRO WAS MODIFIED.  
\*

```

1          .TITLE  CZDHK-F
2 000000  .ENABLE ABS,AMA
3
4          ;MODEM CONTROL DIAGNOSTIC
5          ;THIS PROGRAM CONTAINS TEST OF THE MODEM CONTROL IN
6          ;THE OFF LINE MODE OF OPERATION ONLY
7          ;MODIFIED BY ED CROWLEY APRIL, 1976
8          ;MODIFIED BY S. CARPENTER JULY, 1976 TO SUPPORT THE SOFTWARE SWITCH
9          ;REGISTER.
10         ;ALSO, SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER.
11         ;MODIFIED APRIL 77 TO RUN SCANNER TEST W/O H861 CONNETTOR
12
13         ;
14         ;MODIFIED BY S. SKONETSKI MAY 1985
15         ;REMOVE .EQUIV WHICH IS NOT RECOGNISED BY PDP11 MACRO, AND TO
16         ;DYNAMICALLY SET VECTOR SPACE WHICH IS CLOBBED BY THE NEW XXOP
17
18         ;SWITCH REGISTER OPTIONS
19
20
21         ;SW15=1, HALT ON ERROR
22         ;SW14=1, LOOP ON CURRENT TEST
23         ;SW13=1, SUPPRESS ERROR TYPEOUT
24         ;SW12=1, SUPPRESS TRACE TRAPPING(THIS IS INOPERATIVE IN THIS RELEASE)
25         ;SW11=1, SUPPRESS ITERATIONS
26         ;SW10=1, ESCAPE TO NEXT TEST ON ERROR
27         ;SW09=1, FREEZE DATA
28         ;SW01=1, START DISCONNECT SEQUENCE
29         ;SW00=1, RESELECT VECTOR AND CONTROL REGISTER ADDRESS
30         ;AFTER PROGRAM RESTART
31
32         ;STARTING ADDRESS FOR ALL TESTS IS 000200
33         ;RESTART ADDRESS=000200
34
35         ;TESTS AVAILABLE
36
37         ;TEST GROUP 0-
38         ;OFF LINE TESTS USING NO TEST CONNECTOR-FIRST TEST=0
39         ;TEST GROUP 1-
40         ;OFF LINE TESTS USING DC11 TEST CONNECTOR AND MODEM CABLE-FIRST TEST=100
41         ;TEST GROUP 2-
42         ;CONNECT/DISCONNECT TEST FOR BELL 103A MODEMS-FIRST TEST=200
43         ;TEST GROUP 3-
44         ;CONNECT/DISCONNECT TEST FOR BELL 202C MODEMS-FIRST TEST=300
45
46         ;SYMBOL DEFINITIONS
47
48         100000 SW15=100000
49         040000 SW14=40000
50         020000 SW13=20000
51         010000 SW12=10000
52         004000 SW11=4000
53         002000 SW10=2000
54         001000 SW09=1000
55         000400 SW08=400
56         000100 SW06=100
57
58         .NLIST MC,MD,CND
59         .LIST ME

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;REGISTER DEFINITIONS
R0=%0 ;GENERAL REGISTER
R1=%1 ;GENERAL REGISTER
R2=%2 ;GENERAL REGISTER
R3=%3 ;GENERAL REGISTER
R4=%4 ;GENERAL REGISTER
R5=%5 ;GENERAL REGISTER
SP=%6 ;PROCESSOR STACK POINTER
PC=%7 ;PROGRAM COUNTER

;LOCATION EQUIVALENCIES
PS =17776
PSW=17776 ;PROCESSOR STATUS WORD

.MACRO PS
PSW
.ENDM PS

RADIX=DIVIS ;CONVERSION FACTOR FOR DECIMAL OUTPUT
BINWRD=DIVIDL ;WORD TO BE CONVERTED TO OCTAL ASCII
DIGIT=DIVIDH ;ASCII OCTAL DIGIT

;CONTROL STATUS REGISTER BIT FUNCTIONS
BUSY=20 ;LINE SCANNER RUNNING
SCNENA=40 ;LINE SCANNER ENABLE
INTENA=100 ;INTERRUPT ENABLE
DONE=200 ;SCANNER DONE
STEP=400 ;CAUSES LINE COUNTER TO BE INCREMENTED BY 1 COUNT
MAINT=1000 ;FORCES IS TO INPUT OF SCRATCH PAD MEMORY
CLRMUX=2000 ;CLEAR MULTIPLEXER FUNCTION FLIPFLOPS
CLRSCN=4000 ;CLEARS SCANNER SCRATCHPAD MEMORY
SECRXF=10000 ;SECONDARY RECEIVE TRANSITION WAS DETECTED BY SCANNER
CSF=20000 ;CLEAR TO SEND TRANSITION WAS DETECTED BY SCANNER
COF=40000 ;CARRIER TRANSITION WAS DETECTED BY SCANNER
RINGF=100000 ;RING SIGNAL WAS DETECTED BY SCANNER

;LINE REGISTER BIT FUNCTIONS
LINENA=1 ;=1, RECOGNIZE TRANSITIONS ON THIS LINE
TRMRDY=2 ;=1, SEND TERMINAL READY TO MODEM
RS=4 ;=1, SEND REQUEST TO SEND TO MODEM
SECTX=10 ;=1, SEND SECONDARY TRANSMIT TO MODEM
SECRX=20 ;=1, SECONDARY RECEIVE TURNED ON BY MODEM
CS=40 ;=1, CLEAR TO SEND TURNED ON BY MODEM
CO=100 ;=1, CARRIER TURNED ON BY MODEM
RING=200 ;=1, RING TURNED ON BY MODEM

;SOFTWARE TRANSITION FLAGS
XCO=4 ;CARRIER TRANSITION WAS DETECTED
XCS=2 ;CLEAR TO SEND TRANSITION WAS DETECTED
XSCRX=1 ;SECONDARY RECEIVE TRANSITION WAS DETECTED

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13 000000
14 000000
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;INSTRUCTION DEFINITIONS
PUSH1SP=5746 ;DECREMENT PROCESSOR STACK 1 WORD
POP1SP=5726 ;INCREMENT PROCESSOR STACK 1 WORD
PUSHRO=10046 ;SAVE R0 ON STACK
POPPO=12600 ;RESTORE R0 FROM STACK
PUSH2SP=24646 ;DECREMENT STACK TWICE
POP2SP=22626 ;INCREMENT STACK TWICE

;EMT DEFINITION TABLE
EMTDEF ERRORC,+/CONTROL STATUS ERROR SERVICE/
ERRORC=EMT+X ;CONTROL STATUS ERROR SERVICE
X=X+1
EMTDEF ERRORL,+/LINE STATUS ERROR SERVICE/
ERRORL=EMT+X ;LINE STATUS ERROR SERVICE
X=X+1
EMTDEF SCOPE,+/SCOPE LOOP AND ITERATION SERVICE/
SCOPE=EMT+X ;SCOPE LOOP AND ITERATION SERVICE
X=X+1
EMTDEF SCOPEF,+/DATA FREEZE SERVICE/
SCOPEF=EMT+X ;DATA FREEZE SERVICE
X=X+1
EMTDEF TYPE,+/TELETYPE OUTPUT/
TYPE=EMT+X ;TELETYPE OUTPUT
X=X+1
EMTDEF SAV05P,+/SAVE R0-R5, PC+2 OF CALL/
SAV05P=EMT+X ;SAVE R0-R5, PC+2 OF CALL
X=X+1
EMTDEF OCTASC,+/CONVERT DATA TO ASCII AND TYPE/
OCTASC=EMT+X ;CONVERT DATA TO ASCII AND TYPE
X=X+1
EMTDEF RES05,+/RESTORE R0-R5/
RES05=EMT+X ;RESTORE R0-R5
X=X+1
EMTDEF CONVERT,+/ASCII CONVERSION ROUTINE/
CONVERT=EMT+X ;ASCII CONVERSION ROUTINE
X=X+1
EMTDEF EXTRACT,+/DIGIT EXTRACTION ROUTINE/
EXTRACT=EMT+X ;DIGIT EXTRACTION ROUTINE
X=X+1
EMTDEF ERROR,+/TYPE PC OF FAILING TESTS ONLY/
ERROR=EMT+X ;TYPE PC OF FAILING TESTS ONLY
X=X+1
EMTDEF INSTRG,+/INPUT OCTAL DATA STRING/
INSTRG=EMT+X ;INPUT OCTAL DATA STRING
X=X+1
EMTDEF ERRORT,+/TRANSITION ERROR/
ERRORT=EMT+X ;TRANSITION ERROR
X=X+1
EMTDEF ERRORS,+/ON LINE STATUS ERROR/
ERRORS=EMT+X ;ON LINE STATUS ERROR
X=X+1
EMTDEF ERRORN,+/FATAL TRANSITION/
ERRORN=EMT+X ;FATAL TRANSITION
X=X+1

```

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28 000000      104017      EMTDEF  GETLNS,+/INPUT LINE NUMBERS/
                000020      GETLNS=EMT+X          ;INPUT LINE NUMBERS
                X=X+1
29 000000      104020      EMTDEF  SETUP,+/SET UP FOR ON LINE TEST/
                000021      SETUP=EMT+X          ;SET UP FOR ON LINE TEST
                X=X+1
30 000000      104021      EMTDEF  CKRING,+/CHECK FOR RING ON CORRERT LINE/
                000022      CKRING=EMT+X          ;CHECK FOR RING ON CORRERT LINE
                X=X+1
31 000000      104022      EMTDEF  WAITRN,+/WAIT FOR TRANSITIONS/
                000023      WAITRN=EMT+X          ;WAIT FOR TRANSITIONS
                X=X+1
32 000000      104023      EMTDEF  CKTRAN,+/CHECK TRANSITIONS/
                000024      CKTRAN=EMT+X          ;CHECK TRANSITIONS
                X=X+1
33 000000      104024      EMTDEF  WAITS,+/DELAY FOR TRANSIENTS/
                000025      WAITS=EMT+X          ;DELAY FOR TRANSIENTS
                X=X+1
34 000000      104025      EMTDEF  CNTLUU,+/CHANGE SWREG ROUTINE/
                000026      CNTLUU=EMT+X          ;CHANGE SWREG ROUTINE
                X=X+1
35 000000      104026      EMTDEF  CKINTT,+/CHECK FOR INTERRUPTS-FLAG STYLE/
                000027      CKINTT=EMT+X          ;CHECK FOR INTERRUPTS-FLAG STYLE
                X=X+1
36 000000      104027      EMTDEF  KBDIN,+/FAKE INTERRUPT ENTRY POINT/
                000030      KBDIN=EMT+X          ;FAKE INTERRUPT ENTRY POINT
                X=X+1
37 000000      104030      EMTDEF  ERRINT,+/TIME OUT ERROR FOR INTERRUPTS/
                000031      ERRINT=EMT+X          ;TIME OUT ERROR FOR INTERRUPTS
                X=X+1
```

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7          000000
8          000200
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11
;TRAPCATCAER FOR ILLEGAL INTERRUPTS
.LIST MC,MD
.NLIST ME
.=0
.REPT 200
      .+2
      HALT
.ENDR
000000 000002      .+2
000002 000000      HALT
000004 000006      .+2
000006 000000      HALT
000010 000012      .+2
000012 000000      HALT
000014 000016      .+2
000016 000000      HALT
000020 000022      .+2
000022 000000      HALT
000024 000026      .+2
000026 000000      HALT
000030 000032      .+2
000032 000000      HALT
000034 000036      .+2
000036 000000      HALT
000040 000042      .+2
000042 000000      HALT
000044 000046      .+2
000046 000000      HALT
000050 000052      .+2
000052 000000      HALT
000054 000056      .+2
000056 000000      HALT
000060 000062      .+2
000062 000000      HALT
000064 000066      .+2
000066 000000      HALT
000070 000072      .+2
000072 000000      HALT
000074 000076      .+2
000076 000000      HALT
000100 000102      .+2
000102 000000      HALT
000104 000106      .+2
000106 000000      HALT
000110 000112      .+2
000112 000000      HALT
000114 000116      .+2
000116 000000      HALT
000120 000122      .+2
000122 000000      HALT
000124 000126      .+2
000126 000000      HALT
000130 000132      .+2
000132 000000      HALT
000134 000136      .+2
000136 000000      HALT

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000140	000142	.+2
000142	000000	HALT
000144	000146	.+2
000146	000000	HALT
000150	000152	.+2
000152	000000	HALT
000154	000156	.+2
000156	000000	HALT
000160	000162	.+2
000162	000000	HALT
000164	000166	.+2
000166	000000	HALT
000170	000172	.+2
000172	000000	HALT
000174	000176	.+2
000176	000000	HALT
000200	000202	.+2
000202	000000	HALT
000204	000206	.+2
000206	000000	HALT
000210	000212	.+2
000212	000000	HALT
000214	000216	.+2
000216	000000	HALT
000220	000222	.+2
000222	000000	HALT
000224	000226	.+2
000226	000000	HALT
000230	000232	.+2
000232	000000	HALT
000234	000236	.+2
000236	000000	HALT
000240	000242	.+2
000242	000000	HALT
000244	000246	.+2
000246	000000	HALT
000250	000252	.+2
000252	000000	HALT
000254	000256	.+2
000256	000000	HALT
000260	000262	.+2
000262	000000	HALT
000264	000266	.+2
000266	000000	HALT
000270	000272	.+2
000272	000000	HALT
000274	000276	.+2
000276	000000	HALT
000300	000302	.+2
000302	000000	HALT
000304	000306	.+2
000306	000000	HALT
000310	000312	.+2
000312	000000	HALT
000314	000316	.+2
000316	000000	HALT
000320	000322	.+2

000322	000000	HALT
000324	000326	.+2
000326	000000	HALT
000330	000332	.+2
000332	000000	HALT
000334	000336	.+2
000336	000000	HALT
000340	000342	.+2
000342	000000	HALT
000344	000346	.+2
000346	000000	HALT
000350	000352	.+2
000352	000000	HALT
000354	000356	.+2
000356	000000	HALT
000360	000362	.+2
000362	000000	HALT
000364	000366	.+2
000366	000000	HALT
000370	000372	.+2
000372	000000	HALT
000374	000376	.+2
000376	000000	HALT
000400	000402	.+2
000402	000000	HALT
000404	000406	.+2
000406	000000	HALT
000410	000412	.+2
000412	000000	HALT
000414	000416	.+2
000416	000000	HALT
000420	000422	.+2
000422	000000	HALT
000424	000426	.+2
000426	000000	HALT
000430	000432	.+2
000432	000000	HALT
000434	000436	.+2
000436	000000	HALT
000440	000442	.+2
000442	000000	HALT
000444	000446	.+2
000446	000000	HALT
000450	000452	.+2
000452	000000	HALT
000454	000456	.+2
000456	000000	HALT
000460	000462	.+2
000462	000000	HALT
000464	000466	.+2
000466	000000	HALT
000470	000472	.+2
000472	000000	HALT
000474	000476	.+2
000476	000000	HALT
000500	000502	.+2
000502	000000	HALT

000504	000506	.+2
000506	000000	HALT
000510	000512	.+2
000512	000000	HALT
000514	000516	.+2
000516	000000	HALT
000520	000522	.+2
000522	000000	HALT
000524	000526	.+2
000526	000000	HALT
000530	000532	.+2
000532	000000	HALT
000534	000536	.+2
000536	000000	HALT
000540	000542	.+2
000542	000000	HALT
000544	000546	.+2
000546	000000	HALT
000550	000552	.+2
000552	000000	HALT
000554	000556	.+2
000556	000000	HALT
000560	000562	.+2
000562	000000	HALT
000564	000566	.+2
000566	000000	HALT
000570	000572	.+2
000572	000000	HALT
000574	000576	.+2
000576	000000	HALT
000600	000602	.+2
000602	000000	HALT
000604	000606	.+2
000606	000000	HALT
000610	000612	.+2
000612	000000	HALT
000614	000616	.+2
000616	000000	HALT
000620	000622	.+2
000622	000000	HALT
000624	000626	.+2
000626	000000	HALT
000630	000632	.+2
000632	000000	HALT
000634	000636	.+2
000636	000000	HALT
000640	000642	.+2
000642	000000	HALT
000644	000646	.+2
000646	000000	HALT
000650	000652	.+2
000652	000000	HALT
000654	000656	.+2
000656	000000	HALT
000660	000662	.+2
000662	000000	HALT
000664	000666	.+2

000666	000000	HALT
000670	000672	.+2
000672	000000	HALT
000674	000676	.+2
000676	000000	HALT
000700	000702	.+2
000702	000000	HALT
000704	000706	.+2
000706	000000	HALT
000710	000712	.+2
000712	000000	HALT
000714	000716	.+2
000716	000000	HALT
000720	000722	.+2
000722	000000	HALT
000724	000726	.+2
000726	000000	HALT
000730	000732	.+2
000732	000000	HALT
000734	000736	.+2
000736	000000	HALT
000740	000742	.+2
000742	000000	HALT
000744	000746	.+2
000746	000000	HALT
000750	000752	.+2
000752	000000	HALT
000754	000756	.+2
000756	000000	HALT
000760	000762	.+2
000762	000000	HALT
000764	000766	.+2
000766	000000	HALT
000770	000772	.+2
000772	000000	HALT
000774	000776	.+2
000776	000000	HALT

13  
14

.LIST ME  
.NLIST MC,MD,CND

```

1
2
3
4
5 000024 000024 .-24
6 000026 000340 PFAIL ;POWER FAIL HANDLER
7 000030 013044 340 ;SERVICE AT LEVEL 7
8 000032 000340 EMTSRV ;EMT DISPATCH SERVICE
9 340 ;SERVICE AT LEVEL 7
10
11 000046 013026 .-46 LOGICAL ;ACT11?
12
13 000060 .-60
14 000060 002020 KBDINT ;KEYBOARD MONITOR
15 000062 000340 340 ;SERVICE AT LEVEL 7
16
17 000174 000174 .-174
18 000176 000000 DISPREG: 0
19 SWREG: 0
20
21 000200 000200 000674 .-200 JMP START ;GO TO START OF PROGRAM
22
23
24 .MACRO TS XN,X
25 .NLIST
26 ;REFERANCE NUMBER DEFINITION
27 .LIST
28 T'XN': ;REFERENCE DESIGNATION
29 .NLIST
30 N'X'=N'X'+1
31 .LIST
32 .ENDM
33 .MACRO TSS XNN
34 T'XNN':
35 .NLIST
36 NN=NN+1
37 .LIST
38 .ENDM
39
40 .MACRO COMMENT
41 .NLIST
42 ;EMT GENERATOR
43 X=0
44 .LIST
45 .ENDM
46 000204 COMMENT
000000 ;EMT GENERATOR
X=0
47 .MACRO EMTDEF FNCT,COMMNT
48 FNCT=EMT+X ;'COMMNT'
49 .NLIST
50 X=X+1
51 .LIST
52 .ENDM
53 .MACRO COMMENT
54 .NLIST
55 ;TEST TABLE GENERATOR

```

```
56 .LIST
57 .ENDM
58 000204 COMMENT
;TEST TABLE GENERATOR

59 .MACRO TM XM,Y
60 .NLIST
61 TIMES=4000
62 .IIF GT MO-21,TIMES=400
63 .IIF GT MO-24,TIMES=200
64 .IIF GT MO-34,TIMES=4000
65 .LIST
66 T'XM'
67 TIMES
68 .NLIST
69 M'Y'=M'Y'+1
70 .LIST
71 .ENDM
72
```

```

1
2      001100      . =1100
3      001100      STACK:
4      001100 012702 000024      START:  MOV    #24, R2          ; SET UP VECTOR AREA
5      001104 012722 015204      MOV    #PFAIL, (R2)+      ; POWER FAIL HANDLER
6      001110 012722 000340      MOV    #340, (R2)+      ; SERVICE AT LEVEL 7
7      001114 012722 013044      MOV    #EMTSRV, (R2)+    ; EMT DISPATCH SERVICE
8      001120 012722 000340      MOV    #340, (R2)+      ; SERVICE AT LEVEL 7
9      001124 012737 013026 000046      MOV    #LOGICAL, @#46
10     001132 012737 002020 000060      MOV    #KBDINT, @#60
11     001140 012737 000340 000062      MOV    #340, @#62      ; SERVICE AT LEVEL 7
12     001146 005067 000644      CLR    TIPFLG           ; CLEAR TEST IN PROGRESS FLAG
13     001152 005077 014440      CLR    @TKCSR
14     001156 012706 001100      MOV    #STACK, SP      ; SET UP STACK POINTER
15
16     001162 013746 000006      SUSWR: MOV    @#6, -(SP)      ; SAVE VECTORS
17     001166 013746 000004      MOV    @#4, -(SP)
18     001172 012737 001212 000004      MOV    #64$, @#4      ; SET UP FOR TIMEOUT
19     001200 022777 177777 014420      CMP    #-1, @SWR      ; REFERENCE HARDWARE SWITCH REGISTER
20     001206 001402      BEQ    65$
21     001210 000407      BR     66$
22     001212 022626      64$:  CMP    (SP)+, (SP)+    ; ADJUST STACK
23     001214 012767 000176 014404      65$:  MOV    #SWREG, SWR    ; POINT TO SOFTWARE SWITCH REG
24     001222 012767 000174 014400      MOV    #DISPREG, DISPLAY ; POINT TO SOFT DISPLAY REG
25     001230 012637 000004      66$:  MOV    (SP)+, @#4      ; RESTORE VECTORS
26     001234 012637 000006      MOV    (SP)+, @#6
27     001240 012777 000100 014350      MOV    #INTENA, @TKCSR  ; ENABLE TELETYPE INTERRUPTS
28     001246 005067 000040      CLR    XFLAG           ; XOR = NO
29
30     ;*****
31     ;REPLACE THE FOLLOWING BRANCH WITH A "NOP" (240) TO ACTIVATE "XOR" CODE
32     ;*****
33     001252 000423      BR     STARTO          ; SKIP XOR STUFF
34     001254 016746 176524      MOV    4, -(SP) ; SAVE 4
35     001260 012767 001314 176516      MOV    #XORSVC, 4      ; SET UP SVC ROUTINE
36     001266 005767 175566      TST    177060 ; GOT AN XOR TESTER OUT THERE ?
37     001272 012667 176506      MOV    (SP)+, 4 ; YES
38     001276 005167 000010      COM    XFLAG ; XOR = YES
39     001302 004767 014020      JSR    PC, XOR ; AUTO VECTOR
40     001306 000167 000010      JMP    STARTO ; RESTORE TRAPCATCHER
41     001312 000000      XFLAG: 0 ; XOR FLAG
42     001314 022626      XORSVC: POP2SP
43     001316 012667 176462      MOV    (SP)+, 4 ; RESTORE 4
44     001322 005767 014370      STARTO: TST    TIPFLG ; TYPED TITLE?
45     001326 001005      BNE    .+14 ; YES
46     001330 104004      TYPE   ; TYPE "MODEM CONTROL DIAGNOSTIC"
47     001332 016575      MTITLE
48     001334 012767 000001 014354      MOV    #1, TIPFLG ; SET TITLE TYPED FLAG
49     001342 005767 177744      TST    XFLAG ; X OR ?
50     001346 100422      BMI    VECSTR ; RESTORE TRAPCATCHER
51     001350 005767 176466      TST    42 ; ACT 11?
52     001354 001403      BEQ    START1 ; NO
53     001356 004767 013744      JSR    PC, XOR ; YES AUTO VECTOR
54     001362 000414      BR     VECSTR ; GET VECTOR AND REGISTER ADDRESS
55     001364 005737 000042      START1: TST    @#4 ; UNDER MONITOR?
56     001370 001005      BNE    1$
57     001372 022767 000176 014226      CMP    #SWREG, SWR ; USING SWREG?
58     001400 001001      BNE    1$

```

```

58 001402 104025          CNTLUU
59 001404 032777 000001 014214 1#: BIT #1,@SWR ;IF SW BIT 0=1, ON PROGRAM RESTART
60 001412 001510          BEQ STARTN ;INPUT VECTOR AND REGISTER ADDRESSES
61 001414 012706 001100 VECSTR: MOV #STACK,SP ;SET UP PROCESSOR STACK POINTER
62 001420 012767 000300 011324 MOV #300,DATA1 ;ADDRESS OF FIRST FLOATING VECTOR
63 001426 012767 000302 011320 MOV #302,DATA2 ;ADDRESS OF STATUS WORD
64 001434 016777 011314 011310 VECSTA: MOV DATA2,@DATA1 ;MOVE ADDRESS OF STATUS WORD TO VECTOR
65 001442 005077 011306          CLR @DATA2 ;CLEAR STATUS WORD
66                                ;(FOR HALT ON ILLEGAL INTERRUPT)
67 001446 062767 000004 011276          ADD #4,DATA1 ;NEXT VECTOR
68 001454 062767 000004 011272          ADD #4,DATA2 ;NEXT STATUS WORD
69 001462 026727 011264 001000          CMP DATA1,#1000 ;IS TABLE CLEARED
70 C,1470 001361          BNE VECSTA ;IF NOT, CONTINUE
71 001472 005767 177614          TST XFLAG ;XOR ?
72 001476 100523          BMI TSTGO ;YES
73 001500 005767 176336          TST 42 ;ACT 11 ?
74 001504 001120          BNE TSTGO ;YES
75 001506 104013          INSTRG ;GET VECTOR ADDRESS
76 001510 016661          MVECTOR ;MESSAGE "VECTOR ADDRESS-"
77 001512 000300          300 ;LOWER LIMIT FOR ADDRESS
78 001514 000774          774 ;UPPER LIMIT FOR ADDRESS
79 001516 015606          DHMVEC ;STORAGE FOR ADDRESS
80 001520 032767 000003 014060 1#: BIT #3,DHMVEC ;TEST 2 LSB OF ADDRESS
81 001526 001404          BEQ VECST1 ;IF 0, CONTINUE
82 001530 012716 001520          MOV #1#,(SP)
83 001534 000167 013274          JMP INSTER ;INCORRECT ADDRESS, TRY AGAIN
84 001540 016767 014042 014042 VECST1: MOV DHMVEC,DHMLVL ;GENERATE ADDRESS OF
85 001546 062767 000002 014034          ADD #2,DHMLVL ;INTERRUPT STATUS WORD
86 001554 104013          INSTRG ;GET ADDRESS OF CONTROL REGISTER
87 001556 016703          MREGAD ;MESSAGE "REGISTER ADDRESS-"
88 001560 170500          170500 ;LOWER LIMIT FOR ADDRESS
89 001562 177777          177777 ;UPPER LIMIT FOR ADDRESS
90 001564 015612          DHMCSR ;STORAGE FOR ADDRESS
91 001566 032767 000007 014016 1#: BIT #7,DHMCSR ;IF 3 LSB ARE NOT 0
92 001574 001404          BEQ REGST1
93 001576 012716 001566          MOV #1#,(SP)
94 001602 000167 013226          JMP INSTER ;INCORRECT ADDRESS, TRY AGAIN
95 001606 016767 014000 014000 REGST1: MOV DHMCSR,DHMLSR ;SET UP ADDRESS OF LINE STATUS REGISTER
96 001614 062767 000002 013772          ADD #2,DHMLSR
97 001622 104013          INSTRG ;GET LINE SELECT PARAMETER
98 001624 016737          MLINSL
99 001626 000000          0
100 001630 177777          177777
101 001632 015720          LINSEL

```



```

1
2 001634 012706 001100   STARTN: MOV   #STACK,SP           ;SET UP PROCESSOR STACK
3 001640 104013           INSTRG           ;GET TEST NUMBER
4 001642 016771           MTEST           ;MESSAGE "TEST-"
5 001644 000000           0               ;LOWER LIMIT FOR TEST NUMBER
6 001646 000777           777            ;UPPER LIMIT FOR TEST NUMBER
7 001650 015640           TSTNO          ;STORAGE FOR TEST NUMBER
8 001652 016705 013762   X1A:  MOV   TSTNO,R5           ;GET TEST NUMBER
9 001656 042705 177077   BIC   #177077,R5 ;EXTRACT TEST GROUP NUMBER
10
11 .REPT 5
12 .ENDR  ASR   R5
        ASR   R5
        ASR   R5
        ASR   R5
        ASR   R5
13 001674 016567 017532 013772   MOV   GRO(R5),TSTMAX ;GET HIGHEST TEST IN GROUP
14 001702 016567 017512 013762   MOV   TSTLST(R5),TSTPNT ;GET POINTER TO TEST TABLE
15 001710 005767 013756           TST           ;IF 0, INVALID TEST GROUP
16 001714 001004           BNE   STRTOA
17 001716 012716 001652   X1B:  MOV   #X1A,(SP)
18 001722 000167 013106           JMP   INSTER           ;TRY AGAIN
19 001726 042767 177700 013704   STRTOA: BIC   #177700,TSTNO ;GET NUMBER OF FIRST TEST
20                                     ;TO BE EXECUTED IN SELECTED GROUP
21 001734 026767 013700 013732   CMP   TSTNO,TSTMAX ;IS NUMBER TOO LARGE
22 001742 003401           BLE   TSTGO
23 001744 000764           BR    X1B
24 001746 012746 000340   TSTGO: MOV   #340,-(SP) ;SET UP PRIORITY LEVEL
25 001752 005746           PUSH1SP
26 001754 000005           RESET
27 001756 012767 002242 000260   MOV   #DMYRTI,KRET ;SET UP DUMMY KEYBOARD RETURN
28 001764 005067 013706           CLR   LINFLG ;CLEAR LINE SELECTED FLAG
29 001770 005067 013640           CLR   TRACON ;CLEAR TRACE TRAP FLAG
30 001774 005067 013636           CLR   PASCNT ;CLEAR PASS COUNT
31 002000 104004           TYPE
32 002002 017005           MCRLF
33 002004 012767 000001 000004 1$:  MOV   #1,TIPFLG ;SET TEST IN PROGRESS FLAG
34 002012 000167 011250           JMP   TSTENT ;START TESTING
35 002016 000000           TIPFLG: 0
36
37
38
39
40                                     ;TELETYPE KEYBOARD INTERRUPT SERVICE ROUTINE
41
42 002020 005067 177772   KBDINT: CLR   TIPFLG ;CLEAR TEST IN PROGRESS FLAG
43 002024 005067 012232           CLR   TMP1
44 002030 005067 000212           CLR   SINTFL ;CLEAR SOFTWARE INTERRUPT FLAG
45 002034 117767 013560           MOVB  #TKDBR,TMP1
46 002042 142767 000200 012212   BICB  #200,TMP1
47 002050 122767 000003 012204   CMPB  #3,TMP1 ;IF <CTRL C> WAS TYPED
48 002056 001011           BNE   KBDIN1 ;TYPE "+C" AND
49 002060 104004           TYPE ;SELECT NEW TEST
50 002062 017235           MCONTC
51 002064 022626           POP2SP
52 002066 005077 013520           CLR   @DHMCSR

```

```

53 002072 005077 013520          CLR      @TKCSR
54 002076 000167 177532          JMP      STARTN
55 002102 122767 000026 012152 KBDIN1: CMPB    #26,TMP1          ;IF <CONTROL V> WAS TYPED
56 002110 001011                   BNE     KBDIN2          ;TYPE "+V" AND GET NEW
57 002112 104004                   TYPE    ;VECTOR AND REGISTER ADDRESS
58 002114 017240                   MCONTV
59 002116 022626                   POP2SP
60 002120 005077 013466          CLR      @DMHCSR
61 002124 005077 013466          CLR      @TKCSR
62 002130 000167 177260          JMP      VECSTR
63 002134 122767 000014 012120 KBDIN2: CMPB    #14,TMP1          ;IF <CONTROL L> WAS TYPED
64 002142 001015                   BNE     KBDIN3          ;TYPE "+L" AND GET NEW
65 002144 104004                   TYPE    ;LINE NUMBERS, UNLESS
66 002146 017243                   MCONTL ;TEST GROUP 0 WAS IN PROGRESS
67 002150 022767 002242 000066   CMP     @DMYRTI,KRET    ;IF <CONTROL L> WAS TYPED IN TEST
68 002156 001431                   BEQ     DMYRTI          ;GROUP 0, IGNORE
69 002160 022626                   POP2SP
70 002162 005077 013424          CLR      @DMHCSR
71 002166 005077 013424          CLR      @TKCSR
72 002172 000177 000046          JMP      @KRET
73 002176 005737 000042          KBDIN3: TST     @#42
74 002202 001011                   BNE     1$
75 002204 022767 000176 013414   CMP     #SWREG,SWR
76 002212 001005                   BNE     1$
77 002214 122767 000007 012040   CMPB   #7,TMP1          ;IS IT <+G>
78 002222 001001                   BNE     1$
79 002224 104025                   CNTLUJ
80 002226 012767 000001 000012 1$:  MOV     #1,SINTFL        ;SET SOFTWARE INTERRUPT FLAG
81 002234 012767 000001 177554   MOV     #1,TIPFLG       ;SET TEST IN PROGRESS FLAG
82 002242 000002          DMYRTI: RTI
83                                     .EVEN
84 002244 000000          KRET: 0
85 002246 000000          SINTFL: 0
86
109
131
132
213
214
288
289                                     ;INITIALIZATION CHECK - PERFORMED ONLY AT PROGRAM START
290                                     ;VERIFY THAT CONTROL STATUS REGISTER AND LINE STATUS
291                                     ;REGISTER WERE CLEARED BY INITIALIZE
292
293          .MACRO  COMMENT
294          .NLIST
295          NC 0
296          N=NO
297          XN=NO
298          .LIST
299          .ENDM
300 002250          COMMENT
          NO=0
          N=NO
          XN=NO
301 002250          TS \NO,0
          ;REFERANCE NUMBER DEFINITION

```



338

339

340

341 002402

;VERIFY "MAINTENANCE MODE" CAN BE SET AND CLEARED

002402

TS \NO,0

;REFERANCE NUMBER DEFINITION

T3:

;REFERENCE DESIGNATION

NO=NO+1

342 002402 012777 001000 013202  
 343 002410 032777 001000 013174  
 344 002416 001001  
 345 002420 104012  
 346 002422 042777 001000 013162  
 347 002430 032777 001000 013154  
 348 002436 001401  
 349 002440 104012  
 350 002442 104002

CSTR3: MOV #MAINT,@DHMCSR ;SET MAINTENANCE MODE  
 BIT #MAINT,@DHMCSR ;WAS MAINTENANCE MODE SET  
 BNE .+4  
 ERROR ;NO, ERROR  
 BIC #MAINT,@DHMCSR ;CLEAR MAINTENANCE MODE  
 BIT #MAINT,@DHMCSR ;WAS MAINTENANCE MODE CLEARED  
 BEQ .+4  
 ERROR ;NO, ERROR  
 SCOPE ;CHECK FOR ITERATIONS, LOOP

```

1
2
3
4 002444      TS \NO,0
                ;VERIFY THAT "SCAN ENABLE" CAN BE SET AND CLEARED.
                ;REFERENCE NUMBER DEFINITION
                T4:
                NO=NO+1
                ;REFERENCE DESIGNATION
5 002444      000005
6 002452      012777 000040 013140
7 002460      032777 000040 013132
8
9 002462      001001
10 002464     104012
11 002472     042777 000040 013120
12 002472     032777 000040 013112
13 002500     001401
14 002502     104012
15 002504     104002
16
17
18
19
20 002506     TS \NO,0
                ;VERIFY THAT "BUSY" IS SET WHEN "SCAN ENABLE" IS SET
                ;VERIFY THAT "BUSY" IS CLEARED WHEN "SCAN ENABLE" IS CLEARED
                ;REFERENCE NUMBER DEFINITION
                T5:
                NO=NO+1
                ;REFERENCE DESIGNATION
21 002506     000006
22 002514     012777 000040 013076
23 002514     032777 000020 013070
24 002522     001001
25 002524     104012
26 002526     042777 000040 013056
27 002534     032777 000020 013050
28 002542     001401
29 002544     104012
30 002546     104002
31
32
33
34 002550     TS \NO,0
                ;VERIFY THAT SETTING "DONE" DOES NOT CCAUSE AN
                ;INTERRUPT IF "INTERRUPT ENABLE" IS CLEARED.
                ;REFERENCE NUMBER DEFINITION
                T6:
                NO=NO+1
                ;REFERENCE DESIGNATION
35 002550     000007
36 002556     052767 000340 175220
37 002556     005077 013030
38 002562     012777 002616 013016
39 002570     016777 175202 013012
40 002576     052777 000200 013006
41 002604     042767 000340 175164
42 002612     00024C
43 002614     000402
44 002616     022626
45 002620     104012
46 002622     104002

                INT1:  BIS #340,PS ;LOCK OUT INTERRUPTS
                CLR #DHMCSR ;CLEAR CONTROL REGISTER
                MOV #INT1A,@DHMVEC ;SET UP INTERRUPT SERVICE ADDRESS
                MOV PS,@DHMLVL ;SET UP INTERRUPT PRIORITY
                BIS #DONE,@DHMCSR ;SET DONE
                BIC #340,PS ;ALLOW INTERRUPTS
                NOP ;DELAY FOR INTERRUPT
                BR INT1B ;NO INTERRUPT, CONTINUE
                INT1A: POP2SP ;RESTORE STACK, INTERRUPT
                ERROR ;OCCURED, ERROR
                INT1B: SCOPE ;CHECK FOR LOOP, ITERATIONS

```



49  
50  
51  
52  
53  
54

LVL=LVL-1  
T=T+1

.LIST  
.IIF EQ NO-12,.PAGE  
.IIF EQ NO-15,.PAGE  
.ENDR

002770

NOINT \ST,\LVL,\T

;VERIFY THAT NO INTERRUPT OCCURS WITH  
;"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 7.

002770

TS \NO,0

;REFERENCE NUMBER DEFINITION

002770

T11:  
NO=NO+1  
INT4:

;REFERENCE DESIGNATION

002770 000012  
002770 005077 012616  
002774 042767 000340 174774  
003002 052767 000340 174766  
003010 012777 003052 012570  
003016 016777 174754 012564  
003024 012777 000100 012560  
003032 052777 000200 012552  
003040 000240  
003042 000240  
003044 005077 012542  
003050 000402  
003052 022626  
003054 104012  
003056 104002  
000300  
000006  
000005

CLR @DHMCSR  
BIC #340,PS  
BIS #340,PS  
MOV #INT4A,@DHMVEC  
MOV PS,@DHMLVL  
MOV #INTENA,@DHMCSR  
BIS #DONE,@DHMCSR  
NOP  
NOP  
CLR @DHMCSR  
BR INT4B

;CLEAR CONTROL REGISTER  
;SET PROCESSOR PRIORITY  
;TO LEVEL 7.  
;SET UP INTERRUPT SERVICE ADDRESS  
;SET UP INTERRUPT SERVICE LEVEL  
;SET INTERRUPT ENABLE  
;GENERATE INTERRUPT  
;DELAY FOR INTERRUPT

INT4A: POP2SP  
ERROR

;NO INTERRUPT, CONTINUE  
;RESTORE STACK  
;INTERRUPT OCCURED, ERROR  
;CHECK FOR ITERATION, LOOP

INT4B: SCOPE  
ST=ST-40  
LVL=LVL-1  
T=T+1

```

0
003060      .IIF EQ NO 15..PAGE
            NOINT  \ST,\LVL,\T

            ;VERIFY THAT NO INTERRUPT OCCURS WITH
            ;"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 6.

003060      TS \NO.0
            ;REFERENCE NUMBER DEFINITION

003060      T12:
            NO=NO-1      ;REFERENCE DESIGNATION
            INT5:  CLR    @DHMCSR      ;CLEAR CONTROL REGISTER
                   BIC    @340,PS     ;SET PROCESSOR PRIORITY
                   BIS    @300,PS     ;TO LEVEL 6.
                   MOV    @INT5A,@DHMVEC ;SET UP INTERRUPT SERVICE ADDRESS
                   MOV    PS,@DHMLVL   ;SET UP INTERRUPT SERVICE LEVEL
                   MOV    @INTENA,@DHMCSR ;SET INTERRUPT ENABLE
                   BIS    @DONE,@DHMCSR ;GENERATE INTERRUPT
                   NOP
                   NOP                ;DELAY FOR INTERRUPT
                   CLR    @DHMCSR
                   BR     INT5B        ;NO INTERRUPT, CONTINUE
            INT5A:  POP2SP              ;RESTORE STACK
                   ERROR              ;INTERRUPT OCCURED, ERROR
            INT5B:  SCOPE                ;CHECK FOR ITERATION, LOOP
                   ST=ST-40
                   LVL=LVL-1
                   T=T+1

            .IIF EQ NO 12..PAGE
            .IIF EQ NO-15..PAGE
            NOINT  \ST,\LVL,\T

            ;VERIFY THAT NO INTERRUPT OCCURS WITH
            ;"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 5.

003150      TS \NO.0
            ;REFERENCE NUMBER DEFINITION

003150      T13:
            NO=NO-1      ;REFERENCE DESIGNATION
            INT6:  CLR    @DHMCSR      ;CLEAR CONTROL REGISTER
                   BIC    @340,PS     ;SET PROCESSOR PRIORITY
                   BIS    @240,PS     ;TO LEVEL 5.
                   MOV    @INT6A,@DHMVEC ;SET UP INTERRUPT SERVICE ADDRESS
                   MOV    PS,@DHMLVL   ;SET UP INTERRUPT SERVICE LEVEL
                   MOV    @INTENA,@DHMCSR ;SET INTERRUPT ENABLE
                   BIS    @DONE,@DHMCSR ;GENERATE INTERRUPT
                   NOP                ;DELAY FOR INTERRUPT
                   NOP
                   CLR    @DHMCSR
                   BR     INT6B        ;NO INTERRUPT, CONTINUE
            INT6A:  POP2SP              ;RESTORE STACK
                   ERROR              ;INTERRUPT OCCURED, ERROR
            INT6B:  SCOPE                ;CHECK FOR ITERATION, LOOP
                   ST=ST-40
                   LVL=LVL-1
                   T=T+1

            .IIF EQ NO-12..PAGE
            .IIF EQ NO-15..PAGE

```



003240

NOINT \ST,\LVL,\T

;VERIFY THAT NO INTERRUPT OCCURS WITH  
; "INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 4.

003240

TS \NO,0

;REFERANCE NUMBER DEFINITION

003240

000015

T14:

;REFERENCE DESIGNATION

003240

005077

012346

NO=NO-1

003244

042767

000340

174524

INT7:

CLR @DHMCSR

;CLEAR CONTROL REGISTER

003252

052767

000200

174516

BIC #340,PS

;SET PROCESSOR PRIORITY

003260

012777

003322

012320

BIS #200,PS

;TO LEVEL 4.

003266

016777

174504

012314

MOV #INT7A,@DHMVEC

;SET UP INTERRUPT SERVICE ADDRESS

003274

012777

000100

012310

MOV PS,@DHMLVL

;SET UP INTERRUPT SERVICE LEVEL

003302

052777

000200

012302

MOV #INTENA,@DHMCSR

;SET INTERRUPT ENABLE

003310

000240

BIS #DONE,@DHMCSR

;GENERATE INTERRUPT

003312

000240

NOP

;DELAY FOR INTERRUPT

003314

005077

012272

NOP

003320

000402

CLR @DHMCSR

;NO INTERRUPT, CONTINUE

003322

022626

BR INT7B

;RESTORE STACK

003324

104012

INT7A: POP2SP

;INTERRUPT OCCURED, ERROR

003326

104002

INT7B: SCOPE

;CHECK FOR ITERATION, LOOP

000140

ST=ST-40

000003

LVL=LVL 1

000010

T=T-1

.IIF EQ NO-12,.PAGE

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9 003330
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003330
003330
003330 000016
003330 005077 012256
003334 042767 000340 174434
003342 012777 003412 012236
003350 005077 012234
003354 052767 000000 174414
003362 012777 000100 012222
003370 052777 000200 012214
003376 000240
003400 000240
003402 005077 012204
003406 104012
003410 000401
003412 022626
003414 104002
000040
000001
000011
003416
003416
003416 000017

.MACRO COMMENT
.NLIST
ST=0
LVL=0
T=10
.LIST
.ENDM
COMMENT
ST=0
LVL=0
T=10
.REPT 4
INTS \ST,\LVL,\T
.NLIST
ST=ST+40
LVL=LVL+1
T=T+1
.LIST
.IIF EQ NO-20,.PAGE
.ENDR
INTS \ST,\LVL,\T
;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;ENABLE" SET AND "DONE" SET AT PRIORITY 0.

TS \NO,0
;REFERANCE NUMBER DEFINITION
T15:
NO=NO+1
;REFERENCE DESIGNATION
INT10: CLR @DHMCSR ;CLEAR CONTROL REGISTER
BIC @340,PS ;ALLOW INTERRUPTS
MOV @INT10A,@DHMVEC ;SET UP INTERRUPT SERVICE ADDRESS
CLR @DHMLVL ;SET UP INTERRUPT SERVICE PRIORITY
BIS @0,PS ;SET PROCESSOR PRIORITY TO LEVEL 0.
MOV @INTENA,@DHMCSR ;SET INTERRUPT ENABLE
BIS @DONE,@DHMCSR ;GENERATE INTERRUPT
NOP ;WAIT FOR INTERRUPT
NOP
CLR @DHMCSR
ERROR ;NO INTERRUPT, ERROR
BR INT10B ;CONTINUE
INT10A: POP2SP ;INTERRUPT OCCURED, RESTORE STACK
INT10B: SCOPE ;CHECK FOR INTERATIONS, LOOP.
ST=ST+40
LVL=LVL+1
T=T+1
.IIF EQ NO-20,.PAGE
INTS \ST,\LVL,\T
;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;ENABLE" SET AND "DONE" SET AT PRIORITY 1.

TS \NO,0
;REFERANCE NUMBER DEFINITION
T16:
NO=NO+1
;REFERENCE DESIGNATION

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003416 005077 012170          INT11: CLR      @DHMCSR          ;CLEAR CONTROL REGISTER
003422 042767 000340 174346    BIC      #340,PS          ;ALLOW INTERRUPTS
003430 012777 003500 012150    MOV      @INT11A,@DHMVEC  ;SET UP INTERRUPT SERVICE ADDRESS
003436 005077 012146          CLR      @DHMLVL         ;SET UP INTERRUPT SERVICE PRIORITY
003442 052767 000040 174326    BIS      #40,PS          ;SET PROCESSOR PRIORITY TO LEVEL 1.
003450 012777 000100 012134    MOV      @INTENA,@DHMCSR  ;SET INTERRUPT ENABLE
003456 052777 000200 012126    BIS      @DONE,@DHMCSR   ;GENERATE INTERRUPT
003464 000240          NOP                      ;WAIT FOR INTERRUPT
003466 000240          NOP
003470 005077 012116          CLR      @DHMCSR
003474 104012          ERROR                  ;NO INTERRUPT, ERROR
003476 000401          BR      INT11B          ;CONTINUE
003500          INT11A: POP2SP        ;INTERRUPT OCCURED, RESTORE STACK
003502 104002          INT11B: SCOPE           ;CHECK FOR INTERATIONS, LOOP.
          000100          ST=ST+40
          000002          LVL=LVL+1
          000012          T=T+1

.IIF EQ NO-20, .PAGE
003504          INTS      \ST,\LVL,\T

          ;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
          ;ENABLE" SET AND "DONE" SET AT PRIORITY 2.

003504          TS \NO,0
          ;REFERANCE NUMBER DEFINITION
003504          T17:
          NO=NO+1          ;REFERENCE DESIGNATION
003504 000020          INT12: CLR      @DHMCSR          ;CLEAR CONTROL REGISTER
003510 005077 012102          BIC      #340,PS          ;ALLOW INTERRUPTS
003516 042767 000340 174260    MOV      @INT12A,@DHMVEC  ;SET UP INTERRUPT SERVICE ADDRESS
003524 012777 003566 012062    CLR      @DHMLVL         ;SET UP INTERRUPT SERVICE PRIORITY
003530 052767 000100 174240    BIS      #100,PS         ;SET PROCESSOR PRIORITY TO LEVEL 2.
003536 012777 000100 012046    MOV      @INTENA,@DHMCSR  ;SET INTERRUPT ENABLE
003544 052777 000200 012040    BIS      @DONE,@DHMCSR   ;GENERATE INTERRUPT
003552 000240          NOP                      ;WAIT FOR INTERRUPT
003554 000240          NOP
003556 005077 012030          CLR      @DHMCSR
003562 104012          ERROR                  ;NO INTERRUPT, ERROR
003564 000401          BR      INT12B          ;CONTINUE
003566 022626          INT12A: POP2SP        ;INTERRUPT OCCURED, RESTORE STACK
003570 104002          INT12B: SCOPE           ;CHECK FOR INTERATIONS, LOOP.
          000140          ST=ST+40
          000003          LVL=LVL+1
          000013          T=T+1

```

0 003572

INTS \ST,\LVL,\T

;VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT  
;ENABLE" SET AND "DONE" SET AT PRIORITY 3.

003572

TS \NO,0

;REFERANCE NUMBER DEFINITION

003572

T20:

;REFERENCE DESIGNATION

000021

NO=NO+1

003572 005077 012014

INT13:

;CLEAR CONTROL REGISTER

003576 042767 000340 174172

CLR @DHMCSR

;ALLOW INTERRUPTS

003604 012777 003654 011774

BIC #340,PS

;SET UP INTERRUPT SERVICE ADDRESS

003612 005077 011772

MOV #INT13A,@DHMVEC

;SET UP INTERRUPT SERVICE PRIORITY

003616 052767 000140 174152

CLR @DHMLVL

;SET PROCESSOR PRIORITY TO LEVEL 3.

003624 012777 000100 011760

BIS #140,PS

;SET INTERRUPT ENABLE

003632 052777 000200 011752

MOV #INTENA,@DHMCSR

;GENERATE INTERRUPT

003640 000240

BIS #DONE,@DHMCSR

;WAIT FOR INTERRUPT

003642 000240

NOP

003644 005077 011742

NOP

003650 104012

CLR @DHMCSR

;NO INTERRUPT, ERROR

003652 000401

ERROR

;CONTINUE

003654 022626

BR INT13B

;INTERRUPT OCCURED, RESTORE STACK

000200

INT13A: POP2SP

;CHECK FOR INTERATIONS, LOOP.

000004

INT13B: SCOPE

000014

ST=ST+40

LVL=LVL+1

T=T+1

.IIF EQ NO-20,.PAGE

```

1
2
3           ;VERIFY THAT ALL LINE NUMBERS CAN BE WRITTEN INTO AND
4           ;READ BACK FROM LINE COUNTER
5 003660      TS \NO,0
              ;REFERANCE NUMBER DEFINITION
              T21:
              NO=NO+1           ;REFERENCE DESIGNATION
              LINT1: CLR      @DHMCSR           ;CLEAR CONTROL STATUS REGISTER
6 003660 000022 005077 011726 174104      BIC      #340,PS           ;ENABLE INTERRUPTS
7 003664 042767 000340 012022      CLR      R5           ;CLEAR EXPECTED LINE COUNT
8 003672 012767 000001 012022      MOV      #1,SELMSK       ;SET UP SELECT MASK
9 003700 005005 012701 177777      MOV      #-1,R1        ;INIT LINE COUNTER
10 003702 012700 000020 011576      MOV      #16.,R0       ;SET UP TO TEST 16 VALUES
11 003706 036767 012010 012004 LINT1A: BIT    SELMSK,LINSEL     ;THIS LINE SELECTED ??
12 003714 001407 011702 011676      BEQ     LINT1B         ;BR IF NOT
13 003716 010577 011670 011664      MOV      R5,@DHMCSR    ;SET LINE NUMBER
14 003722 017704 011664 011664      MOV      @DHMCSR,R4    ;READ BACK LINE NUMBER
15 003726 020504 011664 011664      CMP     R5,R4          ;ARE EXPECTED AND RECEIVED
16 003730 001401 011664 011664      BEQ     LINT1B         ;LINE NUMBERS THE SAME
17 003732 104000 011664 011664      ERRORC                    ;LINE NUMBERS DIFFERENT, ERROR
18 003734 104003 011664 011664      LINT1B: SCOPEF          ;CHECK FOR DATA FREEZE
19 003736 003706 011664 011664      LINT1A                    ;RETURN FOR DATA FREEZE
20 003740 005205 011664 011664      INC     R5             ;UPDATE LINE COUNT
21 003742 006367 011754 011664      ASL    SELMSK          ;SELECT NEXT LINE TO TEST
22 003746 005300 011664 011664      DEC     R0             ;UPDATE LINE NUMBER
23 003750 001356 011664 011664      BNE    LINT1A         ;CONTINUE
24 003752 104002 011664 011664      SCOPE                    ;CHECK FOR ITERATION, LOOP
25
26           ;USING "STEP" MODE, VERIFY THAT THE
27           ;LINE COUNTER CAN BE STEPPED THRU ALL STATES.
28
29 003754      TS \NO,0
              ;REFERANCE NUMBER DEFINITION
              T22:
              NO=NO+1           ;REFERENCE DESIGNATION
              LINT2: BIC      #340,PS           ;ENABLE INTERRUPTS
30 003754 042767 000340 0174014      CLR      @DHMCSR       ;CLEAR CONTROL STATUS REGISTER
31 003762 005077 011624 011676      CLR      R5           ;CLEAR EXPECTED LINE COUNT
32 003766 005005 012701 177777      MOV      #1,SELMSK     ;SET UP SELECT MASK
33 003770 012767 000001 011724      MOV      #-1,R1        ;INIT LINE COUNTER
34 003776 012701 177777 011576      MOV      #16.,R0       ;SET UP TO TEST 16 VALUES
35 004002 012700 000020 011576      MOV      #17,@DHMCSR   ;FIRST VALUE =0
36 004006 012777 000017 011576      MOV      @DHMCSR,R4    ;READ LINE COUNTER
37 004014 036767 011702 011676 LINT2A: BIT    SELMSK,LINSEL     ;THIS LINE SELECTED ??
38 004022 001407 011676 011676      BEQ     LINT2B         ;BR IF NOT
39 004024 004767 007354 011676      CALL    STEPER         ;STEP LINE COUNTER
40 004030 017704 011556 011676      MOV      @DHMCSR,R4    ;READ LINE COUNTER
41 004034 020504 011676 011676      CMP     R5,R4          ;COMPARE EXPECTED AND
42 004036 001401 011676 011676      BEQ     LINT2B         ;RECEIVED LINE NUMBERS
43 004040 104000 011676 011676      ERRORC                    ;LINE COUNTER ERROR

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44	004042	104003		LINT2B: SCOPEF		
45	004044	003754		LINT2		;CHECK FOR DATA FREEZE
46	004046	005205		INC	R5	
47	004050	006367	011646	ASL	SELMSK	;UPDATE EXPECTED LINE NUMBER
48	004054	005201		INC	R1	;SHIFT SELECT MASK
49	004056	010177	011530	MOV	R1,@DHMCSR	;GEN NEW LINE NO.
50	004062	005300		DEC	R0	;SET NEW LINE NO. IN CSR
51	004064	001353		BNE	LINT2A	
52	004066	104002		SCOPE		;CHECK FOR ITERATIONS, LOOP

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8 004070          TS \NO,0
                   ;REFERENCE NUMBER DEFINITION
                   T23:
                   NO=NO+1
                   ;REFERENCE DESIGNATION
9 004070 000024
10 004070 012777 002000 011514 MEMT1: MOV #CLRMUX,@DHMCSR ;CLEAR CONTROL STATUS REGISTER
11 004104 012700 000020 173672 BIC #340,PS ;ENABLE INTERRUPTS
12 004110 052777 001017 011474 MOV #16.,R0 ;SET UP TO TEST 16 LOCATIONS
13 004116 004767 007262 MEMT1A: BIS #MAINT+17,@DHMCSR ;SET MAINTANCE MODE
14 004122 005300 CALL STEPER ;SET LINE COUNTER THRU ALL
15 004124 001374 DEC R0 ;STATES, WRITING 1'S INTO
16 004126 012700 000020 BNE MEMT1A ;ALL MEMORY WORDS
17 004132 012705 070000 MOV #16.,R0 ;SET UP TO TEST 16 WORDS
18 004136 012777 000017 011446 MEMT1B: MOV #70000,R5 ;SET UP EXPECTED STATUS REGISTER
19 004144 004767 007234 MEMT1B: CALL STEPER ;START WITH LINE 0
20 004150 017704 011436 MOV @DHMCSR,R4 ;ACCESS SCANNER MEMORY
21 004154 020504 CMP R5,R4 ;READ DATA
22 004156 001403 BEQ MEMT1C ;COMPARE EXPECTED AND RECEIVED
23 004160 104000 ERRORC ;DATA
24 004162 104003 SCOPEF ;CONTROL STATUS OR MEMORY ERROR
25 004164 004070 MEMT1 ;CHECK FOR DATA FREEZE
26 004166 005205 MEMT1C: INC R5 ;UPDATE EEXPECTED STATUS
27 004170 005300 DEC R0 ;UPDATE LINE COUNT
28 004172 001364 BNE MEMT1B ;CONTINUE
29 004174 012777 004000 011410 MEMT1D: MOV #CLRSCN,@DHMCSR ;SET "CLEAR SCAN"
30 004202 032777 000020 011402 BIT #BUSY,@DHMCSR ;WAIT FOR "CLEAR CYCLES"
31 004210 001374 BNE .-6
32 004212 012700 000020 MOV #16.,R0 ;SET UP TO TEST 16 MEMORY
33 004216 005005 CLR R5 ;LOCATIONS
34 004220 012777 000017 011364 MEMT1E: MOV #17,@DHMCSR ;FIRST TO BE TESTED=0
35 004226 004767 007152 MEMT1E: CALL STEPER ;ACCESS SEANNER MEMORY
36 004232 017704 011354 MOV @DHMCSR,R4 ;READ DATA
37 004236 020504 CMP R5,R4 ;COMPARE EXPECTED AND RECEIVED
38 004240 001403 BEQ MEMT1F ;DATA
39 004242 104000 ERRORC ;CONTROL STATUS OF MEMORY ERROR
40 004244 104003 SCOPEFF ;CHECK FOR DATA FREEZE
41 004246 004174 MEMT1D
42 004250 005205 MEMT1F: INC R5 ;UPDATE EXPECTED DATA
43 004252 005300 DEC R0 ;UPDATE LINE COUNT
44 004254 001364 BNE MEMT1E ;CONTINUE
45 004256 104002 SCOPE ;CHECK FOR ITERATIONS, LOOP

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1
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4
5 004260          TS \NO,0
                   ;WRITE 1'S INTO SELECTED SCANNER MEMORY LOCATION.
                   ;VERIFY THAT ONLY SELECTED LOCATION WAS WRITTEN INTO.
                   ;REFERENCE NUMBER DEFINITION
                   T24:
                   NO=NO+1
                   ;REFERENCE DESIGNATION
                   004260          000025
6 004260          005077 011326          MEMT2: CLR @DHMCSR ;CLEAR CONTROL STATUS REGISTER
7 004264          042767 000340 173504   BIC #340,PS ;ENABLE INTERRUPTS
8 004272          012700 000020          MOV #16.,R0 ;SET UP TO TEST 16 ADDRESSES
9 004276          012702 000017          MOV #17,R2 ;FIRST ADDRESS TO BE TESTED=0
10 004302         012777 004000 011302   MEMT2A: MOV #CLRSCN,@DHMCSR ;CLEAR ACANNER MEMORY
11 004310         032777 000020 011274   BIT #BUSY,@DHMCSR ;WAIT FOR CLEAR CYCLE
12 004316         001374          BNE . 6
13 004320         012777 001000 011264   MOV #MAINT,@DHMCSR ;SET "MAINTENANCE MODE"
14 004326         050277 011260          BIS R2,@DHMCSR ;SET LINE COUNTER TO TEST ADDRESS-1
15 004332         004767 007046          CALL STEPER ;WRITE 1'S INTO TEST ADDRESS
16 004336         042777 001000 011246   BIC #MAINT,@DHMCSR ;CLEAR "MAINTENANCE MODE"
17 004344         012703 000020          MOV #16.,R3 ;SET UP TO TEST ALL 16
18 004350         012777 000017 011234   MOV #17,@DHMCSR ;SCANNER MEMORY LOCATIONS
19 004356         005202          INC R2
20 004360         005001          CLR R1
21 004362         004767 007016          MEMT2B: CALL STEPER ;ACCESS SCANNER MEMORY
22 004366         117704 011220          MOVB @DHMCSR,R4 ;READ CONPENTS OF MEMORY
23 004372         010105          MOV R1,R5 ;SET UP EXPECTED CONTENTS
24 004374         120402          CMPB R4,R2 ;OF SCANNER MEMORY
25 004376         001002          BNE MEMT2C
26 004400         052705 070000          BIS #70000,R5
27 004404         020405          MEMT2C: CMP R4,R5 ;COMPARE EXPECTED AND RECEIVED
28 004406         001403          BEQ MEMT2D ;VALUES
29 004410         104000          ERRORC ;SCANNER MEMORY ERROR
30 004412         104003          SCOPEF ;CHECK FOR DATA FREEZE
31 004414         004302          MEMT2A
32 004416         005201          MEMT2D: INC R1
33 004420         005303          DEC R3 ;TEST NEXT SCANNED LOCATION
34 004422         001357          BNE MEMT2B
35 004424         005300          DEC R0 ;UPDATE LINE COUNT
36 004426         001325          BNE MEMT2A
37 004430         104002          SCOPE ;CHECK FOR ITERATION, LOOP

```



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1
2
3           ;WITH ALL ACANNER MEMORY LOCATIONS SET TO 1'S,
4           ;WRITE 0'S INTO SELECTED LOCATION
5           ;VERIFY THAT ONLY SELECTED LOCATION WAS CLEARED.
6 004432    TS \NO 0
           ;REFERENCE NUMBER DEFINITION
           T25:
           NO=NO+1
           MENT3: CLR @DHMCSR ;CLEAR CONTROL STATUS REGISTER
           BIC #340,PS ;ENABLE INTERRUPTS
           MOV #16,R0 ;SET UP TO TEST 16 ADDRESSES
           MOV #17,R2 ;FIRST ADDRESS TO BE TESTED=0
11 004454 012703 000020 011124 MENT3A: MOV #16,R3 ;WRITE 1'S INTO ALL SCANNER
12 004460 012777 001017 011124 MENT3B: MOV #MAINT+17,@DHMCSR ;MEMORY LOCATIONS
13 004466 004767 006712 MENT3B: CALL STEPER ;ACCESS SCANNER MEM
14 004472 005303 DEC R3
15 004474 001374 BNE MENT3B
16 004476 010277 011110 MOV R2,@DHMCSR ;SET LINE COUNTER TO TEST ADDRESS-1
17 004502 004767 006676 CALL STEPER ;WRITE 0'S INTO TEST ADDRESS
18 004506 012703 000020 MOV #16,R3 ;SET UP TO TEST ALL 16
19 004512 012777 000017 011072 MOV #17,@DHMCSR ;SCANNER MEMORY LOCATIONS
20 004520 005202 INC R2
21 004522 005001 CLR R1
22 004524 004767 006654 MENT3C: CALL STEPER ;ACCESS SCANNER MEMORY
23 004530 117704 011056 MOVB @DHMCSR,R4 ;READ CONTENTS OF MEMORY
24 004534 010105 MOV R1,R5 ;SET UP EXPECTED CONTENTS
25 004536 120402 CMPB R4,R2 ;OF SCANNER MEIORY
26 004540 001002 BNE MENT3D
27 004542 052705 070000 MENT3D: BIS #70000,R5 ;COMPARE EXPECTED AND
28 004546 020405 MENT3D: CMP R4,R5 ;RECEIVED VALUES
29 004550 001403 BEQ MENT3E ;SCANNER MEMORY ERROR
30 004552 104000 ERRORC ;CHECK FOR DATA FREEZE
31 004554 104003 SCOPEF
32 004556 004454 MENT3A
33 004560 005201 MENT3E: INC R1
34 004562 005303 DEC R3 ;TEST NEXT SCANNER LOCATION
35 004564 001357 BNE MENT3C
36 004566 005300 DEC R0 ;UPDATE ADDRESS COUNT
37 004570 001331 BNE MENT3A
38 004572 104002 SCOPE ;CHECK FOR ITERATION, LOOP

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1 004574          MUXS1          1.LINENA,↑/LINE ENABLE/
                                ;VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN
                                ;BE SET AND CLEARED FOR SELECTED LINE

                                .IFEQ SINGLE
004574          TS \NO,0
                                ;REFERANCE NUMBER DEFINITION
004574          T26:
000027          NO=NO+1
                                ;REFERENCE DESIGNATION
                                .IFF
                                TS \N1,1
                                .IFTF
004574 005077 011012 MUX1: CLR @DHMCSR ;CLEAR CONTROL STATUS REGISTER
004600 042767 000340 173170 BIC @340,PS ;ENABLE INTERRUPTS
                                .IFT
004606 012700 000020 MOV @16.,R0 ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
004612 012767 000001 011102 MOV @1,SELMSK ;INIT LINE SELECT MASK
004620 005001 CLR R1 ;START AT LINE 0
                                .IFF
                                MOV LINE,R1
                                .IFTF
004622 012777 002000 010762 MUX1A: MOV @CLRMUX,@DHMCSR
004630 012702 000020 MOV @16.,R2
                                .IFT
004634 036767 011062 011056 BIT SELMSK,LINSEL ;IS THIS LINE SELECTED FOR TEST ?
004642 001464 BEQ MUX1F ;BR IF NOT
                                .IFTF
004644 010177 010742 MOV R1,@DHMCSR ;SELECT LINE TO BE TESTED
004650 012777 000001 010736 MOV @LINENA,@DHMLSR ;SET LINE ENABLE FUNCTION FLIP-FLOP
                                .IFT
004656 012767 000001 011040 MOV @1,SLMSK ;INIT ANOTHER SELECT MASK
                                .IFTF
004664 005077 010722 CLR @DHMCSR
004670 005005 MUX1B: CLR R5
                                .IFT
004672 036767 011026 011020 BIT SLMSK,LINSEL ;SELECTED ??
004700 001421 BEQ MUX1D ;BR IF NOT
                                .IFTF
004702 017704 010706 MOV @DHMLSR,R4 ;READ LINE STATUS REGISTER
004706 117703 010700 MOV @DHMCSR,R3 ;READ CONTROL STATUS REGISTER
004712 042703 177760 BIC @177760,R3 ;CLEAR UNWANTED BITS
004716 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,
004720 001002 BNE MUX1C ;EXCEPT LINE ENABLE FUNCTION FLIP FLOP
004722 012705 000001 MOV @LINENA,R5 ;TO BE SET
004726          MUX1C:
004726 042704 000360 .IF IDN <LINENA>,<LINENA>
                                BIC @360,R4 ;CLEAR RING,CO,CS,SECRV
                                ;IF NO LEVEL CONVERTER THESE BITS FLOAT
                                .ENDC
004732 020504 CMP R5,R4 ;CMP EXPECTED AND RECVD
004734 001403 BEQ MUX1D ;RESULTS
004736 104001 ERRORL ;LINE STATUS ERROR
004740 104003 SCOPEF
004742 004744 MUX1D:
004744 004767 006434 MUX1D: CALL STEPER ;EXAMINE NEXT LINE
                                .IFT

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004750 006367 010750          ASL      SLMSK          ;SHIFT MASK
                                .IFTF
004754 005302          DEC      R2
004756 001344          BNE     MUX1B
004760 005005          CLR     R5
004762 010177 010624    MUX1E:  MOV   R1,@DHMCSR
004766 010103          MOV   R1,R3
004770 005077 010620    CLR     @DHMLSR
004774 105227 000000    INCB   #0
005000 001375          BNE     .-4
005002 017704 010606    MOV   @DHMLSR,R4
005006 005704          TST   R4
005010 001401          BEQ   MUX1F
005012 104001          ERRORL

005014 104003          .IFT
005016 004622    MUX1F:  SCOPEF
005020 006367 010676    MUX1A
005024 005201          ASL   SELMSK
005026 005300          INC   R1
005030 001274          DEC   R0
005032 104002          BNE   MUX1A
                                SCOPE
                                .IFF
                                MUX1F:  SCOPE
                                .ENDC

```

```

;SET LINE COUNTER TO SELECTED LINE
;CLEAR LINE ENABLE FLIP FLOP
;DELAY FOR CABLE
;DITTO
;READ LINE STATUS REGISTER
;WAS LINE ENABLE FUNCTION FLIP FLOP
;CLEARED
;NO, LINE STATUS ERROR

;CHECK FOR LOOP ON SAME DATA

;SHIFT SELECT MASK
;SELECT NEXT LINE
;DECREMENT LINE COUNT
;CONTINU IF NOT DONE
;CHECK FOR ITERATIONS, LOOP

;CHECK FOR ITERATIONS, LOOP

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005034          MUXS1          2,TRMRDY,+/TERMINAL READY/
                                ;VERIFY THAT TERMINAL READY FUNCTION FLIP FLOP CAN
                                ;BE SET AND CLEARED FOR SELECTED LINE

005034          .IFEQ SINGLE
TS \NO,0
                                ;REFERANCE NUMBER DEFINITION

005034          000030        T27:
                                NO=NO+1
                                .IFF
TS \N1,1
                                .IFTF
005034 005077 010552          MUX2: CLR   @DHMCSR          ;CLEAR CONTROL STATUS REGISTER
005040 042767 000340 172730    BIC   @340,PS          ;ENABLE INTERRUPTS

                                .IFT
005046 012700 000020          MOV   @16.,R0          ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
005052 012767 000001 010642    MOV   @1,SELMSK        ;INIT LINE SELECT MASK
005060 005001          CLR   R1          ;START AT LINE 0

                                .IFF
                                MOV   LINE,R1

                                .IFTF
005062 012777 002000 010522    MUX2A: MOV  @CLRMUX,@DHMCSR
005070 012702 000020          MOV   @16.,R2

                                .IFT
005074 036767 010622 010616    BIT   SELMSK,LINSEL    ;IS THIS LINE SELECTED FOR TEST ?
005102 001462          BEQ   MUX2F          ;BR IF NOT

                                .IFTF
005104 010177 010502          MOV   R1,@DHMCSR        ;SELECT LINE TO BE TESTED
005110 012777 000002 010476    MOV   @TRMRDY,@DHMLSR  ;SET TERMINAL READY FUNCTION FLIP-FLOP

                                .IFT
005116 012767 000001 010600    MOV   @1,SLMSK         ;INIT ANOTHER SELECT MASK

                                .IFTF
005124 005077 010462          CLR   @DHMCSR
005130 005005          MUX2B: CLR   R5

                                .IFT
005132 036767 010566 010560    BIT   SLMSK,LINSEL     ;SELECTED ??
005140 001417          BEQ   MUX2D          ;BR IF NOT

                                .IFTF
005142 017704 010446          MOV   @DHMLSR,R4        ;READ LINE STATUS REGISTER
005146 117703 010440          MOVB  @DHMCSR,R3        ;READ CONTROL STATUS REGISTER
005152 042703 177760          BIC   @177760,R3        ;CLEAR UNWANTED BITS
005156 020103          CMP   R1,R3            ;IF LINE NUMBER=SELECTED LINE NUMBER,
005160 001002          BNE   MUX2C          ;EXCEPT TERMINAL READY FUNCTION FLIP FLOP
005162 012705 000002          MOV   @TRMRDY,R5        ;TO BE SET

005166          MUX2C:
                                .IF IDN <TRMRDY>,<LINENA>
                                BIC   @360,R4          ;CLEAR RING,CO,CS,SECRV
                                                ;IF NO LEVEL CONVERTER THESE BITS FLOAT

                                .ENDC

005166 020504          CMP   R5,R4            ;CMP EXPECTED AND RECVD
005170 001403          BEQ   MUX2D          ;RESULTS
005172 104001          ERRORL          ;LINE STATUS ERROR
005174 104003          SCOPEF
005176 005200          MUX2D
005200 004767 006200          MUX2D: CALL  STEPER          ;EXAMINE NEXT LINE
                                .IFT

```



```

005270          MUX51          3,RS,+/REQUEST TO SEND/
                                ;VERIFY THAT REQUEST TO SEND FUNCTION FLIP FLOP CAN
                                ;BE SET AND CLEARED FOR SELECTED LINE

                                .IFEQ SINGLE
005270          TS \NO,0
                                ;REFERANCE NUMBER DEFINITION
005270          000031        T30:
                                NO=NO-1
                                .IFF
                                TS \N1,1
                                .IFTF
005270 005077 010316        MUX3: CLR @DHMCSR          ;CLEAR CONTROL STATUS REGISTER
005274 042767 000340 172474 BIC @340,PS          ;ENABLE INTERRUPTS
                                .IFT
005302 012700 000020        MOV @16.,R0          ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
005306 012767 000001 010406 MOV @1,SELMSK      ;INIT LINE SELECT MASK
005314 005001              CLR R1          ;START AT LINE 0
                                .IFF
                                MOV LINE,R1
                                .IFTF
005316 012777 002000 010266 MUX3A: MOV @CLRMUX,@DHMCSR
005324 012702 000020        MOV @16.,R2
                                .IFT
005330 036767 010366 010362 BIT SELMSK,LINSEL      ;IS THIS LINE SELECTED FOR TEST ?
005336 001462              BEQ MUX3F          ;BR IF NOT
                                .IFTF
005340 010177 010246        MOV R1,@DHMCSR      ;SELECT LINE TO BE TESTED
005344 012777 000004 010242 MOV @RS,@DHMLSR    ;SET REQUEST TO SEND FUNCTION FLIP-FLOP
                                .IFT
005352 012767 000001 010344 MOV @1,SLMSK      ;INIT ANOTHER SELECT MASK
                                .IFTF
005360 005077 010226        CLR @DHMCSR
005364 005005        MUX3B: CLR R5
                                .IFT
005366 036767 010332 010324 BIT SLMSK,LINSEL    ;SELECTED ??
005374 001417        BEQ MUX3D          ;BR IF NOT
                                .IFTF
005376 017704 010212        MOV @DHMLSR,R4      ;READ LINE STATUS REGISTER
005402 117703 010204        MOV @DHMCSR,R3      ;READ CONTROL STATUS REGISTER
005406 042703 177760        BIC @177760,R3      ;CLEAR UNWANTED BITS
005412 020103              CMP R1,R3          ;IF LINE NUMBER=SELECTED LINE NUMBER,
005414 001002              BNE MUX3C          ;EXCEPT REQUEST TO SEND FUNCTION FLIP FLOP
005416 012705 000004        MOV @RS,R5          ;TO BE SET
005422          MUX3C:
                                .IF IDN <RS>,<LINENA>
                                BIC @360,R4          ;CLEAR RING.CO,CS,SECRCV
                                                ;IF NO LEVEL CONVERTER THESE BITS FLOAT
                                .ENDC
005422 020504              CMP R5,R4          ;CMP EXPECTED AND RECVD
005424 001403              BEQ MUX3D          ;RESULTS
005426 104001              ERRORL          ;LINE STATUS ERROR
005430 104003              SCOPEF
005432 005434              MUX3D
005434 004767 005744        MUX3D: CALL STEPER      ;EXAMINE NEXT LINE
                                .IFT

```

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005440 006367 010260          ASL      SLMSK          ;SHIFT MASK
                                .IFTF
005444 005302          DEC      R2
005446 001346          BNE     MUX3B
005450 005005          CLR     R5
005452 010177 010134    MUX3E: MOV     R1,@DHMCSR
005456 010103          MOV     R1,R3          ;SET LINE COUNTER TO SELECTED LINE
005460 005077 010130    CLR     @DHMLSR        ;CLEAR REQUEST TO SEND FLIP FLOP
005464 105227 000000    INCB   #0              ;DELAY FOR CABLE
005470 001375          BNE     -.4            ;DITTO
005472 017704 010116    MOV     @DHMLSR,R4    ;READ LINE STATUS REGISTER
005476 005704          TST     R4              ;WAS REQUEST TO SEND FUNCTION FLIP FLOP
005500 001401          BEQ     MUX3F          ;CLEARED
005502 104001          ERRORL          ;NO, LINE STATUS ERROR

005504 104003          .IFT
005506 005316    MUX3F: SCOPEF          ;CHECK FOR LOOP ON SAME DATA
005510 006367 010206    MUX3A
005514 005201          ASL     SELMSK        ;SHIFT SELECT MASK
005516 005300          INC     R1            ;SELECT NEXT LINE
005520 001276          DEC     R0            ;DECREMENT LINE COUNT
005522 104002          BNE     MUX3A        ;CONTINU IF NOT DONE
                                SCOPE          ;CHECK FOR ITERATIONS, LOOP

                                .IFF
                                MUX3F: SCOPE          ;CHECK FOR ITERATIONS, LOOP
                                .ENDC

```

```

005524          MUXS1          4,SECTX,1/SECONDARY TRANSMIT/
                                ;VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP FLOP CAN
                                ;BE SET AND CLEARED FOR SELECTED LINE

                                .IFEQ SINGLE
005524          TS \NO,0
                                ;REFERANCE NUMBER DEFINITION
005524          000032        T31:
                                NO=NO+1
                                ;REFERENCE DESIGNATION
                                .IFF
                                TS \N1,1
                                .IFTF
005524 005077 010062        MUX4: CLR @DHMCSR
005530 042767 000340 172240        BIC #340,PS
                                ;CLEAR CONTROL STATUS REGISTER
                                ;ENABLE INTERRUPTS
                                .IFT
005536 012700 000020        MOV #16.,R0
005542 012767 000001 010152        MOV #1,SELSK
005550 005001                CLR R1
                                ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
                                ;INIT LINE SELECT MASK
                                ;START AT LINE 0
                                .IFF
                                MOV LINE,R1
                                .IFTF
005552 012777 002000 010032        MUX4A: MOV #CLRMUX,@DHMCSR
005560 012702 000020        MOV #16.,R2
                                .IFT
005564 036767 010132 010126        BIT SELMSK,LINSEL
005572 001462                BEQ MUX4F
                                ;IS THIS LINE SELECTED FOR TEST ?
                                ;BR IF NOT
                                .IFTF
005574 010177 010012        MOV R1,@DHMCSR
005600 012777 000010 010006        MOV #SECTX,@DHMLSR
                                ;SELECT LINE TO BE TESTED
                                ;SET SECONDARY TRANSMIT FUNCTION FLIP-FLOP
                                .IFT
005606 012767 000001 010110        MOV #1,SLMSK
                                ;INIT ANOTHER SELECT MASK
                                .IFTF
005614 005077 007772        CLR @DHMCSR
005620 005005        MUX4B: CLR R5
                                .IFT
005622 036767 010076 010070        BIT SLMSK,LINSEL
005630 001417        BEQ MUX4D
                                ;SELECTED ??
                                ;BR IF NOT
                                .IFTF
005632 017704 007756        MOV @DHMLSR,R4
005636 117703 007750        MOV @DHMCSR,R3
005642 042703 177760        BIC #177760,R3
005646 020103        CMP R1,R3
005650 001002        BNE MUX4C
005652 012705 000010        MOV #SECTX,R5
                                ;EXCEPT SECONDARY TRANSMIT FUNCTION FLIP FLOP
                                ;TO BE SET
005656          MUX4C:
                                .IF IDN <SECTX>,<LINENA>
                                BIC #360,R4
                                ;CLEAR RING,CO,CS,SECRV
                                ;IF NO LEVEL CONVERTER THESE BITS FLOAT
                                .ENDC
005656 020504        CMP R5,R4
005660 001403        BEQ MUX4D
                                ;CMP EXPECTED AND RECVD
                                ;RESULTS
005662 104001        ERRORL
005664 104003        SCOPEF
005666 005670        MUX4D
005670 004767 005510        MUX4D: CALL STEPER
                                ;EXAMINE NEXT LINE
                                .IFT

```



```

005674 006367 010024          ASL      SLMSK          ;SHIFT MASK
                                .IFTF
005700 005302          DEC      R2
005702 001346          BNE     MUX4B
005704 005005          CLR     R5
005706 010177 007700    MUX4E: MOV     R1,@DHMCSR
005712 010103          MOV     R1,R3          ;SET LINE COUNTER TO SELECTED LINE
005714 005077 007674    CLR     @DHMLSR        ;CLEAR SECONDARY TRANSMIT FLIP FLOP
005720 105227 000000    INCB   #0             ;DELAY FOR CABLE
005724 001375          BNE     .-4           ;DITTO
005726 017704 007662    MOV     @DHMLSR,R4    ;READ LINE STATUS REGISTER
005732 005704          TST    R4             ;WAS SECONDARY TRANSMIT FUNCTION FLIP FLOP
005734 001401          BEQ    MUX4F          ;CLEARED
005736 104001          ERRORL          ;NO, LINE STATUS ERROR

                                .IFT
005740 104003    MUX4F: SCOPEF          ;CHECK FOR LOOP ON SAME DATA
005742 005552          MUX4A
005744 006367 007752    ASL     SELMSK        ;SHIFT SELECT MASK
005750 005201          INC    R1            ;SELECT NEXT LINE
005752 005300          DEC    R0            ;DECREMENT LINE COUNT
005754 001276          BNE    MUX4A        ;CONTINU IF NOT DONE
005756 104002          SCOPE          ;CHECK FOR ITERATIONS, LOOP

                                .IFF
                                MUX4F: SCOPE          ;CHECK FOR ITERATIONS, LOOP
                                .ENDC

```

```

2
3
4
5 005760          TS \NO.0
                   ;REFERANCE NUMBER DEFINITION
                   T32:
                   NO=NO+1
                   ;REFERENCE DESIGNATION
005760          000033
6 005760 005077 007626          MUX8: CLR @DHMCSR ;CLEAR CONTROL REGISTER
7 005764 042767 000340 172004 BIC @340,PS ;ENABLE INTERRUPTS
8 005772 012700 000020          MOV @16.,R0 ;SET UP TO TEST 16 LINES
9 005776 012777 000017 007610 MUX8A: MOV @17,@DHMLSR ;WRITE 1S INTO ALL MULTIPLEXER
10 006004 004767 005374          CALL STEPER ;FUNCTION FLIPFLOPS
11 006010 005300          DEC R0
12 006012 001371          BNE MUX8A
13 006014 012767 000001 007700 MOV @1,SELMSK ;INIT SELECT MASK
14 006022 005003          CLR R3 ;SET UP FOR 16 LINES
15 006024 012700 000020          MOV @16.,R0
16 006030 012777 002000 007554 MUX8B: MOV @CLRMUX,@DHMCSR ;CLEAR MULTIPLEXER
17 006036 036767 007660 007654 MUX8C: BIT SELMSK,LINSEL ;SELECTED ??
18 006044 001427          BEQ MUX8E ;BR IF NOT
19 006046 010377 007540          MOV R3,@DHMCSR ;SELECT LINE
20 006052 017704 007536          MOV @DHMLSR,R4 ;READ LINE STATUS REGISTER
21 006056 005005          CLR R5 ;EXPECT OS
22 006060 005704          TST R4 ;WAS LINE STATUS REGISTER CLEARED
23 006062 001403          BEQ MUX8D
24 006064 104001          ERRORL ;LINE STATUS ERROR
25 006066 104003          SCOPEF ;CHECK FOR LOOP ON SAME DATA
26 006070 006030          MUX8B
27 006072 005205          MUX8D: INC R5 ;EXPECT LINE ENABLE
28 006074 052777 000001 007512 BIS @LINENA,@DHMLSR ;SET LINE ENABLE ON SELECTED LINE
29 006102 017704 007506          MOV @DHMLSR,R4 ;READ LINE STATUS REGISTER
30 006106 042704 000360          BIC @360,R4 ;CLEAR RING,CO,CS SECRCV-MAY FLOAT HIGH
31 006112 020504          CMP R5,R4 ;IS ANYTHING BUT LINE ENABLE SET
32 006114 001403          BEQ MUX8E
33 006116 104001          ERRORL ;LINE STATUS ERROR
34 006120 104003          SCOPEF ;CHECK FOR LOOP ON SAME DATA
35 006122 006030          MUX8B
36 006124 005203          MUX8E: INC R3 ;UPDATE LINE NUMBER
37 006126 005077 007462          CLR @DHMLSR ;CLEAR CURRENT LINE
38 006132 006367 007564          ASL SELMSK ;SHIFT SELECT MASK
39 006136 005300          DEC R0 ;CONTINUE IF ALL LINES NOT
40 006140 001336          BNE MUX8C ;TESTED
41 006142 104002          SCOPE ;CHECK FOR ITERATIONS, LOOP

```

1  
2  
3  
4  
5  
6  
7  
8  
9  
10

```
;WRITE 1'S INTO ALL SCANNER MEMORY LOCATIONS
;SET *LINE ENABLE FOR ALL LINES
;VERIFY THAT AN INTERRUPT OCCURS FOR EACH LINE
;THIS TEST WILL FAIL ON ANY LINE THAT DOES
;NOT HAVE A LEVEL CONVERTER--CO,CS,RING,SECRV WILL FLOAT
;HIGH,HENCE MAINT. MODE CANNOT TOGGLE THESE SIGNALS TO
;CAUSE A TRANSITION.A PROGRAM TIME OUT WILL OCCUR.....
```

11 006144

TS \NO.0

;REFERANCE NUMBER DEFINITION

006144

T33:  
NO=NO+1

;REFERENCE DESIGNATION

12	006144	012777	002000	007440	SCNT1:	MOV	#CLRMUX,@DHMCSR	;CLEAR ALL MULTIPLEXER FLIPFLOPS
13	006152	005077	007434			CLR	@DHMCSR	;CLEAR CONTROL REGISTER
14	006156	042767	000340	171612		BIC	#340,PS	;ENABLE INTERRUPTS
15	006164	012700	000020			MOV	#16.,R0	;SET UP TO WRITE 1'S INTO
16	006170	012777	001017	007414		MOV	#MAINT+17,@DHMCSR	;ALL SCANNER MEMORY LOCATION
17	006176	012767	000001	007516		MOV	#1,SELMSK	;INIT SELECT MASK
18	006204	004767	005174		SCNT1A:	CALL	STEPEP	;WRITE A LOCATION
19	006210	012777	000001	007376		MOV	#LINENA,@DHMLSR	;LET "LINE ENABLE"
20	006216	005300				DEC	R0	
21	006220	001371				BNE	SCNT1A	
22	006222	012701	177777			MOV	#-1,R1	;INIT LINE NO. GEN.
23	006226	012705	070300			MOV	#70300,R5	;EXPECT "DONE"+"COF"+"CSF"+"SECRXF"
24	006232	012777	006404	007346		MOV	#SCNT1C,@DHMVEC	;SET UP LOCAL INTERRUPT SERVICE
25	006240	012777	000340	007342		MOV	#340,@DHMLVL	;SERVICE AT LEVEL 7
26	006246	012700	000020			MOV	#16.,R0	
27	006252	012777	000117	007332		MOV	#INTENA+17,@DHMCSR	;SET INTERRUPT ENABLE
28	006260	036767	007436	007432	SCNT1B:	BIT	SELMSK,LINSEL	;SELECTED ??
29	006266	001456				BEQ	SCNT1D	;BR IF NOT
30	006270	052767	000340	171500		BIS	#340,PS	;LOCK OUT INTERRUPTS
31	006276	004767	005102			CALL	STEPEP	;HIT THE SCANER ONCE
32	006302	005003				CLR	R3	;CLEAR DELAY
33	006304	042767	000340	171464		BIC	#340,PS	;ENABLE INTERRUPTS
34	006312	005303			1#:	DEC	R3	;WAIT LONG ENOUGH?
35	006314	001404				BEQ	2#	;WE HAVE AN ERROR
36	006316	105777	007270			TSTB	@DHMCSR	;DID DONE SET
37	006322	100373				BPL	1#	;NOT YET
38	006324	100416				BMI	3#	;SET BUT NO INTERRUPT
39	006326	052767	000340	171442	2#:	BIS	#340,PS	
40	006334	017704	007252			MOV	@DHMCSR,R4	;GET FAILING LINE
41	006340	010402				MOV	R4,R2	;GET CSR
42	006342	017703	007246			MOV	@DHMLSR,R3	;GET LSR
43	006346	042704	177760			BIC	#177760,R4	
44	006352	104030				ERRINT		;REPORT ERROR HAS OCCURED
45	006354	104003				SCOPEF		
46	006356	006144				SCNT1		
47	006360	000421				BR	SCNT1D	;CONTINUE THE TEST
48	006362	052767	000340	171406	3#:	BIS	#340,PS	;INTERRUPT DID NOT OCCUR
49	006370	017704	007216			MOV	@DHMCSR,R4	;ERROR
50	006374	104000				ERRORC		;CONTROL STATUS ERROR
51	006376	104003				SCOPEF		;CHECK FOR LOOP ON SAME DATA
52	006400	006144				SCNT1		
53	006402	000410				BR	SCNT1D	
54	006404	022626			SCNT1C:	POP2SP		;INTERRUPT OCCURED, REPOSITION STACK

55	006406	017704	007200		MOV	@DHMCSR,R4		:READ CONTROL STATUS
56	006412	020504			CMP	R5,R4		:ARE EXPECTED AND RECEIVED
57	006414	001403			BEQ	SCNT1D		:REGISTERS THE SAME
58	006416	104000			ERRORC			:NO. LINE STATUS ERROR
59	006420	104003			SCOPEF			:CHECK FOR LOOP WITH CURPENT DATA
60	006422	006144			SCNT1			
61	006424	042777	000217	007160	SCNT1D:	BIC	@DONE+17,@DHMCSR	:CLEAR D DONE
62	006432	005201			INC	R1		:GEN NXT LINE NO.
63	006434	050177	007152		BIS	R1,@DHMCSR		:SET LINE NO. BITS
64	006440	006367	007256		ASL	SELMSK		:SHIFT SELECT MASK
65	006444	005205			INC	R5		:UPDATE EXPECTED RESULT
66	006446	005300			DEC	R0		:CONTINUE IF NOT DONE
67	006450	001303			BNE	SCNT1B		
68	006452	104002			SCOPE			:CHECK FOR ITERATIONS, LOOP

```

1
2
3
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5
6
7 006454      000001
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23 006454      000100
                000100
                000100
24 006454      006474 173562
                006470 104004
                006472 017010
                006474 104013
                006476 017043
                006500 000000
                006502 000017
                006504 015700
                006506 104004
                006510 017005

                .MACRO COMMENT
                .NLIST
                SINGLE=1
                .LIST
                .ENDM
                COMMENT
                SINGLE=1
                ;SINGLE LINE CABLE TEST
                ;FOR USE WITH MODEM CABLE AND DC11 TEST CONNECTOR
                ;NOTE: MODEM CONTROL MULTIPLEXER INPUTS SHOULD BE CONNECTED
                ;TO DISTRIBUTION PANEL VIA DM11-DC

                .MACRO COMMENT
                .NLIST
                N1=100
                N=N1
                XN=N1
                .LIST
                .ENDM
                COMMENT
                N1=100
                N=N1
                XN=N1
                TS
                \N1,1
                ;REFERANCE NUMBER DEFINITION
                T100:
                N1=N1+1
                STRLIN: MOV #STRLNA,KRET
                BIC #340,PS
                TYPE
                MLINE
                STRLNA: INSTRG
                MLINEI
                0
                17
                LINE
                TYPE
                MCRLF

                ;REFERENCE DESIGNATION
                ;SET UP FOR NEW LINE SELECTION
                ;ENABLE INTERRUPTS
                ;TYPE "SINGLE LINE CABLE TEST"
                ;GET LINE NUMBER

```

```

1
2 006512          MUXS1  11,LINENA,+/LINE ENABLE/
                   ;VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN
                   ;BE SET AND CLEARED FOR SELECTED LINE

                   .IFEQ  SINGLE
                   TS  \NO,0
                   .IFF
006512          TS      \N1,1
                   ;REFERANCE NUMBER DEFINITION

006512          T101:   ;REFERENCE DESIGNATION
                   N1=N1+1
                   .IFTF
006512 005077 007074 MUX11: CLR  @DHMCSR           ;CLEAR CONTROL STATUS REGISTER
006516 042767 000340 171252 BIC  #340,PS           ;ENABLE INTERRUPTS

                   .IFT
                   MOV  #16.,R0           ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
                   MOV  #1,SELMSK        ;INIT LINE SELECT MASK
                   CLR  R1                ;START AT LINE 0

                   .IFF
006524 016701 007150          MOV  LINE,R1

                   .IFTF
006530 012777 002000 007054 MUX11A: MOV  @CLRMUX,@DHMCSR
006536 012702 000020          MOV  #16.,R2

                   .IFT
                   BIT  SELMSK,LINSEL     ;IS THIS LINE SELECTED FOR TEST ?
                   BEQ  MUX11F           ;BR IF NOT

                   .IFTF
006542 010177 007044          MOV  R1,@DHMCSR           ;SELECT LINE TO BE TESTED
006546 012777 000001 007040 MOV  #LINENA,@DHMLSR     ;SET LINE ENABLE FUNCTION FLIP-FLOP

                   .IFT
                   MOV  #1,SLMSK         ;INIT ANOTHER SELECT MASK

                   .IFTF
006554 005077 007032          CLR  @DHMCSR
006560 005005          MUX11B: CLR  R5

                   .IFT
                   BIT  SLMSK,LINSEL     ;SELECTED ??
                   BEQ  MUX11D           ;BR IF NOT

                   .IFTF
006562 017704 007026          MOV  @DHMLSR,R4 >           ;READ LINE STATUS REGISTER
006566 117703 007020          MOVB @DHMCSR,R3           ;READ CONTROL STATUS REGISTER
006572 042703 177760          BIC  #177760,R3           ;CLEAR UNWANTED BITS
006576 020103          CMP  R1,R3           ;IF LINE NUMBER=SELECTED LINE NUMBER,
006600 001002          BNE  MUX11C       ;EXCEPT LINE ENABLE FUNCTION FLIP FLOP
006602 012705 000001          MOV  #LINENA,R5           ;TO BE SET

006606          MUX11C:
                   .IF IDN <LINENA>,<LINENA>
006606 042704 000360          BIC  #360,R4           ;CLEAR RING.CO,CS,SECRV
                   ;IF NO LEVEL CONVERTER THESE BITS FLOAT

                   .ENDC

006612 020504          CMP  R5,R4           ;CMP EXPECTED AND RECD
006614 001403          BEQ  MUX11D       ;RESULTS
006616 104001          ERRORL           ;LINE STATUS ERROR
006620 104003          SCOPEF
006622 006624          MUX11D
006624 004767 004554 MUX11D: CALL  STEPER           ;EXAMINE NEXT LINE

```

```

.IFT
.IFTF
006630 005302
006632 001352
006634 005005
006636 010177 006750
006642 010103
006644 005077 006744
006650 105227 000000
006654 001375
006656 017704 006732
006662 005704
006664 001401
006666 104001

MUX11E: ASL SLMSK ;SHIFT MASK
        DEC R2
        CLR R5
        MOV R1,@DHMCSR
        MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
        CLR @DHMLSR ;CLEAR LINE ENABLE FLIP FLOP
        INCB #0 ;DELAY FOR CABLE
        BNE .-4 ;DITTO
        MOV @DHMLSR,R4 ;READ LINE STATUS REGISTER
        TST R4 ;WAS LINE ENABLE FUNCTION FLIP FLOP
        BEQ MUX11F ;CLEARED
        ERRORL ;NO, LINE STATUS ERROR

.IFT
MUX11F: SCOPEF ;CHECK FOR LOOP ON SAME DATA
        MUX11A
        ASL SELMSK ;SHIFT SELECT MASK
        INC R1 ;SELECT NEXT LINE
        DEC R0 ;DECREMENT LINE COUNT
        BNE MUX11A ;CONTINU IF NOT DONE
        SCOPE ;CHECK FOR ITERATIONS, LOOP

.IFF
006670 104002 MUX11F: SCOPE ;CHECK FOR ITERATIONS, LOOP
.ENDC

```

```

1 006672          MUXS1  12,TRMRDY,+/TERMINAL READY/
                    ;VERIFY THAT TERMINAL READY FUNCTION FLIP-FLOP CAN
                    ;BE SET AND CLEARED FOR SELECTED LINE

                    .IFEQ  SINGLE
                    TS  \NO,0
                    .IFF
006672          TS      \N1,1
                    ;REFERANCE NUMBER DEFINITION
006672          T102:   ;REFERENCE DESIGNATION
                    N1=N1+1
                    .IFTF
006672 005077 006714 MUX12: CLR  @DHMCSR          ;CLEAR CONTROL STATUS REGISTER
006676 042767 000340 171072 BIC  @340,PS      ;ENABLE INTERRUPTS

                    .IFT
                    MOV  @16.,R0          ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
                    MOV  @1,SELMSK        ;INIT LINE SELECT MASK
                    CLR  R1               ;START AT LINE 0

                    .IFF
006704 016701 006770          MOV  LINE,R1
                    .IFTF
006710 012777 002000 006674 MUX12A: MOV  @CLRMUX,@DHMCSR
006716 012702 000020          MOV  @16.,R2

                    .IFT
                    BIT  SELMSK,LINSEL      ;IS THIS LINE SELECTED FOR TEST ?
                    BEQ  MUX12F            ;BR IF NOT

                    .IFTF
006722 010177 006664 006660 MUX12B: MOV  R1,@DHMCSR          ;SELECT LINE TO BE TESTED
006726 012777 000002          MOV  @TRMRDY,@DHMLSR      ;SET TERMINAL READY FUNCTION FLIP-FLOP

                    .IFT
                    MOV  @1,SLMSK          ;INIT ANOTHER SELECT MASK

                    .IFTF
006734 005077 006652          CLR  @DHMCSR
006740 005005          MUX12B: CLR  R5

                    .IFT
                    BIT  SLMSK,LINSEL      ;SELECTED ??
                    BEQ  MUX12D            ;BR IF NOT

                    .IFTF
006742 017704 006646          MOV  @DHMLSR,R4          ;READ LINE STATUS REGISTER
006746 117703 006640          MOV  @DHMCSR,R3          ;READ CONTROL STATUS REGISTER
006752 042703 177760          BIC  @177760,R3        ;CLEAR UNWANTED BITS
006756 020103          CMP  R1,R3           ;IF LINE NUMBER=SELECTED LINE NUMBER,
006760 001002          BNE  MUX12C        ;EXCEPT TERMINAL READY FUNCTION FLIP FLOP
006762 012705 000002          MOV  @TRMRDY,R5

                    ;TO BE SET

006766          MUX12C:
                    .IF  IDN <TRMRDY>,<LINENA>
                    BIC  @360,R4          ;CLEAR RING.CO,CS,SECRCV
                    ;IF NO LEVEL CONVERTER THESE BITS FLOAT

                    .ENDC

006766 020504          CMP  R5,R4           ;CMP EXPECTED AND RECVD
006770 001403          BEQ  MUX12D        ;RESULTS
006772 104001          ERRORL           ;LINE STATUS ERROR
006774 104003          SCOPEF
006776 007000          MUX12D
007000 004767 004400          MUX12D: CALL  STEPER          ;EXAMINE NEXT LINE
                    .IFT

```





```

007046          MUXS1  13,RS,+/REQUEST TO SEND/
                  ;VERIFY THAT REQUEST TO SEND FUNCTION FLIP FLOP CAN
                  ;BE SET AND CLEARED FOR SELECTED LINE

                  .IFEQ SINGLE
                  TS \NO,0
                  .IFF
007046          TS      \N1,1
                  ;REFERENCE NUMBER DEFINITION

007046          T103:
                  N1=N1-1
                  ;REFERENCE DESIGNATION
                  .IFTF
007046 005077 006540 MUX13: CLR  @DHMCSR
007052 042767 000340 170716 BIC  @340,PS
                  ;CLEAR CONTROL STATUS REGISTER
                  ;ENABLE INTERRUPTS
                  .IFT
                  MOV  @16.,R0
                  ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
                  MOV  @1,SELSK
                  ;INIT LINE SELECT MASK
                  CLR  R1
                  ;START AT LINE 0
                  .IFF
007060 016701 006614 MOV  LINE,R1
                  .IFTF
007064 012777 002000 006520 MUX13A: MOV @CLRMUX,@DHMCSR
007072 012702 000020 MOV  @16.,R2
                  .IFT
                  BIT  SELMSK,LINSEL
                  ;IS THIS LINE SELECTED FOR TEST ?
                  BEQ  MUX13F
                  ;BR IF NOT
                  .IFTF
007076 010177 006510 MOV  R1,@DHMCSR
007102 012777 000004 006504 MOV  @RS,@DHMLSR
                  ;SELECT LINE TO BE TESTED
                  ;SET REQUEST TO SEND FUNCTION FLIP-FLOP
                  .IFT
                  MOV  @1,SLMSK
                  ;INIT ANOTHER SELECT MASK
                  .IFTF
007110 005077 006476 CLR  @DHMCSR
007114 005005 MUX13B: CLR  R5
                  .IFT
                  BIT  SLMSK,LINSEL
                  ;SELECTED ??
                  BEQ  MUX13D
                  ;BR IF NOT
                  .IFTF
007116 017704 006472 MOV  @DHMLSR,R4
007122 117703 006464 MOVB @DHMCSR,R3
007126 042703 177760 BIC  @177760,R3
007132 020103 CMP  R1,R3
007134 001002 BNE  MUX13C
007136 012705 000004 MOV  @RS,R5
                  ;EXCEPT REQUEST TO SEND FUNCTION FLIP FLOP
                  ;TO BE SET

007142          MUX13C:
                  .IF IDN <RS>,<LINENA>
                  BIC  @360,R4
                  ;CLEAR RING,CO,CS,SECRV
                  ;IF NO LEVEL CONVERTER THESE BITS FLOAT

                  .ENDC
007142 020504 CMP  R5,R4
007144 001403 BEQ  MUX13D
007146 104001 ERRORL
007150 104003 SCOPEF
007152 007154 MUX13D
007154 004767 004224 MUX13D: CALL  STEPER
                  .IFT
                  ;EXAMINE NEXT LINE

```

```

                                ASL      SLMSK      ;SHIFT MASK
                                .IFTF
007160 005302                    DEC      R2
007162 001354                    BNE    MUX13B
007164 005005                    CLR    R5
007166 010177 006420             MUX13E: MOV   R1,@DHMCSR
007172 010103                    MOV   R1,R3      ;SET LINE COUNTER TO SELECTED LINE
007174 005077 006414             CLR    @DHMLSR   ;CLEAR REQUEST TO SEND FLIP FLOP
007200 105227 000000             INCB  #0        ;DELAY FOR CABLE
007204 001375                    BNE    .-4      ;DITTO
007206 017704 006402             MOV   @DHMLSR,R4 ;READ LINE STATUS REGISTER
007212 005704                    TST   R4        ;WAS REQUEST TO SEND FUNCTION FLIP FLOP
007214 001401                    BEQ   MUX13F    ;CLEARED
007216 1040C1                    ERRORL ;NO, LINE STATUS ERROR

                                .IFT
                                MUX13F: SCOPEF
                                MUX13A
                                ASL      SELMSK
                                INC      R1        ;SHIFT SELECT MASK
                                DEC      R0        ;SELECT NEXT LINE
                                BNE    MUX13A    ;DECREMENT LINE COUNT
                                SCOPE   ;CONTINU IF NOT DONE
                                .IFF           ;CHECK FOR ITERATIONS, LOOP
007220 104002             MUX13F: SCOPE
                                .ENDC         ;CHECK FOR ITERATIONS, LOOP

```

```

007222          MUXS1  14,SECTX,+/SECONDARY TRANSMIT/
                  ;VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP FLOP CAN
                  ;BE SET AND CLEARED FOR SELECTED LINE

                  .IFEQ  SINGLE
                  TS  \NO,0
                  .IFF
007222          TS      \N1,1
                  ;REFERANCE NUMBER DEFINITION

007222          T104:   ;REFERENCE DESIGNATION
                  N1=N1+1
                  .IFTF
007222  005077  006364  MUX14:  CLR  @DHMCSR           ;CLEAR CONTROL STATUS REGISTER
007226  042767  000340  170542  BIC  #340,PS       ;ENABLE INTERRUPTS
                  .IFT
                  MOV  #16.,R0           ;SET UP TO TEST 16 FUNCTION FLIP-FLOP
                  MOV  #1,SELMSK        ;INIT LINE SELECT MASK
                  CLR  R1                ;START AT LINE 0
                  .IFF
007234  016701  006440  MOV  LINE,R1
                  .IFTF
007240  012777  002000  006344  MUX14A: MOV  #CLRMUX,@DHMCSR
007246  012702  000020  MOV  #16.,R2
                  .IFT
                  BIT  SELMSK,LINSEL     ;IS THIS LINE SELECTED FOR TEST ?
                  BEQ  MUX14F           ;BR IF NOT
                  .IFTF
007252  010177  006334  MOV  R1,@DHMCSR           ;SELECT LINE TO BE TESTED
007256  012777  000010  006330  MOV  #SECTX,@DHMLSR      ;SET SECONDARY TRANSMIT FUNCTION FLIP-FLOP
                  .IFT
                  MOV  #1,SLMSK         ;INIT ANOTHER SELECT MASK
                  .IFTF
007264  005077  006322  MUX14B: CLR  @DHMCSR
007270  005005  CLR  R5
                  .IFT
                  BIT  SLMSK,LINSEL     ;SELECTED ??
                  BEQ  MUX14D           ;BR IF NOT
                  .IFTF
007272  017704  006316  MOV  @DHMLSR,R4         ;READ LINE STATUS REGISTER
007276  117703  006310  MOV  @DHMCSR,R3        ;READ CONTROL STATUS REGISTER
007302  042703  177760  BIC  #177760,R3        ;CLEAR UNWANTED BITS
007306  020103  CMP  R1,R3              ;IF LINE NUMBER=SELECTED LINE NUMBER,
007310  001002  BNE  MUX14C           ;EXCEPT SECONDARY TRANSMIT FUNCTION FLIP FLOP
007312  012705  000010  MOV  #SECTX,R5
                  ;TO BE SET

007316          MUX14C:
                  .IF  IDN <SECTX>,<LINENA>
                  BIC  #360,R4         ;CLEAR RING,CO,CS,SECRCV
                  ;IF NO LEVEL CONVERTER THESE BITS FLOAT
                  .ENDC

007316  020504  CMP  R5,R4             ;CMP EXPECTED AND RECVD
007320  001403  BEQ  MUX14D           ;RESULTS
007322  104001  ERRORL          ;LINE STATUS ERROR
007324  104003  SCOPEF
007326  007330  MUX14D
007330  004767  004050  MUX14D: CALL  STEPER           ;EXAMINE NEXT LINE
                  .IFT

```

```

                                ASL      SLMSK      ;SHIFT MASK
                                .IFTF
007334 005302
007336 001354
007340 005005
007342 010177 006244
007346 010103
007350 005077 006240
007354 105227 000000
007360 001375
007362 017704 006226
007366 005704
007370 001401
007372 104001

                                MUX14E: MOV    R1,@DHMCSR
                                MOV    R1,R3
                                CLR    @DHMLSR
                                INCB   #0
                                BNE    . 4
                                MOV    @DHMLSR,R4
                                TST    R4
                                BEQ    MUX14F
                                ERRORL

                                .IFT
                                MUX14F: SCOPEF
                                MUX14A
                                ASL    SELMSK
                                INC    R1
                                DEC    R0
                                BNE    MUX14A
                                SCOPE

                                .IFF
0C7374 104002
                                MUX14F: SCOPE
                                .ENDC

```

```

;SET LINE COUNTER TO SELECTED LINE
;CLEAR SECONDARY TRANSMIT FLIP FLOP
;DELAY FOR CABLE
;DITTO
;READ LINE STATUS REGISTER
;WAS SECONDARY TRANSMIT FUNCTION FLIP FLOP
;CLEARED
;NO, LINE STATUS ERROR

;CHECK FOR LOOP ON SAME DATA

;SHIFT SELECT MASK
;SELECT NEXT LINE
;DECREMENT LINE COUNT
;CONTINU IF NOT DONE
;CHECK FOR ITERATIONS, LOOP

;CHECK FOR ITERATIONS, LOOP

```

```

007376          MUXS2  15,TRMRDY,CO+CS,+/CLEAR TO SEND AND CARRIER ARE/,+/TERMINAL/
                ;VERIFY THAT CLEAR TO SEND AND CARRIER ARE SET IF "LINE ENABLE"
                ;AND TERMINAL ARE SET FOR SELECTED LINE.

                .IFEQ  SINGLE
                TS  \NO,0
                .IFF
007376          TS      \N1,1
                ;REFERENCE NUMBER DEFINITION

007376          000106  T105:
                N1=N1+1      ;REFERENCE DESIGNATION
                .IFTF
007376  005077  006210  MUX15: CLR  @DHMCSR      ;CLEAR CONTROL REGISTER
007402  042767  000340  170366 BIC  #340,PS      ;ENABLE INTERRUPTS
                .IFT
                MOV  #16.,R0      ;SET UP TO TEST 16 LINES
                CLR  R1          ;START AT LINE 0
                MOV  #1,SELSK     ;INIT LINE SELECT MASK
                .IFF
007410  016701  006264  MOV  LINE,R1
                .IFTF
007414  012702  000020  MUX15A: MOV #16.,R2      ;16 LINES
                .IFT
                BIT  SELMSK,LINSEL ;THIS LINE SELECTED FOR TEST ?
                BEQ  MUX15F      ;BR IF NOT
                .IFTF
007420  010177  006166  MOV  R1,@DHMCSR      ;SELECT A LINE
007424  012777  000003  006162 MOV  #LINENA+TRMRDY,@DHMLSR ;SET LINE ENABLE +TRMRDY

                ;THESE TWO NOP'S ADDED FOR SOME DELAY FOR PDP-11/44
                ;WITH CACHE MEMORY TURNED ON.
007432  000240  NOP
007434  000240  NOP      ;KR 10-JULY-84 REV E
                        ;KR 10-JULY-84 REV E

007436  005077  006150  MUX15B: CLR  @DHMCSR      ;CLEAR CONTROL REGISTER
007442  005005  CLR  R5          ;CLEAR EXPECTED RESULT
007444  017704  006144  MOV  @DHMLSR,R4      ;READ LINE STATUS
007450  117703  006136  MOVB @DHMCSR,R3      ;READ LINE NUMBER
007454  042703  177760  BIC  #177760,R3      ;CLEAR UNWANTED BITS
007460  020103  CMP  R1,R3          ;IF RECEIVED LINE=SELECTED LINE
007462  001002  BNE  MUX15C      ;EXPECT LINE ENABLE AND
007464  012705  000143  MOV  #LINENA+TRMRDY+CO+CS,R5

                ;CLEAR TO SEND AND CARRIER ARE SET
007470  020405  MUX15C: CMP  R4,R5      ;COMPARE EXPECTED AND
007472  001403  BEQ  MUX15D      ;RECEIVED RESULTS
007474  104001  ERRORL      ;LINE STATUS ERROR
007476  104003  SCOPEF
007500  007502  MUX15D
007502  004767  003676  MUX15D: CALL  STEPER      ;UPDATE LINE COUNTER
007506  005302  DEC  R2          ;CONTINUE IF ALL CHECKS
007510  001354  BNE  MUX15B      ;ARE NOT DONE FOR THIS LINE
007512  012705  000001  MOV  #LINENA,R5      ;EXPECT LINE ENABLE
007516  010103  MUX15E: MOV  R1,R3      ;ON SELECTED LINE
007520  010177  006066  MOV  R1,@DHMCSR      ;SELECT LINE
007524  042777  000002  006062 BIC  #TRMRDY,@DHMLSR ;CLEAR TERMINAL
007532  105227  000000  INCB #0          ;DELAY FOR CABLE

```

007536 001375  
007540 017704 006050  
007544 020504  
007546 001401  
007550 104001

BNE . 4  
MOV @DHMLSR,R4  
CMP R5,R4  
BEQ MUX15F  
ERRORL

;DITTO  
;READ LINE STATUS REGISTER  
;ONLY LINE ENABLE SHOULD BE  
;SET ON THIS LINE  
;LINE STATUS ERROR

.IFT  
MUX15F: SCOPEF  
MUX15A  
INC R1  
CLR @DHMLSR  
ASL SELMSK  
DEC R0  
BNE MUX15A  
SCOPE

;CHECK FOR LOOP ON SAME DATA  
;UPDATE LINE NUMBER  
;CLEAR LINE STATUS REGISTER  
;SHIFT MARK TO TEST NEXT LINE  
;CONTINUE IF ALL LINES NOT  
;TESTED  
;CHECK FOR ITERATIONS, LOOP

007552 104002

.IFF  
MUX15F: SCOPE  
.ENDC

;CHECK FOR ITERATIONS, LOOP

```

007554          MUXS2  16,RS,RING,+/RING IS/,+/REQUEST TO SEND/
                ;VERIFY THAT RING IS SET IF "LINE ENABLE"
                ;AND REQUEST TO SEND ARE SET FOR SELECTED LINE.

                .IFEQ  SINGLE
                TS  \NO,0
                .IFF
007554          TS      \N1,1
                ;REFERENCE NUMBER DEFINITION
007554          T106:   ;REFERENCE DESIGNATION
                000107  N1=N1+1
                .IFTF
007554 005077 006032  MUX16: CLR      @DHMCSR      ;CLEAR CONTROL REGISTER
007560 042767 000340 170210 BIC      #340,PS      ;ENABLE INTERRUPTS
                .IFT
                MOV      #16.,R0      ;SET UP TO TEST 16 LINES
                CLR      R1           ;START AT LINE 0
                MOV      #1,SELSK     ;INIT LINE SELECT MASK
                .IFF
007566 016701 006106          MOV      LINE,R1
                .IFTF
007572 012702 000020  MUX16A: MOV     #16.,R2      ;16 LINES
                .IFT
                BIT      SELMSK,LINSEL ;THIS LINE SELECTED FOR TEST ?
                BEQ      MUX16F      ;BR IF NOT
                .IFTF
007576 010177 006010          MOV      R1,@DHMCSR      ;SELECT A LINE
007602 012777 000005 006004  MOV     @LINENA+RS,@DHMLSR ;SET LINE ENABLE +RS

                ;THESE TWO NOP'S ADDED FOR SOME DELAY FOR PDP-11/44
                ;WITH CACHE MEMORY TURNED ON.
007610 000240          NOP
007612 000240          NOP
                ;KR 10-JULY-84 REV E
                ;KR 10-JULY-84 REV E

007614 005077 005772          CLR      @DHMCSR      ;CLEAR CONTROL REGISTER
007620 005005          MUX16B: CLR     R5           ;CLEAR EXPECTED RESULT
007622 017704 005766          MOV     @DHMLSR,R4      ;READ LINE STATUS
007626 117703 005760          MOV     @DHMCSR,R3      ;READ LINE NUMBER
007632 042703 177760          BIC     #177760,R3      ;CLEAR UNWANTED BITS
007636 020103          CMP      R1,R3           ;IF RECEIVED LINE=SELECTED LINE
007640 001002          BNE     MUX16C      ;EXPECT LINE ENABLE AND
007642 012705 000205          MOV     @LINENA+RS+RING,R5

                ;RING IS SET
007646 020405          MUX16C: CMP     R4,R5      ;COMPARE EXPECTED AND
007650 001403          BEQ     MUX16D      ;RECEIVED RESULTS
007652 104001          ERRORL      ;LINE STATUS ERROR
007654 104003          SCOPEF
007656 007660          MUX16D
007660 004767 003520          MUX16D: CALL   STEPER      ;UPDATE LINE COUNTER
007664 005302          DEC      R2           ;CONTINUE IF ALL CHECKS
007666 001354          BNE     MUX16B      ;ARE NOT DONE FOR THIS LINE
007670 012705 000001          MOV     @LINENA,R5      ;EXPECT LINE ENABLE
007674 010103          MUX16E: MOV     R1,R3           ;ON SELECTED LINE
007676 010177 005710          MOV     R1,@DHMCSR      ;SELECT LINE
007702 042777 000004 005704  BIC     #RS,@DHMLSR      ;CLEAR REQUEST TO SEND
007710 105227 000000          INCB     #0           ;DELAY FOR CABLE

```



007714 001375  
007716 017704 005672  
007722 020504  
007724 001401  
007726 104001

BNE .-4  
MOV @DHMLSR,R4  
CMP R5,R4  
BEQ MUX16F  
ERRORL

;DITTO  
;READ LINE STATUS REGISTER  
;ONLY LINE ENABLE SHOULD BE  
;SET ON THIS LINE  
;LINE STATUS ERROR

.IFT  
MUX16F: SCOPEF  
MUX16A  
INC R1  
CLR @DHMLSR  
ASL SELMSK  
DEC R0  
BNE MUX16A  
SCOPE

;CHECK FOR LOOP ON SAME DATA

;UPDATE LINE NUMBER  
;CLEAR LINE STATUS REGISTER  
;SHIFT MARK TO TEST NEXT LINE  
;CONTINUE IF ALL LINES NOT  
;TESTED

007730 104002

.IFF  
MUX16F: SCOPE  
.ENDC

;CHECK FOR ITERATIONS, LOOP  
;CHECK FOR ITERATIONS, LOOP

```

007732          MUXS2  17,SECTX,SECRX,+/SECONDARY RECEIVE IS/,+/SECONDARY TRANSMIT/
                ;VERIFY THAT SECONDARY RECEIVE IS SET IF "LINE ENABLE"
                ;AND SECONDARY TRANSMIT ARE SET FOR SELECTED LINE.

                .IFEQ  SINGLE
                TS  \NO,0
                .IFF
007732          TS      \N1,1
                ;REFERENCE NUMBER DEFINITION
007732          T107:   ;REFERENCE DESIGNATION
                N1=N1+1
                .IFTF
007732 005077 005654  MUX17: CLR  @DHMCSR      ;CLEAR CONTROL REGISTER
007736 042767 000340 170032 BIC  #340,PS  ;ENABLE INTERRUPTS
                .IFT
                MOV  #16.,R0      ;SET UP TO TEST 16 LINES
                CLR  R1           ;START AT LINE 0
                MOV  #1,SELMSK    ;INIT LINE SELECT MASK
                .IFF
007744 016701 005730  MOV  LINE,R1
                .IFTF
007750 012702 000020  MUX17A: MOV #16.,R2      ;16 LINES
                .IFT
                BIT  SELMSK,LINSEL ;THIS LINE SELECTED FOR TEST ?
                BEQ  MUX17F        ;BR IF NOT
                .IFTF
007754 010177 005632  MOV  R1,@DHMCSR      ;SELECT A LINE
007760 012777 000011 005626 MOV  #LINENA+SECTX,@DHMLSR ;SET LINE ENABLE +SECTX

                ;THESE TWO NOP'S ADDED FOR SOME DELAY FOR PDP-11/44
                ;WITH CACHE MEMORY TURNED ON.
007766 000240  NOP
007770 000240  NOP      ;KR 10-JULY-84 REV E
                        ;KR 10-JULY-84 REV E
007772 005077 005614  MUX17B: CLR  @DHMCSR      ;CLEAR CONTROL REGISTER
007776 005005  CLR  R5           ;CLEAR EXPECTED RESULT
010000 017704 005610  MOV  @DHMLSR,R4      ;READ LINE STATUS
010004 117703 005602  MOV  @DHMCSR,R3     ;READ LINE NUMBER
010010 042703 177760  BIC  #177760,R3     ;CLEAR UNWANTED BITS
010014 020103  CMP  R1,R3          ;IF RECEIVED LINE=SELECTED LINE
010016 001002  BNE  MUX17C        ;EXPECT LINE ENABLE AND
010020 012705 000031  MOV  #LINENA+SECTX+SECRX,R5 ;SECONDARY RECEIVE IS SET
010024 020405  MUX17C: CMP  R4,R5      ;COMPARE EXPECTED AND
010026 001403  BEQ  MUX17D        ;RECEIVED RESULTS
010030 104001  ERRORL ;LINE STATUS ERROR
010032 104003  SCOPEF
010034 010036  MUX17D
010036 004767 003342  MUX17D: CALL  STEPER      ;UPDATE LINE COUNTER
010042 005302  DEC  R2           ;CONTINUE IF ALL CHECKS
010044 001354  BNE  MUX17B        ;ARE NOT DONE FOR THIS LINE
010046 012705 000001  MOV  #LINENA,R5     ;EXPECT LINE ENABLE
010052 010103  MUX17E: MOV  R1,R3      ;ON SELECTED LINE
010054 010177 005532  MOV  R1,@DHMCSR     ;SELECT LINE
010060 042777 000010 005526 BIC  #SECTX,@DHMLSR ;CLEAR SECONDARY TRANSMIT
010066 105227 000000  INCB  #0           ;DELAY FOR CABLE

```

010072 001375  
 010074 017704 005514  
 010100 020504  
 010102 001401  
 010104 104001

BNE . 4  
 MOV @DHMLSR,R4  
 CMP R5,R4  
 BEQ MUX17F  
 ERRORL  
 .IFT  
 MUX17F: SCOPEF  
 MUX17A  
 INC R1  
 CLR @DHMLSR  
 ASL SELMSK  
 DEC R0  
 BNE MUX17A  
 SCOPE

;DITTO  
 ;READ LINE STATUS REGISTER  
 ;ONLY LINE ENABLE SHOULD BE  
 ;SET ON THIS LINE  
 ;LINE STATUS ERROR  
 ;CHECK FOR LOOP ON SAME DATA  
 ;UPDATE LINE NUMBER  
 ;CLEAR LINE STATUS REGISTER  
 ;SHIFT MARK TO TEST NEXT LINE  
 ;CONTINUE IF ALL LINES NOT  
 ;TESTED  
 ;CHECK FOR ITERATIONS, LOOP  
 ;CHECK FOR ITERATIONS, LOOP

010106 104002

.IFF  
 MUX17F: SCOPE  
 .ENDC

```

2          ;MODEM CONTROL ON LINE TEST USING 103A TYPE MODEMS
3          ;ANSWER STATION TO BE OPERATED IN AUTO ANSWER MODE
4          ;THIS TEST VERIFIES THE CONNECT AND DISCONNECT SEQUENCES
5          ;USING THE MODEM CONTROL TO CONTROL 103A TYPE MODEMS
6
7          ;NOTE: IF THE DM11-AA IS NOT CONNECTED TO THE
8          ;DISTRIBUTION PANEL, AN M974 DM11 MAINTENANCE JUMPER
9          ;SHOULD BE INSTALLED IN SLOT B1 OR B3 OF THE DISTRIBUTION
10         ;PANNEL TO PREVENT A POSSIBLE LONG SPACE
11         ;DISCONNECT FROM HANGING UP THE MODEM
12
13         .MACRO COMMENT
14         .NLIST
15         N2=200
16         N=N2
17         XN=N2
18         .LIST
19         .ENDM
20
21 010110          COMMENT
                000200          N2=200
                000200          N=N2
                000200          XN=N2
22 010110          TS \N2,2
                ;REFERANCE NUMBER DEFINITION
                T200:          ;REFERENCE DESIGNATION
                N2=N2+1
23 010110          000201          ST103A: RESET          ;INITIALIZE INTERFACE
24 010112          012767          000340 167656          MOV #340,PS          ;DISABLE ALL INTERRUPTS
25 010120          104004          TYPE          ;TYPE "103A MODEM CONNECT-
26 010122          016227          MT103T          ;DISCONNECT TEST"
27 010124          022767          000176 005474          CMP #SWREG,SWR
28 010132          001001          BNE 1$
29 010134          104025          CNTLUU
30 010136          012767          010154 001600 1$: MOV #T103A,FATRET          ;SET UP FOR FATAL ERROR
31 010144          012767          010152 172072          MOV #ST103B,KRET          ;SET UP FOR LINE CHANGE
32 010152          104017          ST103B: GETLNS          ;INPUT ORIGINATE AND
33                                     ;AND ANSWER LINE NUMBERS
34 010154          104020          T103A: SETUP          ;SET UP TO RECEIVE INTERRUPTS
35                                     ;WAIT FOR RING
36 010156          010166          T103B          ;GO HERE IF RING OK
37 010160          010162          T103A1          ;GO HERE IF NO RING
38 010162          104012          T103A1: ERROR          ;NO RING WITHIN 5 MINUTES
39 010164          000772          BR ST103B          ;SELECT NEW LINES AND REDIAL
40
41         ;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
42         ;IF AN INCORRECT TRANSITION OCCURS, THE PROGRAM
43         ;WILL TYPE AN ERROR MESSAGE, AND THE OPERATOR
44         ;WILL BE REQUESTED TO RESELECT LINES AND REDIAL
45
46 010166          104021          T103B: CKRING          ;CHECK FOR RING INTERRUPT
47                                     ;ONLY ON ANSWER LINE
48                                     ;AND NO TRANSITIONS ON
49                                     ;ORIGINATE LINE
50 010170          010206          T103C          ;GO HERE IF TRANSITIONS
51                                     ;ARE CORRECT

```

52 010172 010176  
53  
54 010174 010202  
55  
56 010176 104014  
57 010200 000207  
58 010202 104014  
59 010204 000762

T103B1  
T103B2  
T103B1: ERROR  
RTS PC  
T103B2: ERROR  
BR ST103B

;GO HERE IF INCORRECT  
;TRANSITION ON ANSWER LINE  
;GO HERE IF INCORRECT TRANSITION  
;ON ORIGINATE LINE  
;TRANSITION ERROR ON ANSWER LINE  
;CONTINUE CHECKING  
;TRANSITION ERROR ON ORIGINATE LINE  
;RESELECT LINES AND REDIAL

```

1
2           ;SET TERMINAL READY ON SELECTED ANSWER LINE
3           ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
4
5 010206 016777 005472 005376 T103C: MOV     LINANS,@DHMCSR           ;SET LINE COUNTER TO
6                                           ;ANSWER LINE NUMBER
7 010214 052777 000002 005372     BIS     @TRMRDY,@DHMLSR       ;SET TERMINAL READY ON
8                                           ;SELECTED ANSWER LINE
9 010222 104026                       CKINTT
10 010224 104022                       WAITRN           ;WAIT FOR TRANSITIONS TO OCCUR
11
12           ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
13           ;SELECTED ORIGINATE AND ANSWER LINES
14
15 010226 104023                       CKTRAN           ;CHECK TRANSITIONS AND
16                                           ;STATUS ON SELECTED
17                                           ;ANSWER AND ORIGINATE LINES
18 010230 000143                       CO+CS+LINENA+TRMRDY ;EXPECT CARRIER, CLEAR TO SEND,
19                                           ;LINE ENABLE AND TERMINAL
20                                           ;READY STATUS BITS SET ON
21                                           ;ANSWER LINE
22 010232 000143                       CO+CS+LINENA+TRMRDY ;EXPECT CARRIER, CLEAR TO SEND,
23                                           ;LINE ENABLE , AND TERMINAL
24                                           ;READY STATUS BITS ON
25                                           ;ORIGINATE LINE
26 010234 100006                       RINGF+XCO+XCS    ;EXPECT CARRIER, CLEAR TO SEND
27                                           ;AND POSSIBLE RING TRANSITIONS
28                                           ;ON ANSWER LINE
29 010236 000006                       XCO+XCS          ;EXPECT CARRIER AND CLEAR
30                                           ;TO SEND TRANSITIONS ON
31                                           ;ORIGINATE LINE
32 010240 010252                       T103D1          ;GO HERE ON ANSWER LINE STATUS ERROR
33
34 010242 010256                       T103D2          ;GO HERE ON ORIGINATE LINE STATUS ERROR
35 010244 010262                       T103D3          ;GO HERE ON ANSWER LINE TRANSITION ERROR
36 010246 010266                       T103D4          ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
37 010250 010272                       T103E          ;GO TO NEXT TEST IF NO ERRORS
38 010252 104015 T103D1: ERRORS                       ;ANSWER LINE STATUS ERROR
39 010254 000207 RTS PC          ;CONTINUE CHECKING
40 010256 104015 T103D2: ERRORS                       ;ORIGINATE LINE STATUS ERROR
41 010260 000207 RTS PC          ;CONTINUE CHECKING
42 010262 104014 T103D3: ERRORT                      ;ANSWER LINE TRANSITION ERROR
43 010264 000207 RTS PC          ;CONTINUE CHECKING
44 010266 104014 T103D4: ERRORT                      ;ORIGINATE LINE TRANSITION ERROR
45 010270 000207 RTS PC          ;CONTINUE CHECKING

```

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1
2
3 ;SET UP TO TEST DISCONNECT SEQUENCE
4 ;THE PROGRAM WILL REQUEST THE OPERATOR TO TYPE A CHARACTER
5 ;TO INITIATE THE DISCONNECT SEQUENCE
6 ;THE OPERATOR MAY MANUALLY SWITCH THE DATA SETS FROM
7 ;DATA TO TALK MODE AS MANY TIMES AS DESIRED
8 ;BEFORE THE SWITCH SEETIN IS MADE
9 ;ANY TRANSITIONS DETECTED DURING THIS TIME WILL BE
10 ;REPORTED BY TYPEOUT
11 010272 104004          T103E: TYPE          ;TYPE 'STRIKE ANY TTY KEY
12 010274 016472          MDISC          ;TEST DISCONNECT"
13 010276 012767 000340 167472      MOV      #340,PS          ;LOCK OUT INTERRUPTS
14 010304 012777 011766 005274      MOV      #TRNTYP,@DHMVEC ;SET UP TO DETECT TRANSITIONS
15                                     ;BEFORE DISCONNECT SEQUENCE STARTS
16 010312 012767 010332 005412      MOV      #T103ES,RNGRET  ;SET UP DUMMY RETURN FOR
17                                     ;RING INTERRUPT
18 010320 012777 000140 005264      MOV      #SCNENA-INTENA,@DHMCSR ;SET SCAN ENABLE AND INTERRUPT ENABLE
19 010326 005067 167444          CLR      PS              ;ALLOW INTERRUPTS
20 010332 005077 005262          T103ES: CLR      @TKOBR
21 010336 105777 005254          1$:  TSTB   @TKCSR      ;WAIT FOR TTY TO HIT
22 010342 100375          BPL      1$
23 010344 005777 005250          TST      @TKOBR
24 010350 012767 000340 167420      MOV      #340,PS          ;START DISCONNECT SEQUENCE
25 010356 005077 005230          CLP      @DHMCSR          ;CLEAR CONROL REGISTER
26 010362 016777 005314 005222      MOV      LINORG,@DHMCSR  ;SET LINE COUNTER TO SELECTED ORIGINATE LINE
27 010370 042777 000002 005216      BIC      #TRMRDY,@DHMLSR ;SET TERMINAL READY ON SELECTED LINE
28 010376 104026          CKINTT
29 010400 104022          WAITRN          ;WAIT FOR TRANSITIONS TO OCCUR

```

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1
2
3 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON SELECTED
4 ;ORIGINATE AND ANSWER LINES
5 010402 104023 CKTRAN ;CHECK TRANSITIONS AND
6 ;STATUS ON SELECTED
7 ;ANSWER AND ORIGINATE LINES
8 010404 000003 LINENA*TRMRDY ;EXPECT LINE ENABLE AND
9 ;TERMINAL READY STATUS BITS
10 ;SET ON ANSWER LINE
11 010406 000001 LINENA ;EXPECT LINE ENABLE STATUS BIT
12 ;SET ON ORIGINATE LINE
13 010410 000006 XCO-XCS ;EXPECT CARRIER AND CLEAR
14 ;TO SEND TRANSITIONS ON
15 ;ANSWER LINE
16 010412 000006 XCO-XCS ;EXPECT CARRIER AND CLEAR
17 ;TO SEND TRANSITIONS ON
18 ;ORIGINATE LINE
19 010414 010426 T103E1 ;GO HERE ON ANSWER LINE STATUS ERROR
20
21 010416 010432 T103E2 ;GO HERE ON ORIGINATE LINE STATUS ERROR
22 010420 010436 T103E3 ;GO HERE ON ANSWER LINE TRANSITION ERROR
23 010422 010442 T103E4 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
24 010424 010446 T103EN ;GO TO NEXT TEST IF NO ERRORS
25 010426 104015 T103E1: ERRORS ;ANSWER LINE STATUS ERROR
26 010430 000207 RTS PC ;CONTINUE CHECKING
27 010432 104015 T103E2: ERRORS ;ORIGINATE LINE STATUS ERROR
28 010434 000207 RTS PC ;CONTINUE CHECKING
29 010436 104014 T103E3: ERRORT ;ANSWER LINE TRANSITION ERROR
30 010440 000207 RTS PC ;CONTINUE CHECKING
31 010442 104014 T103E4: ERRORT ;ORIGINATE LINE TRANSITION ERROR
32 010444 000207 RTS PC ;CONTINUE CHECKING
33
34 010446 TS \N2,2 ;REFERANCE NUMBER DEFINITION
35 010446 000202 T201: ;REFERENCE DESIGNATION
36 010450 016414 N2=N2+1
37 010452 005067 005162 T103EN: TYPE ;TYPE " 103A TEST COMPLETE"
38 010456 104026 CLR TSTNO ;CLEAR TEST NUMBER FOR LOOPING
39 010460 000167 177466 CKINTT
40 JMP ST103B ;SELECT NEW LINE NUMBERS AND
;RESTART TEST

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9
10
11
12
13
14
15
16
17 010464
18 010464
19 010464
20 010466
21 010474
22 010476
23 010500
24 010506
25 010510
26 010512
27 010520
28 010526
29
30 010530
31
32 010532
33 010534
34 010536
35 010540

;MODEM CONTROL ON LINE TEST USING 202C TYPE MODEMS
;ANSWER STATION TO BE OPERATED IN AUTO-ANSWER MODE
;THIS TEST VERIFIES THE CONNECT AND DISCONNECT SEQUENCES
;USING THE MODEM CONTROL TO CONTROL 202C TYPE MODEMS

;ALSO TESTED ARE LINE TURN AROUND AND
;SECONDARY TRANS 1IT SECONDARY RECEIVE

.MACRO COMMENT
.NLIST
N3=300
N=N3
XN=N3
.LIST
.ENDM COMMENT
N3=300
N=N3
XN=N3
TS \N3,3 ;REFERANCE NUMBER DEFINITION
T300: ;REFERENCE DESIGNATION
N3=N3+1
ST202A: RESET ;INITIALIZE INTERFACE
MOV #340,PS ;DISABLE ALL INTERRUPTS
TYPE ;TYPE "202C MODEM CONNECT-
MT202T ;DISCONNECT TEST"
CMP #SWREG,SWR
BNE 1$
CNTLUU
1$: MOV #T202A,FATRET ;SET UP FOR FATAL ERROR
MOV #ST202B,KRET ;SET UP FOR LINE CHANGE
ST202B: GETLNS ;INPUT ORIGINATE AND
;ANSWER LINE NUMBERS
T202A: SETUP ;SET UP TO RECEIVE INTERRUPTS
;WAIT FOR RING
T202B ;GO HERE IF RING OK
T202A1 ;GO HERE IF NO RING
ERROR ;NO RING WITHIN 5 MINUTES
BR ST202B ;SELECT NEW LINES AND REDIAL

000300
000300
000300
000301
012767 000340 167302
012767 000176 005120
012767 010530 001224 1$:
012767 010526 171516
104017
104020
010542
010536
104012
000772

```

```
36
37 ;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
38 ;IF AN INCORRECT TRANSITION OCCURS, THE PROGRAM
39 ;WILL TYPE AN ERROR MESSAGE, AND THE OPERATOR
40 ;WILL BE REQUESTED TO RESELECT LINES AND REDIAL
41
42 010542 104021 T202B: CKRING ;CHECK FOR RING INTERRUPT
43 ;ONLY ON ANSWER LINE
44 ;AND NO TRANSITIONS ON
45 ;ORIGINATE LINE
46 010544 010562 T202C ;GO HERE IF TRANSITIONS
47 ;ARE CORRECT
48 010546 010552 T202B1 ;GO HERE IF INCORRECT
49 ;TRANSITION ON ANSWER LINE
50 010550 010556 T202B2 ;GO HERE IF INCORRECT
51 ;TRANSITION ON ORIGINATE LINE
52 010552 104014 T202B1: ERRORT ;ANSWER LINE TRANSITION ERROR
53 010554 000207 RTS PC ;CONTINUE CHECKING
54 010556 104014 T202B2: ERRORT ;ORIGINATE LINE TRANSITION ERROR
55 010560 000762 BR ST202B ;RESELECT LINES AND REDIAL
```

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1
2
3 ;SET TERMINAL READY ON SELECTED ANSWER LINE
4 ;SET REQUEST TO SEND ON SELECTED ORIGINATE LINE
5 ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
6 010562 016777 005116 005022 T202C: MOV LINANS,@DHMCSR ;SET LINE COUNTER TO ANSWER LINE
7 010570 052777 000002 005016 BIS #TRMRDY,@DHMLSR ;SET TERMINAL READY ON ANSWER LINE
8 010576 016777 005100 005006 T202D: MOV LINORG,@DHMCSR ;SET LINE COUNTER TO ORIGINATE LINE
9 010604 052777 000004 005002 BIS #RS,@DHMLSR ;SET REQUEST TO SEND ON ORIGINATE LINE
10 010612 104026 CKINTT
11 010614 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
12
13 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
14 ;SELECTED ORIGINATE AND ANSWER LINES
15
16 010616 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
17 ;ON SELECTED ANSWER AND
18 ;ORIGINATE LINES
19 010620 000103 CO+LINENA+TRMRDY ;EXPECT CARRIER, LINE ENABLE
20 ;AND TERMINAL READY STATUS
21 ;BITS SET ON ANSWER LINE
22 010622 000147 RS+CO+CS+LINENA+TRMRDY ;EXPECT REQUEST TO SEND, CLEAR
23 ;TO SEND, CARRIER, LINE ENABLE
24 ;AND TERMINAL READY STATUS BITS
25 ;SET ON ORIGINATE LINE
26 010624 100004 RINGF+XCO ;EXPECT CARRIER AND POSSIBLE
27 ;RING TRANSITIONS ON
28 ;ANSWER LINE
29 010626 000006 XCO+XCS ;EXPECT CARRIER AND CLEAR
30 ;TO SEND TRANSITIONS ON
31 ;ORIGINATE LINE
32 010630 010642 T202D1 ;GO HERE ON ANSWER LINE STATUS ERROR
33 010632 010646 T202D2 ;GO HERE ON ORIGINATE LINE STATUS ERROR
34 010634 010652 T202D3 ;GO HERE ON ANSWER LINE STATUS ERROR
35 010636 010656 T202D4 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
36 010640 010662 T202E ;GO TO NEXT TEST IF NO ERRORS
37 010642 104015 T202D1: ERRORS ;ANSWER LINE STATUS ERROR
38 010644 000207 RTS PC ;CONTINUE CHECKING
39 010646 104015 T202D2: ERRORS ;ORIGINATE LINE STATUS ERROR
40 010650 000207 RTS PC ;CONTINUE CHECKING
41 010652 104014 T202D3: ERRORT ;ANSWER LINE TRANSITION ERROR
42 010654 000207 RTS PC ;CONTINUE CHECKING
43 010656 104014 T202D4: ERRORT ;ORIGINATE LINE TRANSITION ERROR
44 010660 000207 RTS PC ;CONTINUE CHECKING

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1
2
3           ;SET SECONDARY TRANSMIT ON ANSWER LINE
4           ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
5 010662 016777 005016 004722 T202E: MOV     LINANS,@DHMCSR           ;SET LINE COUNTER TO ANSWER LINE
6 010670 052777 000010 004716     BIS     #SECTX,@DHMLSR        ;SET SECONDARY RECEIVE ON ANSWER LINE
7 010676 104026                                     CKINTT
8 010700 104022                                     WAITRN           ;WAIT FOR TRANSITIONS TO OCCUR
9
10          ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
11          ;SELECTED ORIGINATE AND ANSWER LINES
12
13 010702 104023     CKTRAN           ;CHECK TRANSITIONS AND STATUS
14                                     ;ON SELECTED ANSWER AND
15                                     ;ORIGINATE LINES
16 010704 000133     SECTX+CO+LINENA+TRMRDY+SECRX ;EXPECT SECONDARY TRANSMIT
17                                     ;SECONDARY RECEIVE, CARRIER
18                                     ;LINE ENABLE AND TERMINAL READY
19                                     ;STATUS BITS SET ON ANSWER LINE
20 010706 000167     SECRX+RS+CO+CS+LINENA+TRMRDY ;EXPECT SECONDARY RECEIVE,
21                                     ;REQUEST TO SEND, CLEAR TO SEND
22                                     ;CARRIER, LINE ENABLE AND
23                                     ;TERMINAL READY STATUS BITS
24                                     ;SET ON ORIGINATE LINE
25 010710 000001     XSCRX           ;EXPECT SECONDARY RECEIVE
26                                     ;TRANSITION ON ANSWER LINE
27 010712 000001     XSCRX           ;EXPECT SECONDARY RECEIVE
28                                     ;TRANSITION ON ORIGINATE LINE
29 010714 010726     T202E1          ;GO HERE ON ANSWER LINE STATUS ERROR
30 010716 010732     T202E2          ;GO HERE ON ORIGINATE LINE STATUS ERROR
31 010720 010736     T202E3          ;GO HERE ON ANSWER LINE TRANSITION ERROR
32 010722 010742     T202E4          ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
33 010724 010746     T202F           ;GO TO NEXT TEST IF NO ERRORS
34 010726 104015     T202E1: ERRORS   ;ANSWER LINE STATUS ERROR
35 010730 000207     RTS             ;CONTINUE CHECKING
36 010732 104015     T202E2: ERRORS   ;ORIGINATE LINE STATUS ERROR
37 010734 000207     RTS             ;CONTINUE CHECKING
38 010736 104014     T202E3: ERRORT   ;ANSWER LINE TRANSITION ERROR
39 010740 000207     RTS             ;CONTINUE CHECKING
40 010742 104014     T202E4: ERRORT   ;ORIGINATE LINE TRANSITION ERROR
41 010744 000207     RTS             ;CONTINUE CHECKING

```

```

1
2
3 ;DROP REQUEST TO SEND ON ORIGINATE LINE
4 ;DROP SECONDARY TRANSMIT ON ANSWER LINE
5 ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
6 010746 016777 004730 004636 T202F: MOV LINORG,@DHMCSR ;SET LINE COUNTER TO ORIGINATE LINE
7 010754 042777 000004 004632 BIC @RS,@DHMLSR ;DROP REQUEST TO SEND
8 010762 016777 004716 004622 MOV LINANS,@DHMCSR ;SET LINE COUNTER TO ANSWER LINE
9 010770 042777 000010 004616 BIC @SECTX,@DHMLSR ;DROP SECONDARY RECEIVE
10 010776 104026 CKINTT
11 011000 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
12
13 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
14 ;SELECTED ORIGINATE AND ANSWER LINES
15
16 011002 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
17 ;ON SELECTED ANSWER AND
18 ;ORIGINATE LINES
19 011004 000003 LINENA+TRMRDY ;EXPECT LINE ENABLE AND
20 ;TERMINAL READY STATUS BITS
21 ;SET ON ANSWER LINE
22 011006 000003 LINENA+TRMRDY ;EXPECT LINE ENABLE AND
23 ;TERMINAL READY STATUS BITS
24 ;SET ON ORIGINATE LINE
25 011010 000005 XCO+XSCRX ;EXPECT CARRIER AND SECONDARY
26 ;RECEIVE TRANSITIONS ON
27 ;ANSWER LINE
28 011012 000007 XCO+XCS+XSCRX ;EXPECT CARRIER, CLEAR TO SEND
29 ;AND SECONDARY RECEIVE
30 ;TRANSITIONS ON ORIGINATE LINE
31 011014 011026 T202F2 ;GO HERE ON ANSWER LINE STATUS ERROR
32 011016 011032 T202F3 ;GO HERE ON ORIGINATE LINE STATUS ERROR
33 011020 011036 T202F4 ;GO HERE ON ANSWER LINE TRANSITION ERROR
34 011022 011042 T202F5 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
35 011024 011046 T202G ;GO TO NEXT TEST IF NO ERRORS
36 011026 104015 T202F2: ERRORS ;ANSWER LINE STATUS ERROR
37 011030 000207 RTS PC ;CONTINUE CHECKING
38 011032 104015 T202F3: ERRORS ;ORIGINATE LINE STATUS ERROR
39 011034 000207 RTS PC ;CONTINUE CHECKING
40 011036 104014 T202F4: ERRORT ;ANSWER LINE TRANSITION ERROR
41 011040 000207 RTS PC ;CONTINUE CHECKING
42 011042 104014 T202F5: ERRORT ;ORIGINATE LINE TRANSITION ERROR
43 011044 000207 RTS PC ;CONTINUE CHECKING

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1
2
3
4
5
6 011046 016777 004632 004536 T202G: MOV LINANS,@DHMCSR ;SET LINE COUNTER TO ANSWER LINE
7 011054 052777 000004 004532 BIS #RS,@DHMLSR ;SET REQUEST TO SEND
8 011062 104026 CKINTT
9 011064 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
10
11 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
12 ;SELECTED ORIGINATE AND ANSWER LINES
13
14 011066 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
15 ;ON SELECTED ANSWER AND
16 ;ORIGINATE LINES
17 011070 000147 RS+CO+CS+LINENA+TRMRDY ;EXPECT LINE ENABLE, TERMINAL
18 ;READY, REQUEST TO SEND, CLEAR
19 ;TO SEND, AND CARRIER
20 ;STATUS BITS SET ON ANSWER LINE
21 011072 000103 CO+LINENA+TRMRDY ;EXPECT LINE ENABLE, TERMINAL
22 ;READY AND CARRIER STATUS
23 ;BITS SET ON ORIGINATE LINE
24 011074 000006 XCO+XCS ;EXPECT CARRIER AND CLEAR
25 ;TO SEND TRANSITIONS ON
26 ;ANSWER LINE
27 011076 000004 XCO ;EXPECT CARRIER TRANSITION
28 ;ON ORIGINATE LINE
29 011100 011112 T202G1 ;GO HERE ON ANSWER LINE STATUS ERROR
30 011102 011116 T202G2 ;GO HERE ON ORIGINATE LINE STATUS ERROR
31 011104 011122 T202G3 ;GO HERE ON ANSWER LINE TRANSITION ERROR
32 011106 011126 T202G4 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
33 011110 011132 T202H ;GO TO NEXT TEST IF NO ERRORS
34 011112 104015 T202G1: ERRORS ;ANSWER LINE STATUS ERROR
35 011114 000207 RTS PC ;CONTINUE TESTING
36 011116 104015 T202G2: ERRORS ;ORIGINATE LINE STATUS ERROR
37 011120 000207 RTS PC ;CONTINUE TESTING
38 011122 104014 T202G3: ERRORT ;ANSWER LINE TRANSITION ERROR
39 011124 000207 RTS PC ;CONTINUE TESTING
40 011126 104014 T202G4: ERRORT ;ORIGINATE LINE TRANSITION ERROR
41 011130 000207 RTS PC ;CONTINUE TESTING

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1
2
3 ;SET SECONDARY TRANSMIT ON ORIGINATE LINE
4 ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
5 011132 016777 004544 004452 T202H: MOV LINORG,@DHMCSR ;SET LINE COUNTER TO ORIGINATE LINE
6 011140 052777 000010 004446 BIS #SECTX,@DHMLSR ;SET SECONDARY TRANSMIT
7 011146 104026 CKINTT
8 011150 104022 WAITRN ;WAIT FOR TRANSITIONS TO OCCUR
9
10 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
11 ;SELECTED ORIGINATE AND ANSWER LINES
12
13 011152 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
14 ;ON SELECTED ANSWER AND
15 ;ORIGINATE LINES
16 011154 000167 RS+CS+CO+LINENA+TRMRDY+SECRX ;EXPECT LINE ENABLE, TERMINAL
17 ;READY, REQUEST TO SEND, CLEAR
18 ;TO SEND, CARRIER AND SECONDARY
19 ;RECEIVE STATUS BITS SET
20 ;ON ANSWER LINE
21 011156 000133 SECTX+CO+LINENA+TRMRDY+SECRX ;EXPECT LINE ENABLE, TERMINAL
22 ;READY, CARRIER, SECONDARY
23 ;TRANSMIT AND SECONDARY
24 ;RECEIVE STATUS BITS SET
25 ;ON ORIGINATE LINE
26 011160 000001 XSCRX ;EXPECT SECONDARY RECEIVE
27 ;TRANSITION ON ANSWER LINE
28 011162 000001 XSCRX ;EXPECT SECONDARY RECEIVE
29 ;TRANSITION ON ORIGINATE LINE
30 011164 011176 T202H2 ;GO HERE ON ANSWER LINE STATUS ERROR
31 011166 011202 T202H3 ;GO HERE ON ORIGINATE LINE STATUS ERROR
32 011170 011206 T202H4 ;GO HERE ON ANSWER LINE TRANSITION ERROR
33 011172 011212 T202H5 ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
34 011174 011216 T202I ;GO TO NEXT TEST IF NO ERRORS
35 011176 104015 T202H2: ERRORS ;ANSWER LIN STATUS ERROR
36 011200 000207 RTS PC ;CONTINUE CHECKING
37 011202 104015 T202H3: ERRORS ;ORIGINATE LINE STATUS ERROR
38 011204 000207 RTS PC ;CONTINUE CHECKING
39 011206 104014 T202H4: ERRORT ;ANSWER LINE TRANSITION ERROR
40 011210 000207 RTS PC ;CONTINUE CHECKING
41 011212 104014 T202H5: ERRORT ;ORIGINATE LINE TRANSITION ERROR
42 011214 000207 RTS PC ;CONTINUE CHECKING

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1
2
3           ;DROP REQUEST TO SEND ON ANSWER LINE
4           ;WAIT FOR TRANSITIONS TO OCCUR ON SELECTED LINES
5 011216 016777 004462 004366 T202I: MOV LINANS,@DHMCSR           ;SET LINE COUNTER TO ANSWER LINE
6 011224 042777 000004 004362 BIC #RS,@DHMLSR           ;CLEAR REQUEST TO SEND
7 011232 016777 004444 004352 MOV LINORG,@DHMCSR       ;SET LINE COUNTER TO ORIGINATE LINE
8 011240 042777 000010 004346 BIC #SECTX,@DHMLSR      ;CLEAR SECONDARY TRANSMIT
9 011246 104026 CKINTT
10 011250 104022 WAITRN           ;WAIT FRO TRANSITIONS TO OCCUR
11
12           ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
13           ;SELECTED ORIGINATE AND ANSWER LINES
14
15 011252 104023 CKTRAN           ;CHECK TRANSITION S AND STATUS
16                                     ;ON SELECTED ANSWE AND
17                                     ;ORIGINATE LINES
18 011254 000003 LINENA+TRMRDY   ;EXPECT LINE ENABLE AND
19                                     ;TERMINAL READY STATUS BITS SET
20                                     ;ON ANSWER LINE
21 011256 000003 LINENA+TRMRDY   ;EXPECT LINE ENABLE AND
22                                     ;TERMINAL READY STATUS BITS
23                                     ;SET ON ORIGINATE LINE
24 011260 000007 XCO+XCS+XSCRX   ;EXPECT CARRIER, CLEAR TO SEND
25                                     ;AND SECONDARY RECEIVE TRANSITIONS
26                                     ;ON ANSWER LINE
27 011262 000005 XCO+XSCRX       ;EXPECT CARRIER AND SECONDARY
28                                     ;RECEIVE TRANSITIONS ON
29                                     ;ORIGINATE LINE
30 011264 011276 T202I2           ;GO HERE ON ANSWER LINE STATUS ERROR
31 011266 011302 T202I3           ;GO HERE ON ORIGINATE LINE STATUS ERROR
32 011270 011306 T202I4           ;GO HERE ON ANSWER LINE TRANSITIN ERROR
33 011272 011312 T202I5           ;GO HERE ON ORIGINATE LINE TRANSITION ERROR
34 011274 011316 T202J
35 011276 104015 T202I2: ERRORS   ;ANSWER LINE STATUS ERROR
36 011300 000207 RTS PC           ;CONTINUE CHECKING
37 011302 104015 T202I3: ERRORS   ;ORIGINATE LINE STATUS ERROR
38 011304 000207 RTS PC           ;CONTINUE CHECKING
39 011306 104014 T202I4: ERRORT   ;ANSWE LINE TRANSITION ERROR
40 011310 000207 RTS PC           ;CONTINUE CHECKING
41 011312 104014 T202I5: ERRORT   ;ORIGINATE LINE TRANSITION ERROR
42 011314 000207 RTS PC           ;CONTINUE CHECKING

```



```

1
2
3 ;SET UP TO TEST DISCONNECT SEQUENCE
4 ;THE PROGRAM WILL REQUEST THE OPERATOR TO TYPE A CHARACTER
5 ;TO INITIATE THE DISCONNECT SEQUENCE
6 ;THE OPERATOR MAY MANUALLY SWITCH THE DATA SETS FROM
7 ;DATA TO TALK MODE AS MANY TIMES AS DESIRED
8 ;BEFORE THE SWITCH SEETIN IS MADE
9 ;ANY TRANSITIONS DETECTED DURING THIS TIME WILL BE
10 ;REPORTED BY TYPEOUT
11 011316 104004          T202J: TYPE          ;TYPE "STRIKE ANY TTY KEY
12 011320 016472          MOISC          ;TEST DISCONNECT"
13 011322 012767 000340 166446      MOV      #340,PS      ;LOCK OUT INTERRUPTS
14 011330 012777 011766 004250      MOV      #TRNTYP,@DHMVEC ;SET UP TO DETECT TRANSITIONS
15 011336 012767 011356 004366      MOV      #T202JS,RNGRET ;SET UP DUMMY RETURN FOR RING
16                                     ;FROM RING INTERRUPT
17 011344 012777 000140 004240      MOV      #SCNENA+INTENA,@DHMCSR ;ENABLE LINE SCANNER
18                                     ;START SCANNER
19 011352 005067 166420          CLR      PS          ;ENABLE INTERRUPTS
20 011356 005077 004236          T202JS: CLR      @TKDDBR
21 011362 105777 004230          1$:  TSTB   @TKCSR
22 011366 100375          BPL      1$
23 011370 005777 004224          TST      @TKDDBR
24
25 ;DISCONNECT SEQUENCE REQUESTED
26
27 011374 012767 000340 166374      MOV      #340,PS      ;LOCK OUT INTERRUPTS
28 011402 005077 004204          CLR      @DHMCSR      ;STOP SCANNER
29 011406 016777 004270 004176      MOV      LINORG,@DHMCSR ;SET LINE COUNTER TO SELECTED ORIGINATE LINE
30 011414 042777 000002 004172      BIC      #TRMRDY,@DHMLSR ;SET TERMINAL READY ON SELECTED LINE
31 011422 104024          WAITS
32 011424 104026          CKINTT
33 011426 104022          WAITRN          ;WAIT FOR TRANSITIONS TO OCCUR

```

```

1
2
3 ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON SELECTED
4 ;ORIGINATE AND ANSWER LINES
5 011430 104023 CKTRAN ;CHECK TRANSITIONS AND STATUS
6 ;ON SELECTED ASNWER AND
7 ;ORIGINATE LINES
8 011432 000003 LINENA+TRMRDY ;EXPECT LINE ENABLE AND
9 ;TERMINAL READY STATUS BITS
10 ;SET ON ANSWER LINE
11 011434 000001 LINENA ;EXPECT LINE ENABLE STATUS
12 ;BIT SET ON ORIGINATE LINE
13 011436 000000 0 ;EXPECT NO TRANSITIONS ON
14 ;ANSWER LINE
15 011440 000000 0 ;EXPECT NO TRANSITIONS ON
16 ;ORIGINATE LINE
17 011442 011454 T202J1 ;GO HERE IF ANSWER LINE STATUS ERROR
18 011444 011460 T202J2 ;GO HERE IF ORIGINATE LINE STATUS ERROR
19 011446 011464 T202J3 ;GO HERE IF ANSWER LINE TRANSITION ERROR
20 011450 011470 T202J4 ;GO HERE IF ORIGINATE LINE TRANSITIONS ERROR
21 011452 011474 T202JN ;GO TO END OF TEST IF NO ERRORS
22 011454 104015 T202J1: ERRORS ;ANSWER LINE STATUS ERRORO
23 011456 000207 RTS PC ;CONTINUE CHECKING
24 011460 104015 T202J2: ERRORS ;ORIGINATE LINE STATUS ERROR
25 011462 000207 RTS PC ;CONTINUE CHECKING
26 011464 104014 T202J3: ERRORT ;ANSWER LINE TRANSITION ERROR
27 011466 000207 RTS PC ;CONTINUE CHECKING
28 011470 104014 T202J4: ERRORT ;ORIGINATE LINE TRANSITION ERROR
29 011472 000207 RTS PC ;CONTINUE CHECKING
30
31 011474 104004 T202JN: TYPE ;TYPE "202C TEST COMPLETE"
32 011476 016443 MT202A
33 011500 104026 CKINTT
34 011502 000167 177020 JMP ST202B ;GET NEW LINE NUMBERS
35 ;RESTART TEST

```

```

1
2
3 ;DETECT AND RECORD TRANSITIONS ON SELECTED
4 ;ORIGINATE AND ANSWER LINES
5
6 ;TRANSITION DATA IS STORED IN LOCATIONS ANSFLG AND ORGFLG
7 ;FOR ANSWER AND ORIGINATE LINES RESPECTIVELY
8 ;FORMAT OF DATA IS (FOR BOTH LINES)
9
10 ;BIT0=1, SECONDARY RECEIVE CAUSED INTERRUPT
11 ;BIT1=1, CLEAR TO SEND CAUSED INTERRUPT
12 ;BIT2=1, CARRIER CAUSED INTERRUPT
13 ;BIT3=1, RING CAUSED INTERRUPT
14 011506 017704 004100 TRANS: MOV @DHMCSR,R4 ;GET LINE NUMBER AND
15 ;INTERRUPT FLAGS
16 011512 010405 MOV R4,R5
17 011514 042705 177760 BIC #177760,R5 ;EXTRACT LINE NUMBER
18 011520 026705 004156 CMP LINORG,R5 ;DID ORIGINATE LINE INTERRUPT
19 011524 001411 BEQ ORGTR ;IF YES, SERVICE
20 011526 026705 004152 CMP LINANS,R5 ;DID ANSWER LINE INTERRUPT
21 011532 001443 BEQ ANSTR ;IF YES, SERVICE
22 011534 010577 004052 MOV R5,@DHMCSR
23 011540 017703 004050 MOV @DHMLSR,R3
24 011544 104016 ERRORN ;INTERRUPT ON INCORRECT LINE
25 011546 000471 BR FATEX
26
27 ;RECORD TRANSITIONS FOR ORIGINATE LINE
28
29 011550 032704 100000 ORGTR: BIT #RINGF,R4 ;IF RING CAUSED INTERRUPT,
30 011554 001403 BEQ ORGTR1 ;SET RING TRANSITION BIT
31 011556 052767 000010 004124 BIS #10,ORGFLG
32 011564 032704 040000 ORGTR1: BIT #COF,R4 ;IF CARRIER CAUSED INTERRUPT
33 011570 001403 BEQ ORGTR2 ;SET CARRIER TRANSITION BIT
34 011572 052767 000004 004110 BIS #4,ORGFLG
35 011600 032704 020000 ORGTR2: BIT #CSF,R4 ;IF CLEAR TO SEND
36 ;CAUSED INTERRUPT
37 011604 001403 BEQ ORGTR3 ;SET CLEAR TO SEND
38 ;TRANSITION BIT
39 011606 052767 000002 004074 BIS #2,ORGFLG
40 011614 032704 010000 ORGTR3: BIT #SECRXF,R4 ;IF SECONDARY RECEIVE
41 ;CAUSED INTERRUPT
42 011620 001403 BEQ ORGTR4 ;SET SECONDARY RECEIVE
43 011622 052767 000001 004060 BIS #1,ORGFLG ;TRANSITION BIT
44 011630 032704 170000 ORGTR4: BIT #RINGF+COF+CSF+SECRXF,R4
45 ;IF NO INTERRUPT FLAGS SET
46 011634 001044 BNE TRANEX ;EXIT TRANSITION DETECTION
47 011636 104016 ORGTRR: ERRORN
48 011640 000434 BR FATEX

```

```

1
2
3
4 011642 032704 100000 ANSTR: BIT @RINGF,R4 ;IF RING CAUSED INTERRUPT,
5 011646 001403 BEQ ANSTR1 ;SET RING TRANSITION BIT
6 011650 052767 000010 004030 BIS #10,ANSFLG
7 011656 032704 040000 ANSTR1: BIT @COF,R4 ;IF CARRIER CAUSED INTERRUPT
8 011662 001403 BEQ ANSTR2 ;SET CARRIER TRANSITION BIT
9 011664 052767 000004 004014 BIS #4,ANSFLG
10 011672 032704 020000 ANSTR2: BIT @CSF,R4 ;IF CLEAR TO SEND
11 ;CAUSED INTERRUPT
12 011676 001403 BEQ ANSTR3 ;SET CLEAR TO SEND
13 ;TRANSITION BIT
14 011700 052767 000002 004000 BIS #2,ANSFLG
15 011706 032704 010000 ANSTR3: BIT @SECRXF,R4 ;IF SECONDARY RECEIVE
16 ;CAUSED INTERRUPT
17 011712 001403 BEQ ANSTR4 ;SET SECONDARY RECEIVE
18 011714 052767 000001 003764 BIS #1,ANSFLG ;TRANSITION BIT
19 011722 032704 170000 ANSTR4: BIT @RINGF+COF+CSF+SECRXF,R4
20 ;IF NO INTERRUPT FLAGS SET
21 011726 001007 BNE TRANEX ;EXIT TRANSITION DETECTION
22 011730 104016 ANSTR: ERRORN
23 011732 005067 003702 FATEX: CLR TSTNO
24 011736 022626 POP2SP
25 011740 000177 000000 JMP @FATRET
26 011744 000000 FATRET: 0
27
28 ;EXIT TRANSITION DETECTION
29
30 011746 005704 TRANEX: TST R4 ;IF RING FLAG WAS SET
31 011750 100002 BPL .+6 ;SET UP SPECIAL RETURN
32 011752 016716 003754 MOV RINGRET,(SP)
33 011756 012777 000140 003626 TRANX1: MOV @SCNENA+INTENA,@DHMCSR ;RESTART SCANNER
34 011764 000002 RTI
35
36 ;TYPE TRANSITION DATA AND RETURN
37
38 011766 017767 003620 000756 TRNTYP: MOV @DHMCSR,DATA1
39 011774 017767 003614 000752 MOV @DHMLSR,DATA2
40 012002 104004 TYPE
41 012004 017114 MTRNDT
42 012006 104006 OCTASC
43 012010 012014 TRNTAB
44 012012 000761 BR TRANX1
45 012014 000002 TRNTAB: 2
46 012016 000006 6
47 012020 012752 DATA1
48 012022 000003 3
49 012024 012754 DATA2

```

```

1
2
3
4 012026 000005          GETLIN: RESET
5 012030 104013          INSTRG          ;TYPE "ORIGINATE LINE"
6 012032 016351          MSELOR          ;AND GET LINE NUMBER
7 012034 000000          0
8 012036 000017          17
9 012040 015702          LINORG
10 012042 104013         INSTRG          ;TYPE "ANSWER LINE-"
11 012044 016375         MSELANS         ;AND GET LINE NUMBER
12 012046 000000          0
13 012050 000017          17
14 012052 015704         LINANS
15 012054 104004         TYPE
16 012056 017005         MCRLF
17 012060 000002         RTI          ;RETURN TO CALLING ROUTINE
18
19
20
21 012062 000005          SETUPS: RESET
22 012064 012767 000340 165704  MOV    #340,PS          ;LOCK OUT ALL INTERRUPTS
23 012072 011605          MOV    (SP),R5
24 012074 012567 000662  MOV    (R5)+,NXTTS
25 012100 012567 000636  MOV    (R5)+,ERR1
26 012104 010516          MOV    R5,(SP)
27 012106 012777 006000 003476  MOV    @CLRSCN+CLRMUX,@DHMCSR ;CLEAR LINE SCANNER AND MULTIPLEXER
28 012114 032777 000020 003470  SETUP1: BIT    @BUSY,@DHMCSR    ;WAIT FOR SCANNER TO CLEAR
29 012122 001374          BNE    SETUP1
30 012124 005067 003502  CLR    ERRFLG
31
32
33
34
35 012130 016777 003546 003454  SETUP2: MOV    LINORG,@DHMCSR    ;SET UP TO ENABLE ORIGINATE LINE
36
37 012136 012777 000003 003450  MOV    @LINENA+TRMRDY,@DHMLSR ;ORIGINATE LINE NUMBER
38
39 012144 016777 003534 003440  MOV    LINANS,@DHMCSR    ;SET LINE ENABLE AND
40 012152 012777 000001 003434  MOV    @LINENA,@DHMLSR    ;TERMINAL READY ON ORIGINATE LINE
41
42
43
44
45
46 012160 012777 011506 003420  MOV    @TRANS,@DHMVEC    ;SET UP TO RECEIVE INTERRUPTS
47
48 012166 012777 000340 003414  MOV    #340,@DHMLVL    ;START LINE SCANNER
49 012174 012777 000140 003410  MOV    @SCNENA+INTENA,@DHMCSR ;SET UP INTERRUPT VECTOR
50 012202 005067 003500          CLR    ANSFLG          ;FOR TRANSITION DETECTION
51 012206 005067 003476          CLR    ORGFLG          ;SET UP INTERRUPT SERVICE LEVEL
52 012212 012767 012242 003512  MOV    @SETUP4,RNGRET    ;START SCANNER, ENABLE INTERRUPTS
53
54 012220 104004          CLR    ORGFLG          ;CLEAR TRANSITION DETECTED FLAGS
55 012222 016171          MOV    @SETUP4,RNGRET    ;SET UP RETURN FROM
56 012224 005067 165546          TYPE          ;DETECTION OF RING INTERRUPT
57 012230 005067 003456          DIALM          ;REQUEST OPERATOR TO DIAL
                    CLR    PS          ;CLEAR PROCESSOR STATUS WORD
                    CLR    TIME1       ;CLEAR TIMER

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58 012234 012767 001000 003452      MOV      #1000,TIME2      ;SET UP FOR 5 MINUTE DELAY
59 012242 005767 003440      SETUP4: TST      ANSFLG      ;IF TRANSITION HAS OCCURED,
60 012246 001014              BNE      SETUPB          ;EXIT WAIT LOOP
61 012250 005767 003434      TST      ORGFLG
62 012254 001011              BNE      SETUPB
63 012256 005267 003430      INC      TIME1          ;ALLOW OPERATOR 5 MINUTES TO DIAL
64 012262 001367              BNE      SETUP4
65 012264 005367 003424      DEC      TIME2
66 012270 001364              BNE      SETUP4
67 012272 022626              POP2SP
68 012274 000177 000442      JMP      @ERR1
69 012300 022626      SETUPB: POP2SP
70 012302 000177 000454      JMP      @NXTTS
71 012306 012766 000340 000002      MOV      #340,+2(SP)
72 012314 000002              RTI
73
74                          ;CHECK FOR RING INTERRUPT ON SELECTED ANSWER LINE
75
76 012316 011605      CKRNG:  MOV      (SP),R5
77 012320 012567 000436      MOV      (R5)+,NXTTS
78 012324 012567 000412      MOV      (R5)+,ERR1
79 012330 012567 000410      MOV      (R5)+,ERR2
80 012334 010516      MOV      R5,(SP)
81 012336 012705 000010      MOV      #10,R5          ;EXPECT RING ONLY ON ANSWER LINE
82 012342 016704 003340      MOV      ANSFLG,R4      ;GET ACTUAL TRANSITION DATA
83 012346 016703 003332      MOV      LINANS,R3      ;SET UP LINE NUMBER
84 012352 020504      CMP      R5,R4          ;DID RING CAUSE INTERRUPT
85 012354 001402      BEQ      CKRNG1        ;ON ANSWER LINE
86 012356 004777 000360      JSR      PC,@ERR1
87 012362 005005      CKRNG1: CLR      R5
88 012364 016704 003320      MOV      ORGFLG,R4
89 012370 016703 003306      MOV      LINORG,R3
90 012374 005704      TST      R4          ;IF TRANSITION OCCURED
91 012376 001403      BEQ      CKRNG2        ;ON ORIGINATE LINE, ERROR
92 012400 022626      POP2SP
93 012402 000177 000336      JMP      @ERR2
94 012406 022626      CKRNG2: POP2SP
95 012410 000177 000346      JMP      @NXTTS

```

```

1
2 012414 005067 003266          WAITR: CLR      ANSFLG
3 012420 005067 003264          CLR      ORGFLG
4 012424 012777 011506 003154   MOV      @TRANS,@DHMVEC
5 012432 012767 012452 003272   MOV      @WAITRR,RNGRET          ;SET UP FOR RETURN
6                                     ;FROM RING DETECTION
7 012440 012777 000140 003144   MOV      @SCENEA+INTENA,@DHMCSR ;START SCANNER
8 012446 005067 165324          CLR      PS
9 012452 005067 003234          WAITRR: CLR      TIME1
10 012456 012767 000025 003230  MOV      @25,TIME2
11 012464 005267 003222          WAITR1: INC     TIME1          ;WAIT FOR TRANSITIONS OF
12 012470 001375                BNE     WAITR1          ;CARRIER AND CLEAR TO SEND
13 012472 005367 003216          DEC     TIME2
14 012476 001372                BNE     WAITR1
15 012500 000002                RTI
16
17                                     ;CHECK FOR CORRECT STATUS AND TRANSITIONS ON
18                                     ;SELECTED ORIGINATE AND ANSWER LINES
19
20 012502 012767 000340 165266  CKTRN: MOV      @340,PS          ;LOCK OUT FURTHER INTERRUPTS
21 012510 005077 003076          CLR      @DHMCSR          ;STOP LINE SCANNER
22 012514 011605                MOV      (SP),R5
23 012516 012567 000230          MOV      (R5)+,DATA1
24 012522 012567 000226          MOV      (R5)+,DATA2
25 012526 012567 000224          MOV      (R5)+,DATA3
26 012532 012567 000222          MOV      (R5)+,DATA4
27 012536 012567 000200          MOV      (R5)+,ERR1
28 012542 012567 000176          MOV      (R5)+,ERR2
29 012546 012567 000174          MOV      (R5)+,ERR3
30 012552 012567 000172          MOV      (R5)+,ERR4
31 012556 012567 000200          MOV      (R5)+,NXTTS
32 012562 010516                MOV      R5,(SP)
33 012564 016705 000162          MOV      DATA1,R5
34 012570 016777 003110 003014   MOV      LINANS,@DHMCSR      ;SET LINE COUNTER TO ANSWER LINE
35 012576 017704 003012          MOV      @DHMLSR,R4        ;GET ACTUAL ANSWER LINE STATUS
36 012602 016703 003076          MOV      LINANS,R3
37 012606 020504                CMP      R5,R4              ;COMPARE
38 012610 001402                BEQ     CKTRN1
39 012612 004777 000124          JSR     PC,@ERR1
40 012616 016777 003060 002766  CKTRN1: MOV      LINORG,@DHMCSR   ;SET LINE COUNTER TO ORIGINATE LINE
41 012624 017704 002764          MOV      @DHMLSR,R4        ;GET ACTUAL ORIGINATE LINE STATUS
42 012630 016705 000120          MOV      DATA2,R5
43 012634 016703 003042          MOV      LINORG,R3
44 012640 020504                CMP      R5,R4              ;COMPARE
45 012642 001402                BEQ     CKTRN2
46 012644 004777 000074          JSR     PC,@ERR2

```

```

1
2
3           ;CHECK FOR CORRECT TRANSITIONS ON
4           ;SELECTED ORIGINATE AND ANSWER LINES
5 012650 105767 000103      CKTRN2: TSTB  DATA3+1
6 012654 100003              BPL      .+10
7 012656 042767 000010 003022  BIC      #10,ANSFLG
8 012664 116704 003016      MOVB     ANSFLG,R4           ;GET TRANSITION DATA FOR
9 012670 116705 000062      MOVB     DATA3,R5
10 012674 016703 003004     MOV      LINANS,R3
11 012700 020504              CMP      R5,R4           ;DID CORRECT TRANSITIONS OCCUR
12 012702 001402              BEQ      CKTRN3
13 012704 004777 000036     JSR      PC,@ERR3
14 012710 016704 002774     CKTRN3: MOV     ORGFLG,R4           ;GET TRANSITION DATA FOR
15 012714 016705 000040     MOV     DATA4,R5
16 012720 016703 002756     MOV     LINORG,R3
17 012724 020504              CMP     R5,R4           ;DID CORRECT TRANSITIONS OCCUR
18 012726 001402              BEQ     CKTRN4
19 012730 004777 000014     JSR     PC,@ERR4
20 012734 022626              CKTRN4: POP2SP
21 012736 000177 000020     JMP     @NXTTS
22 012742 000000      ERR1:  0
23 012744 000000      ERR2:  0
24 012746 000000      ERR3:  0
25 012750 000000      ERR4:  0
26 012752 000000      DATA1: 0
27 012754 000000      DATA2: 0
28 012756 000000      DATA3: 0
29 012760 000000      DATA4: 0
30 012762 000000      NXTTS: 0

```



```

1
2
3      ;END OF PASS
4      ;UPDATE PASS COUNT
5      ;TYPE END OF PASS MESSAGE
6 012764      EOP:
7 012764 005267 002646      INC      PASCNT      ;UPDATE PASS COUNT
8 012770 012767 000001 002642      MOV      #1,TSTNO      ;START AT FIRST TEST OF GROUP
9 012776 000005      RESET      ;CLEAR THE WORLD
10 013000 005067 002724      CLR      FILLA      ;INIT COUNTER
11 013004 005367 002720      1$:      DEC      FILLA      ;COUNT THE CTR
12 013010 001375      BNE      1$      ;BR TIL STALL TIMES OUT
13 013012 104004      TYPE      ; RING BELL
14 013014 017271      MEPASS
15 013016 016701 165020      MOV      42,R1      ;ARE YOU ON ACT11?
16 013022 001521      BEQ      TSTENT      ;NO
17 013024 000005      RESET
18 013026 004711      LOGICAL:      JSR      PC,(R1)
19 013030 000240      NOP
20 013032 000240      NOP
21 013034 000240      NOP
22 013036 000240      NOP
23 013040 000167 000222      JMP      TSTENT      ;GET ADDRESS OF FIRST TEST
24
25      ;EMT DISPATCH SERVICE
26      ;ARGUMENT OF EMT IS EXTRACTED
27      ;AND USED AS OFFSET TO OBTAIN POINTER
28      ;TO SELECTED SUBROUTINE
29
30 013044 011646      EMTSRV:      MOV      (SP),-(SP)      ;GET PC OF RETURN
31 013046 162716 000002      SUB      #2,(SP)      ;=PC OF EMT
32 013052 017616 000000      MOV      @ (SP),(SP)      ;GET EMT
33 013056 006316      EMTOK:      ASL      (SP)      ;MULTIPLY EMT ARG BY 2
34 013060 042716 177001      BIC      #177001,(SP)      ;CLEAR UNWANTED BITS
35 013064 062716 017426      ADD      #EMTTAB,(SP)      ;POINTER TO SUBROUTINE ADDRESS
36 013070 017616 000000      MOV      @ (SP),(SP)      ;SUBROUTINE ADDRESS
37 013074 000136      JMP      @ (SP)+      ;GO TO SUBROUTINE
38
39 013076 105777 002514      CKINT:      TSTB      @TKCSR
40 013102 100001      BPL      1$
41 013104 104027      KBDIN
42 013106 000002      1$:      RTI
43

```

```

1
2
3
4
5
6
7
8
9 013110 005767 166176          LOOP:  TST    XFLAG  ;IS THERE AN XOR TESTER OUT THERE ?
10 013114 100022                BPL    4$      ;NO
11 013116 016746 164662          MOV    4,-(SP) ;SAVC 4
12 013122 012767 013142 164654  MOV    #1$,4   ;SET UP SVC ROUTINE
13 013130 005767 163724          TST    177060 ;GOT SOMETHING LIKE SLAVE SYNC
14 013134 012667 164644          MOV    (SP)+,4 ;YOU BETCHUM
15 013140 000404                BR     2$
16 013142 022626                1$:   POP2SP  ;RESTORE STACK
17 013144 012667 164634          MOV    (SP)+,4 ;RESTORE 4
18 013150 000402                BR     3$
19 013152 000167 000104          2$:   JMP    LOOPX  ;GO TO NEXT TEST
20 013156 000167 000104          3$:   JMP    TSTENT ;GO
21 013162
22 013162 005067 164610          4$:   CLR    PS
23 013166 052777 000100 002422  BIS    #INTENA,@TKCSR
24 013174 005767 002436          TST    PASCNT
25 013200 001430                BEQ    LOOPX  ;1ST PASS
26 013202 005767 002424          5$:   TST    ERRFLG ;NO ITERATIONS
27 013206 001404                BEQ    LOOPS  ;IF ERROR OCCURED FLAG=1,
28 013210 032777 002000 002410  BIT    #SW10,@SWR ;CHECK FOR ESCAPE TO NEXT TEST
29 013216 001021                BNE    LOOPX  ;IF SW10=1,
30 013220 032777 040000 002400  LOOPS: BIT    #SW14,@SWR ;ESCAPE TO NEXT TEST
31 013226 001041                BNE    LOOPX  ;IF SW14=1,
32 013230 032777 004000 002370  LOOPS: BIT    #SW11,@SWR ;LOOP ON CURRENT TEST
33 013236 001011                BNE    LOOPX  ;IF SW11=1,
34 013240 005367 002400          DEC    ICOUNT ;INHIBIT ITERATIONS
35 013244 001406                BEQ    LOOPX  ;UPDATE ITERATION COUNT
36 013246 016716 002370          LOOPER: MOV   RETURN,(SP) ;IF ICOUNT=0, GO TO NEXT TEST
37 013252 042777 000100 002336  BIC    #INTENA,@TKCSR ;SET UP FOR RETURN TO CURRENT TEST
38 013260 000002                RTI
39 013262 005267 002352          LOOPX: INC    TSTNO  ;RETURN TO CURRENT TEST
40 013266 016705 002346          TSTENT: MOV   TSTNO,R5 ;UPDATE TEST NUMBER
41 013272 006305                ASL    R5      ;GET TEST NUMBER
42 013274 006305                ASL    R5      ;MULTIPLY TEST NUMBER BY 4
43 013276 066705 002370          ADD    TSTPNT,R5 ;GET POINTER FOR TEST ENTRY
44 013302 011567 002334          MOV    (R5),RETURN ;GET STARTING ADDRESS OF NEXT TEST
45 013306 001626                BEQ    EOP     ;IF ADDRESS=0, GO TO END OF PASS
46 013310 012516                MOV    (R5)+,(SP) ;PUT STARTING ADDRSS ON STACK
47 013312 011567 002326          MOV    (R5),ICOUNT ;GET ITERATION COUNT FOR TEST
48 013316 005067 002310          CLR    ERRFLG  ;CLEAR ERROR OCCURED FLAG
49 013322 042777 000100 002266  BIC    #INTENA,@TKCSR
50 013330 000002                RTI
51 013332 012767 000001 002304  LOOPL: MOV   #1,ICOUNT ;GO TO TEST
52 013340 000742                BR     LOOPER  ;SET UP TO EXIT TEST AFTER LOOP
53
54
55
56
57 013342 005767 002264          FREEZE: TST   ERRFLG ;GO TO LOOP SERVICE
;CHECK FOR LOOPING WITH SAME DATA
;CHECK FOR ESCAPE TO NEXT TEST ON ERROR
;IF ERROR FLAG=0,

```

```
58 013346 001413          BEQ    FREEZX          ;DO NOT TEST FOR ESCAPE
59 013350 032777 002000 002250 BIT    #SW10,@SWR      ;IF SW10=1,
60 013356 001341          BNE    LOOPX          ;ESCAPE TO NEXT TEST
61 013360 032777 001000 002240 BIT    #SW09,@SWR      ;IF SW09=1,
62 013366 001403          BEQ    FREEZX          ;FREEZE CURRENT DATA
63 013370 017616 000000    MOV    @(SP),(SP)      ;GET LOOPING ADDRESS
64 013374 000002          RTI                    ;LOOP
65 013376 062716 000002    FREEZX: ADD   #2,(SP)  ;CONTINUE IN CURRENT TEST
66 013402 000002          RTI
67
68
69                          ;ROUTINE TO STEP THRU LINES
70 013404 052777 000400 002200 STEPER: BIS   #STEP,@DHMCSR
71 013412 032777 000020 002172 1$: BIT    #BUSY,@DHMCSR
72 013420 001374          BNE    1$
73 013422 000207          RETURN
74
75
```

```

1
2
3           ;GENERAL ERROR SERVICE
4           ;ONLY PC OF FAILING TEST IS OUTPUT TO TELEPRINTER
5 013424 005067 002202      ERR:  CLR      ERRFLG           ;ALWAYS TYPE PC+2
6                               ;OF TEST THAT FAILED
7 013430 005067 000210      CLR      ERRMSG           ;NO MESSAGE
8 013434 005067 000216      CLR      ERTAB           ;NO TABLE OF DATA
9 013440 000451              BR       ERRGEN           ;OUTPUT ERROR MESSAGE
10
11          ;TRANSITION DETECTION ERROR SERVICE
12
13          ;FORMAT FOR ERROR TYPEOUT IS
14
15          ;XXXXXX TRANSITION ERROR
16          ;EXP  REC  LINE
17          ;AA   BB   CC
18
19          ;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
20          ;      AA=EXPECTED INTERRUPT FLAGS (CORRESPONDS TO 4 MSB OF CONTROL REGISTER)
21          ;      BB=RECEIVED INTERRUPT FLAGS (AS ABOVE)
22          ;      CC=LINE ON WHICH ERROR OCCURED
23 013442 005067 002164      ERRRT: CLR      ERRFLG           ;ALWAYS OUTPUT ALL DATA
24 013446 012767 016132 000170  MOV     #MTRANE,ERRMSG       ;TYPE "TRANSITION ERROR"
25 013454 012767 013750 000174  MOV     #ERTAB1,ERTAB       ;TABLE OF DATA
26 013462 000440              BR       ERRGEN           ;OUTPUT ERROR MESSAGE
27
28          ;ON-LINE STATUS ERROR SERVICE
29
30          ;FORMAT FOR LINE STATUS ERROR IS
31
32          ;XXXX LINE ERROR
33          ;EXP  REC  LINE
34          ;AAA  BBB  CC
35
36          ;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
37          ;      AAA=EXPECTED LINE STATUS AT TIME OF ERROR
38          ;      BBB=RECEIVED LINE STATUS AT TIME OF ERROR
39          ;      CC=LINE ON WHICH ERROR OCCURED
40
41
42 013464 005067 002142      ERRS:  CLR      ERRFLG           ;ALWAYS OUTPUT ALL DATA
43 013470 012767 016101 000146  MOV     #MLINE1,ERRMSG      ;TYPE "LINE ERROR"
44                               ;EXP REC LINE"
45 013476 012767 013766 000152  MOV     #ERTAB2,ERTAB       ;TABLE OF DATA
46 013504 000427              BR       ERRGEN           ;OUTPUT ERROR MESSAGE

```

```

1
2
3           ;FATAL TRANSITION ERROR
4           ;FORMAT FOR FATAL ERROR TYPEOUT IS
5
6           ;XXXXXX FATAL ERROR
7           ;CSTAT LSTAT
8           ;AAAAAA BBB
9
10          ;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
11          ; AAAAAA=RECEIVED CONTROL STATUS ON LINE THAT INTERRUPTED
12          ; BBB=RECEIVED LINE STATUS ON LINE THAT INTERRUPTED
13 013506 005067 002120          ERRN: CLR   ERRFLG           ;ALWAYS OUTPUT ALL DATA
14 013512 012767 017062 000124  MOV   #MFATAL,ERRMSG       ;TYPE "FATAL ERROR"
15                                     ;CSTAT LSTAT"
16 013520 012767 014004 000130  MOV   #ERTAB3,ERTAB       ;TABLE OF DATA
17 013526 000416                BR    ERRGEN           ;OUTPUT ERROR MESSAGE
18
19          ;"CONTROL STATUS" ERROR SERVICE
20          ;FORMAT FOR CONTROL STATUS ERROR IS
21
22          ;XXXXXX STATUS ERROR
23          ;EXP REC
24          ;AAAAAA BBBB
25
26          ;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
27          ; AAAAAA=EXPECTED CONTROL STATUS AT TIME OF ERROR
28          ; BBBB=RECEIVED(ACTUAL) CONTROL STATUS AT TIME OF ERROR
29
30          ;
31 013530 012767 016013 000106  ERRCS: MOV   #MSTATE,ERRMSG       ;TYPE "STATUS ERROR"
32                                     ;"EXP REC"
33 013536 012767 014016 000112  MOV   #ERTAB4,ERTAB       ;TABLE OF DATA
34 013544 000407                BR    ERRGEN           ;OUTPUT DATA
35
36          ;LINE STATUS ERROR SERVICE
37
38          ;FORMAT FOR LINE STATUS ERROR IS
39
40          ;XXXX LINE ERROR
41          ;EXP REC LINE SEL
42          ;AAA DDD CC DD
43
44          ;WHERE XXXXXX=PC+2 OF CALL TO ERROR ROUTINE
45          ; AAA=EXPECTED LINE STATUS AT TIME OF ERROR
46          ; BBB=RECEIVED LINE STATUS AT TIME OF ERROR
47          ; CC=LINE ON WHICH ERROR OCCURED
48          ; DD=THE LINE ON WHICH THE PROGRAM WAS OPERATING
49
50          ;
51 013546 012767 016044 000070  ERRLS: MOV   #MLINER,ERRMSG
52 013554 012767 014030 000074  MOV   #ERTAB5,ERTAB
53 013562 000400                BR    ERRGEN

```

```

1
2
3           ;GENERAL ERROR HANDLER
4           ;TYPE PC+2 OF TEST THAT FAILED
5           ;TYPE ERROR MESSAGE (IF ANY)
6           ;TYPE DATA RELATING TO FAILURE (IF ANY)
7 013564 005067 164206          ERRGEN: CLR    PS
8 013570 012777 000100 002020  MOV    #INTENA,@TKCSR
9 013576 032777 020000 002022  BIT    #SW13,@SWR          ;IF SW13=1, DO NOT
10 013604 001026                BNE    .3                  ;TYPE ERROR MESSAGE
11 013606 021667 002052          CMP    (SP),SAVPC          ;SAME ERROR AGAIN
12 013612 001402                BEQ    .+6
13 013614 005067 002012          CLR    ERRFLG
14 013620 104005                SAV05P
15 013622 005767 002004          TST    ERRFLG            ;IF ERROR OCCURED FLAG=1,
16 013626 001007                BNE    .1                  ;TYPE DATA ONLY
17 013630 104006                OCTASC          ;TYPE PC+2 OF CALL TO ERROR ROUTINE
18 013632 013742                ERTAB0
19 013634 005767 000004          TST    ERRMSG
20 013640 001407                BEQ    .2
21 013642 104004                TYPE                    ;TYPE ERROR MESSAGE
22 013644 000000                ERRMSG: 0
23 013646 005767 000004          .1:  TST    ERTAB
24 013652 001402                BEQ    .2
25 013654 104006                OCTASC          ;TYPE DATA
26 013656 000000                ERTAB: 0
27 013660 104007                .2:  RES05          ;RESTORE R0 R5
28
29           ;ERROR HALT SERVICE
30
31 013662 032777 100000 001736 .3:  BIT    #SW15,@SWR          ;IF SW15=0, DO NOT
32 013670 001406                BEQ    .4                  ;HALT ON ERROR
33 013672 000000                HALT          ;HALT AND DISPLAY ADDRESS OF FAILING TEST
34 013674 022767 000176 001724  CMP    #SWREG,SWR
35 013702 001001                BNE    .4
36 013704 104025                CNTLUU
37 013706 012767 000001 001716 .4:  MOV    #1,ERRFLG          ;SET ERROR OCCURED FLAG
38 013714 042777 000100 001674  BIC    #INTENA,@TKCSR
39 013722 000002                RTI                    ;RETURN TO TEST
40
41           ;TIMEOUT ERROR WAITING FOR INTERRUPT ON TEST 33
42 013724 012767 015734 177712  ERRQ:  MOV    #MNOINT,ERRMSG
43 013732 012767 014060 177716  MOV    #ERTAB6,ERTAB
44 013740 000711                BR     ERRGEN          ;TYPE LN#,CSR,LSR+MSG
45                                     ;OUTPUT DATA
46

```

```

1
2
3
4
5 013742 000001
6 013744 000006
7 013746 015664
8 013750 000003
9 013752 000002
10 013754 015660
11 013756 000002
12 013760 015656
13 013762 000002
14 013764 015654
15 013766 000003
16 013770 000003
17 013772 015660
18 013774 000003
19 013776 015656
20 014000 000002
21 014002 015654
22 014004 000002
23 014006 000006
24 014010 015656
25 014012 000003
26 014014 015654
27 014016 000002
28 014020 000006
29 014022 015660
30 014024 000006
31 014026 015656
32 014030 000004
33 014032 000003
34 014034 015660
35 014036 000003
36 014040 015656
37 014042 000002
38 014044 015654
39 014046 000002
40 014050 015650
41
42
43 014052 000001
44 014054 000006
45 014056 000176
46
47 014060 000003
48 014062 000003
49 014064 015656
50 014066 000006
51 014070 015652
52 014072 000006
53 014074 015654
54

```

;TABLE S OF DATA FOR ERROR TYPEOUT  
;TABLE FOR TRANSITION STATUS ERROR

```

ERTAB0: 1
6
SAVPC
ERTAB1: 3
2
SAVR5 ;CONTAINS EXPECTED TRANSITION STATUS
2
SAVR4 ;CONTAINS RECEIVED TRANSITION STATUS
2
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
ERTAB2: 3
3
SAVR5 ;CONTAINS EXPECTED LINE STATUS
3
SAVR4 ;CONTAINS RECEIVED LINE STATUS
2
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
ERTAB3: 2
6
SAVR4
3
SAVR3
ERTAB4: 2
6
SAVR5 ;CONTAINS EXPECTED CONTROL STATUS
5
SAVR4 ;CONTAINS RECEIVED CONTROL STATUS
ERTAB5: 4
3
SAVR5 ;CONTAINS EXPECTED LINE STATUS
3
SAVR4 ;CONTAINS RECEIVED LINE STATUS
2
SAVR3 ;CONTAINS NUMBER OF LINE WHERE ERROR OCCURED
2
SAVR1 ;CONTAINS NUMBER OF LINE UNDER TEST
SWRTB: 1
6
SWREG
ERTAB6: 3
3
SAVR4 ;FAILING LINE #
6
SAVR2 ;CSR OF DEVICE
6
SAVR3 ;LSR OF DEVICE

```

```

1
2
3           ;CONVERT OCTAL TO ASCII AND
4           ;OUTPUT ON TELETYPE
5 014076 017605 000000      OCTASN: MOV      @ (SP),R5           ;GET POINTER TO TABLE OF DATA
6 014102 062716 000002      ADD      @2,(SP)
7 014106 012767 000010      MOV      #10,RADIX
8 014114 012704 017311      MOV      #MBCD+2,R4
9 014120 012567 001542      MOV      (R5)+,WRDCNT           ;SET UP POINTER FOR CONVERTED DATA
10 014124 012567 001540     OCTAS1: MOV      (R5)+,CHRCNT        ;GET NUMBER OF WORDS TO BE CONVERTED
11 014130 013567 000276      MOV      @ (R5)+,BINWRD        ;GET NUMBER OF DIGITS IN WORD
12 014134 104010             CONVERT                          ;GET DATA TO BE CONVERTED
13 014136 005367 001524      DEC      WRDCNT                 ;CONVERT TO ASCII
14 014142 001370             BNE      OCTAS1                 ;IF ALL DATA IS NOT CONVERTED
15 014144 112714 000100      MOV      #100,(R4)             ;CONTINUE
16 014150 005767 000112      TST      SMLN                   ;PUT TERMINATOR AT END OF MESSAGE
17 014154 001002             BNE      1$
18 014156 104004             TYPE
19 014160 017307             MBCD
20 014162 000002      1$: RTI                       ;OUTPUT CONVERTED DATA
21                                     ;TO TELETYPE
22                                     ;RETURN TO CALLING ROUTINE
23
24 014164 005067 000072      CNTLU: CLR      TMP1
25 014170 012767 000001 000066  MOV      #1,TMP2
26 014176 104004             TYPE
27 014200 017246             #SWREQ
28 014202 052767 000001 000056  BIS      #1,SMLN
29 014210 104006             OCTASC
30 014212 014052             SWRTB
31 014214 104004             TYPE
32 014216 017311             MBCD+2
33 014220 104013             INSTRG
34 014222 017257             #NEWIS
35 014224 000000             0
36 014226 177777             177777
37 014230 014262             TMP1
38 014232 126727 000724 000015  CMPB     INBUF,#15
39 014240 001403             BEQ      1$
40 014242 016777 000014 001356  MOV      TMP1,@SWR
41 014250 005067 000010      1$: CLR      TMP2
42 014254 005067 000006      CLR      SMLN
43 014260 000002             RTI
44 014262 000000      TMP1: 0
45 014264 000000      TMP2: 0
46 014266 000000      SMLN: 0
47
48

```



```

1
2
3
4 014270 016700 001374
5 014274 012701 017412
6 014300 104011
7 014302 062767 000060 000124
8 014310 116721 000120
9 014314 005300
10 014316 001370
11 014320 114124
12 014322 005367 001342
13 014326 001374
14 014330 112724 000040
15 014334 000002
16
17
18
19 014336 005067 000072
20 014342 026767 000066 000066
21 014350 103027
22 014352 012767 000021 000032
23 014360 000407
24 014362 026767 000046 000046
25 014370 103403
26 014372 166767 000040 000034
27 014400 006167 000026
28 014404 006167 000024
29 014410 005327
30 014412 000000
31 014414 001362
32 014416 006067 000012
33 014422 005167 000004
34 014426 000002
35 014430 000000
36 014432 000000
37 014434 000000
38 014436 000000
39
40
41
42 014440 016667 000004 001216
43
44
45
46 014446 010567 001206
47 014452 010467 001200
48 014456 010367 001172
49 014462 010267 001164
50 014466 010167 001156
51 014472 010067 001150
52 014476 000002

```

;INTEGER BINARY TO ASCII CONVERSION COMMON ROUTINE

```

BINASC: MOV     CHRCNT,R0           ;SET UP COUNT FOR DIGITS TO BE CONVERTED
          MOV     @TEMTAB,R1        ;SET UP POINTER FOR TEMPORARY STORAGE
BINASA: EXTRACT
          ADD     @60,DIGIT         ;EXTRACT ONE DIGIT
          MOVB    DIGIT,(R1).       ;CONVERT FROM BCD TO ASCII
          DEC     R0                ;STORE DIGIT
          BNE     BINASA            ;IF ALL DIGITS NOT DONE,
          CONTINUE                  ;CONTINUE
BINASB: MOVB    (R1),(R4).         ;REVERSE ORDER OF DIGITS
          DEC     CHRCNT            ;IF ALL CHARACTERS ARE NOT
          BNE     BINASB            ;IN ORDER, CONTINUE
          MOVB    @40,(R4).        ;INSERT SPACE AFTER LAST DIGIT
          RTI                       ;RETURN TO CALLING ROUTINE

```

;SINGLE PRECISION UNSIGNED DIVIDE LOOP

```

DIVI:   CLR     DIVIDH
DIVIU:  CMP     DIVIDH,DIVIS
          BHS    DIVIB
          MOV     @17.,DIVCNT
          BR     DIVIC
DIVIA:  CMP     DIVIDH,DIVIS
          BLO   DIVIC
          SUB     DIVIS,DIVIDH
DIVIC:  ROL     DIVIDL
          ROL     DIVIDH
          DEC     (PC).
          DIVCNT: 0
          BNE    DIVIA
          ROR     DIVIDH
          COM     DIVIDL
          RTI
DIVIB:  HALT
DIVIDL: 0
DIVIDH: 0
DIVIS:  0

```

;SAVE PC OF TEST THAT FAILED AND R0 R5

```

SV05P: MOV     4(SP),SAVPC

```

;SAVE R0-R5

```

SV05:  MOV     R5,SAVR5
          MOV     R4,SAVR4
          MOV     R3,SAVR3
          MOV     R2,SAVR2
          MOV     R1,SAVR1
          MOV     R0,SAVR0
          RTI

```

```

1
2
3
4 014500 016700 001142      RS05:  MOV    SAVR0,R0
5 014504 016701 001140      MOV    SAVR1,R1
6 014510 016702 001136      MOV    SAVR2,R2
7 014514 016703 001134      MOV    SAVR3,R3
8 014520 016704 001132      MOV    SAVR4,R4
9 014524 016705 001130      MOV    SAVR5,R5
10 014530 000002              RTI

11
12
13
14 014532 017605 000000      TYPER:  MOV    @2(SP),R5      ;GET POINTER TO MESSAGE (ON STACK)
15 014536 062716 000002      ADD    @2,(SP)              ;CORRECT STACK FOR RETURN
16 014542 105777 001054      TYPERA: TSTB   @TPCSR        ;WAIT FOR TELEPRINTER READY
17 014546 100375              BPL    TYPERA
18 014550 122765 000012 177777      CMPB   @12,-1(R5)          ;WAS LAST ONE A L.F. ??
19 014556 001405              BEQ    1$                  ;BR IF YES
20 014560 122765 000015 177777      CMPB   @15,-1(R5)          ;WAS LAST ONE A C.R. ??
21 014566 001401              BEQ    1$                  ;BR IF YES
22 014570 000402              BR     2$                  ;CONTINUE IF NEITHER
23 014572 004767 000044      1$:   JSR    PC,TYFILL      ;GO OUT PUT FILLERS
24 014576 122715 000100      2$:   CMPB   @100,(R5)      ;IF CHARACTER IS NOT TERMINATOR, TYPE IT
25 014602 001001              BNE    TYPER1
26 014604 000002              RTI                          ;CHARACTER IS TERMINATOR, EXIT
27 014606 122715 000042      TYPER1: CMPB   @42,(R5)      ;IF CHARACTER=42,
28 014612 001406              BEQ    TYPECL              ;TYPE LINE FEED
29 014614 122715 000045      CMPB   @45,(R5)          ;IF CHARACTER=45,
30 014620 001403              BEQ    TYPECL              ;TYPE CARRIAGE RETURN
31 014622 112577 000776      TYPER2: MOVB   (R5)+,@TPDBR  ;GET CHARACTER
32 014626 000745              BR     TYPERA              ;TYPE IT
33 014630 142715 000040      TYPECL: BICB   @40,(R5)      ;CONVERT CODE OF 42 OR 45
34 014634 152715 000010      BISB   @10,(R5)          ;TO 12 OR 15
35 014640 000770              BR     TYPER2              ;TYPE IT
36
37
38
39
40 014642 116767 001060 001060      TYFILL: MOVB   FILL,FILLA      ;GET FILL COUNT
41 014650 116777 001053 000746      1$:   MOVB   FILL+1,@TPDBR    ;OUT PUT ONE FILLER
42 014656 105777 000740      2$:   TSTB   @TPCSR        ;WAIT FOR TTY TO FINISH OUTPUT
43 014662 100375              BPL    2$                  ;BR IF TTY NOT DONE
44 014664 105367 001040      DECB   FILLA              ;COUNT ONE FILLER
45 014670 001367              BNE    1$                  ;BR TIL ALL DONE
46 014672 000207              RTS    PC                  ;RETURN TO CALLER ABOVE
47
48
49
50
51
52
53
54 014674
55 014674 011605              INSTR: MOV    (SP),R5        ;GET POINTER TO ARGUMENTS
56 014676 012567 000020      MOV    (R5)+,MSG          ;GET MESSAGE TO BE TYPED
57 014702 012567 000246      MOV    (R5)+,LOLIM        ;GET LOWER LIMIT

```

```

58 014706 012567 000244      MOV      (R5)+,HILIM      ;GET UPPER LIMIT
59 014712 012567 000242      MOV      (R5)+,STORE     ;GET DATA STORAGE LOCATION
60 014716 010516              MOV      R5,(SP)        ;RESTORE STACK
61 014720 104004              INSTR1: TYPE           ;TYPE MESSAGE
62 014722 000000              MSG:      C
63 014724 012704 015162      MOV      @INBUF,R4      ;SET UP CHARACTER INPUT BUFFER
64 014730 012703 000007      MOV      @7,R3          ;SET UP INPUT COUNT
65 014734 105777 000656      INSTRB: TSTB @TKCSR     ;WAIT FOR CHARACTER
66 014740 100375              BPL      INSTRB
67 014742 005067 165300      INSTRBB: CLR SINTFL
68 014746 017767 000646      MOV      @TKDBR,TMP1    177306
69 014754 142767 000200      BICB    @200,TMP1      177300
70 014762 116714 177274      MOV      TMP1,(R4)
71 014766 121427 000007      MOV      (R4),@7
72 014772 001420              BEQ      INSTR
73 014774 121427 000015      CMPB    (R4),@15      ;IS CHARACTER TERMINATOR
74 015000 001420              BEQ      INSTR2       ;IF IT IS, CONVERT INPUT STRING
75 015002 121427 000025      CMPB    (R4),@25
76 015006 001003              BNE     1$
77 015010 005067 177246      CLR      TMP1
78 015014 000741              BR       INSTR1
79 015016 112477 000602      1$:     MOV      (R4)+,@TPDBR
80 015022 105777 000574      INSTRC: TSTB @TPCSR     ;WAIT TO FINISH TYPING
81 015026 100375              BPL      INSTRC
82 015030 005303              DEC      R3            ;UPDATE RECEIVED COUNT
83 015032 001340              BNE     INSTRB        ;AND CONTINUE
84 015034 104004              INSTR:  TYPE           ;TYPE "?" AND RE-REQUEST INPUT
85 015036 017001              MQM
86 015040 000727              BR       INSTR1
87
88                          ;CONVERT ASCII STRING TO OCTAL
89
90 015042 104004              INSTR2: TYPE
91 015044 017005              MCRLF
92 015046 012704 015162      MOV      @INBUF,R4      ;GET POINTER TO ASCII STRING
93 015052 005003              CLR      R3
94 015054 122714 000015      CMPB    @15,(R4)       ;IS TERMINATOR FIRST
95                          ;CHARACTER IN STRING
96 015060 001431              BEQ      CHCK
97 015062 121427 000060      INSTRD: CMPB (R4),@60   ;IS CHARACTER OCTAL DIGIT
98 015066 002762              BLT     INSTR          ;IF 67>=CHAR>=60
99 015070 121427 000067      CMPB    (R4),@67      ;CHARACTER IS OCTAL DIGIT
100 015074 003357              BGT     INSTR
101 015076 142714 000060      BICB    @60,(R4)      ;STRIP ASCII
102 015102 152403              BISB    (R4)+,R3      ;GENERATE OCTAL NUMBER
103 015104 121427 000015      CMPB    (R4),@15     ;IF END OF STRING, CHECK LIMITS
104 015110 001404              BEQ     INSTR3
105 015112 006303              ASL     R3            ;MULTIPLY DIGIT BY 10 (OCTAL)
106 015114 006303              ASL     R3
107 015116 006303              ASL     R3
108 015120 000760              BR      INSTRD       ;GET NEXT DIGIT
109
110                          ;TEST NUMBER TO SEE IF IT IS WITHIN LIMITS
111
112 015122 020367 000030      INSTR3: CMP R3,HILIM    ;TEST HI LIMIT
113 015126 101342              BHI     INSTR         ;IF R3>HILIM, ERROR
114 015130 020367 000020      CMP     R3,LOLIM     ;TEST LOW LIMIT

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115 015134 103737          BLO  INSTER          ;IF R3<LOLIM, ERROR
116 015136 010377 000016  MOV  R3,@STORE    ;STORE NUMBER
117 015142 000002          RTI                    ;EXIT
118 015144 005767 177114  CMCK: TST  TMP2
119 015150 001731          BEQ  INSTER
120 015152 000002          RTI
121 015154 000000          LOLIM: 0
122 015156 000000          HILIM: 0
123 015160 000000          STORE: 0
124 015162 000000          INBUF: 0
125          015204          . = . + 20
126
127          ;ENTER HERE ON POWER FAILURE
128
129 015204 010046          PFAIL: MOV  R0,-(SP)          ;SAVE R0-R5 ON PROCESSOR STACK
130 015206 010146          MOV  R1,-(SP)
131 015210 010246          MOV  R2,-(SP)
132 015212 010346          MOV  R3,-(SP)
133 015214 010446          MOV  R4,-(SP)
134 015216 010546          MOV  R5,-(SP)
135 015220 016746 162600  MOV  24,-(SP)
136 015224 010667 000432  MOV  SP,SAVSP          ;SAVE STACK POINTER
137 015230 012767 015242 162566  MOV  @RESTART,24      ;SET UP FOR POWER UP TRAP
138 015236 000000          HALT
139 015240 000776          BR   .-2             ;HALT ON POWER DOWN NORMAL
140
141          ;PROCESSOR WILL TRAP HERE WHEN POWER IS RESTORED
142
143 015242 016706 000414  RESTAR: MOV  SAVSP,SP          ;RESTORE STACK POINTER
144 015246 012605          MOV  (SP)+,R5          ;RESTORE R0-R5
145 015250 012604          MOV  (SP)+,R4
146 015252 012603          MOV  (SP)+,R3
147 015254 012602          MOV  (SP)+,R2
148 015256 012601          MOV  (SP)+,R1
149 015260 012600          MOV  (SP)+,R0
150 015262 012767 015204 162534  MOV  @PFAIL,24        ;SET UP FOR POWER FAILURE
151 015270 005726          POP1SP
152 015272 104004          TYPE
153 015274 017160          MPFAIL
154 015276 005767 164514  TST  TIPFLG
155 015302 001002          BNE  RESTA1
156 015304 000167 164012  JMP  STARTO
157 015310 104004          RESTA1: TYPE
158 015312 017200          MPF1
159 015314 012746 000340  MOV  @340,-(SP)
160 015320 005746          PUSH1SP
161 015322 000167 175740  JMP  TSTENT
162
163
164          ;THE FOLLOWING AUTO VECTORS USING THE FIRST BASE ADDRESS
165 015326 016746 162466  XOR:  MOV  20,-(SP)          ;SAVE 20
166 015332 016746 162464  MOV  22,-(SP)          ;SAVE 22
167 015336 012767 015530 162454  MOV  @2#,20           ;IOT INTR VECTOR
168 015344 012767 000340 162450  MOV  @340,22          ;IOT INTR LVL
169 015352 012767 000300 175372  MOV  @300,DATA1
170 015360 012767 000302 175366  MOV  @302,DATA2
171 015366 016777 175362 175356 1$:  MOV  DATA2,@DATA1

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172 015374 012777 000004 175352      MOV    #IOT,@DATA2      :IOT TRAP
173 015402 062767 000004 175342      ADD    #4,DATA1
174 015410 062767 000004 175336      ADD    #4,DATA2
175 015416 026727 175330 001000      CMP    DATA1,#1000
176 015424 001360                                BNE    1$
177 015426 012767 000000 000204      MOV    #0,TSTNO        :SET UP DEFAULT
178 015434 012767 017542 000230      MOV    #TSTBO,TSTPNT   ;
179 015442 052767 000340 162326      BIS    #340,PS        :PREVENT INTERRUPTS
180 015450 005077 000136                                CLR    @DHMCSR         ;
181 015454 012777 000100 000130      MOV    #INTENA,@DHMCSR ;SET INTERRUPT ENABLE
182 015462 042767 000340 162306      BIC    #340,PS ;ALLOW INTERRUPTS
183 015470 052777 000200 000114      BIS    #DONE,@DHMCSR   ;SET DONE..AND INTERRUPT
184 015476 000240                                NOP
185 015500 012667 162316                                MOV    (SP)+,22        :YOU DIDN'T INTERRUPT ?
186 015504 012667 162310                                MOV    (SP)+,20        :RESTORE 20 & 22
187 015510 005077 000076                                CLR    @DHMCSR        :STOP ALL INTERRUPT
188 015514 052767 000340 162254      BIS    #340,PS        ;
189 015522 104012      ERROR
190 015524 000000      HALT                    :YOU SHOULD HAVE INTERRUPTED
191 015526 000426                                BR     3$
192 015530 011667 000052 2$:      MOV    (SP),DHMVEC     ;EXTRACT VECTOR +4
193 015534 162767 000002 000044      SUB    #2,DHMVEC      ;CREATE LVL
194 015542 016767 000040 000040      MOV    DHMVEC,DHMLVL ;SAVE
195 015550 162767 000002 000030      SUB    #2,DHMVEC      ;CREATE AND SAVE VEC
196 015556 012767 000340 162212      MOV    #340,PS       ;PREVENT INTERRUPTS
197 015564 005077 000022                                CLR    @DHMCSR        ;
198 015570 022626      POP2SP
199 015572 022626      POP2SP
200 015574 012667 162222                                MOV    (SP)+,22        :RESTORE 22
201 015600 012667 162214                                MOV    (SP)+,20        :RESTORE 20
202 015604 000207 3$:      RTS    PC
203

```

```

1
2
3
4 015606 000300      DHMVEC: 300          ;MODEM CONTROL INTERRUPT VECTOR
5 015610 000302      DHMLVL: 302         ;MODEM CONTROL ONTERRUPT PRIORITY
6 015612 170500      DHMCSR: 170500     ;MODEM CONTROL CONTROL STATUS REGISTER
7 015614 170502      DHMLSR: 170502     ;MODEM CONTROL CONTROL STATUS REGISTER
8 015616 177560      TKCSR: 177560
9 015620 177562      TKOBR: 177562
10 015622 177564     TPCSR: 177564
11 015624 177566     TPDBR: 177566
12 015626 177570     SWR: 177570
13 015630 177570     DISPLAY:177570
14
15
16
17 015632 000000     ERRFLG: 0
18 015634 000000     TRACON: 0
19 015636 000000     PASCNT: 0
20 015640 000000     TSTNO: 0
21 015642 000000     RETURN: 0
22 015644 000000     ICOUNT: 0
23 015646 000000     SAVRO: 0
24 015650 000000     SAVR1: 0
25 015652 000000     SAVR2: 0
26 015654 000000     SAVR3: 0
27 015656 000000     SAVR4: 0
28 015660 000000     SAVR5: 0
29 015662 000000     SAVSP: 0
30 015664 000000     SAVPC: 0
31 015666 000000     WRDCNT: 0
32 015670 000000     CHRCNT: 0
33 015672 017542     STPNT: TSTTBO
34 015674 000000     TSTMAX: 0
35 015676 000000     LINFLG: 0
36 015700 000000     LINE: 0
37 015702 000000     LINORG: 0
38 015704 000000     LINANS: 0
39 015706 000000     ANSFLG: 0
40 015710 000000     ORGFLG: 0
41 015712 000000     TIME1: 0
42 015714 000000     TIME2: 0
43 015716 000000     TIFLG: 0
44 015720 177777     LINSEL: 177777
45 015722 000000     SELMSK: 0
46 015724 000000     SLMSK: 0
47 015726 000002     FILL: 2          ;FILL CHAR/COUNT
48 015730 000000     FILLA: 0        ;TEMP STORAGE FOR FILL COUNT
49 015732 000000     RNGRET: 0
50
51
52 015734 124 111 115 .NLIST BEX
53 015774 114 116 040 MNOINT: .ASCII ;TIME OUT WAITING FOR INTERRUPT";
54 016013 123 124 101 .ASCII ;LN CSR LSR;
55 016044 114 111 116 MSTATE: .ASCII ;STATUS ERROR"EXP REC;
56 016101 114 111 116 MLINER: .ASCII ;LINE ERROR"EXP REC LINE;
57 016132 124 122 101 MLINE1: .ASCII ;LINE ERROR"EXP REC LINE;
MTRANE: .ASCII ;TRANSITION ERROR"EXP REC LINE;

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58	016171	045	042	045	DIALM: .ASCII	; "DIAL ANSWERING DATA SET" ;
59	016227	045	042	045	MT103T: .ASCII	; "103A MODEM CONNECT-DISCONNECT TEST" ;
60	016300	045	042	045	MT202T: .ASCII	; "202C MODEM CONNECT-DISCONNECT TEST" ;
61	016351	045	042	045	MSELOR: .ASCII	; "ORIGINATE LINE-0;" ;
62	016375	045	042	101	MSELAN: .ASCII	; "ANSWER LINE 0;" ;
63	016414	045	042	061	MT103A: .ASCII	; "103A TEST COMPLETE" ;
64	016443	045	042	062	MT202A: .ASCII	; "202C TEST COMPLETE" ;
65	016472	045	042	123	MDISC: .ASCII	; "STRIKE ANY TTY KEY TO TEST DISCONNECT0;" ;
66	016542	045	042	045	M16: .ASCII	; "16 LINE SCANNER TEST" ;
67	016575	045	042	045	MTITLE: .ASCII	; "CZDHK-F - --MODEM CONTROL DIAGNOSTIC - -" ;
68	016661	045	042	126	MVECTO: .ASCII	; "VECTOR ADDRESS-0;" ;
69	016703	045	042	103	MREGAD: .ASCII	; "CONTROL REGISTER ADDRESS-0;" ;
70	016737	045	042	114	MLINSL: .ASCII	; "LINE SELECT PARAMETER -0;" ;
71	016771	045	042	124	MTEST: .ASCII	; "TEST 0;" ;
72	017001	040	040	077	MQM: .ASCII	; "?0;" ;
73	017005	045	042	100	MCRLF: .ASCII	; "0;" ;
74	017010	045	042	123	MLINE: .ASCII	; "SINGLE LINE CABLE TEST" ;
75	017043	045	042	114	MLINEI: .ASCII	; "LINE NUMBER-0;" ;
76	017062	106	101	124	MFATAL: .ASCII	; "FATAL ERROR" CSTAT LSTAT0;" ;
77	017114	045	042	124	MTRNDE: .ASCII	; "TRANSITION DETECTED" CSTAT LSTAT0;" ;
78	017160	045	042	120	MPFAIL: .ASCII	; "POWER FAILURE0;" ;
79	017200	055	103	125	MPF1: .ASCII	; "-CURRENT TEST WILL RESTART" ;
80	017235	136	103	100	MCONTC: .ASCII	; "+C0;" ;
81	017240	136	126	100	MCONTV: .ASCII	; "+V0;" ;
82	017243	136	114	100	MCONTL: .ASCII	; "+L0;" ;
83	017246	045	042	123	\$SWREQ: .ASCII	; "SWR= 0;" ;
84	017257	040	040	040	\$NEWIS: .ASCII	; "NEW= 0;" ;
85	017271	045	042	105	MEPASS: .ASCII	; "END PASS 0;" ;
86	017307	045	042		MBCD: .ASCII	; "0;" ;
87		017411			.+.100	
88					.EVEN	
89	017412	000000			TEMTAB: 0	
90		017424			.+.10	
91						
92	017424	000000			0	
93						
94					;EMT DISPATCH TABLE	
95						
96	017426	013530			EMTTAB: ERRCS	
97	017430	013546			ERRLS	
98	017432	013110			LOOP	
99	017434	013342			FREEZE	
100	017436	014532			TYPFR	
101	017440	014440			SV05P	
102	017442	014076			OCTASN	
103	017444	014500			RS05	
104	017446	014270			BINASC	
105	017450	014336			DIVI	
106	017452	013424			ERR	
107	017454	014674			INSTR	
108	017456	013442			ERRT	
109	017460	013464			ERRS	
110	017462	013506			ERRN	
111	017464	012026			GETLIN	
112	017466	012062			SETUPS	
113	017470	012316			CKRNG	
114	017472	012414			WAITR	

115 017474 012502  
 116 017476 012452  
 117 017500 014164  
 118 017502 013076  
 119 017504 002020  
 120 017506 013724  
 121 017510 000000  
 122 017512 0175#2  
 123 017514 017724  
 124 017516 017766  
 125 017520 017774  
 126 017522 000000  
 127 017524 000000  
 128 017526 000000  
 129 017530 000000  
 130 017532 000033  
 131 017534 000007  
 132 017536 000001  
 133 017540 000000  
 134 017542 002250  
 135 017544 000001  
 136  
 137  
 138  
 139  
 140  
 141  
 142 017546  
 000001  
 000001  
 000033  
 143  
 144  
 145  
 017546  
 004000  
 017546 002276  
 017550 004000  
 000002  
 017552  
 004000  
 017552 002340  
 017554 004000  
 000003  
 017556  
 004000  
 017556 002402  
 017560 004000  
 000004  
 017562  
 004000  
 017562 002444  
 017564 004000  
 000005  
 017566  
 004000  
 017566 002506  
 017570 004000

CKTRN  
 WAITRR  
 CNTLU  
 CKINT  
 KBDINT  
 ERRQ  
 EMTLIM: 0  
 TSTLST: TSTTB0  
 TSTTB1  
 TSTTB2  
 TSTTB3  
 0  
 0  
 0  
 0  
 GRO: NO-1  
 N1-100-1  
 N2-200-1  
 N3-300 1  
 TSTTB0: T0  
 1  
 .MACRO COMMENT  
 .NLIST  
 MO=1  
 XM=MO  
 .LIST  
 .ENDM  
 COMMENT  
 MO=1  
 XM=MO  
 .REPT NO-1  
 TM \MO,0  
 .ENDR  
 TM \MO,0  
 TIMES=4000  
 T1  
 TIMES  
 MO=MO+1  
 TM \MO,0  
 TIMES=4000  
 T2  
 TIMES  
 MO=MO+1  
 TM \MO,0  
 TIMES=4000  
 T3  
 TIMES  
 MO=MO+1  
 TM \MO,0  
 TIMES=4000  
 T4  
 TIMES  
 MO=MO+1  
 TM \MO,0  
 TIMES=4000  
 T5  
 TIMES

:CALL BY EMT CNTLUU  
 :CALL BY EMT CKINTT  
 :CALLBY EMT KBDIN  
 :CALLED BY EMT ERRINT



017572	000006	MO=MO+1	
		TM	\MO,0
	004000	TIMES=4000	
017572	002550	T6	
017574	004000	TIMES	
	000007	MO=MO+1	
017576		TM	\MO,0
	004000	TIMES=4000	
017576	002624	T7	
017600	004000	TIMES	
	000010	MO=MO+1	
017602		TM	\MO,0
	004000	TIMES=4000	
017602	002700	T10	
017604	004000	TIMES	
	000011	MO=MO+1	
017606		TM	\MO,0
	004000	TIMES=4000	
017606	002770	T11	
017610	004000	TIMES	
	000012	MO=MO+1	
017612		TM	\MO,0
	004000	TIMES=4000	
017612	003060	T12	
017614	004000	TIMES	
	000013	MO=MO+1	
017616		TM	\MO,0
	004000	TIMES=4000	
017616	003150	T13	
017620	004000	TIMES	
	000014	MO=MO+1	
017622		TM	\MO,0
	004000	TIMES=4000	
017622	003240	T14	
017624	004000	TIMES	
	000015	MO=MO+1	
017626		TM	\MO,0
	004000	TIMES=4000	
017626	003330	T15	
017630	004000	TIMES	
	000016	MO=MO+1	
017632		TM	\MO,0
	004000	TIMES=4000	
017632	003416	T16	
017634	004000	TIMES	
	000017	MO=MO+1	
017636		TM	\MO,0
	004000	TIMES=4000	
017636	003504	T17	
017640	004000	TIMES	
	000020	MO=MO+1	
017642		TM	\MO,0
	004000	TIMES=4000	
017642	003572	T20	
017644	004000	TIMES	
	000021	MO=MO+1	
017646		TM	\MO,0

	004000	TIMES=4000	
017646	003660	T21	
017650	004000	TIMES	
	000022	MO=MO+1	
017652		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
017652	003754	T22	
017654	000400	TIMES	
	000023	MO=MO+1	
017656		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
017656	004070	T23	
017660	000400	TIMES	
	000024	MO=MO+1	
017662		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
017662	004260	T24	
017664	000400	TIMES	
	000025	MO=MO+1	
017666		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
	000200	TIMES=200	
017666	004432	T25	
017670	000200	TIMES	
	000026	MO=MO+1	
017672		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
	000200	TIMES=200	
017672	004574	T26	
017674	000200	TIMES	
	000027	MO=MO+1	
017676		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
	000200	TIMES=200	
017676	005034	T27	
017700	000200	TIMES	
	000030	MO=MO+1	
017702		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
	000200	TIMES=200	
017702	005270	T30	
017704	000200	TIMES	
	000031	MO=MO+1	
017706		TM	\MO,0
	004000	TIMES=4000	
	000400	TIMES=400	
	000200	TIMES=200	
017706	005524	T31	
017710	000200	TIMES	
	000032	MO=MO+1	

```

017712      004000      TM      \M0.0
             000400      TIMES=4000
             000200      TIMES=400
017712      005760      T32
017714      000200      TIMES
             000033      MO=MO+1
017716      004000      TM      \M0.0
             000400      TIMES=4000
             000200      TIMES=400
             000200      TIMES=200
017716      006144      T33
017720      000200      TIMES
             000034      MO=MO+1
146 017722      000000      0
147 017724      006454      TSTTB1: T100
148 017726      000001      1
149             .MACRO      COMMENT
150             .NLIST
151             M1=101
152             XM=M1
153             .LIST
154             .ENDM
155 017730      COMMENT
             000101      M1=101
             000101      XM=M1
156             000007      .REPT      N1-101
157             TM      \M1.1
158             .ENDR
017730      TM      \M1.1
             004000      TIMES=4000
             000400      TIMES=400
             000200      TIMES=200
017730      006512      T101
017732      000200      TIMES
             000102      M1=M1+1
017734      TM      \M1.1
             004000      TIMES=4000
             000400      TIMES=400
             000200      TIMES=200
017734      006672      T102
017736      000200      TIMES
             000103      M1=M1+1
017740      TM      \M1.1
             004000      TIMES=4000
             000400      TIMES=400
             000200      TIMES=200
017740      007046      T103
017742      000200      TIMES
             000104      M1=M1+1
017744      TM      \M1.1
             004000      TIMES=4000
             000400      TIMES=400
             000200      TIMES=200
017744      007222      T104
017746      000200      TIMES
             000105      M1=M1+1

```

```
017750      004000      TM      \M1,1
              000400      TIMES=4000
              000200      TIMES=400
017750      007376      T105
017752      000200      TIMES
              000106      M1=M1+1
017754      004000      TM      \M1,1
              000400      TIMES=4000
              000200      TIMES=400
              000200      TIMES=200
017754      007554      T106
017756      000200      TIMES
              000107      M1=M1+1
017760      004000      TM      \M1,1
              000400      TIMES=4000
              000200      TIMES=400
              000200      TIMES=200
017760      007732      T107
017762      000200      TIMES
              000110      M1=M1+1
159 017764      000000      0
160 017766      010110      TSTTB2: T200
161 017770      000001      1
162 017772      000000      0
163 017774      010464      TSTTB3: T300
164 017776      000001      1
165      000001      .END
```





T202J5	011356	T26	004574	T6	002550	WAITS	=	104024	XSCRX	=	000001
T202J1	011454	T27	005034	T7	002624	WRDCNT		015666	X1A		001652
T202J2	011460	T3	002402	UP	*	X	=	000000	X1B		001716
T202J3	011464	T30	005270	VECSTA		XCO	=	000004	Y	=	000000
T202J4	011470	T300	010464	VECSTR		XCS	=	000002	\$NEWIS		017257
T21	003660	T31	005524	VECST1		XFLAG		001312	\$SWREQ		017246
T22	003754	T32	005760	WAITR		XM	=	000101	.1		013646
T23	004070	T33	006144	WAITRN	=	XN	=	000300	.2		013660
T24	004260	T4	002444	WAITRR		XOR		015326	.3		013662
T25	004432	T5	002506	WAITR1		XORSVC		001314	.4		013706

. ABS. 020000 000  
 000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 19456 WORDS ( 76 PAGES)  
 DYNAMIC MEMORY AVAILABLE FOR 71 PAGES  
 CZDHKF.BIN,CZDHKF.SEG=DHMACA.MAC,CZDHKF.P11