

1 .REM !  
2  
3  
4  
5  
6  
7  
8  
9

10 IDENTIFICATION  
11  
12  
13

14  
15  
16  
17  
18 PRODUCT CODE: AC-8198C-MC  
19  
20 PRODUCT NAME: CVKADC0 LSI-11 TRAPS TEST  
21  
22 DATE CREATED: AUG, 1978  
23  
24 MAINTAINER: DIAGNOSTIC GROUP  
25  
26 AUTHGR: AL LOSCHAK  
27 REVISED BY: M. MCNALLY JUNE 1976  
28 J. RICH JULY 1978  
29

30  
31 COPYRIGHT (C) 1975,1977,1978 DIGITAL EQUIPMENT CORP., MAYNARD, MASS.  
32

33  
34 THIS SOFTWARE IS FURNISHED TO PURCHASER UNDER A LICENSE FOR USE  
35 ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION  
36 OF DEC'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT  
37 AS MAY OTHERWISE BE PROVIDED IN WRITING BY DEC.  
38

39 THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT  
40 NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL  
41 EQUIPMENT CORPORATION.  
42

43 DEC ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF  
44 ITS SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DEC.  
45  
46

47  
48  
49  
50  
51  
52  
53  
54  
55  
56  
57  
58  
59  
60  
61  
62  
63  
64  
65  
66  
67  
68  
69

- 1. ABSTRACT  
THIS IS A TEST OF ALL OPERATIONS AND INSTRUCTIONS THAT CAUSE TRAPS, ODDITIES OF REGISTER 6, INTERRUPTS, THE RESET AND WAIT INSTRUCTIONS.
- 2. REQUIREMENTS
  - 2.1 EQUIPMENT  
PDT-11 OR LSI-11 STANDARD COMPUTER WITH AN SLU UNIT AND 4K OF MEMORY. THE CLOCK MUST BE DISABLED.
  - 2.2 STORAGE  
2.2.1 PROGRAM STORAGE - THE ROUTINE USES 4K MEMORY
- 3. LOADING PROCEDURE
  - 3.1 METHOD  
PROCEDURE FOR NORMAL ABSOLUTE TAPES SHOULD BE FOLLOWED.

70 4. SETUP PROCEDURE  
71  
72 THE PROGRAM STARTS AT 200.  
73 IF IT IS DESIRED TO RESET THE PASS COUNT BACK TO ZERO  
74 START AT LOCATION 210.  
75 THE CLOCK MUST BE DISABLED FOR THIS PROGRAM TO RUN CORRECTLY.  
76  
77 4.1 SWITCH REGISTER BITS  
78  
79 THE OPERATOR HAS THE FOLLOWING OPTIONS BY SETTING  
80 THE SOFTWARE SWITCH REGISTER (LOCATION 422)  
81  
82 BIT 6=1 (100 OCTAL) TO SUPPRESS TESTING EIS AND FIS OPCODES  
83 (70000-75037) FOR RESERVED INSTRUCTION TRAPPING  
84 IN THE LAST TEST OF THIS DIAGNOSTIC.  
85  
86 BIT 5=1 (40 OCTAL) IF WE WANT TO SUPPRESS 'END OF PASS' TYPEOUT  
87  
88 BIT 4=1 (20 OCTAL) TO SUPPRESS TESTING OPCODES 75400-76777 FOR  
89 RESERVED INSTRUCTIONS TRAPS IN THE LAST TEST OF THIS DIAGNOSTIC.  
90  
91 BIT 3=1 (10 OCTAL) TO SUPPRESS TESTING OPCODES 170000-177777 FOR  
92 RESERVED INSTRUCTIONS TRAPS IN THE LAST TEST OF  
93 THIS DIAGNOSTIC.  
94  
95 BIT 2=1 (4 OCTAL) TO SUPPRESS TESTING OPCODES 76030-76057 (DIS RESERVED OPCODE SPACE)  
96 NOR EIS OPCODES FOR RESERVED INSTRUCTION TRAPS IN THE LAST  
97 TEST OF THIS DIAGNOSTIC  
98  
99 BIT 1=1 (2 OCTAL) IF THERE IS NO I/O DEVICE IN THE ADDRESS SPACE  
100 160000-167777. IF THIS BIT IS SET, THIS ADDRESS AREA WILL BE  
101 CHECKED BY TEST 70, EXPECTING IT TO ALL TRAP, OR ALL NOT.  
102 NOTE: THIS BIT WOULD NORMALLY BE SET WHEN RUNNING ON TIM, SINCE  
103 IT CANNOT HAVE PERIPHERALS IN THIS ADDRESS AREA.  
104  
105 4.2 APT -- CPU OPTIONS  
106  
107 WHEN RUNNING UNDER APT, THE DON'T SIZE BIT SHOULD BE SET  
108 (\$ENVM=200, OR 240 TO INHIBIT ALL TYPEOUTS) AND THE CPU OPTIONS  
109 WORD (\$CPUOP) SHOULD BE SETUP TO FLAG THE PRESENCE OR ABSENCE  
110 OF EIS/FIS OR DIS. TO INDICATE EIS/FIS PRESENT, SET CPU OPTIONS  
111 BITS 6,7 (OCTAL 300). TO INDICATE DIS PRESENT, SET CPU OPTIONS  
112 BIT 5 (OCTAL 40). THESE BITS INHIBIT TRAP TESTING OF THEIR  
113 RESPECTIVE OPCODES. THE SAME THING CAN BE ACCOMPLISHED USING  
114 THE SWITCH REGISTER AS DESCRIBED IN SECTION 4.1; HOWEVER, THE  
115 DON'T SIZE BIT SHOULD STILL BE SET.  
116  
117 4.3 PROGRAM AND/OR OPERATOR ACTION  
118  
119 LOAD PROGRAM INTO MEMORY. (BOTTOM 4K)  
120 SET THE DESIRED SWITCH REGISTER BITS, IF ANY.  
121 LOAD ADDRESS 200.  
122 START.  
123 THE PROGRAM WILL SIZE FOR EIS/FIS AND FOR DIS. TO INHIBIT  
124 SIZING, SET THE DON'T SIZE BIT (BIT 7 OF \$ENVM (ODD BYTE),  
125 I.E. \$ENV,\$ENVM 100000)

126 THE PROGRAM WILL PRINT END OF PASS AFTER THE 1ST ITERATION AND  
127 THEN PRINT IT EVERY 15 TIMES; APROXIMATELY 2 MINUTES.  
128

129  
130 5. OPERATION PROCEDURE  
131

132  
133 5.2 SUBROUTINE ABSTRACTS  
134

135 5.2.1 TRAPCATCHER  
136

137 -----  
138 THIS IS A SERIES OF INSTRUCTIONS DESIGNED TO DETECT AND  
139 ISOLATE UNEXPECTED TRAPS AND INTERRUPTS, THAT OCCUR IN THE  
140 TRAP AND INTERRUPT VECTOR AREA OF MEMORY.  
141

142 THE PRINCIPLE OF THIS ROUTINE IS: THE VECTOR ENTRANCE  
143 ADDRESS POINTS TO THE NEXT SEQUENTIAL WORD WHICH WILL CON-  
144 TAIN A HALT (00000) (THIS LOCATION IS ALSO THE STATUS  
145 WORD FOR THAT VECTOR ENTRANCE. BUT THIS WILL HAVE NO EFFECT  
146 ON IT.  
147

148 IF A HALT OCCURS IN THE TRAP OR INTERRUPT VECTOR AREA,  
149 REGISTER SIX SHOULD BE EXAMINED TO DETERMINE ITS CONTENTS,  
150 THEN USE REGISTER SIX CONTENTS AS AN ADDRESS TO DETERMINE  
151 THE LOCATION THE PROGRAM WAS AT, WHEN THE INTERRUPT OR  
152 TRAP OCCURRED. (MEMORY AS SPECIFIED BY R6 CONTAINS THE  
153 PC OF THE INSTRUCTION FOLLOWING THE INSTRUCTION WHERE THE  
154 TRAP OCCURRED.

155 ALSO THE CONTENTS OF '\$TESTN' CONTAIN THE TEST NUMBER  
156 THAT IT WAS DOING BEFORE IT TRAPPED.  
157

158 5.3 PROGRAM AND/OR OPERATOR ACTION  
159

160 5.3.1 LOADING AND STARTING AT 200 STARTS THE TEST. IF  
161 AN ERROR IS DETECTED, THERE WILL BE A HALT.

162  
163  
164  
165  
166  
167  
168  
169  
170  
171  
172  
173  
174  
175  
176  
177  
178  
179  
180  
181  
182  
183  
184  
185  
186  
187  
188  
189  
190  
191  
192  
193  
194  
195

6. ERRORS

6.1 ALL ERRORS WILL CAUSE A HALT.  
THE PC+2 OF THE HALT INSTRUCTION IS PRINTED  
ON THE CONSOLE DEVICE BY THE LSI-11'S U-ODT.

6.1.1 THE PROGRAM CHECKS TO SEE THAT THE P.C. DOESN'T JUMP  
WITHIN THE TESTS, BY A SEQUENCE COUNT CALLED '\$STSN'  
THIS TEST IS A SEQUENTIAL INCREMENT AND COMPARE COUNT.

EX: CODE

```
INC    @#$STSN      ;UPDATE TEST NUMBER
CMP    #N,@#$STSN  ;SEQUENCE ERROR?
BNE    SOME LOCATION ;BRANCH TO ERROR HALT ON SEQ ERROR
IMPORTANT
```

\*\*\*\*\*

IF AN ERROR IS DETECTED ;IT COULD BE BECAUSE OF TWO REASONS.

- A) WRONG TEST NUMBER
- B) ERROR IN THE PRESENT TEST.

```
////////////////////////////////////
THE TEST SEQUENCE LOCATION 'TESTN' SHOULD BE CHECKED FIRST
TO SEE IF IT MATCHES THE PRESENT TEST.
IF IT DOESN'T MATCH ; THEN THE CONTENTS OF THIS LOCATION
TELL YOU WHICH TEST IT WAS DOING BEFORE IT HALTED.
////////////////////////////////////
```

6.2 ERROR RECOVERY  
ON TRAP ERRORS - RESTART AT STARTING ADDRESS

196  
197  
198 7. RESTRICTIONS  
199 7.1 STARTING RESTRICTION  
200  
201 NONE  
202  
203 7.2 OPERATIONAL RESTRICTION  
204  
205 NONE  
206  
207 8. MISCELLANEOUS  
208 THERE IS A TEST THAT WILL CHECK THAT ODD ADDRESSING  
209 WILL IGNORE BIT '0'  
210  
211 8.1 EXECUTION TIME  
212  
213 FOR ONE PASS APPROXIMATELY 8 SECONDS; THEN IT TYPES  
214 'END OF PASS' APPROXIMATELY EVERY 2 MINUTES.  
215  
216 9. PROGRAM DESCRIPTION  
217  
218 THIS PROGRAM CHECKS THAT ON ALL TRAP OPERATIONS REGISTER  
219 6 IS DECREMENTED THE CORRECT AMOUNT, THAT THE CORRECT  
220 PC IS SAVED ON THE STACK, THAT THE OLD CONDITION CODES AND  
221 PRIORITY ARE PLACED ON THE STACK AND THAT THE NEW STATUS AND  
222 CONDITION CODES ARE CORRECT. BOTH THE 'TRAP' AND 'EMT'  
223 TRAP INSTRUCTIONS ARE TESTED TO SEE THAT ALL COMBINATIONS WILL  
224 TRAP. CHECKED ALSO ARE THE RTT AND THE RTI INSTRUCTIONS AND THAT ALL  
225 RESTRICTED INSTRUCTIONS WILL TRAP. VERIFICATION OF THE 'BPT' INSTRUCTION (00003)  
226 WHICH IS USED FOR SOFTWARE DEBUG ROUTINES: ODT, DDT, IS DONE.  
227 ALSO, THE TRACE BIT IS CHECKED TO SEE IF IT CAUSES A TRAP.  
228 SPECIAL CHECKS ARE MADE TO SEE IF BUS  
229 ERROR TRAPS OCCUR ON NON-EXISTENT MEMORY.  
230

231  
232  
233

.LIST BIN,LOC

234  
 235  
 236  
 237  
 238  
 239 000007  
 240 000006  
 241 000000  
 242 104400  
 243 104000  
 244 000003  
 245 000004  
 246 000004  
 247 000014  
 248 000030  
 249 000020  
 250 000034  
 251 177564  
 252 177560  
 253 177564  
 254 177566  
 255 000240  
 256 000240  
 257 000007  
 258 000010  
 259 004700  
 260 000100  
 261 000404  
 262 000402  
 263

.NLIST MD,CND,MC  
 .LIST ME

PC=%7  
 SP=%6  
 HLT-HALT  
 TRAP=104400  
 EMT=104000  
 TRT=3  
 ITRAP5=4  
 RTRAP5=4  
 RTRAP4=14  
 RTRAP3=30  
 RTRAP2=20  
 RTRAP1=34  
 TTCSR=177564  
 TRCSR=177560  
 TPS=177564  
 TPB=177566  
 BELL=240  
 NOP=240  
 TRAPA=000007  
 RTRAP=10  
 ILLA=004700  
 ILLB=100  
 \$STNM-\$TESTN  
 \$ERROR-\$FATAL  
 .ENABL ABS

:RESERVED INST AND ILLEGAL ADDRESSES  
 :FOR TRACE TRAP  
 :FOR EMULATOR TRAP  
 :FOR IOT TRAP  
 :FOR TRAP INST



```
264  
265  
266          000400  
267          . =400  
268          .SBTTL ACT11 HOOKS  
269  
270          ;*****  
271          ;HOOKS REQUIRED BY ACT11  
272          $SVPC-.          ;SAVE PC  
273          .-46  
274          $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP  
275          .-52  
276          .WORD 0          ;;2)SET LOC.52 TO ZERO  
277          .-$SVPC          ;; RESTORE PC  
278          .SBTTL APT MAILBOX-ETABLE  
279  
280          ;*****  
281          .EVEN  
282          $MAIL:          ;;APT MAILBOX  
283          $MSGTY: .WORD  AMSGTY  ;;MESSAGE TYPE CODE  
284          $FATAL: .WORD  AFATAL  ;;FATAL ERROR NUMBER  
285          $TESTN: .WORD  ATESTN  ;;TEST NUMBER  
286          $PASS: .WORD  APASS   ;;PASS COUNT  
287          $DEVCT: .WORD  ADEVCT  ;;DEVICE COUNT  
288          $UNIT: .WORD  AUNIT   ;;I/O UNIT NUMBER  
289          $MSGAD: .WORD  AMSGAD  ;;MESSAGE ADDRESS  
290          $MSGLG: .WORD  AMSGLG  ;;MESSAGE LENGTH  
291          $ETABLE:          ;;APT ENVIRONMENT TABLE  
292          $ENV: .BYTE  AENV     ;;ENVIRONMENT BYTE  
293          $ENVM: .BYTE  AENVM   ;;ENVIRONMENT MODE BITS  
294          $SWREG: .WORD  ASWREG  ;;APT SWITCH REGISTER  
295          $USWR: .WORD  AUSWR   ;;USER SWITCHES  
296          $CPUOP: .WORD  ACPUOP  ;;CPU TYPE,OPTIGNS  
297          ;*          BITS 15-11=CPU TYPE  
298          ;*          11/04=01,11/05=02,11/20=03,11/40-04,11/45-05  
299          ;*          11/70=06,PDQ=07,Q=10  
300          ;*          BIT 10=REAL TIME CLOCK  
301          ;*          BIT 9=FLOATING POINT PROCESSOR  
302          ;*          BIT 8=MEMORY MANAGEMENT  
303          $MAMS1: .BYTE  AMAMS1  ;;HIGH ADDRESS,M.S. BYTE  
304          $MTYP1: .BYTE  AMTYP1  ;;MEM. TYPE,BLK#1  
305          ;*          MEM.TYPE BYTE -- (HIGH BYTE)  
306          ;*          900 NSEC CORE=001  
307          ;*          300 NSEC BIPOLAR=002  
308          ;*          500 NSEC MOS=003  
309          $MADR1: .WORD  AMADR1  ;;HIGH ADDRESS,BLK#1  
310          ;*          MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF 'TYPE' ABOVE  
311          $MAMS2: .BYTE  AMAMS2  ;;HIGH ADDRESS,M.S. BYTE  
312          $MTYP2: .BYTE  AMTYP2  ;;MEM.TYPE,BLK#2  
313          $MADR2: .WORD  AMADR2  ;;MEM.LAST ADDRESS,BLK#2  
314          $MAMS3: .BYTE  AMAMS3  ;;HIGH ADDRESS,M.S.BYTE  
315          $MTYP3: .BYTE  AMTYP3  ;;MEM.TYPE,BLK#3  
316          $MADR3: .WORD  AMADR3  ;;MEM.LAST ADDRESS,BLK#3  
317          $MAMS4: .BYTE  AMAMS4  ;;HIGH ADDRESS,M.S.BYTE  
318          $MTYP4: .BYTE  AMTYP4  ;;MEM.TYPE,BLK#4  
319          $MADR4: .WORD  AMADR4  ;;MEM.LAST ADDRESS,BLK#4  
          $ETEND:
```

```
320 .MEXIT
321 .SBITL APT PARAMETER ELOCK
322
323 :*****
324 :SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
325 :*****
326 000450 .SX=. ;;SAVE CURRENT LOCATION
327 000024 =24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
328 000024 200 ;;FOR APT START UP
329 000044 -.44 ;;POINT TO APT INDIRECT ADDRESS PNTR.
330 000044 $APTHDR ;;POINT TO APT HEADER BLOCK
331 000450 --.SX ;;RESET LOCATION COUNTER
332 :*****
333 :SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
334 :INTERFACE SPEC.
335
336 000450 $APTHD:
337 000450 000000 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
338 000452 000400 $MBADR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
339 000454 000010 $STIM: .WORD 10 ;;RUN TIM OF LONGEST TEST
340 000456 000015 $PASTM: .WORD 15 ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
341 000460 000000 $UNITM: .WORD 0 ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
342 000462 000024 .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
```

```

343
344
345          000200          . =200
346 000200 000167 000276  JMP      START
347          000210          . =210
348 000210 005037 000406  CLR      @#$PASS      ;CLEAR THE PASS COUNT
349 000214 000167 000262  JMP      START
350          000500          . =500
351 000500 000000          BUFF: 00000
352 000502 012767 013714 177314 START: MOV     #PWRDWN,24      ;SET UP THE POWER DOWN VECTOR
353 000510 012767 000340 177310 MOV     #340,26        ;SET UP POWER DOWN PRIORITY
354 000516 012706 000500          MOV     #BUFF,SP      ;SET STACK POINTER
355 000522 105737 177565          TSTB    @#TPS+1      ;TEST ODD BYTE OF CONSOLE IN CASE ON A
356          ;PDT-11. THIS WILL RESWAP COMM AND CONSOLE
357          ;ADDRESSES BACK TO NORMAL IN CASE ON APT.
358 000526 004567 013120          JSR     R5,TYPE      ;TYPE THE TITLE
359 000532 014024          TITLE
360 000534 105767 177661          TSTB    $ENVM        ;DON'T SIZE BIT SET?
361 000540 100402          BMI     BEGIN        ;BRANCH IF YES
362 000542 004767 012662          JSR     PC,SIZE      ;SIZE IF ALLOWED
363
364
365 000546 012737 177777 013426 BEGIN: MOV     #-1,@#PASSPT
366 000554 012702 000400          RESTRT: MOV     # $MSGTY,%2
367 000560 005067 177614          CLR     $MSGTY
368 000564 005067 177614          CLR     $STSTM
369 000570 005067 177606          CLR     $ERROR
370 000574 000167 000026          JMP     TST1
371 000600 000000          K1:    0
372 000602 000000          K2:    0
373 000604 000000          K3:    0
374 000606 000000          K4:    0
375 000610 000000          K5:    0
376 000612 000000          K6:    0
377 000614 052525          K7:    052525
378 000616 052400          K10:   052400
379 000620 000000          K11:   0
380 000622 000000          K12:   0
381 000624 000000          HERE:  0

```

```
382  
383  
384  
385  
386 000626 005237 000404  
387 000632 022737 000001 000404  
388 000640 001124  
389 000642 005006  
390 000644 112667 177754  
391 000650 020627 000002  
392 000654 001405  
393 000656 012737 000001 000402  
394 000664 005212  
395 000666 000000  
396  
397  
398  
399 000670 012706 001000  
400 000674 114667 177724  
401 000700 020627 000776  
402 000704 001405  
403 000706 012737 000002 000402  
404 000714 005212  
405 000716 000000  
406  
407  
408  
409 000720 005006  
410 000722 112626  
411 000724 020627 000004  
412 000730 001405  
413 000732 012737 000003 000402  
414 000740 005212  
415 000742 000000  
416  
417  
418  
419 000744 005006  
420 000746 005004  
421 000750 122624  
422 000752 020627 000002  
423 000756 001405  
424 000760 012737 000004 000402  
425 000766 005212  
426 000770 000000  
427  
428  
429  
430 000772 005006  
431 000774 005004  
432 000776 122426  
433 001000 020627 000002  
434 001004 001405  
435 001006 012737 000005 000402  
436 001014 005212  
437 001016 000000
```

\*\*\*\*\*  
: TEST 1 AUTO INCREMENT/DECREMENT OF R6 FOR WORD AND BYTES  
\*\*\*\*\*

TST1: INC @%STESTN ;UPDATE TEST NUMBER  
CMP #1,@%STESTN ;SEQUENCE ERROR?  
BNE TST2-12 ;BR TO ERROR HALT ON SEQ ERROR

R6TST: CLR %6  
MOV (6)+,HERE ;SIX SHOULD INCREMENT BY TWO  
CMP %6,#2  
BEQ 1\$

MOV #1,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 1 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;R6 DID NOT AUTO INCREMENT BY TWO  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 764

1\$: MOV #1000,%6  
MOV (6)+,HERE ;SHOULD DECREMENT BY TWO  
CMP %6,#776  
BEQ 2\$

MOV #2,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 2 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;R6 DID NOT AUTO DECREMENT BY 2  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 750

2\$: CLR %6  
MOV (6)+,(6)+ ;DOUBLES AUTO INCREMENT OF R6  
CMP %6,#4  
BEQ 3\$

MOV #3,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 3 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;WRONG AUTO INCREMENT OF R6  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 736

3\$: CLR %6  
CLR %4  
CMP (6)+,(4)+ ;TEST INCREMENT OF R6  
CMP %6,#2  
BEQ 4\$

MOV #4,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 4 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;WRONG INCREMENT OF R6  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 723

4\$: CLR %6  
CLR %4  
CMP (4)+,(6)+ ;TEST INCREMENT OF R6  
CMP %6,#2  
BEQ 5\$

MOV #5,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 5 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;WRONG INCREMENT OF R6

```
438 ; TO SCOPE REPLACE HALT W/ 240
439 ; AND REPLACE NEXT INST W/ 710
440
441 001020 005006 5$: CLR %6
442 001022 005004 CLR %4
443 001024 122624 CMPB (6)+,(4)+ ;TEST INCREMENT OF R4
444 001026 020427 000001 CMP %4,#1
445 001032 001405 BEQ 6$
446 001034 012737 000006 000402 MOV #6,@$FATAL ;MOVE TO MAILBOX # ***** 6 *****
447 001042 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
448 001044 000000 HALT ;WRONG INCREMENT OF R4
449 ; TO SCOPE REPLACE HALT W/ 240
450 ; AND REPLACE NEXT INST W/ 675
451
452 001046 005006 6$: CLR %6
453 001050 005004 CLR %4
454 001052 122426 CMPB (4)+,(6)+ ;TEST INCREMENT OF R4
455 001054 020427 000001 CMP %4,#1
456 001060 001405 BEQ 7$
457 001062 012737 000007 000402 MOV #7,@$FATAL ;MOVE TO MAILBOX # ***** 7 *****
458 001070 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
459 001072 000000 HALT ;WRONG INCREMENT OF R4
460 ; TO SCOPE REPLACE HALT W/ 240
461 ; AND REPLACE NEXT INST W/ 662
462
463 001074 012706 001000 7$: MOV #1000,%6
464 001100 124667 177520 CMPB -(6),HERE ;TEST DECREMENT OF R6
465 001104 022706 000776 CMP #776,%6
466 001110 001405 BEQ TST2
467 001112 012737 000010 000402 MOV #10,@$FATAL ;MOVE TO MAILBOX # ***** 10 *****
468 001120 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
469 001122 000000 HALT ;WRONG DECREMENT OF R6,OR WRONG $TESTN
470 ; TO SCOPE REPLACE HALT W/ 240
471 ; AND REPLACE NEXT INST W/ 646
```

```
472  
473  
474  
475  
476 001124 005257 000404  
477 001130 022737 000002 000404  
478 001136 001137  
479 001140 012767 123456 177442  
480 001146 012767 050505 177424  
481 001154 012705 000600  
482 001160 012706 000610  
483 001164 112625  
484 001166 022767 050456 177404  
485 001174 001405  
486 001176 012737 000011 000402  
487 001204 005212  
488 001206 000000  
489  
490  
491  
492 001210 012767 123456 177372 1$:  
493 001216 012767 050505 177354  
494 001224 012705 000600  
495 001230 012706 000612  
496 001234 114625  
497 001236 026727 177336 050456  
498 001244 001405  
499 001246 012737 000012 000402  
500 001254 005212  
501 001256 000000  
502  
503  
504  
505 001260 012767 123456 177312 2$:  
506 001266 012767 050505 177314  
507 001274 012705 000600  
508 001300 012706 000610  
509 001304 112526  
510 001306 022767 050456 177274  
511 001314 001405  
512 001316 012737 000013 000402  
513 001324 005212  
514 001326 000000  
515  
516  
517  
518 001330 012767 123456 177242 3$:  
519 001336 012767 050505 177244  
520 001344 012705 000601  
521 001350 012706 000610  
522 001354 112526  
523 001356 026727 177226 050647  
524 001364 001405  
525 001366 012737 000014 000402  
526 001374 005212  
527 001376 000000
```

\*\*\*\*\*  
;TEST 2 TRANSFER OF BYTE USING R6  
\*\*\*\*\*  
TST2:  INCL    @#STESTN            ;UPDATE TEST NUMBER  
        CMP     #2,@#STESTN       ;SEQUENCE ERROR?  
        BNE    TST3-12 ;BR TO ERROR HALT ON SEQ ERROR  
        MOV     #123456,K5  
        MOV     #050505,K1  
        MOV     #K1,%5            ;%5-(050505)K1  
        MOV     #K5,%6            ;%6=(123456)K5  
        MOVB   (6)+,(5)+         ;LOW .BYTE OF R6 TO R5  
        CMP     #050456,K1  
        BEQ     1\$  
        MOV     #11,@#SFATAL      ;MOVE TO MAILBOX # \*\*\*\*\* 11 \*\*\*\*\*  
        INC     (R2)             ;SET MSGTYP TO FATAL ERROR  
                  ;FALSE TRANSFER OF .BYTE  
                  ; TO SCOPE REPLACE HALT W/ 240  
                  ; AND REPLACE NEXT INST W/ 753  
1\$:     MOV     #123456,K5  
        MOV     #050505,K1  
        MOV     #K1,%5            ;%5(C50505)K1  
        MOV     #K6,%6            ;%6(123456)K5  
        MOVB   -(6),(5)+         ;LOW .BYTE OF R6 TO R5 (DECREMENT)  
        CMP     K1,#050456  
        BEQ     2\$  
        MOV     #12,@#SFATAL      ;MOVE TO MAILBOX # \*\*\*\*\* 12 \*\*\*\*\*  
        INC     (R2)             ;SET MSGTYP TO FATAL ERROR  
                  ;FALSE R6 .BYTE TRANSFER  
                  ; TO SCOPE REPLACE HALT W/ 240  
                  ; AND REPLACE NEXT INST W/ 727  
2\$:     MOV     #123456,K1  
        MOV     #050505,K5  
        MOV     #K1,%5            ;(123456)  
        MOV     #K5,%6            ;(050505)  
        MOVB   (5)+,(6)+         ;LOW OF R5 TO LOW OF R6  
        CMP     #050456,K5  
        BEQ     3\$  
        MOV     #13,@#SFATAL      ;MOVE TO MAILBOX # \*\*\*\*\* 13 \*\*\*\*\*  
        INC     (R2)             ;SET MSGTYP TO FATAL ERROR  
                  ;FALSE R6 .BYTE TRANSFER  
                  ; TO SCOPE REPLACE HALT W/ 240  
                  ; AND REPLACE NEXT INST W/ 703  
3\$:     MOV     #123456,K1  
        MOV     #050505,K5  
        MOV     #K1+1,%5          ;123456  
        MOV     #K5,%6            ;050505  
        MOVB   (5)+,(6)+         ;HIGH OF R5 TO LOW OF R6  
        CMP     K5,#050647  
        BEQ     4\$  
        MOV     #14,@#SFATAL      ;MOVE TO MAILBOX # \*\*\*\*\* 14 \*\*\*\*\*  
        INC     (R2)             ;SET MSGTYP TO FATAL ERROR  
                  ;FALSE R6 .BYTE TRANSFER  
4\$:     HALT



```

543
544
545
546
547 001450 005237 000404
548 001454 022737 000003 000404
549 001462 001103
550 001464 126767 177124 177123
551 001472 001405
552 001474 012737 000016 000402
553 001502 005212
554 001504 000000
555
556
557
558 001506 126767 177103 177100 1$: CMPB K7+1,K7
559 001514 001405 BEQ 2$
560 001516 012737 000017 000402 MOV #17,@$FATAL
561 001524 005212 INC (R2)
562 001526 000000 HALT
563
564
565
566 001530 126767 177063 177056 2$: CMPB K10+1,K7
567 BEQ
568 001536 001462 TST4
569 001540 012737 000020 000402 MOV #20,@$FATAL
570 001546 005212 INC (R2)
571 001550 000000 HALT
572
573
574
575 001552 126767 177040 177032 CMPB K10,K6
576 001560 001405 BEQ 3$
577 001562 012737 000021 000402 MOV #21,@$FATAL
578 001570 005212 INC (R2)
579 001572 000000 HALT
580
581
582 001574 126767 177015 177015 3$: CMPB K7+1,K10+1
583 001602 001405 BEQ 4$
584 001604 012737 000022 000402 MOV #22,@$FATAL
585 001612 005212 INC (R2)
586 001614 000000 HALT
587
588
589
590 001616 126767 176774 176773 4$: CMPB K10,K10+1
591 001624 001005 BNE 5$
592 001626 012737 000023 000402 MOV #23,@$FATAL
593 001634 005212 INC (R2)
594 001636 000000 HALT
595
596
597
598 001640 126767 176753 176751 5$: CMPB K10+1,K10+1

```

```

:*****
:TEST 3 BYTE OPERATION WITH SEQUENTIAL ODD/EVEN ADDRESS
:*****
TST3: INC @$$TESTN ;UPDATE TEST NUMBER
CMP #3,@$$TESTN ;SEQUENCE ERROR?
BNE TST4-12 ;BR TO ERROR HALT ON SEQ ERROR
CMPB K7,K7+1 ;SAME .WORD LOW TO HIGH
BEQ 1$
MOV #16,@$$FATAL ;MOVE TO MAILBOX # ***** 16 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;SHOULD COMPARE LOW TO HIGH
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 766

;COMPARE ODD TO .EVEN SAME .WORD
BEQ 2$
MOV #17,@$$FATAL ;MOVE TO MAILBOX # ***** 17 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;ODD TO .EVEN .BYTE FAILURE
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 755

;SEQUENTIAL .BYTES
;DIFFERENT .WORDS
BEQ TST4
MOV #20,@$$FATAL ;MOVE TO MAILBOX # ***** 20 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;ODD TO .EVEN FAILED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 744

CMPB K10,K6
BEQ 3$
MOV #21,@$$FATAL ;MOVE TO MAILBOX # ***** 21 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;.EVEN TO EVEN FAILED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 733

CMPB K7+1,K10+1
BEQ 4$
MOV #22,@$$FATAL ;MOVE TO MAILBOX # ***** 22 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;ODD TO ODD FAILED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 72

CMPB K10,K10+1
BNE 5$
MOV #23,@$$FATAL ;MOVE TO MAILBOX # ***** 23 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;LOW TO HIGH IN SAME .WORD FAILED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 711

```



.MAIN. MACY11 30A(1052) 18-SEP-78 12:00  
CVKADC.MAC 18-SEP-78 11:54 T3

PAGE 17

BYTE OPERATION WITH SEQUENTIAL ODD/EVEN ADDRESS

SEQ 0017

|     |        |        |        |        |      |
|-----|--------|--------|--------|--------|------|
| 599 | 001646 | 001405 |        |        |      |
| 600 | 001650 | 012737 | 000024 | 000402 |      |
| 601 | 001656 | 005212 |        |        |      |
| 602 | 001660 | 000000 |        |        |      |
| 603 |        |        |        |        |      |
| 604 |        |        |        |        |      |
| 605 |        |        |        |        |      |
| 606 | 001662 | 126767 | 176730 | 176725 | 6\$: |
| 607 | 001670 | 001005 |        |        |      |
| 608 | 001672 | 012737 | 000025 | 000402 |      |
| 609 | 001700 | 005212 |        |        |      |
| 610 | 001702 | 000000 |        |        |      |
| 611 |        |        |        |        |      |
| 612 |        |        |        |        |      |

BEQ  
MOV  
INC  
HALT

6\$  
#24,@#FATAL  
(R2)

;MOVE TO MAILBOX # \*\*\*\*\* 24 \*\*\*\*\*  
;SET MSGTYP TO FATAL ERROR  
;HIGH TO LOW IN SAME .WORD FAILED  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 700

CMPB  
BNE  
MOV  
INC  
HALT

K10,K7+1  
TST4  
#25,@#FATAL  
(R2)

;MOVE TO MAILBOX # \*\*\*\*\* 25 \*\*\*\*\*  
;SET MSGTYP TO FATAL ERROR  
;.EVEN TO ODD FAILED,OR WRONG \$TESTN,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 667

613  
614  
615  
616  
617  
618  
619  
620  
621  
622  
623  
624  
625  
626  
627  
628  
629  
630  
631  
632  
633  
634  
635  
636  
637  
638  
639  
640  
641  
642  
643  
644  
645  
646  
647  
648  
649  
650  
651  
652  
653  
654  
655  
656  
657  
658  
659  
660  
661  
662  
663  
664  
665  
666  
667  
668

001704 005237 000404  
001710 022737 000004 000404  
001716 001070  
001720 000277  
001722 005067 011360  
001726 106467 011354  
001732 103005  
001734 012737 000026 000402  
001742 005212  
001744 000000  
  
001746  
001746 102005  
001750 012737 000027 000402  
001756 005212  
001760 000000  
  
001762  
001762 001005  
001764 012737 000030 000402  
001772 005212  
001774 000000  
  
001776  
001776 100005  
002000 012737 000031 000402  
002006 005212  
002010 000000  
  
002012 000257  
002014 106767 011266  
002020 052767 000017 011260  
002026 106467 011254  
  
002032 103405  
002034 012737 000032 000402  
002042 005212  
002044 000000  
  
002046  
002046 102405  
002050 012737 000033 000402  
002056 005212  
002060 000000

\*\*\*\*\*  
:TEST 4 THE CC BITS  
\*\*\*\*\*  
TST4: INC @#STESTN :UPDATE TEST NUMBER  
CMP #4,@#STESTN :SEQUENCE ERROR?  
BNE TST5-12 ;BR TO ERROR HALT ON SEQ ERROR  
SCC :SET STATUS  
CLR STATUS :CLEAR STATUS  
MTPS STATUS  
BCC 1\$  
MOV #26,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 26 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :C NOT CLEAR  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 764  
  
1\$: BVC 2\$  
MOV #27,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 27 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :V NOT CLEAR  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 756  
  
2\$: BNE 3\$  
MOV #30,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 30 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :Z NOT CLEAR  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 750  
  
3\$: BPL 4\$  
MOV #31,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 31 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :N NOT CLEAR  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 742  
  
4\$: CCC :CLEAR CONDITION CODES  
MFPS STATUS  
BIS #17,STATUS :SET STATUS TO ONES  
MTPS STATUS  
  
5\$: BCS 5\$  
MOV #32,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 32 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :C NOT SET  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 724  
  
5\$: BVS 6\$  
MOV #33,@#SFATAL :MOVE TO MAILBOX # \*\*\*\*\* 33 \*\*\*\*\*  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :V NOT SET  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 716

|     |        |        |        |        |     |      |               |  |                                   |  |
|-----|--------|--------|--------|--------|-----|------|---------------|--|-----------------------------------|--|
| 669 | 002062 |        |        |        | 6S: |      |               |  |                                   |  |
| 670 | 002062 | 001405 |        |        |     | BEQ  | 7S            |  |                                   |  |
| 671 | 002064 | 012737 | 000034 | 000402 |     | MOV  | #34, @#SFATAL |  | :MOVE TO MAILBOX # ***** 34 ..... |  |
| 672 | 002072 | 005212 |        |        |     | INC  | (R2)          |  | :SET MSGTYP TO FATAL ERROR        |  |
| 673 | 002074 | 000000 |        |        |     | HALT |               |  | :Z NOT SET                        |  |
| 674 |        |        |        |        |     |      |               |  | : TO SCOPE REPLACE HALT W/ 240    |  |
| 675 |        |        |        |        |     |      |               |  | : AND REPLACE NEXT INST W/ 710    |  |
| 676 | 002076 |        |        |        | 7S: |      |               |  |                                   |  |
| 677 | 002076 | 100405 |        |        |     | BMI  | TST5          |  |                                   |  |
| 678 | 002100 | 012737 | 000035 | 000402 |     | MOV  | #35, @#SFATAL |  | :MOVE TO MAILBOX # ***** 35 ..... |  |
| 679 | 002106 | 005212 |        |        |     | INC  | (R2)          |  | :SET MSGTYP TO FATAL ERROR        |  |
| 680 | 002110 | 000000 |        |        |     | HALT |               |  | :N NOT SET, OR WRONG \$TESTN      |  |
| 681 |        |        |        |        |     |      |               |  | : TO SCOPE REPLACE HALT W/ 240    |  |
| 682 |        |        |        |        |     |      |               |  | : AND REPLACE NEXT INST W/ 702    |  |

```
683  
684  
685 :*****  
686 :TEST 5 THAT A TRAP OCCURES ON A RESERVED INSTRUCTION  
687 :*****  
687 002112 005237 000404 TST5: INC @%STESTN ;UPDATE TEST NUMBER  
688 002116 022737 000005 000404 CMP #5,@%STESTN ;SEQUENCE ERROR?  
689 002124 001006 BNE TST6-12 ;BR TO ERROR HALT ON SEQ ERROR  
690 002126 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP  
691 002132 012767 002154 175650 MOV #RETA,RTRAP ;RETURN LOCATION  
692 002140 000007 TRAPA ;RESERVED INSTRUCTION, SHOULD TRAP  
693 002142 012737 000036 000402 MOV #36,@%SFATAL ;MOVE TO MAILBOX # ***** 36 *****  
694 002150 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
695 002152 000000 HALT ;DID NOT TRAP OR WRONG $TESTN  
696 ; TO SCOPE REPLACE HALT W/ 240  
697 ; AND REPLACE NEXT INST W/ 764  
698 002154 RETA:  
699 :*****  
700 :TEST 6 DECREMENT OF STACK POINTER ON A TRAP OPERATION  
701 :*****  
702 002154 005237 000404 TST6: INC @%STESTN ;UPDATE TEST NUMBER  
703 002160 022737 000006 000404 CMP #6,@%STESTN ;SEQUENCE ERROR?  
704 002166 001011 BNE TST7-12 ;BR TO ERROR HALT ON SEQ ERROR  
705 002170 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP  
706 002174 012767 002204 175606 MOV #RETB,RTRAP ;RETURN POINTER  
707 002202 000007 TRAPA ;RESERVED INSTRUCTION  
708 002204 020627 000474 RETB: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP  
709 002210 001405 BEQ TST7  
710 002212 012737 000037 000402 MOV #37,@%SFATAL ;MOVE TO MAILBOX # ***** 37 *****  
711 002220 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
712 002222 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN  
713 ; TO SCOPE REPLACE HALT W/ 240  
714 ; AND REPLACE NEXT INST W/ 761
```



```
764
765
766
767
768 002434 005237 000404
769 002440 022737 000011 000404
770 002446 001125
771 002450 012706 000500
772 002454 012767 002470 175326
773 002462 005067 175324
774 002466 000007
775 002470
776 002470 100005
777 002472 012737 000043 000402
778 002500 005212
779 002502 000000
780
781
782 002504
783 002504 001005
784 002506 012737 000044 000402
785 002514 005212
786 002516 000000
787
788
789 002520
790 002520 102005
791 002522 012737 000045 000402
792 002530 005212
793 002532 000000
794
795
796 002534
797 002534 103005
798 002536 012737 000046 000402
799 002544 005212
800 002546 000000
801
802
803 002550 106767 010532
804 002554 032767 000340 010524
805 002562 001405
806 002564 012737 000047 000402
807 002572 005212
808 002574 000000
809
810
811 002576 012706 000500
812 002602 012767 002620 175200
813 002610 012767 000357 175174
814 002616 000007
815 002620
816 002620 100405
817 002622 012737 000050 000402
818 002630 005212
819 002632 000000

:*****
:TEST 11 THAT 'NEW' STATUS IS CORRECT
:*****
TST11:  INCL @#STESTN      ;UPDATE TEST NUMBER
        CMP  #11,@#STESTN ;SEQUENCE ERROR?
        BNE  RSTP1        ;BR TO ERROR HALT ON SEQ ERROR
        MOV  #BUFF,SP
        MOV  #RETF,RTRAP
        CLR  RTRAP+2      ;CLEAR FUTURE PRIORITY AND CC
        TRAPA

RETF:   BPL  1$
        MOV  #43,@#SFATAL ;MOVE TO MAILBOX # ***** 43 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;C NOT CLEARED
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 761

1$:     BNE  2$
        MOV  #44,@#SFATAL ;MOVE TO MAILBOX # ***** 44 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;Z NOT CLEARED
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 753

2$:     BVC  3$
        MOV  #45,@#SFATAL ;MOVE TO MAILBOX # ***** 45 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;V NOT CLEARED
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 745

3$:     BCC  4$
        MOV  #46,@#SFATAL ;MOVE TO MAILBOX # ***** 46 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;C NOT CLEARED
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 737

4$:     MFPS STATUS
        BIT  #340,STATUS  ;TEST PRIORITY
        BEQ  5$
        MOV  #47,@#SFATAL ;MOVE TO MAILBOX # ***** 47 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;PRIORITY NOT ZERO
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 724

5$:     MOV  #BUFF,SP
        MOV  #RETF,RTRAP
        MOV  #357,RTRAP+2 ;SET NEW 'CC' AND PRIORITY
        TRAPA             ;TRAP HERE

RETG:   BMI  1$
        MOV  #50,@#SFATAL ;MOVE TO MAILBOX # ***** 50 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;N NOT SET
```

```

820 ; TO SCOPE REPLACE HALT W/ 240
821 ; AND REPLACE NEXT INST W/ 705
822 002634 1$: BEQ 2$
823 002634 001405 MOV #51,@#$FATAL ;MOVE TO MAILBOX # ***** 51 *****
824 002636 012737 000051 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
825 002644 005212 HALT ;Z NOT SET
826 002646 000000 ; TO SCOPE REPLACE HALT W/ 240
827 ; AND REPLACE NEXT INST W/ 677
828
829 002650 2$: BVS 3$
830 002650 102405 MOV #52,@#$FATAL ;MOVE TO MAILBOX # ***** 52 *****
831 002652 012737 000052 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
832 002660 005212 HALT ;V NOT SET
833 002662 000000 ; TO SCOPE REPLACE HALT W/ 240
834 ; AND REPLACE NEXT INST W/ 671
835
836 002664 3$: BCS 4$
837 002664 103405 MOV #53,@#$FATAL ;MOVE TO MAILBOX # ***** 53 *****
838 002666 012737 000053 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
839 002674 005212 HALT ;C NOT SET
840 002676 000000 ; TO SCOPE REPLACE HALT W/ 240
841 ; AND REPLACE NEXT INST W/ 663
842
843 002700 4$: MFPS STATUS
844 002704 016706 010402 MOV STATUS,SP
845 002710 042706 000017 BIC #17,SP
846 002714 022706 000340 CMP #340,SP
847 002720 001405 BEQ RST1
848 002722
849 002722 012737 000054 000402 RSTP1: MOV #54,@#$FATAL ;MOVE TO MAILBOX # ***** 54 *****
850 002730 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
851 002732 000000 HALT ;PRIORITY WAS CHANGED,OR WRONG $TESTN
852 ; TO SCOPE REPLACE HALT W/ 240
853 ; AND REPLACE NEXT INST W/ 645
854 002734 012767 000012 175046 RST1: MOV #12,10
855 002742 005067 175044 CLR 12

```

```
856  
857  
858  
859  
860 002746 005237 000404  
861 002752 022737 000012 000404  
862 002760 001006  
863 002762 012706 000500  
864 002766 012767 003010 175040  
865 002774 104400  
866 002776 012737 000055 000402  
867 003004 005212  
868 003006 000000  
869  
870  
871 003010  
872  
873  
874  
875 003010 005237 000404  
876 003014 022737 000013 000404  
877 003022 001011  
878 003024 012706 000500  
879 003030 012767 003040 174776  
880 003036 104400  
881 003040 020627 000474  
882 003044 001405  
883 003046 012737 000056 000402  
884 003054 005212  
885 003056 000000  
886  
887  
888  
889  
890  
891 003060 005237 000404  
892 003064 022737 000014 000404  
893 003072 001012  
894 003074 012706 000500  
895 003100 012767 003110 174726  
896 003106 104400  
897 003110 022767 003110 175356  
898 003116 001405  
899 003120 012737 000057 000402  
900 003126 005212  
901 003130 000000  
902  
903
```

```
*****  
:TEST 12 THAT A TRAP OCCURES FOR A 'TRAP' INSTRUCTION  
*****  
TST12: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #12,@#STESTN ;SEQUENCE ERROR?  
BNE TST13-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETA1,RTRAP1 ;RETURN LOCATION  
TRAP ;RESERVED INSTRUCTION, SHOULD TRAP  
MOV #55,@#SFATAL ;MOVE TO MAILBOX # ***** 55 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;DID NOT TRAP,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 764  
  
RETA1:  
*****  
:TEST 13 DECREMENT OF STACK POINTER ON A TRAP OPERATION  
*****  
TST13: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #13,@#STESTN ;SEQUENCE ERROR?  
BNE TST14-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETB1,RTRAP1 ;RETURN POINTER  
TRAP ;RESERVED INSTRUCTION  
RETB1: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP  
BEQ TST14  
MOV #56,@#SFATAL ;MOVE TO MAILBOX # ***** 56 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 761  
  
*****  
:TEST 14 THAT PROPER P.C. IS SAVED  
*****  
TST14: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #14,@#STESTN ;SEQUENCE ERROR?  
BNE TST15-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETC1,RTRAP1 ;RETURN FROM TRAP POINTER  
TRAP ;TRAP ON THIS INSTRUCTION  
RETC1: CMP #.,BUFF-4 ;CHECK INCREMENTED P.C.  
BEQ TST15  
MOV #57,@#SFATAL ;MOVE TO MAILBOX # ***** 57 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT P.C.,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 760
```





```

936
937
938
939
940 003266 005237 000404
941 003272 022737 000016 000404
942 003300 001125
943 003302 012706 000500
944 003306 012767 003322 174520
945 003314 005067 174516
946 003320 104400
947 003322
948 003322 100005
949 003324 012737 000062 000402
950 003332 005212
951 003334 000000
952
953
954 003336
955 003336 001005
956 003340 012737 000063 000402
957 003346 005212
958 003350 000000
959
960
961 003352
962 003352 102005
963 003354 012737 000064 000402
964 003362 005212
965 003364 000000
966
967
968 003366
969 003366 103005
970 003370 012737 000065 000402
971 003376 005212
972 003400 000000
973
974
975 003402 106767 007700
976 003406 032767 000340 007672
977 003414 001405
978 003416 012737 000066 000402
979 003424 005212
980 003426 000000
981
982
983 003430 012706 000500
984 003434 012767 003452 174372
985 003442 012767 000357 174366
986 003450 104400
987 003452
988 003452 100405
989 003454 012737 000067 000402
990 003462 005212
991 003464 000000

```

```

:*****
:TEST 16 THAT 'NEW' STATUS IS CORRECT
:*****
TST16: INC @#STESTN ;UPDATE TEST NUMBER
CMP #16,@#STESTN ;SEQUENCE ERROR?
BNE TST17-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP
MOV #RETF1,RTRAP1
CLR RTRAP1+2 ;CLEAR FUTURE PRIORITY AND CC
TRAP

RETF1: BPL 1$
MOV #62,@#SFATAL ;MOVE TO MAILBOX # ***** 62 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 761

1$: BNE 2$
MOV #63,@#SFATAL ;MOVE TO MAILBOX # ***** 63 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;Z NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 753

2$: BVC 3$
MOV #64,@#SFATAL ;MOVE TO MAILBOX # ***** 64 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;V NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 745

3$: BCC 4$
MOV #65,@#SFATAL ;MOVE TO MAILBOX # ***** 65 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 737

4$: MFPS STATUS
BIT #340,STATUS ;TEST PRIORITY
BEQ 5$
MOV #66,@#SFATAL ;MOVE TO MAILBOX # ***** 66 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;PRIORITY NOT ZERO
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 724

5$: MOV #BUFF,SP
MOV #RETG1,RTRAP1
MOV #357,RTRAP1+2 ;SET NEW 'CC' AND PRIORITY
TRAP ;TRAP HERE

RETG1: BMI 1$
MOV #67,@#SFATAL ;MOVE TO MAILBOX # ***** 67 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;N NOT SET

```



```

1025
1026 ;*****
1027 ;TEST 17 THAT ALL COMBINATION OF 'TRAP' WILL TRAP
1028 ;*****
1029 003566 005237 000404 TST17: INC @%$TESTN ;UPDATE TEST NUMBER
1030 003572 022737 000017 000404 CMP #17,@%$TESTN ;SEQUENCE ERROR?
1031 003600 001011 BNE RB1AA ;BR TO ERROR HALT ON SEQ ERROR
1032 003602 012767 104400 000012 MOV #TRAP,RB1 ;INITIALIZE BASE TRAP INSTRUCTION
1033 003610 012767 003636 174216 MOV #RA1,34 ;RETURN FROM TRAP TO RA1
1034 003616 012706 000500 RC1: MOV #BUFF,SP ;SET UP STACK POINTER
1035 003622 104400 RB*: TRAP ;TRAP INST WILL BE MODIFIED TO TRAP+377
1036 003624 RB1AA:
1037 003624 012737 000074 000402 MOV #74,@%$FATAL ;MOVE TO MAILBOX ***** 74 *****
1038 003632 005212 INC (R2) ;SET MSGTYP TO FAIAL ERPOR
1039 003634 000000 HALT ;PREVIOUS INST FAILED TO TRAP,OR WRONG $TESTN
1040 ; TO SCOPE REPLACE HALT W/ 240
1041 ; AND REPLACE NEXT INST W/ 761
1042 003636 005267 177760 RA1: INC RB1
1043 003642 022767 104777 177752 CMP #104777,RB1 ;TRAP+377 TO UPPER LIMIT
1044 003650 103362 BHIS RC1 ;HAVE WE TESTED ALL
1045 003652 012767 000036 174154 MOV #36,34
1046 003660 005067 174152 CLR 36
1047 ;*****
1048 ;TEST 20 THAT A TRAP OCCURES ON AN 'IOT' INSTRUCTION
1049 ;*****
1050 003664 005237 000404 TST20: INC @%$TESTN ;UPDATE TEST NUMBER
1051 003670 022737 000020 000404 CMP #20,@%$TESTN ;SEQUENCE ERROR?
1052 003676 001006 BNE TST21-12 ;BR TO ERROR HALT ON SEQ ERROR
1053 003700 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1054 003704 012767 003726 174106 MOV #RETA2,RTRAP2 ;RETURN LOCATION
1055 003712 000004 IOT ;RESERVE INSTRUCTION, SHOULD TRAP
1056 003714 012737 000075 000402 MOV #75,@%$FATAL ;MOVE TO MAILBOX # ***** 75 *****
1057 003722 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1058 003724 000000 HALT ;IOT DID NOT TRAP,OR WRONG $TESTN
1059 ; TO SCOPE REPLACE HALT W/ 240
1060 ; AND REPLACE NEXT INST W/ 764
1061 003726 RETA2:
1062 ;*****
1063 ;TEST 21 DECREMENT OF STACK POINTER ON A TRAP OPERATION
1064 ;*****
1065 003726 005237 000404 TST21: INC @%$TESTN ;UPDATE TEST NUMBER
1066 003732 022737 000021 000404 CMP #21,@%$TESTN ;SEQUENCE ERROR?
1067 003740 001011 BNE TST22-12 ;BR TO ERROR HALT ON SEQ ERROR
1068 003742 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1069 003746 012767 003756 174044 MOV #RETB2,RTRAP2 ;RETURN POINTER
1070 003754 000004 IOT ;RESERVED INSTRUCTION
1071 003756 020627 000474 RETB2: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
1072 003762 001405 BEQ TST22
1073 003764 012737 000076 000402 MOV #76,@%$FATAL ;MOVE TO MAILBOX # ***** 76 *****
1074 003772 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1075 003774 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
1076 ; TO SCOPE REPLACE HALT W/ 240
1077 ; AND REPLACE NEXT INST W/ 761
    
```

```
1078
1079
1080 ;*****
1081 ;TEST 22 THAT PROPER P.C. IS SAVED
1082 ;*****
1082 003776 005237 000404 TST22: INCL @%$TESTN ;UPDATE TEST NUMBER
1083 004002 022737 000022 000404 CMP #22,@%$TESTN ;SEQUENCE ERROR?
1084 004010 001012 BNE TST23-12 ;BR TO ERROR HALT ON SEQ ERROR
1085 004012 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1086 004016 012767 004026 173774 MOV #RETC2,RTRAP2 ;RETURN FROM TRAP POINTER
1087 004024 000004 IOT ;TRAP ON THIS INSTRUCTION
1088 004026 022767 004026 174440 RETC2: CMP #.,BUFF-4 ;CHECK FOR INCREMENTED P.C.
1089 004034 001405 BEQ TST23
1090 004036 012737 000077 000402 MOV #77,@%$FATAL ;MOVE TO MAILBOX # ***** 77 *****
1091 004044 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1092 004046 000000 HALT ;INCORRECT P.C.,OR WRONG $TESTN
1093 ; TO SCOPE REPLACE HALT W/ 240
1094 ; AND REPLACE NEXT INST W/ 760
1095 ;*****
1096 ;TEST 23 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
1097 ;*****
1098 004050 005237 000404 TST23: INC @%$TESTN ;UPDATE TEST NUMBER
1099 004054 022737 000023 000404 CMP #23,@%$TESTN ;SEQUENCE ERROR?
1100 004062 001044 BNE TST24-12 ;BR TO ERROR HALT ON SEQ ERROR
1101 004064 012706 000500 MOV #BUFF,SP ;SET UP
1102 004070 012767 004112 173722 MOV #RETD2,RTRAP2 ;SET UP
1103 004076 005067 007204 CLR STATUS ;CLEAR STATUS AND PRIORITY
1104 004102 106467 007200 MTPS STATUS
1105 004106 000257 CCC
1106 004110 000004 IOT ;TRAP
1107 004112 026727 174360 000000 RETD2: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
1108 004120 001405 BEQ 1$
1109 004122 012737 000100 000402 MOV #100,@%$FATAL ;MOVE TO MAILBOX # ***** 100 *****
1110 004130 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1111 004132 000000 HALT ;INCORRECT STATUS
1112 ; TO SCOPE REPLACE HALT W/ 240
1113 ; AND REPLACE NEXT INST W/ 753
1114 004134 012706 000500 1$: MOV #BUFF,SP ;SET UP
1115 004140 012767 004164 173652 MOV #RETE2,RTRAP2 ;SET UP
1116 004146 012767 000357 007132 MOV #357,STATUS ;SET PRIORITY
1117 004154 106467 007126 MTPS STATUS
1118 004160 000277 SCC ;SET CC
1119 004162 000004 IOT ;TRAP
1120 004164 026727 174306 000357 RETE2: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
1121 004172 001405 BEQ TST24
1122 004174 012737 000101 000402 MOV #101,@%$FATAL ;MOVE TO MAILBOX # ***** 101 *****
1123 004202 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1124 004204 000000 HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
1125 ; TO SCOPE REPLACE HALT W/ 240
1126 ; AND REPLACE NEXT INST W/ 726
```

```
1127
1128
1129 :*****
1130 :TEST 24 THAT 'NEW' STATUS IS CORRECT
1131 :*****
1131 004206 005237 000404 TST24: INC @#STESTN :UPDATE TEST NUMBER
1132 004212 022737 000024 000404 CMP #24,@#STESTN :SEQUENCE ERROR?
1133 004220 001125 BNE STP :BR TO ERROR HALT ON SEQ ERROR
1134 004222 012706 000500 MOV #BUFF,SP
1135 004226 012767 004242 173564 MOV #RETF2,RTRAP2
1136 004234 005067 173562 CLR RTRAP2+2 :CLEAR FUTURE PRIORITY AND CC
1137 004240 000004 IOT
1138 004242 RETF2:
1139 004242 100005 BPL 1$
1140 004244 012737 000102 000402 MOV #102,@#SFATAL :MOVE TO MAILBOX # ***** 102 *****
1141 004252 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1142 004254 000000 HALT :C NOT CLEARED
1143 : TO SCOPE REPLACE HALT W/ 240
1144 : AND REPLACE NEXT INST W/ 761
1145 004256 1$:
1146 004256 001005 BNE 2$
1147 004260 012737 000103 000402 MOV #103,@#SFATAL :MOVE TO MAILBOX # ***** 103 *****
1148 004266 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1149 004270 000000 HALT :Z NOT CLEARED
1150 : TO SCOPE REPLACE HALT W/ 240
1151 : AND REPLACE NEXT INST W/ 753
1152 004272 2$:
1153 004272 102005 BVC 3$
1154 004274 012737 000104 000402 MOV #104,@#SFATAL :MOVE TO MAILBOX # ***** 104 *****
1155 004302 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1156 004304 000000 HALT :V NOT CLEARED
1157 : TO SCOPE REPLACE HALT W/ 240
1158 : AND REPLACE NEXT INST W/ 745
1159 004306 3$:
1160 004306 103005 BCC 4$
1161 004310 012737 000105 000402 MOV #105,@#SFATAL :MOVE TO MAILBOX # ***** 105 *****
1162 004316 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1163 004320 000000 HALT :C NOT CLEARED
1164 : TO SCOPE REPLACE HALT W/ 240
1165 : AND REPLACE NEXT INST W/ 737
1166 004322 106767 006760 006752 4$: MFPS STATUS
1167 004326 032767 000340 006752 BIT #340,STATUS :TEST PRIORITY
1168 004334 001405 BEQ 5$
1169 004336 012737 000106 000402 MOV #106,@#SFATAL :MOVE TO MAILBOX # ***** 106 *****
1170 004344 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1171 004346 000000 HALT :PRIORITY NOT ZERO
1172 : TO SCOPE REPLACE HALT W/ 240
1173 : AND REPLACE NEXT INST W/ 724
1174 004350 012706 000500 5$: MOV #BUFF,SP
1175 004354 012767 004372 173436 MOV #RETF2,RTRAP2
1176 004362 012767 000357 173432 MOV #357,RTRAP2+2 :SET NEW 'CC' AND PRIORITY
1177 004370 000004 IOT :TRAP HERE
1178 004372 RETG2:
1179 004372 100405 BMI 1$
1180 004374 012737 000107 000402 MOV #107,@#SFATAL :MOVE TO MAILBOX # ***** 107 *****
1181 004402 005212 INC (R2) :SET MSGTYP TO FATAL ERROR
1182 004404 000000 HALT :N NOT SET
```

```

1183                                     ; TO SCOPE REPLACE HALT W/ 240
1184                                     ; AND REPLACE NEXT INST W/ 705
1185 004406                               1$:
1186 004406 001405                         BEQ 2$
1187 004410 012737 000110 000402          MOV #110,@#$FATAL ;MOVE TO MAILBOX # ***** 110 *****
1188 004416 005212                         INC (R2)           ;SET MSGTYP TO FATAL ERROR
1189 004420 000000                         HALT              ;Z NOT SET
1190                                     ; TO SCOPE REPLACE HALT W/ 240
1191                                     ; AND REPLACE NEXT INST W/ 677
1192 004422                               2$:
1193 004422 102405                         BVS 3$
1194 004424 012737 000111 000402          MOV #111,@#$FATAL ;MOVE TO MAILBOX # ***** 111 *****
1195 004432 005212                         INC (R2)           ;SET MSGTYP TO FATAL ERROR
1196 004434 000000                         HALT              ;V NOT SET
1197                                     ; TO SCOPE REPLACE HALT W/ 240
1198                                     ; AND REPLACE NEXT INST W/ 671
1199 004436                               3$:
1200 004436 105'05                         BCS 4$
1201 004440 012737 000112 000402          MOV #112,@#$FATAL ;MOVE TO MAILBOX # ***** 112 *****
1202 004446 005212                         INC (R2)           ;SET MSGTYP TO FATAL ERROR
1203 004450 000000                         HALT              ;C NOT SET
1204                                     ; TO SCOPE REPLACE HALT W/ 240
1205                                     ; AND REPLACE NEXT INST W/ 663
1206 004452 106767 006630                 4$: MFPS STATUS
1207 004456 016706 006624                 MOV STATUS,SP
1208 004462 042706 000017                 BIC #17,SP
1209 004466 022706 000340                 CMP #340,SP
1210 004472 001405                         BEQ STPA
1211 004474                                     STP:
1212 004474 012737 000113 000402          MOV #113,@#$FATAL ;MOVE TO MAILBOX # ***** 113 *****
1213 004502 005212                         INC (R2)           ;SET MSGTYP TO FATAL ERROR
1214 004504 000000                         HALT              ;PRIORITY WAS CHANGED,OR WRONG $TESTN
1215                                     ; TO SCOPE REPLACE HALT W/ 240
1216                                     ; AND REPLACE NEXT INST W/ 645
1217 004506 012767 000022 173304 STPA: MOV #22,20
1218 004514 005067 173302                 CLR 22
    
```

```

1219
1220 ;*****
1221 ;TEST 25 THAT TRAP OCCURS ON AN EMT RESTRICTED INSTRUCTION
1222 ;*****
1223 004520 005237 000404 TST25: INC @%STESTN ;UPDATE TEST NUMBER
1224 004524 022737 000025 000404 CMP #25,@%STESTN ;SEQUENCE ERROR?
1225 004532 001006 BNE TST26-12 ;BR TO ERROR HALT ON SEQ ERROR
1226 004534 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1227 004540 012767 004562 173262 MOV #RETA3,RTRAP3 ;RETURN LOCATION
1228 004546 104000 EMT ;RESERVE INSTRUCTION, SHOULD TRAP
1229 004550 012737 000114 000402 MOV #114,@%SFATAL ;MOVE TO MAILBOX # ***** 114 *****
1230 004556 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1231 004560 000000 HALT ;EMT DID NOT TRAP,OR WRONG $TESTN
1232 ; TO SCOPE REPLACE HALT W/ 240
1233 ; AND REPLACE NEXT INST W/ 764
1234 004562 RETA3:
1235 ;*****
1236 ;TEST 26 DECREMENT OF STACK POINTER ON A TRAP OPERATION
1237 ;*****
1238 004562 005237 000404 TST26: INC @%STESTN ;UPDATE TEST NUMBER
1239 004566 022737 000026 000404 CMP #26,@%STESTN ;SEQUENCE ERROR?
1240 004574 001011 BNE TST27-12 ;BR TO ERROR HALT ON SEQ ERROR
1241 004576 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1242 004602 012767 004612 173220 MOV #RETB3,RTRAP3 ;RETURN POINTER
1243 004610 104000 EMT ;RESERVED INSTRUCTION
1244 004612 020627 000474 RETB3: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
1245 004616 001405 BEQ TST27
1246 004620 012737 000115 000402 MOV #115,@%SFATAL ;MOVE TO MAILBOX # ***** 115 *****
1247 004626 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1248 004630 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
1249 ; TO SCOPE REPLACE HALT W/ 240
1250 ; AND REPLACE NEXT INST W/ 761
1251 ;*****
1252 ;TEST 27 THAT PROPER P.C. IS SAVED
1253 ;*****
1254 004632 005237 000404 TST27: INC @%STESTN ;UPDATE TEST NUMBER
1255 004636 022737 000027 000404 CMP #27,@%STESTN ;SEQUENCE ERROR?
1256 004644 001012 BNE TST30-12 ;BR TO ERROR HALT ON SEQ ERROR
1257 004646 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1258 004652 012767 004662 173150 MOV #RETC3,RTRAP3 ;RETURN FROM TRAP POINTER
1259 004660 104000 EMT ;TRAP ON THIS INSTRUCTION
1260 004662 022767 004662 173604 RETC3: CMP #,BUFF-4 ;CHECK FOR INCREMENTED P.C.
1261 004670 001405 BEQ TST30
1262 004672 012737 000116 000402 MOV #116,@%SFATAL ;MOVE TO MAILBOX # ***** 116 *****
1263 004700 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1264 004702 000000 HALT ;INCORRECT P.C.,OR WRONG $TESTN
1265 ; TO SCOPE REPLACE HALT W/ 240
1266 ; AND REPLACE NEXT INST W/ 760

```



1267  
1268  
1269  
1270  
1271  
1272  
1273  
1274  
1275  
1276  
1277  
1278  
1279  
1280  
1281  
1282  
1283  
1284  
1285  
1286  
1287  
1288  
1289  
1290  
1291  
1292  
1293  
1294  
1295  
1296  
1297  
1298  
1299

004704 005237 000404  
004710 022737 000030 000404  
004716 001044  
004720 012706 000500  
004724 012767 004746 173076  
004732 005067 006350  
004736 106467 006344  
004742 000257  
004744 104000  
004746 026727 173524 000000 RETD3:  
004754 001405  
004756 012737 000117 000402  
004764 005212  
004766 000000  
  
004770 012706 000500 1\$:  
004774 012767 005020 173026  
005002 012767 000357 006276  
005010 106467 006272  
005014 000277  
005016 104000  
005020 026727 173452 000357 RETE3:  
005026 001405  
005030 012737 000120 000402  
005036 005212  
005040 000000

\*\*\*\*\*  
:TEST 30 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK  
\*\*\*\*\*  
TST30: INC @%STESTN ;UPDATE TEST NUMBER  
CMP #30,@%STESTN ;SEQUENCE ERROR?  
BNE TST31-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;SET UP  
MOV #RETD3,RTRAP3 ;SET UP  
CLR STATUS ;CLEAR STATUS AND PRIORITY  
M'PS STATUS  
CLC  
EMT ;TRAP  
CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK  
BEQ 1\$  
MOV #117,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 117 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 753  
MOV #BUFF,SP ;SET UP  
MOV #RETE3,RTRAP3 ;SET UP  
MOV #357,STATUS ;SET PRIORITY  
MTPS STATUS  
SCC ;SET CC  
EMT ;TRAP  
CMP BUFF-2,#357 ;COMPARES STATUS ON STACK  
BEQ TST31  
MOV #120,@%SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 120 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS ON STACK,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 726

```
1300
1301 ;*****
1302 ;TEST 31 THAT 'NEW' STATUS IS CORRECT
1303 ;*****
1304 005042 005237 000404 TST31: INC @#STESTN ;UPDATE TEST NUMBER
1305 005046 022737 000031 000404 CMP #31,@#STESTN ;SEQUENCE ERROR?
1306 005054 001125 BNE TST32-12 ;BR TO ERROR HALT ON SEQ ERROR
1307 005056 012706 000500 MOV #BUFF,SP
1308 005062 012767 005076 172740 MOV #RETG3,RTRAP3
1309 005070 005067 172736 CLR RTRAP3+2 ;CLEAR FUTURE PRIORITY AND CC
1310 005074 104000 EMT
1311 005076 RETG3: ;TEST FOR 'C' CLEARED
1312 005076 100005 BPL 1$
1313 005100 012737 000121 000402 MOV #121,@#SFATAL ;MOVE TO MAILBOX # ***** 121 *****
1314 005106 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1315 005110 000000 HALT ;C NOT CLEARED
1316 ; TO SCOPE REPLACE HALT W/ 240
1317 ; AND REPLACE NEXT INST W/ 761
1318 005112 1$:
1319 005112 001005 BNE 2$
1320 005114 012737 000122 000402 MOV #122,@#SFATAL ;MOVE TO MAILBOX # ***** 122 *****
1321 005122 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1322 005124 000000 HALT ;Z NOT CLEARED
1323 ; TO SCOPE REPLACE HALT W/ 240
1324 ; AND REPLACE NEXT INST W/ 753
1325 005126 2$:
1326 005126 102005 BVC 3$
1327 005130 012737 000123 000402 MOV #123,@#SFATAL ;MOVE TO MAILBOX # ***** 123 *****
1328 005136 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1329 005140 000000 HALT ;V NOT CLEARED
1330 ; TO SCOPE REPLACE HALT W/ 240
1331 ; AND REPLACE NEXT INST W/ 745
1332 005142 3$:
1333 005142 103005 BCC 4$
1334 005144 012737 000124 000402 MOV #124,@#SFATAL ;MOVE TO MAILBOX # ***** 124 *****
1335 005152 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1336 005154 000000 HALT ;C NOT CLEARED
1337 ; TO SCOPE REPLACE HALT W/ 240
1338 ; AND REPLACE NEXT INST W/ 737
1339 005156 106767 006124 006116 4$: MFPS STATUS
1340 005162 032767 000340 006116 BIT #340,STATUS ;TEST PRIORITY
1341 005170 001405 BEQ 5$
1342 005172 012737 000125 000402 MOV #125,@#SFATAL ;MOVE TO MAILBOX # ***** 125 *****
1343 005200 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1344 005202 000000 HALT ;PRIORITY NOT ZERO
1345 ; TO SCOPE REPLACE HALT W/ 240
1346 ; AND REPLACE NEXT INST W/ 724
1347 005204 012706 000500 5$: MOV #BUFF,SP
1348 005210 012767 005226 172612 MOV #RETG3,RTRAP3
1349 005216 012767 000357 172606 MOV #357,RTRAP3+2 ;SET NEW 'CC' AND PRIORITY
1350 005224 104000 EMT ;TRAP HERE
1351 005226 RETG3:
1352 005226 100405 BMI 1$
1353 005230 012737 000126 000402 MOV #126,@#SFATAL ;MOVE TO MAILBOX # ***** 126 *****
1354 005236 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1355 005240 000000 HALT ;N NOT SET
```



1389  
1390  
1391  
1392  
1393 005342 005237 000404  
1394 005346 022737 000032 000404  
1395 005354 001011  
1396 005356 012767 104000 000012  
1397 005364 012767 005412 172436  
1398 005372 012706 000500  
1399 005376 104000  
1400 005400  
1401 005400 012737 000133 000402  
1402 005406 005212  
1403 005410 000000  
1404  
1405  
1406 005412 005267 177760  
1407 005416 022767 104377 177752  
1408 005424 103362  
1409 005426 012767 000032 172374  
1410 005434 005067 172372  
1411  
1412  
1413  
1414 005440 005237 000404  
1415 005444 022737 000033 000404  
1416 005452 001006  
1417 005454 012706 000500  
1418 005460 012767 005502 172326  
1419 005466 000003  
1420 005470 012737 000134 000402  
1421 005476 005212  
1422 005500 000000  
1423  
1424  
1425 005502

```

:*****
:TEST 32 THAT ALL COMBINATION OF EMT WILL CAUSE A TRAP
:*****
TST32:  INC  @W$TESTN      ;UPDATE TEST NUMBER
        CMP  #32,@W$TESTN ;SEQUENCE ERROR?
        BNE  RBBB        ;BR TO ERROR HALT ON SEQ ERROR
        MOV  #EMT,RB     ;INITIALIZE BASE EMT INSTRUCTION
        MOV  #RA,30     ;RETURN FROM TRAP TO RA
RC:     MOV  #BUFF,SP   ;SET UP STACK POINTER
RB:     EMT             ;TRAP INST. WILL BE MODIFIED TO EMT+377
RBBB:
        MOV  #133,@W$FATAL ;MOVE TO MAILBOX # ***** 133 *****
        INC  (R2)        ;SET MSGTYP TO FATAL ERROR
        HALT            ;PREVIOUS INST FAILED TO TRAP,OR WRONG $TESTN
                        ; TO SCOPE REPLACE HALT W/ 240
                        ; AND REPLACE NEXT INST W/ 761
RA:     INC  RB
        CMP  #104377,RB ;EMT+377 TO EMT?
        BHIS RC         ;HAVE WE TESTED ALL
        MOV  #32,30
        CLR  32         ;HALT
:*****
:TEST 33 THAT A TRAP OCCURES ON AN 'BPT' INSTRUCTION
:*****
TST33:  INC  @W$TESTN      ;UPDATE TEST NUMBER
        CMP  #33,@W$TESTN ;SEQUENCE ERROR?
        BNE  TST34-12    ;BR TO ERROR HALT ON SEQ ERROR
        MOV  #BUFF,SP   ;STACK POINTER SETUP
        MOV  #RETA4,RTRAP4 ;RETURN LOCATION
        TRT                ;RESERVED INSTRUCTION, SHOULD TRAP
        MOV  #134,@W$FATAL ;MOVE TO MAILBOX # ***** 134 *****
        INC  (R2)        ;SET MSGTYP TO FATAL ERROR
        HALT            ;DID NOT TRAP,OR WRONG $TESTN
                        ; TO SCOPE REPLACE HALT W/ 240
                        ; AND REPIACE NEXT INST W/ 764
RETA4.

```

1426  
1427  
1428  
1429  
1430 005502 005237 000404  
1431 005506 022737 000034 000404  
1432 005514 001011  
1433 005516 012706 000500  
1434 005522 012767 005532 172264  
1435 005530 000003  
1436 005532 020627 000474  
1437 005536 001405  
1438 005540 012737 000135 000402  
1439 005546 005212  
1440 005550 000000  
1441  
1442  
1443  
1444  
1445  
1446 005552 005237 000404  
1447 005556 022737 000035 000404  
1448 005564 001012  
1449 005566 012706 000500  
1450 005572 012767 005602 172214  
1451 005600 000003  
1452 005602 022767 005602 172664  
1453 005610 001405  
1454 005612 012737 000136 000402  
1455 005620 005212  
1456 005622 000000  
1457  
1458

```

:*****
:TEST 34 DECREMENT OF STACK POINTER ON A TRAP OPERATION
:*****
TST34: INC @#STESTN :UPDATE TEST NUMBER
      CMP #34,@#STESTN :SEQUENCE ERROR?
      BNE TST35-12 :BR TO ERROR HALT ON SEQ ERROR
      MOV #BUFF,SP :STACK POINTER SETUP
      MOV #RETB4,RTRAP4 :RETURN POINTER
      TRT :RESERVED INSTRUCTION
RETB4: CMP SP,#BUFF-4 :TEST DECREMENT OF SP
      BEQ TST35
      MOV #135,@#SFATAL :MOVE TO MAILBOX # ***** 135 *****
      INC (R2) :SET MSGTYP TO FATAL ERROR
      HALT :NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
          : TO SCOPE REPLACE HALT W/ 240
          : AND REPLACE NEXT INST W/ 761
:*****
:TEST 35 THAT PROPER P.C. IS SAVED
:*****
TST35: INC @#STESTN :UPDATE TEST NUMBER
      CMP #35,@#STESTN :SEQUENCE ERROR?
      BNE TST36-12 :BR TO ERROR HALT ON SEQ ERROR
      MOV #BUFF,SP :STACK POINTER SETUP
      MOV #RETC4,RTRAP4 :RETURN FROM TRAP POINTER
      TRT :TRAP ON THIS INSTRUCTION
RETC4: CMP #.,BUFF-4 :CHECK FOR INCREMENTED P.C.
      BEQ TST36
      MOV #136,@#SFATAL :MOVE TO MAILBOX # ***** 136 *****
      INC (R2) :SET MSGTYP TO FATAL ERROR
      HALT :INCORRECT P.C.,OR WRONG $TESTN
          : TO SCOPE REPLACE HALT W/ 240
          : AND REPLACE NEXT INST W/ 760

```

```

1459
1460
1461 :*****
1462 :TEST 36 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
1463 :*****
1463 005624 005237 000404 TST36: INC @#$TESTN ;UPDATE TEST NUMBER
1464 005630 022737 000036 000404 CMP #36,@#$TESTN ;SEQUENCE ERROR?
1465 005636 001044 BNE TST37-12 ;BR TO ERROR HALT ON SEQ ERROR
1466 005640 012706 000500 MOV #BUFF,SP ;SET UP
1467 005644 012767 005666 172142 MOV #RETD4,RTRAP4 ;SET UP
1468 005652 005067 005430 CLR STATUS ;CLEAR STATUS AND PRIORITY
1469 005656 106467 005424 MTPS STATUS
1470 005662 000257 CCC
1471 005664 000003 TRT ;TRAP
1472 005666 026727 172604 000000 RETD4: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
1473 005674 001405 BEQ 1$
1474 005676 012737 000137 000402 MOV #137,@#$FATAL ;MOVE TO MAILBOX # ***** 137 *****
1475 005704 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1476 005706 000000 HALT ;INCORRECT STATUS
1477 ; TO SCOPE REPLACE HALT W/ 240
1478 ; AND REPLACE NEXT INST W/ 753
1479 005710 012706 000500 1$: MOV #BUFF,SP ;SET UP
1480 005714 012767 005740 172072 MOV #RETE4,RTRAP4 ;SET UP
1481 005722 012767 000357 005356 MOV #357,STATUS ;SET PRIORITY
1482 005730 106467 005352 MTPS STATUS
1483 005734 000277 SCC ;SET-SET CC
1484 005736 000003 TRT ;TRAP
1485 005740 026727 172532 000357 RETE4: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
1486 005746 001405 BEQ TST37
1487 005750 012737 000140 000402 MOV #140,@#$FATAL ;MOVE TO MAILBOX # ***** 140 *****
1488 005756 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1489 005760 000000 HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
1490 ; TO SCOPE REPLACE HALT W/ 240
1491 ; AND REPLACE NEXT INST W/ 726

```

```
1492  
1493 ;*****  
1494 ;TEST 37 THAT 'NEW' STATUS IS CORRECT  
1495 ;*****  
1496 005762 005237 000404 TST37: INC @#STESTN ;UPDATE TEST NUMBER  
1497 005766 022737 000037 000404 CMP #37,@#STESTN ;SEQUENCE ERROR?  
1498 005774 001125 BNE RSTP2 ;BR TO ERROR HALT ON SEQ ERROR  
1499 005776 012706 000500 MOV #BUFF,SP  
1500 006002 012767 006016 172004 MOV #RSTF4,RTRAP4  
1501 006010 005067 172002 CLR RTRAP4+2 ;CLEAR FUTURE PRIORITY AND CC  
1502 006014 000003 TRT  
1503 006016 RETF4:  
1504 006016 100005 BPL 1$  
1505 006020 012737 000141 000402 MOV #141,@#SFATAL ;MOVE TO MAILBOX # ***** 141 *****  
1506 006026 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1507 006030 000000 HALT ;C NOT CLEARED  
1508 ; TO SCOPE REPLACE HALT W/ 240  
1509 ; AND REPLACE NEXT INST W/ 761  
1510 006032 1$:  
1511 006032 001005 BNE 2$  
1512 006034 012737 000142 000402 MOV #142,@#SFATAL ;MOVE TO MAILBOX # ***** 142 *****  
1513 006042 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1514 006044 000000 HALT ;Z NOT CLEARED  
1515 ; TO SCOPE REPLACE HALT W/ 240  
1516 ; AND REPLACE NEXT INST W/ 753  
1517 006046 2$:  
1518 006046 102005 BVC 3$  
1519 006050 012737 000143 000402 MOV #143,@#SFATAL ;MOVE TO MAILBOX # ***** 143 *****  
1520 006056 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1521 006060 000000 HALT ;V NOT CLEARED  
1522 ; TO SCOPE REPLACE HALT W/ 240  
1523 ; AND REPLACE NEXT INST W/ 745  
1524 006062 3$:  
1525 006062 103005 BCC 4$  
1526 006064 012737 000144 000402 MOV #144,@#SFATAL ;MOVE TO MAILBOX # ***** 144 *****  
1527 006072 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1528 006074 000000 HALT ;C NOT CLEARED  
1529 ; TO SCOPE REPLACE HALT W/ 240  
1530 ; AND REPLACE NEXT INST W/ 737  
1531 006076 106767 005204 4$: MFPS STATUS  
1532 006102 032767 000340 005176 BIT #340,STATUS ;TEST PRIORITY  
1533 006110 001405 BEQ 5$  
1534 006112 012737 000145 000402 MOV #145,@#SFATAL ;MOVE TO MAILBOX # ***** 145 *****  
1535 006120 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1536 006122 000000 HALT ;PRIORITY NOT ZERO  
1537 ; TO SCOPE REPLACE HALT W/ 240  
1538 ; AND REPLACE NEXT INST W/ 724  
1539 006124 012706 000500 5$: MOV #BUFF,SP  
1540 006130 012767 006146 171656 MOV #RETG4,RTRAP4  
1541 006136 012767 000357 171652 MOV #357,RTRAP4+2 ;SET 'NEW 'CC' AND PRIORITY  
1542 006144 000003 TRT ;TRAP HERE  
1543 006146 RETG4:  
1544 006146 100405 BMI 1$  
1545 006150 012737 000146 000402 MOV #146,@#SFATAL ;MOVE TO MAILBOX # ***** 146 *****  
1546 006156 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1547 006150 000000 HALT ;N NOT SET
```

```

1548                                     : TO SCOPE REPLACE HALT W/ 240
1549                                     : AND REPLACE NEXT INST W/ 705
1550 006162                               1$: BEQ      2$
1551 006162 001405                         MOV      #147,@#$FATAL ;MOVE TO MAILBOX # ***** 147 *****
1552 006164 012737 000147 000402          INC      (R2)          ;SET MSGTYP TO FATAL ERROR
1553 006172 005212                         HALT
1554 006174 000000                         ;Z NOT SET
1555                                     ; TO SCOPE REPLACE HALT W/ 240
1556                                     ; AND REPLACE NEXT INST W/ 677
1557 006176                               2$: BVS      3$
1558 006176 102405                         MOV      #150,@#$FATAL ;MOVE TO MAILBOX # ***** 150 *****
1559 006200 012737 000150 000402          INC      (R2)          ;SET MSGTYP TO FATAL ERROR
1560 006206 005212                         HALT
1561 006210 000000                         ;V NOT SET
1562                                     ; TO SCOPE REPLACE HALT W/ 240
1563                                     ; AND REPLACE NEXT INST W/ 671
1564 006212                               3$: BCS      4$
1565 006212 103405                         MOV      #151,@#$FATAL ;MOVE TO MAILBOX # ***** 151 *****
1566 006214 012737 000151 000402          INC      (R2)          ;SET MSGTYP TO FATAL ERROR
1567 006222 005212                         HALT
1568 006224 000000                         ;C NOT SET
1569                                     ; TO SCOPE REPLACE HALT W/ 240
1570                                     ; AND REPLACE NEXT INST W/ 663
1571 006226 106767 005054                 4$: MFPS    STATUS
1572 006232 016706 005050                 MOV      STATUS,SP
1573 006236 042706 000017                 BIC      #17,SP
1574 006242 022706 000340                 CMP      #340,SP
1575 006246 001405                         BEQ      RST2
1576 006250                               RSTP2:
1577 006250 012737 000152 000402          MOV      #152,@#$FATAL ;MOVE TO MAILBOX # ***** 152 *****
1578 006256 005212                         INC      (R2)          ;SET MSGTYP TO FATAL ERROR
1579 006260 000000                         HALT
1580                                     ;PRIORITY WAS CHANGED,OR WRONG $TESTN
1581                                     ; TO SCOPE REPLACE HALT W/ 240
1582 006262 012767 000016 171524          RST2: MOV      #16,14
1583 006270 005067 171522                 CLR      16
1584

```



```
1585  
1586 ;PDP-11 ILLEGAL AND ADDRESS INSTRUCTION TEST  
1587 ;ALL INSTRUCTIONS THAT ARE RESERVED  
1588 ;SHOULD TRAP TO LOCATION 4, AND THE  
1589 ;PC THAT POINTS TO THE TRAPPING INSTRUCTION  
1590 ;SHOULD BE PLACED ON THE STACK  
1591  
1592 ;*****  
1593 ;TEST 40 THAT A TRAP OCCURS ON AN ILLEGAL INSTRUCTION  
1594 ;*****  
1595 006274 005237 000404 TST40: INC @%STESTN ;UPDATE TEST NUMBER  
1596 006300 022737 000040 000404 CMP #40,%STESTN ;SEQUENCE ERROR?  
1597 006306 001006 BNE TST41-12 ;BR TO ERROR HALT ON SEQ ERROR  
1598 006310 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP  
1599 006314 012767 006336 171462 MOV #RETA5,RTRAP5 ;RETURN LOCATION  
1600 006322 000100 JMP %0 ;ILLEGAL INSTRUCTION, SHOULD TRAP  
1601 006324 012737 000153 000402 MOV #153,%SFATAL ;MOVE TO MAILBOX # ***** 153 *****  
1602 006332 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1603 006334 000000 HALT ;DID NOT TRAP,OR WRONG $TESTN  
1604 ; TO SCOPE REPLACE HALT W/ 240  
1605 ; AND REPLACE NEXT INST W/ 764  
1606 006336 RETA5:  
1607 ;*****  
1608 ;TEST 41 DECREMENT OF STACK POINTER ON A TRAP OPERATION  
1609 ;*****  
1610 006336 005237 000404 TST41: INC @%STESTN ;UPDATE TEST NUMBER  
1611 006342 022737 000041 000404 CMP #41,%STESTN ;SEQUENCE ERROR?  
1612 006350 001011 BNE TST42-12 ;BR TO ERROR HALT ON SEQ ERROR  
1613 006352 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP  
1614 006356 012767 006366 171420 MOV #RETB5,RTRAP5 ;RETURN POINTER  
1615 006364 000100 JMP %0 ;RESERVED INSTRUCTION  
1616 006366 020627 000474 RETB5: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP  
1617 006372 001405 BEQ TST42  
1618 006374 012737 000154 000402 MOV #154,%SFATAL ;MOVE TO MAILBOX # ***** 154 *****  
1619 006402 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1620 006404 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN  
1621 ; TO SCOPE REPLACE HALT W/ 240  
1622 ; AND REPLACE NEXT INST W/ 761
```



```
1672
1673
1674
1675
1676 006616 005237 000404 000404
1677 006622 022737 000044 000404
1678 006630 001123
1679 006632 012706 000500
1680 006636 012767 006652 171140
1681 006644 005067 171136
1682 006650 000100
1683 006652
1684 006652 100005
1685 006654 012737 000160 000402
1686 006662 005212
1687 006664 000000
1688
1689
1690 006666
1691 006666 001005
1692 006670 012737 000161 000402
1693 006676 005212
1694 006700 000000
1695
1696
1697 006702
1698 006702 102005
1699 006704 012737 000162 000402
1700 006712 005212
1701 006714 000000
1702
1703
1704 006716
1705 006716 103005
1706 006720 012737 000163 000402
1707 006726 005212
1708 006730 000000
1709
1710
1711 006732 106767 004350 004342
1712 006736 032767 000357 004342
1713 006744 001405
1714 006746 012737 000164 000402
1715 006754 005212
1716 006756 000000
1717
1718
1719 006760 012706 000500
1720 006764 012767 007002 171012
1721 006772 012767 000357 171006
1722 007000 000100
1723 007002
1724 007002 100405
1725 007004 012737 000165 000402
1726 007012 005212
1727 007014 000000

:*****
:TEST 44 THAT 'NEW' STATUS IS CORRECT
:*****
TST44: INC @#STESTN ;UPDATE TEST NUMBER
CMP #44,@#STESTN ;SEQUENCE ERROR?
BNE TST45-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP
MOV #RETF5,RTRAP5
CLR RTRAP5+2 ;CLEAR FUTURE PRIORITY AND CC
JMP %0

RETF5: BPL 1$
MOV #160,@#SFATAL ;MOVE TO MAILBOX # ***** 160 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 761

1$: BNE 2$
MOV #161,@#SFATAL ;MOVE TO MAILBOX # ***** 161 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;Z NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 753

2$: BVC 3$
MOV #162,@#SFATAL ;MOVE TO MAILBOX # ***** 162 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;V NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 745

3$: BCC 4$
MOV #163,@#SFATAL ;MOVE TO MAILBOX # ***** 163 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 737

4$: MFPS STATUS
BIT #357,STATUS ;TEST PRIORITY
BEQ 5$
MOV #164,@#SFATAL ;MOVE TO MAILBOX # ***** 164 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;PRIORITY NOT ZERO
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 724

5$: MOV #BUFF,SP
MOV #RETF5,RTRAP5
MOV #357,RTRAP5+2 ;SET NEW 'CC' AND PRIORITY
JMP %0 ;TRAP HERE

RETF5: BMI 1$
MOV #165,@#SFATAL ;MOVE TO MAILBOX # ***** 165 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;N NOT SET
```



1760  
1761  
1762  
1763  
1764  
1765  
1766  
1767  
1768  
1769  
1770  
1771  
1772  
1773  
1774  
1775  
1776  
1777  
1778  
1779  
1780  
1781  
1782  
1783  
1784  
1785  
1786  
1787  
1788  
1789  
1790  
1791

007112 005237 000404  
007116 022737 000045 000404  
007124 001006  
007126 012706 000500  
007132 012767 007154 170644  
007140 004000  
007142 012737 000172 000402  
007150 005212  
007152 000000  
  
007154  
  
007154 005237 000404  
007160 022737 000046 000404  
007166 001011  
007170 012706 000500  
007174 012767 007204 170602  
007202 004000  
007204 020627 000474  
007210 001405  
007212 012737 000173 000402  
007220 005212  
007222 000000

\*\*\*\*\*  
:TEST 45 THAT A TRAP OCCURES ON ALL ILLEGAL INSTRUCTION  
\*\*\*\*\*  
TST45: INC @#\$TESTN ;UPDATE TEST NUMBER  
CMP #45,@#\$TESTN ;SEQUENCE ERROR?  
BNE TST46-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETH5,RTRAP5 ;RETURN LOCATION  
JSR %0,%0 ;RESERVED INSTRUCTION, SHOULD TRAP  
MOV #172,@#\$FATAL ;MOVE TO MAILBOX # \*\*\*\*\* 172 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;DID NOT TRAP,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 764  
  
RETH5:  
\*\*\*\*\*  
:TEST 46 DECREMENT OF STACK POINTER ON A TRAP OPERATION  
\*\*\*\*\*  
TST46: INC @#\$TESTN ;UPDATE TEST NUMBER  
CMP #46,@#\$TESTN ;SEQUENCE ERROR?  
BNE TST47-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETJ,RTRAP5 ;RETURN POINTER  
JSR %0,%0 ;RESERVED INSTRUCTION  
RETJ: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP  
BEQ TST47  
MOV #173,@#\$FATAL ;MOVE TO MAILBOX # \*\*\*\*\* 173 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;NOT DECREMENTED TWO WORDS,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 761

```
1792
1793
1794
1795
1796 007224 005237 000404
1797 007230 022737 000047 000404
1798 007236 001012
1799 007240 012706 000500
1800 007244 012767 007254 170532
1801 007252 004000
1802 007254 022767 007254 171212
1803 007262 001405
1804 007264 012737 000174 000402
1805 007272 005212
1806 007274 000000
1807
1808
1809
1810
1811
1812
1813 007276 005237 000404
1814 007302 022737 000050 000404
1815 007310 001044
1816 007312 012706 000500
1817 007316 012767 007340 170460
1818 007324 005067 003756
1819 007330 106467 003752
1820 007334 000257
1821 007336 004000
1822 007340 026727 171132 000000
1823 007346 001405
1824 007350 012737 000175 000402
1825 007356 005212
1826 007360 000000
1827
1828
1829 007362 012706 000500
1830 007366 012767 007412 170410
1831 007374 012767 000357 003704
1832 007402 106467 003700
1833 007406 000277
1834 007410 004000
1835 007412 026727 171060 000357
1836 007420 001405
1837 007422 012737 000176 000402
1838 007430 005212
1839 007432 000000
1840
1841
1842
1843
1844
1845
1846 007434 005237 000404
1847 007440 022737 000051 000404
```

\*\*\*\*\*  
:TEST 47 THAT PROPER P.C. IS SAVED  
\*\*\*\*\*

TST47: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #47,@#STESTN ;SEQUENCE ERROR?  
BNE TST50-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETK,RTRAP5 ;RETURN FROM TRAP POINTER  
INSTK: JSR %0,%0 ;TRAP ON THIS INSTRUCTION  
RETK: CMP #INSTK+2,BUFF-4 ;CHECK FOR INCREMENTED P.C.  
BEQ TST50  
MOV #174,@#SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 174 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT P.C.,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 760

\*\*\*\*\*  
:TEST 50 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON : ACK  
\*\*\*\*\*

TST50: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #50,@#STESTN ;SEQUENCE ERROR?  
BNE TST51-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;SET UP  
MOV #RETL,RTRAP5 ;SET UP  
CLR STATUS ;CLEAR STATUS AND PRIORITY  
MTPS STATUS  
CCC  
JSR %0,%0 ;TRAP  
RETL: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK  
BEQ 1\$  
MOV #175,@#SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 175 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 753

1\$: MOV #BUFF,SP ;SET UP  
MOV #RETM,RTRAP5 ;SET UP  
MOV #357,STATUS ;SET PRIORITY  
MTPS STATUS  
SCC ;SET CC  
JSR %0,%0 ;TRAP  
RETM: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK  
BEQ TST51  
MOV #176,@#SFATAL ;MOVE TO MAILBOX # \*\*\*\*\* 176 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS ON STACK,OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 726

\*\*\*\*\*  
:TEST 51 THAT 'NEW' STATUS IS CORRECT  
\*\*\*\*\*

TST51: INC @#STESTN ;UPDATE TEST NUMBER  
CMP #51,@#STESTN ;SEQUENCE ERROR?

```
1848 007446 001122      BNE      STP1      ;BR TO ERROR HALT ON SEQ ERROR
1849 007450 012706 000500      MOV      #BUFF,SP
1850 007454 012767 007470 170322      MOV      #RETN,RTRAPS
1851 007462 005067 170320      CLR      RTRAP5+2      ;CLEAR FUTURE PRIORITY AND CC
1852 007466 004000      JSR      %0,%0
1853 007470      RETN:      ;TEST FOR 'C' CLEARED
1854 007470 100005      BPL      1$
1855 007472 012737 000177 000402      MOV      #177,@#FATAL      ;MOVE TO MAILBOX # ***** 177 *****
1856 007500 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1857 007502 000000      HALT      ;C NOT CLEARED
1858      ; TO SCOPE REPLACE HALT W/ 240
1859      ; AND REPLACE NEXT INST W/ 761
1860 007504      1$:
1861 007504 001005      BNE      2$
1862 007506 012737 000200 000402      MOV      #200,@#FATAL      ;MOVE TO MAILBOX # ***** 200 *****
1863 007514 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1864 007516 000000      HALT      ;Z NOT CLEARED
1865      ; TO SCOPE REPLACE HALT W/ 240
1866      ; AND REPLACE NEXT INST W/ 753
1867 007520      2$:
1868 007520 102005      BVC      3$
1869 007522 012737 000201 000402      MOV      #201,@#FATAL      ;MOVE TO MAILBOX # ***** 201 *****
1870 007530 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1871 007532 000000      HALT      ;V NOT CLEARED
1872      ; TO SCOPE REPLACE HALT W/ 240
1873      ; AND REPLACE NEXT INST W/ 745
1874 007534      3$:
1875 007534 103005      BCC      4$
1876 007536 012737 000202 000402      MOV      #202,@#FATAL      ;MOVE TO MAILBOX # ***** 202 *****
1877 007544 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1878 007546 000000      HALT      ;C NOT CLEARED
1879      ; TO SCOPE REPLACE HALT W/ 240
1880      ; AND REPLACE NEXT INST W/ 737
1881 007550 106767 003532      4$:      MFPS      STATUS
1882 007554 016700 003526      MOV      STATUS,%0      ;TEMP STORAGE
1883 007560 001405      BEQ      5$
1884 007562 012737 000203 000402      MOV      #203,@#FATAL      ;MOVE TO MAILBOX # ***** 203 *****
1885 007570 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1886 007572 000000      HALT      ;PRIORITY NOT ZERO
1887      ; TO SCOPE REPLACE HALT W/ 240
1888      ; AND REPLACE NEXT INST W/ 725
1889 007574 012706 000500      5$:      MOV      #BUFF,SP
1890 007600 012767 007616 170176      MOV      #RETN,RTRAPS
1891 007606 012767 000357 170172      MOV      #357,RTRAP5+2      ;SET NEW 'CC' AND PRIORITY
1892 007614 004000      JSR      %0,%0      ;TRAP HERE
1893 007616      RETO:
1894 007616 100405      BMI      1$
1895 007620 012737 000204 000402      MOV      #204,@#FATAL      ;MOVE TO MAILBOX # ***** 204 *****
1896 007626 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
1897 007630 000000      HALT      ;N NOT SET
1898      ; TO SCOPE REPLACE HALT W/ 240
1899      ; AND REPLACE NEXT INST W/ 706
1900 007632      1$:
1901 007632 001405      BEQ      2$
1902 007634 012737 000205 000402      MOV      #205,@#FATAL      ;MOVE TO MAILBOX # ***** 205 *****
1903 007642 005212      INC      (R2)      ;SET MSGTYP TO FATAL ERROR
```

```

1904 007644 000000          HALT          ;Z NOT SET
1905                                     ; TO SCOPE REPLACE HALT W/ 240
1906                                     ; AND REPLACE NEXT INST W/ 700
1907 007646                2$:
1908 007646 102405          BVS          3$
1909 007650 012737 000206 000402  MOV        #206,@#$FATAL ;MOVE TO MAILBOX # ***** 206 *****
1910 007656 005212          INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1911 007660 000000          HALT          ;V NOT SET
1912                                     ; TO SCOPE REPLACE HALT W/ 240
1913                                     ; AND REPLACE NEXT INST W/ 672
1914 007662                3$:
1915 007662 103405          BCS          4$
1916 007664 012737 000207 000402  MOV        #207,@#$FATAL ;MOVE TO MAILBOX # ***** 207 *****
1917 007672 005212          INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1918 007674 000000          HALT          ;C NOT SET
1919                                     ; TO SCOPE REPLACE HALT W/ 240
1920                                     ; AND REPLACE NEXT INST W/ 664
1921 007676 106767 003404          4$:  MFPS      STATUS
1922 007702 016700 003400          MOV        STATUS,%0
1923 007706 022700 000357          CMP        #357,%0
1924 007712 001405          BEQ        STPBB
1925 007714                STP1:
1926 007714 012737 000210 000402  MOV        #210,@#$FATAL ;MOVE TO MAILBOX # ***** 210 *****
1927 007722 005212          INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1928 007724 000000          HALT          ;PRIORITY WAS CHANGED,OR WRONG $TESTN
1929                                     ; TO SCOPE REPLACE HALT W/ 240
1930                                     ; AND REPLACE NEXT INST W/ 650
1931 007726 012767 000006 170050  STPBB:  MOV        #6,4
1932 007734 005067 170046          CLR        6

```



```
1933  
1934  
1935 ;*****  
1935 ;TEST 52 THAT THE T BIT (BIT4) WILL CAUSE TRACE TRAP TO 14  
1936 ;*****  
1937 007740 005237 000404 TST52: INC @%$TESTN ;UPDATE TEST NUMBER  
1938 007744 022737 000052 000404 CMP #52,@%$TESTN ;SEQUENCE ERROR?  
1939 007752 001013 BNE TST53-12 ;BR TO ERROR HALT ON SEQ ERROR  
1940 007754 012706 000500 MOV #BUFF,SP  
1941 007760 012767 010014 170026 MOV #RETAT,RTRAP4 ;SET UP TO TRAP TO 14  
1942 007766 012746 000020 MOV #20,-(SP) ;PUSH T BIT  
1943 007772 012746 010000 MOV #.+6,-(SP) ;PUSH PC  
1944 007776 000002 RTI ;SET T BIT  
1945 010000 000240 NOP ;TRAP HERE  
1946 010002 012737 000211 000402 MOV #211,@%$FATAL ;MOVE TO MAILBOX # ***** 211 *****  
1947 010010 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1948 010012 000000 HALT ;TRACE BIT DID NOT TRAP.,OR WRONG $TESTN  
1949 ; TO SCOPE REPLACE HALT W/ 240  
1950 ; AND REPLACE NEXT INST W/ 757  
1951 010014 RETAT:  
1952 ;*****  
1953 ;TEST 53 STACK POINTER DECREMENTS  
1954 ;*****  
1955 010014 005237 000404 TST53: INC @%$TESTN ;UPDATE TEST NUMBER  
1956 010020 022737 000053 000404 CMP #53,@%$TESTN ;SEQUENCE ERROR?  
1957 010026 001023 BNE TST54-12 ;BR TO ERROR HALT ON SEQ ERROR  
1958 010030 012706 000500 MOV #BUFF,SP  
1959 010034 012767 010070 167752 MOV #RETBT,RTRAP4  
1960 010042 012746 000020 MOV #20,-(SP) ;PUSH T BIT  
1961 010046 012746 010054 MOV #.+6,-(SP) ;PUSH PC  
1962 010052 000002 RTI ;SET T BIT  
1963 010054 000240 NOP ;TRAP HERE  
1964 010056 012737 000212 000402 MOV #212,@%$FATAL ;MOVE TO MAILBOX # ***** 212 *****  
1965 010064 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1966 010066 000000 HALT ;TRACE BIT DID NOT TRAP!  
1967 ; TO SCOPE REPLACE HALT W/ 240  
1968 ; AND REPLACE NEXT INST W/ 757  
1969 010070 020627 000474 RETBT: CMP SP,#BUFF-4  
1970 010074 001405 BEQ TST54  
1971 010076 012737 000213 000402 MOV #213,@%$FATAL ;MOVE TO MAILBOX # ***** 213 *****  
1972 010104 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1973 010106 000000 HALT ;STACK POINTER WAS NOT PUSHED BY TRAP,OR WRONG $TESTN  
1974 ; TO SCOPE REPLACE HALT W/ 240  
1975 ; AND REPLACE NEXT INST W/ 747
```

```

1976
1977
1978
1979
1980 010110 005237 000404
1981 010114 022737 000054 000404
1982 010122 001016
1983 010124 012706 000500
1984 010130 012767 010150 167656
1985 010136 012746 000020
1986 010142 012746 010150
1987 010146 000002
1988
1989 010150 022767 010150 170316 RETCT:
1990 010156 001405
1991 010160 012737 000214 000402
1992 010166 005212
1993 010170 000000
1994
1995
1996
1997
1998
1999
2000
2001 010172 005237 000404
2002 010176 022737 000055 000404
2003 010204 001015
2004
2005 010206 012706 000500
2006 010212 005001
2007 010214 012746 000020
2008 010220 012746 010234
2009 010224 012767 010252 167562
2010 010232 000006
2011 010234 000240
2012 010236 001405
2013 010240 012737 000215 000402
2014 010246 005212
2015 010250 000000
2016
2017
2018
2019 010252

```

```

:*****
:TEST 54 FOR PROPER PC ON STACK
:*****
TST54: INC @%STESTN ;UPDATE TEST NUMBER
        CMP #54,@%STESTN ;SEQUENCE ERROR?
        BNE TST55-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #RETCT,RTRAP4
        MOV #20,-(SP) ;PUSH T BIT
        MOV #.+6,-(SP) ;PUSH PC
        RTI ;SET T BIT
        ;TRAP HERE
RETCT: CMP #.,BUFF-4
        BEQ TST55
        MOV #214,@%SFATAL ;MOVE TO MAILBOX # ***** 214 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;CORRECT PC WAS NOT SAVED ON STACK,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 754

:*****
:TEST 55 THAT RTT POPS T- BIT
:*****
TST55: INC @%STESTN ;UPDATE TEST NUMBER
        CMP #55,@%STESTN ;SEQUENCE ERROR?
        BNE TST56-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        CLR R1 ;CLEAR R1
        MOV #20,-(SP)
        MOV #RTT1,-(SP)
        MOV #RTT2,14
        RTT
RTT1: NOP
        BEQ TST56
        MOV #215,@%SFATAL ;MOVE TO MAILBOX # ***** 215 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;T-BIT DID NOT TRAP,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 755

RTT2:

```

```
2020
2021
2022 :*****
2023 :TEST 56 THAT RTT ALLOWS ONE INST. BEFORE TRAP
2024 :*****
2024 010252 005237 000404 TST56: INC @#STESTN ;UPDATE TEST NUMBER
2025 010256 022737 000056 000404 CMP #56,@#STESTN ;SEQUENCE ERROR?
2026 010264 001031 BNE TST57-12 ;BR TO ERROR HALT ON SEQ ERROR
2027 010266 012705 177777 MOV #177777,%5
2028 010272 012706 000500 RTT5: MOV #BUFF,SP
2029 010276 012746 000020 MOV #20,-(SP)
2030 010302 012746 010320 MOV #RTT3,-(SP)
2031 010306 012767 010340 167500 MOV #RTT4,14
2032 010314 005001 CLR R1 ;CLEAR R0
2033 010316 000006 RTT ;SET T-BIT
2034 010320 005201 RTT3: INC R1
2035 010322 005201 INC %5
2036 010324 001762 BEQ RTT5 ;DO THIS TEST NO MORE THAN 2 TIMES
2037 010326 012737 000216 000402 MOV #216,@#SFATAL ;MOVE TO MAILBOX # ***** 216 *****
2038 010334 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2039 010336 000000 HALT ;DID NOT TRAP
2040 ; TO SCOPE REPLACE HALT W/ 240
2041 ; AND REPLACE NEXT INST W/ 752
2042 ;SEE IF RTT ALLOWS 1 INST.
2042 010340 005301 RTT4: DEC R1
2043 010342 001407 BEQ RTT6
2044 010344 005205 INC %5 ;DO THIS TEST NO MORE THAN TWO TIMES
2045 010346 001751 BEQ RTT5
2046 010350 012737 000217 000402 MOV #217,@#SFATAL ;MOVE TO MAILBOX # ***** 217 *****
2047 010356 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2048 010360 000000 HALT ;RTT DID NOT ALLOW 1 INST.,OR WRONG $TESTN
2049 ; TO SCOPE REPLACE HALT W/ 240
2050 ; AND REPLACE NEXT INST W/ 741
2051 010362 RTT5:
```

```

2052
2053
2054
2055
2056 010362 005237 000404
2057 010366 022737 000057 000404
2058 010374 001023
2059 010376 012706 000500
2060 010402 012746 000020
2061 010406 012746 010424
2062 010412 012767 010440 167374
2063 010420 005001
2064 010422 000002
2065 010424 005201
2066 010426 012737 000220 000402
2067 010434 005212
2068 010436 000000
2069
2070
2071 010440 005701
2072
2073 010442 001405
2074 010444 012737 000221 000402
2075 010452 005212
2076 010454 000000
2077
2078

```

```

;*****
;TEST 57 THAT RTI DOES NOT ALLOW 1 INST.
;*****
TST57: INC @#STESTN ;UPDATE TEST NUMBER
        CMP #57,@#STESTN ;SEQUENCE ERROR?
        BNE TST60-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #20,-(SP)
        MOV #RTI1,-(SP)
        MOV #RTI2,14
        CLR R1
RTI1: INC R1 ;SET T-BIT
        MOV #220,@#SFATAL ;RTI SHOULD NOT ALLOW THIS
        INC (R2) ;MOVE TO MAILBOX # ***** 220 *****
        HALT ;SET MSGTYP TO FATAL ERROR
        ;T- BIT DID NOT CAUSE TRAP
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 756
RTI2: TST R1 ;RTI SHOULD NOT ALLOW 1 INST. BEFORE TRAP
        BEQ TST60
        MOV #221,@#SFATAL ;MOVE TO MAILBOX # ***** 221 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;RTI DID ALLOW 1 INST. BEFORE TRAP,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 747

```

```
2079
2080 :*****
2081 :TEST 60 TRAP ON TRAP
2082 :*****
2083 010456 005237 000404 000404 TST60: INC @#STESTN ;UPDATE TEST NUMBER
2084 010462 022737 000060 000404 CMP #60,@#STESTN ;SEQUENCE ERROR?
2085 010470 001033 BNE TRACE ;BR TO ERROR HALT ON SEQ ERROR
2086 :TEST THAT TRACE BIT TRAPS ARE INHIBITED ON TRAP INST
2087
2088 010472 012705 177777 TRPTRP: MOV #177777,%5
2089 010476 012706 000500 MOV #BUFF,%6
2090 010502 012767 010554 167304 MOV #TRACE1,14 ;TRACE TRAP
2091 010510 005027 000016 CLR #16 ;
2092 010514 005027 000022 CLR #22 ;
2093 010520 012767 010572 167272 MOV #TONT1,20 ;IOT TRAP
2094 010526 012746 000020 MOV #20,-(SP) ;PUSH T BIT
2095 010532 012746 010540 MOV #.+6,-(SP) ;PUSH PC
2096 010536 000006 RTT ;SET T BIT
2097 010540 000004 IOT ;TRAP, NEW STATUS HAVE TRACE RESET
2098 010542 012737 000222 000402 MOV #222,@#SFATAL ;MOVE TO MAILBOX # ***** 222 *****
2099 010550 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2100 010552 000000 HALT ;NO TRAP OCCURRED
2101 ; TO SCOPE REPLACE HALT W/ 240
2102 ; AND REPLACE NEXT INST W/ 746
2103 010554 005205 TRACE1: INC %5 ;IF FAILED TRY THIS TEST TWICE BUT NO MORE
2104 010556 001747 BEQ TRPTRP
2105 010560 TRACE:
2106 010560 012737 000223 000402 MOV #223,@#SFATAL ;MOVE TO MAILBOX # ***** 223 *****
2107 010566 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2108 010570 000000 HALT ;IOT SHOULD HAVE CLEARED THE T BIT,OR WRONG $TESTN
2109 ; TO SCOPE REPLACE HALT W/ 240
2110 ; AND REPLACE NEXT INST W/ 737
2111 010572 012767 000016 167214 TONT1: MOV #16,14
2112 010600 012767 000022 167212 MOV #22,20
```

```
2113  
2114  
2115 ;*****  
2116 ;TEST 61 THAT THE TRACE BIT WILL CAUSE A TRAP  
2117 ;*****  
2117 010606 005237 000404 TST61: INC @#STESTN ;UPDATE TEST NUMBER  
2118 010612 022737 000061 000404 CMP #61,@#STESTN ;SEQUENCE ERROR?  
2119 010620 001026 BNE TST62-12 ;BR TO ERROR HALT ON SEQ ERROR  
2120 010622 012706 000500 MOV #BUFF,%6 ;SET UP STACK POINTER  
2121 010626 012767 010666 167160 MOV #TRC1,14 ;TRACE TRAP RETURN  
2122 010634 005067 167156 CLR 16  
2123 010640 012746 000020 MOV #20,-(SP) ;PUSH T BIT  
2124 010644 012746 010652 MOV #.+6,-(SP) ;PUSH PC  
2125 010650 000002 RTI ;SET T BIT  
2126 010652 000240 NOP  
2127 010654 012737 000224 000402 MOV #224,@#SFATAL ;MOVE TO MAILBOX # ***** 224 *****  
2128 010662 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
2129 010664 000000 HALT ;DO NOT TRAP  
2130 ; TO SCOPE REPLACE HALT W/ 240  
2131 ; AND REPLACE NEXT INST W/ 755  
2132 010666 036727 167604 000020 TRC1: BIT BUFF-2,#20 ;CHECK FOR T BIT ON STACK  
2133 010674 001005 BNE TST62  
2134 010676 012737 000225 000402 MOV #225,@#SFATAL ;MOVE TO MAILBOX # ***** 225 *****  
2135 010704 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
2136 010706 000000 HALT ;T BIT NOT SAVED ON STACKED,OR WRONG $TESTN  
2137 ; TO SCOPE REPLACE HALT W/ 240  
2138 ; AND REPLACE NEXT INST W/ 744  
2139 ;** *****  
2140 ;TEST 62 THAT AN RTI POPS THE T BIT  
2141 ;*****  
2142 010710 005237 000404 TST62: INC @#STESTN ;UPDATE TEST NUMBER  
2143 010714 022737 000062 000404 CMP #62,@#STESTN ;SEQUENCE ERROR?  
2144 010722 001020 BNE TST63-12 ;BR TO ERROR HALT ON SEQ ERROR  
2145 010724 012706 000500 MOV #BUFF,%6 ;SET UP THE STACK  
2146 010730 012746 000020 MOV #20,-(6) ;FUTURE T BIT ON STACK  
2147 010734 012746 010 50 MOV #TRC2,-(6) ;RTI RETURN  
2148 010740 012767 010764 167046 MOV #TRC3,14 ;TRACE TRAP INTERRUPT POINTER  
2149 010746 000002 RTI  
2150  
2151 010750 000240 TRC2: NOP ;TRACE IS SET SHOULD TRAP TO 14  
2152 010752 012737 000226 000402 MOV #226,@#SFATAL ;MOVE TO MAILBOX # ***** 226 *****  
2153 010760 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
2154 010762 000000 HALT ;DID NOT TRACE TRAP,OR WRONG $TESTN  
2155 ; TO SCOPE REPLACE HALT W/ 240  
2156 ; AND REPLACE NEXT INST W/ 757  
2157  
2158 010764 012767 000016 167022 TRC3: MOV #16,14  
2159 010772 005067 167020 CLR 16
```

```

2160
2161
2162
2163
2164 010776 005237 000404
2165 011002 022737 000063 000404
2166 011010 001055
2167 011012 032767 000001 167400
2168 011020 001403
2169 011022 005767 167360
2170 011026 001055
2171 011030
2172 011030 105737 177564
2173 011034 100375
2174 011036 012706 000500
2175 011042 012767 00034C 002236
2176 011050 106467 002232
2177 011054 012767 011132 167002
2178 011062 012767 000100 166474
2179 011070 012704 000120
2180 011074 077401
2181 011076 012767 011144 166730
2182 011104 012767 011156 166752
2183 011112 012767 000340 166716
2184 011120 005067 002162
2185 011124 106467 002156
2186 011130 104400
2187 011132
2188 011132 012737 000227 000402
2189 011140 005212
2190 011142 000000
2191
2192
2193 011144
2194 011144 012737 000230 000402
2195 011152 005212
2196 011154 000000
2197
2198
2199 011156 005067 166654

```

```

:*****
:TEST 63 THAT A PENDING INTERRUPT OCCURS BEFORE TRAP
:*****
TST63:  INC  @#STESTN      ;UPDATE TEST NUMBER
        CMP  #63,@#STESTN ;SEQUENCE ERROR?
        BNE  TR1         ;BR TO ERROR HALT ON SEQ ERROR
        BIT  #1,$ENV      ; CHECK IF ON APT
        BEQ  NOAPT        ; IF NOT ON APT
        TST  $PASS        ; CHECK IF ON FIRST PASS
        BNE  TST64        ; IF NOT FIRST PASS

NOAPT:  TSTB  @#TPS
        BPL  .-4
        MOV  #BUFF,%6
        MOV  #340,STATUS ;HIGHEST PRIORITY LEVEL
        MTPS STATUS
        MOV  #TR0,64
        MOV  #100,TTCSR  ;INTERRUPT FOR TTY PUNCH/PRINTER
        MOV  #120,R4     ;SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG
        SOB  R4          ;TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)
        MOV  #TR1,34     ;TRAP VECTOR
        MOV  #TR2,64     ;TTY VECTOR
        MOV  #340,36     ;IF TRAP TRAPS, MOVE 340 TO PRIORITY
        CLR  STATUS      ;SHOULD TRAP AT END OF CLR INST
        MTPS STATUS
        TRAP              ;TTY INTERRUPT SHOULD OVERRIDE TRAP

TR0:    MOV  #227,@#$FATAL ;MOVE TO MAILBOX # ***** 227 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT              ;TTY SHOULDN'T HAVE INTERRUPTED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 722

TR1:    MOV  #230,@#$FATAL ;MOVE TO MAILBOX # ***** 230 *****
        INC  (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT              ;INTERRUPT DID NOT OCCUR FIRST,OR WRONG $STESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 715

TR2:    CLR  36

```

```

2200
2201
2202
2203
2204 011162 005237 000404
2205 011166 022737 000064 000404
2206 011174 001045
2207 011176 032767 000001 167214
2208 011204 001403
2209 011206 005767 167174
2210 011212 001060
2211 011214
2212 011214 042767 000100 166342
2213 011222 012706 000500
2214 011226 012767 000340 002052
2215 011234 106467 002046
2216 011240 012767 000100 166316
2217 011246 012767 011306 166560
2218 011254 012767 011322 166602
2219 011262 012767 011310 166530
2220 011270 012767 000340 166524
2221 011276 012704 000120
2222 011302 077401
2223 011304 104400
2224 011306 000004
2225 011310
2226 011310 012737 000231 000402
2227 011316 005212
2228 011320 000000
2229
2230
2231 011322 005067 166474
2232 011326 005067 166534
2233 011332 012767 000066 166524
2234 011340 012767 000036 166466
2235 011346 012767 000022 166444
2236

```

```

:*****
:TEST 64 THAT PENDING INTERRUPT OCCURS BETWEEN TRAPS
:*****
TST64:  INC  @#$STESTN      :UPDATE TEST NUMBER
        CMP  #64,@#$STESTN :SEQUENCE ERROR?
        BNE  TR5           :BR TO ERROR HALT ON SEQ ERROR
        BIT  #1,$ENV       :CHECK IF ON APT
        BEQ  NOAPT1        : IF NOT
        TST  $PASS        : CHECK IF ON FIRST PASS
        BNE  TST65        : IF NOT
NOAPT1:
        BIC  #100,TTCSR    :
        MOV  #BUFF,%6     :
        MOV  #340,STATUS  :
        MTPS STATUS       :
        MOV  #100,TTCSR   :
        MOV  #TR3,34      :TRAP
        MOV  #TR4,64      :TTY OUTPUT
        MOV  #TR5,20      :IOT
        MOV  #340,22      :IOT PRIORITY
        MOV  #120,R4      :SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG
        SOB  R4           :TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)
        TRAP              :THE ACT OF TRAPPING LOWER PRIORITY
        IOT               :INTERRUPT SHOULD OCCUR IN PLACE OF IOT TRAP
TR3:    IOT
TR5:    MOV  #231,@#$FATAL :MOVE TO MAILBOX # ***** 231 *****
        INC  (R2)         :SET MSGTYP TO FATAL ERROR
        HALT              :NO INTERRUPT BETWEEN TRAPS,OR WRONG $STESTN
                          : TO SCOPE REPLACE HALT W/ 240
                          : AND REPLACE NEXT INST W/ 725
TR4:    CLR  22          :CLR IOT PRIORITY
        CLR  66
        MOV  #66,64
        MOV  #36,34
        MOV  #22,20

```



```
2237  
2238  
2239 ;*****  
2240 ;TEST 65 THAT 'RESET' GOES TO OUTSIDE WORLD  
2241 ;*****  
2241 011354 005237 000404 TST65: INC @#STESTN ;UPDATE TEST NUMBER  
2242 011360 022737 000065 000404 CMP #65,@#STESTN ;SEQUENCE ERROR?  
2243 011366 001031 BNE TST66-12 ;BR TO ERROR HALT ON SEQ ERROR  
2244 011370 106427 000340 MTPS #340  
2245 011374 012767 000100 166162 MOV #100,TTCSR ;SET INTERRUPT ENABLE  
2246 011402 012767 000100 166150 MOV #100,TRCSR ;SET INTERRUPT ENABLE  
2247 011410 000005 RESET ;SHOULD CLEAR INTERRUPT ENABLE  
2248 011412 012704 000014 MOV #14,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO  
2249 011416 077401 SOB R4 ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE  
2250 011420 032767 000100 166136 BIT #100,TTCSR ;TEST FOR CLEAR  
2251 011426 001405 BEQ 1$  
2252 011430 012737 000232 000402 MOV #232,@#SFATAL ;MOVE TO MAILBOX # ***** 232 *****  
2253 011436 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
2254 011440 000000 HALT ;RESET FAILED TO CLEAR TTCSR  
2255 ; TO SCOPE REPLACE HALT W/ 240  
2256 ; AND REPLACE NEXT INST W/ 752  
2257 011442 032767 000100 166110 1$: BIT #100,TRCSR ;TEST FOR CLEAR  
2258 011450 001405 BEQ TST66  
2259 011452 012737 000233 000402 MOV #233,@#SFATAL ;MOVE TO MAILBOX # ***** 233 *****  
2260 011460 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
2261 011462 000000 HALT ;RESET FAILED TO CLEAR TRCSR,OR WRONG $TESTN  
2262 ; TO SCOPE REPLACE HALT W/ 240  
2263 ; AND REPLACE NEXT INST W/ 741
```

2264  
2265  
2266  
2267  
2268  
2269  
2270  
2271  
2272  
2273  
2274  
2275  
2276  
2277  
2278  
2279  
2280  
2281  
2282  
2283  
2284  
2285  
2286  
2287

011464 005237 000404  
011470 022737 000066 000404  
011476 001014  
011500 012706 000500  
011504 012767 011542 166302  
011512 012746 000020  
011516 012746 011524  
011522 000006  
011524 000005  
011526 000005  
011530  
011530 012737 000234 000402  
011536 005212  
011540 000000  
  
011542 005067 001540  
011546 106467 001534  
011552 012767 000016 166234  
011560 005067 166232

```

:*****
:TEST 66 THAT RESET HAS NO EFFECT ON THE TRACE TRAP
:*****
TST66:  INC    @#STESTN      ;UPDATE TEST NUMBER
        CMP    #66,@#STESTN ;SEQUENCE ERROR?
        BNE   RSTP3        ;BR TO ERROR HALT ON SEQ ERROR
        MOV   #BUFF,%6     ;SET STACK
        MOV   #RESET2,14   ;SET UP TRACE VECTOR
        MOV   #20,-(SP)    ;PUSH T BIT
        MOV   #.+6,-(SP)   ;PUSH PC
        RTT                    ;SET T BIT
        RESET                ;SHOULD HAVE NO EFFECT
        RESET                ;NO EFFECT

RSTP3:  MOV   #234,@#SFATAL ;MOVE TO MAILBOX # ***** 234 *****
        INC   (R2)          ;SET MSGTYP TO FATAL ERROR
        HALT                ;TRACE TRAP FAILED,OR WRONG $TESTN
                                ; TO SCOPE REPLACE HALT W/ 240
                                ; AND REPLACE NEXT INST W/ 756

RESET2: CLR   STATUS       ;CLEAR TRACK
        MTPS  STATUS
        MOV   #16,14
        CLR   16           ;TRACE STATUS

```

```
2288
2289
2290
2291
2292 011564 005237 000404
2293 011570 022737 000067 000404
2294 011576 001103
2295 011600 032767 000001 166612
2296 011606 001402
2297 011610 005767 166572
2298 011614 001106
2299 011616
2300 011616 000005
2301 011620 012704 000012
2302 011624 077401
2303 011626 012706 000500
2304 011632 012767 011700 166224
2305 011640 106427 000000
2306 011644 012767 000357 166214
2307 011652 052767 000100 165704
2308 011660 012704 000120
2309 011664 077401
2310 011666 012737 000235 000402
2311 011674 005212
2312 011676 000000
2313
2314
2315 011700 106767 001402
2316 011704 022767 000357 001374
2317 011712 001405
2318 011714 012737 000236 000402
2319 011722 005212
2320 011724 000000
2321
2322
2323 011726 000005
2324 011730 012704 000012
2325 011734 077401
2326 011736 012706 000500
2327 011742 012767 011774 166114
2328 011750 005067 166112
2329 011754 106427 000017
2330 011760 052767 000100 165576
2331 011766 012704 000120
2332 011772 077401
2333 011774 106767 001306
2334 012000 005767 001302
2335 012004 001405
2336 012006
2337 012006 012737 000237 000402
2338 012014 005212
2339 012016 000000
2340
2341
2342 012020 005067 165540
2343 012024 012767 000066 166032
```

```
*****
;TEST 67 THAT WHEN TTY INTERRUPTS IT POPS NEW STATUS
*****
TST67: INC @#STESTN ;UPDATE TEST NUMBER
        CMP #67,@#STESTN ;SEQUENCE ERROR?
        BNE RSTP4 ;BR TO ERROR HALT ON SEQ ERROR
        BIT #1,$ENV ; CHECK IF ON APT
        BEQ NOAPT2 ; IF NOT ON APT
        TST $PASS ;CHECK IF FIRST PASS
        BNE TST70 ; IF NOT

NOAPT2:
        RESET
        MOV #12,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO
        SOB R4, ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE
        MOV #BUFF,%6 ;SET UP STACK
        MOV #TTY3,64 ;INTERRUPT VECTOR
        MTPS #0
        MOV #357,66 ;HIGH PRIORITY ON INTERRUPT
        BIS #100,TTCSR ;SHOULD SET INTERRUPT ENABLE & INTERRUPT
        MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG
        SOB R4, ;TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)
        MOV #235,@#SFATAL ;MOVE TO MAILBOX # ***** 235 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;NO INTERRUPT
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737

TTY3: MFPS STATUS
        CMP #357,STATUS
        BEQ 1$
        MOV #236,@#SFATAL ;MOVE TO MAILBOX # ***** 236 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INTERRUPT DID NOT POP CORRECT STATUS
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 724

1$: RESET ;CLR INTERRUPT ENABLE
        MOV #2,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO
        SOB R4, ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE
        MOV #BUFF,%6 ;STACK SET UP
        MOV #TTY4,64 ;INTERRUPT VECTOR
        CLR 66 ;CLR NEW STATUS
        MTPS #17
        BIS #100,TTCSR ;SET INTERRUPT ENABLE
        MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11
        SOB R4, ;WAIT FOR TTY INTERRUPT REQUEST TO GET THRU TO CPU (ON T

TTY4: MFPS STATUS
        TST STATUS
        BEQ RSTP4

RSTP4: MOV #237,@#SFATAL ;MOVE TO MAILBOX # ***** 237 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INTERRUPT DID NOT POP CORRECT STATUS,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 667

RST4: CLR TTCSR
        MOV #66,64
```

```

2344
2345 ;THIS ROUTINE TESTS THAT NO LEGAL ADDRESS TRAPS AND THAT AN ILLEGAL
2346 ;ADDRESS TRAPS TO LOCATION 4. THIS WILL RUN ON 30K SYSTEMS. IF SWITCH
2347 ;REGISTER BIT 1 = 0, THEN THE MEMORY FROM 28K-30K IS NOT LOOKED
2348 ;AT, SINCE IT MAY HAVE I/O DEVICES. IF SWR BIT 1 = 1, THEN THAT
2349 ;AREA IS CHECKED. (IT SHOULD EITHER ALL TRAP OR ALL NOT TRAP). LOC 160000
2350 ;IS NO LONGER GUARANTEED TO TRAP, SINCE IT MAY CONTAIN MEMORY. LOCATION
2351 ;177700 (THE UNIBUS ADDRESS FOR R0 ON OLDER SYSTEMS) IS USED FOR FORCING
2352 ;A TIMEOUT IN THE EVENT THAT THERE WAS NONE FROM OK-28K(30K).
2353 ;THIS ROUTINE WILL ALSO WORK IF THERE IS A ROM ABOVE THE HIGHEST
2354 ;MEMORY LOCATION (WHETHER CONTIGUOUS WITH R/W MEMORY OR NOT).
2355 ;*****
2356 ;TEST 70 NON-EXISTENT ADDRESS TRAPS
2357 ;*****
2358 012032 005237 000404 TST70: INC @#STESTN ;UPDATE TEST NUMBER
2359 012036 022737 000070 000404 CMP #70,@#STESTN ;SEQUENCE ERROR?
2360 012044 001141 BNE AUTO1 ;BR TO ERROR HALT ON SEQ ERRGR
2361
2362 ;THIS ROUTINE TESTS MEMORY UNTIL IT DOES A NXM STOP
2363 0 2046 042737 010000 012116 BIC #10000,@#HICORE ;SET HIGH CORE LIMIT TO 160000
2364 012054 032737 000002 000422 BIT #2,@#SWREG ;CHECK IF BIT 1 IS SET
2365 012062 001403 BEQ 1$ ;BRANCH IF IT IS, LEAVE LIMIT 160000
2366 012064 052737 010000 012116 BIS #10000,@#HICORE ;SET UPPER CORE LIMIT TO 30K (170000)
2367 012072 005000 1$: CLR R0
2368 012074 005067 165706 CLR 6
2369 012100 012767 012222 165676 MOV #ATRAP,4 ;SET UP ADDRESS TRAP ENTRANCE
2370 012106 012706 000500 MOV #BUFF,SP
2371 012112 105720 NOR: TSTB (0)+ ;IF OUTSIDE OF CORE, TRAP TO 4
2372 012114 020027 CMP R0,(PC)+ ;IS POINTER INSIDE 28K (30K) CORE
2373 012116 160000 HICORE: .WORD 160000 ;MAY BE CHANGED TO 170000 IF 30K
2374 012120 103774 BLO NOR ;TEST THE REST OF CORE
2375 012122 013737 012116 012266 MOV @#HICORE,@#CORH ;FOR USE BY END OF PASS CHECKER IF ON TIM
2376 012130 012737 012154 000004 MOV #ROTRAP,@#4 ;SET UP NEW VECTOR POINTER
2377 012136 105737 177700 TSTB @#177700 ;CHECK R0 UNIBUS ADDRESS--SHOULD TRAP ON LSI
2378 012142 TRPADR:
2379 012142 012737 000240 000402 MOV #240,@#SFATAL ;MOVE TO MAILBOX # ***** 240 *****
2380 012150 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2381 012152 000000 HALT ;SHOULD HAVE TRAPED
2382 ; TO SCOPE REPLACE HALT W/ 240
2383 ; AND REPLACE NEXT INST W/ 734
2384
2385 ;TRAP TO HERE IF FORCING TRAP BY TESTING 177700
2386 012154 106767 001126 ROTRAP: MFPS STATUS
2387 012160 005767 001122 TST STATUS
2388 012164 001405 BEQ 1$
2389 012166 012737 000241 000402 MOV #241,@#SFATAL ;MOVE TO MAILBOX # ***** 241 *****
2390 012174 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2391 012176 000000 HALT ;NEW PSW SHOULD HAVE BEEN ZERO
2392 ; TO SCOPE REPLACE HALT W/ 240
2393 ; AND REPLACE NEXT INST W/ 722
2394 012200 026727 166270 012142 1$: CMP BUFF-4,#TRPADR
2395 012206 001465 BEQ TRAPB
2396 012210 012737 000242 000402 MOV #242,@#SFATAL ;MOVE TO MAILBOX # ***** 242 *****
2397 012216 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2398 012220 000000 HALT ;OLD PC WAS NOT SAVED
2399 ; TO SCOPE REPLACE HALT W/ 240

```

```

2400 ; AND REPLACE NEXT INST W/ 711
2401 ; RETURN HERE ON AN ADDRESS TRAP FROM MEMORY BELOW 28K (OR 30K)
2402 012222 005300 ATRAP: DEC R0
2403 012224 010067 000036 MOV R0,CORH ; MOVE THE FIRST NXM LOCATION IN CORH
2404 ; THIS ROUTINE DOES NXM TRAPS UNTIL IT FINDS AN EXISTANT MEMORY LOCATION
2405 012230 013700 012116 MOV @WHICORE,R0 ; SET UP THE HIGHEST MEM LOCATION
2406 012234 005300 DEC R0 ; MAKE 1 LESS THAN THE HIGHEST CORE BOUNDARY
2407 012236 000402 BR NOSUB ; DON'T SUBTRACT 1K FIRST TIME
2408 012240 162700 001000 CTRAP: SUB #1000,R0 ; SUBTRACT 1K OCTAL BYTE FROM ADDRESS BECAUSE
2409 ; IT TAKES FOREVER TO GET TIMEOUT ON TIM
2410 012244 012767 012304 165532 NOSUB: MOV #BTRAP,4 ; SET UP THE VECTOR
2411 012252 012706 000500 MOV #BUFF,SP
2412 012256 011005 MOV (R0),R5 ; SAVE THE WORD IF IT IS THERE--TRAP IF NOT
2413 012260 005010 DTRAP1: CLR (R0) ; IT WAS THERE--TRAP IF IT IS ROM
2414 012262 010510 DTRAP2: MOV R5,(R0) ; NOT ROM--RESTORE WORD
2415 012264 020027 CMP R0,(PC)+ ; DO LOCATIONS MATCH?
2416 012266 000000 CORH: .WORD 0
2417 012270 101434 BLOS TRAPB
2418 012272 012737 000243 000402 MOV #243,@#FATAL ; MOVE TO MAILBOX # ***** 243 *****
2419 012300 005212 INC (R2) ; SET MSGTYP TO FATAL ERROR
2420 012302 000000 HALT ; CONTENTS OF R0 SHOULD HAVE BEEN LESS THAN OR EQUAL TO C
2421 ; TO SCOPE REPLACE HALT W/ 240
2422 ; AND REPLACE NEXT INST W/ 660
2423 ; IF THIS COMPARISON FAILS IT MEANS
2424 ; THAT SOME LEGAL ADDRESS TRAPPED OR
2425 ; THAT AN ILLEGAL ADDRESS DID NOT TRAP
2426 012304 106767 000776 BTRAP: MFPS STATUS
2427 012310 005767 000772 TST STATUS
2428 012314 001405 BEQ 1$
2429 012316 012737 000244 000402 MOV #244,@#FATAL ; MOVE TO MAILBOX # ***** 244 *****
2430 012324 005212 INC (R2) ; SET MSGTYP TO FATAL ERROR
2431 012326 000000 HALT ; NEW PSW SHOULD HAVE BEEN ZERO
2432 ; TO SCOPE REPLACE HALT W/ 240
2433 ; AND REPLACE NEXT INST W/ 646
2434 012330 026727 166140 012260 1$: CMP BUFF-4,#DTRAP1
2435 012336 001740 BEQ CTRAP ; BRANCH IF TRAP PC IS OK
2436 012340 026727 166130 012262 CMP BUFF-4,#DTRAP2 ; CHECK IF IT TRAPPED ON THE CLR INSTR
2437 012346 001734 BEQ CTRAP
2438 012350 AUTO1:
2439 012350 012737 000245 000402 MOV #245,@#FATAL ; MOVE TO MAILBOX # ***** 245 *****
2440 012356 005212 INC (R2) ; SET MSGTYP TO FATAL ERROR
2441 012360 000000 HALT ; OLD PC WAS NOT SAVED OR WRONG $TESTN
2442 ; TO SCOPE REPLACE HALT W/ 240
2443 ; AND REPLACE NEXT INST W/ 631
2444 012362 012767 000006 165414 TRAPB: MOV #6,4
2445 012370 005067 165412 CLR 6

```

```
2446  
2447  
2448  
2449  
2450 012374 005237 000404  
2451 012400 022737 000071 000404  
2452 012406 001070  
2453 012410 032767 000001 166002  
2454 012416 001403  
2455 012420 005767 165762  
2456 012424 001066  
2457 012426 042767 000100 165130 NOAPT3:  
2458 012434 012706 000500  
2459 012440 012767 012534 165416  
2460 012446 005067 165414  
2461 012452 105767 165106 WATE1:  
2462 012456 100375  
2463 012460 012767 000015 165100  
2464 012466 105767 165072 WATE2:  
2465 012472 100375  
2466 012474 012767 000015 165064  
2467 012502 052767 000100 165054  
2468 012510 005067 000572  
2469 012514 106467 000566  
2470 012520 000001 WATE3:  
2471 012522 012737 000246 000402  
2472 012530 005212  
2473 012532 000000  
2474  
2475  
2476 012534 106767 000546 WATF:  
2477 012540 005767 000542  
2478 012544 001405  
2479 012546 012737 000247 000402  
2480 012554 005212  
2481 012556 000000  
2482  
2483  
2484 012560 026727 165710 012522 1$:  
2485 012566 001405  
2486 012570  
2487 012570 012737 000250 000402 REES1:  
2488 012576 005212  
2489 012600 000000  
2490  
2491  
2492 012602 042767 000100 164754 REES:  
2493 012610 012767 000066 165246
```

\*\*\*\*\*  
:TEST 71 THE 'WAIT' INSTRUCTION  
\*\*\*\*\*

TST71: INC @#\$STESTN ;UPDATE TEST NUMBER  
CMP #71,@#\$STESTN ;SEQUENCE ERROR?  
BNE REES1 ;BR TO ERROR HALT ON SEQ ERROR  
BIT #1,\$ENV ;CHECK IF ON APT  
BEQ NOAPT3 ;BR, IF NOT ON APT  
TST \$PASS ;CHECK IF FIRST PASS  
BNE REES ;BR, IF NOT  
NOAPT3: BIC #100,TPS ;CLEAR INTERRUPT ENABLE  
MOV #BUFF,SP ;SET UP THE STACK  
MOV #WATE,64 ;SET UP THE INTERRUPT VECTOR  
CLR 66  
WATE1: TSTB TPS ;WAIT FOR READY  
BPL WATE1 ;TO BE UP  
MOV #15,TPB ;DO A CARRIAGE RETURN  
WATE2: TSTB TPS ;WAIT FOR READY TO COME UP  
BPL WATE2  
MOV #15,TPB ;DO ANOTHER CARRIAGE RETURN  
BIS #100,TPS ;SET THE INTERRUPT ENABLE  
CLR STATUS ;CLEAR THE PSW  
MTPS STATUS  
WATE3: WAIT ;WAIT FOR THE INTERRUPT  
MOV #246,@#\$FATAL ;MOVE TO MAILBOX # \*\*\*\*\* 246 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;WAIT INSTRUCTION DID NOT LOOP  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 725  
WATF: MFPS STATUS  
TST STATUS ;IS THE PSW CORRECT?  
BEQ 1\$  
MOV #247,@#\$FATAL ;MOVE TO MAILBOX # \*\*\*\*\* 247 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;NEW PSW SHOULD HAVE BEEN ZERO  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 713  
1\$: CMP BUFF-4,#WATE3+2 ;IS THE OLD PC SAVED  
BEQ REES  
REES1: MOV #250,@#\$FATAL ;MOVE TO MAILBOX # \*\*\*\*\* 250 \*\*\*\*\*  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;OLD PC WAS NOT SAVED OR WRONG \$TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 702  
REES: BIC #100,TPS ;CLEAR THE INTERRUPT ENABLE  
MOV #66,64

```

2494
2495 ;*****
2496 ;TEST 72 ,THAT ODD ADDRESSING WILL IGNORE BIT 0
2497 ;*****
2498 012616 005237 000404 TST72: INC @#$TESTN ;UPDATE TEST NUMBER
2499 012622 022737 000072 000404 CMP #72,@#$TESTN ;SEQUENCE ERROR?
2500 012630 001002 BNE RSTP5 ;RR TO ERROR HALT ON SEQ ERROR
2501 012632 000167 000013 JMP ODD+1
2502 012636 RSTP5:
2503 012636 012737 000251 000402 MOV #251,@#$FATAL ;MOVE TO MAILBOX # ***** 251 *****
2504 012644 005212 inc (R?) ;SET MSGTYP TO FATAL ERROR
2505 012646 000000 HALT ;SHOULD HAVE JUMPED,OR WRONG $TESTN
2506 ; TO SCOPE REPLACE HALT W/ 240
2507 ; AND REPLACF NEXT INST W/ 770
2508 012650 005307 ODD: DEC PC

```

```
2509
2510
2511 ;*****
2512 ;TEST 73 THAT ALL RESERVED INSTRUCTIONS TRAP
2513 ;*****
2513 012652 005237 000404 TST73: INC @#STESTN ;UPDATE TEST NUMBER
2514 012656 022737 000073 000404 CMP #73,@#STESTN ;SEQUENCE ERROR?
2515 012664 001142 BNE RET4 ;BR TO ERROR HALT ON SEQ ERROR
2516 012666 010267 000532 MOV R2,R2STOR ;SAVE REG 2
2517 012672 010700 MOV PC,%0 ;SET THESE
2518 012674 010704 MOV PC,%4 ;REGISTERS
2519 012676 010705 MOV PC,%5 ;TO EXISTENT MEMORY LOCATIONS
2520 012700 012703 013222 MOV #TABLE,R3 ;TABLE POINTER
2521 012704 012302 GIN1: MOV (R3)+,R2 ;FIRST OR CURRENT INSTRUCTION
2522 012706 012301 MOV (R3)+,R1 ;LAST INSTRUCTION OR GROUP
2523 012710 020267 000326 CMP R2,EISFIS ;IS IT THE 'EISFIS' GROUP?
2524 012714 001020 BNE 3$ ;NO
2525 012716 032767 000100 165476 BIT #100,$SWREG ;SUPPRESS EIS/FIS?
2526 012724 001004 BNE 1$ ;BRANCH IF YES
2527 012726 032767 000300 165472 BIT #300,$CPUOP ;DO WE HAVE EISFIS OPTION?
2528 012734 001403 BEQ 2$ ;BRANCH IF NOT
2529 012736 062703 000004 1$: ADD #4,R3 ;IF YES DO NO DO THE
2530 012742 000760 BR GIN1 ;EIS FIS OP CODES
2531 012744 032767 000004 165450 2$: BIT #4,$SWREG ;DO WE HAVE DIS INSTRUCTION SET
2532 012752 001401 BEQ 3$ ;NO
2533 012754 000753 BR GIN1 ;IF YES, DO NOT DO EIS OP CODES - DO JUST FIS
2534 012756 020267 000270 3$: CMP R2,STOP ;IS IT THE STOP GROUP
2535 012762 001007 BNE 4$ ;NO
2536 012764 032767 000020 165430 BIT #20,$SWREG ;DO WE WANT TO DO IT?
2537 012772 001403 BEQ 4$ ;YES
2538 012774 062703 000010 ADD #10,R3 ;SKIP ENTIRE STOP GROUP
2539 013000 000741 BR GIN1 ;NO
2540 013002 020267 000250 4$: CMP R2,DIS ;IS THIS THE DIS GROUP?
2541 013006 001010 BNE 5$ ;NO
2542 013010 032767 000040 165410 BIT #40,$CPUOP ;DIS PRESENT??
2543 013016 001332 BNE GIN1 ;BRANCH IF YES--DON'T DO DIS
2544 013020 032767 000004 165374 BIT #4,$SWREG ;SUPPRESS DIS OPTION?
2545 013026 001326 BNE GIN1 ;BRANCH IF YES, SKIP THE DIS GROUP
2546 013030 020267 000232 5$: CMP R2,STOP1 ;IS IT THE STOP1 GROUP?
2547 013034 001005 BNE 6$ ;NO
2548 013036 032767 000010 165356 BIT #10,$SWREG ;DO WE WANT TO DO IT?
2549 013044 001401 BEQ 6$ ;YES
2550 013046 000716 BR GIN1 ;NO
2551 013050 020267 000216 6$: CMP R2,FINISH ;TESTED ALL
2552 013054 001524 BEQ GIN3 ;BRANCH IF YES, GO TU END OF PASS ROUTINE
2553 013056 010267 000212 MOV R2,INST ;SET UP INST
2554 013062 005267 000206 GIN2: INC INST
2555 013066 012767 013112 164714 MOV #RET,10 ;SET UP RETURN FROM TRAP
2556 013074 012706 000500 MOV #BUFF,SP ;SET UP STACK POINTER
2557 013100 005067 000202 CLR STATUS ;CLEAR PRIORITY
2558 013104 106467 000176 MTPS STATUS
2559 013110 000471 BR INST ;EXECUTE RESERVED INSTRUCTION
2560
2561 ;TRAPPING SHOULD SEND YOU HERE
2562 013112 010267 000102 RET: MOV R2,R2SAVE ;SAVE REG 2
2563 013116 016702 000302 MOV R2STOR,R2 ;RESTORE MAILBOX POINTER
2564 013122 020627 000474 CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
```



```

2565 013126 001405          BEQ      RET1
2566 013130 012737 000252 000402  MOV      #252,@#$FATAL  :MOVE TO MAILBOX # ***** 252 *****
2567 013136 005212          INC      (R2)          :SET MSGTYP TO FATAL ERROR
2568 013140 000000          HALT     :WRONG DECREMENT
2569                               : TO SCOPE REPLACE HALT W/ 240
2570                               : AND REPLACE NEXT INST W/ 651
2571 013142 026727 165326 013276 RET1:  CMP      BUFF-4,#INST+2 :LOC OF INST UNINCREMENTED
2572 013150 001405          BEQ      RET2
2573 013152 012737 000253 000402  MOV      #253,@#$FATAL  :MOVE TO MAILBOX # ***** 253 *****
2574 013160 005212          INC      (R2)          :SET MSGTYP TO FATAL ERROR
2575 013162 000000          HALT     :INST INC ON TRAP
2576                               : TO SCOPE REPLACE HALT W/ 240
2577                               : AND REPLACE NEXT INST W/ 640
2578 013164 005767 165306          RET2:  TST      BUFF-2
2579 013170 001405          BEQ      RET3
2580 013172                               RET4:
2581 013172 012737 000254 000402  MOV      #254,@#$FATAL  :MOVE TO MAILBOX # ***** 254 *****
2582 013200 005212          INC      (R2)          :SET MSGTYP TO FATAL ERROR
2583 013202 000000          HALT     :CONDITION CODES SET ON TRAP OR WRONG $TESTN
2584                               : TO SCOPE REPLACE HALT W/ 240
2585                               : AND REPLACE NEXT INST W/ 630
2586 013204 016702 000010          RET3:  MOV      R2SAVE,R2  :RESTORE REG 2
2587 013210 026701 000060          CMP      INST,R1
2588 013214 001633          BEQ      GIN1          :SET UP NEW GROUP
2589 013216 000721          BR       GIN2          :FINISH OLD GROUP
2590 013220 000000          R2SAVE: .WORD 0
2591                               :END OF INSTRUCTION GROUP
2592 013222 000006          TABLE: 6
2593 013224 000077          77
2594
2595 013226 106477          106477
2596 013230 106677          106677
2597
2598 013232 006777          6777
2599 013234 007777          7777
2600 013236 106777          106777
2601 013240 107777          107777
2602 013242 067777          EISFIS: 67777 :IF WE HAVE THE EIS FIS OPTION
2603 013244 073777          73777 :THEN THE EISFIS GROUP
2604 013246 074777          FIS: 74777 :WILL BE SKIPED
2605 013250 075037          75037
2606 013252 075377          STOP: 75377
2607 013254 076027          76027
2608 013256 076027          DIS: 76027
2609 013260 076057          76057
2610 013262 076057          76057
2611 013264 076777          76777
2612 013266 167777          STOP1: 167777
2613 013270 177777          177777
2614 013272 013272          FINISH: . :END FLAG
2615 013274 000000          INST: HALT :WILL CONTINUE RESERVED INST
2616 013276 000404          BR       TERR
2617 013300 000403          BR       TERR
2618 013302 000402          BR       TERR
2619 013304 000401          BR       TERR
2620 013306 000000          STATUS: 0

```

MAIN. MACY11 30A(1052) 18-SEP-78 12:00 PAGE 66  
 CVKADC.MAC 18-SEP-78 11:54 T73 THAT ALL RESERVED INSTRUCTIONS TRAP

SEQ 0066

|      |        |        |        |        |          |       |                |   |                                     |
|------|--------|--------|--------|--------|----------|-------|----------------|---|-------------------------------------|
| 2621 | 013310 | 016702 | 000110 |        | TERR:    | MOV   | R2STOR,R2      | : | RESTORE R2                          |
| 2622 | 013314 | 012737 | 000255 | 000402 |          | MOV   | #255,@#SFATAL  | : | INDICATE ERROR                      |
| 2623 | 013322 | 005212 |        |        |          | INC   | (R2)           |   |                                     |
| 2624 | 013324 | 000000 |        |        |          | HALT  |                |   |                                     |
| 2625 |        |        |        |        |          |       |                |   |                                     |
| 2626 |        |        |        |        |          |       |                |   |                                     |
| 2627 | 013326 | 005237 | 000406 |        | GIN3:    | INC   | @#SPASS        |   |                                     |
| 2628 | 013332 | 005267 | 000070 |        |          | INC   | PASSPT         | : | SHOULD PRINT THIS PASS?             |
| 2629 | 013336 | 001013 |        |        |          | BNE   | ACT            | : | NO                                  |
| 2630 | 013340 | 032767 | 000040 | 165054 |          | BIT   | #40,\$SWREG    | : | TYPE END OF PASS?                   |
| 2631 | 013346 | 001004 |        |        |          | BNE   | 1\$            | : | BRANCH IF NOT                       |
| 2632 | 013350 | 004567 | 000276 |        |          | JSR   | R5,TYPE        |   |                                     |
| 2633 | 013354 | 013766 |        |        |          |       | EOPMSG         |   |                                     |
| 2634 | 013356 | 000005 |        |        |          | RESET |                |   |                                     |
| 2635 | 013360 | 012767 | 177761 | 000040 | 1\$:     | MOV   | #177761,PASSPT | : | DO IT 1 <sup>st</sup> DECIMAL TIMES |
| 2636 | 013366 | 013700 | 000042 |        | ACT:     | MOV   | @#42,R0        | : | CHECK ACT                           |
| 2637 | 013372 | 001405 |        |        |          | BEQ   | GOAGIN         | : | KEEP GOING                          |
| 2638 | 013374 | 000005 |        |        |          | RESET |                |   |                                     |
| 2639 | 013376 | 004710 |        |        | \$ENDAD: | JSR   | PC,(R0)        | : | ACT HOOKS                           |
| 2640 | 013400 | 000240 |        |        |          | NOP   |                |   |                                     |
| 2641 | 013402 | 000240 |        |        |          | NOP   |                |   |                                     |
| 2642 | 013404 | 000240 |        |        |          | NOP   |                |   |                                     |
| 2643 | 013406 | 012767 | 000012 | 164374 | GCAGIN:  | MOV   | #12,10         |   |                                     |
| 2644 | 013414 | 005067 | 164372 |        |          | CLR   | 12             |   |                                     |
| 2645 | 013420 | 000167 | 165130 |        |          | JMP   | RESTR          | : | DO NEXT PASS                        |
| 2646 | 013424 | 000000 |        |        | R2STOR:  | .WORD | 0              |   |                                     |
| 2647 | 013426 | 177777 |        |        | PASSPT:  | -1    |                |   |                                     |

2648  
 2649  
 2650  
 2651  
 2652  
 2653  
 2654  
 2655  
 2656  
 2657  
 2658  
 2659  
 2660  
 2661  
 2662  
 2663  
 2664  
 2665  
 2666  
 2667  
 2668  
 2669  
 2670  
 2671  
 2672  
 2673  
 2674  
 2675  
 2676  
 2677  
 2678  
 2679  
 2680  
 2681  
 2682  
 2683  
 2684  
 2685  
 2686  
 2687  
 2688  
 2689  
 2690  
 2691  
 2692  
 2693  
 2694  
 2695  
 2696  
 2697  
 2698  
 2699  
 2700

013430 010246  
 013432 010446  
 013434 004567 000212  
 013440 014062  
 013442 012702 013652  
 013446 005042  
 013450 005042  
 013452 005042  
 013454 005042  
 013456 012737 013504 000010  
 013464 075002  
 013466 012737 000012 000010  
 013474 052767 000300 164724  
 013502 000413  
 013504 012737 000012 000010  
 013512 062706 000004  
 013516 042767 000300 164702  
 013524 004567 000122  
 013530 014104  
 013532 004567 000114  
 013536 014111  
 013540 004567 000106  
 013544 014123  
 013546 005002  
 013550 005004  
 013552 012737 013600 000010  
 013560 076030  
 013562 012737 000012 000010  
 013570 052767 000040 164630  
 013576 000413  
 013600 012737 000012 000010  
 013606 042767 000040 164612  
 013614 062706 000004  
 013620 004567 000026  
 013624 014104  
 013626 004567 000020  
 013632 014111  
 013634 012604  
 013636 012602  
 013640 000207  
 013642 000004  
 013652

```

:*****
:SIZE ROUTINE
:*****
SIZE:  MOV    R2,-(SP)      ;SAVE R2
      MOV    R4,-(SP)      ;SAVE R4
      JSR    R5,TYPE      ;TYPE 'EIS/FIS OPTION ''
      FISOPT
      MOV    #FSTACK,R2   ;SET UP FLOATING POINT STACK
      CLR    -(R2)
      CLR    -(R2)
      CLR    -(R2)
      CLR    -(R2)
      MOV    #1$,@#10     ;SET UP RESERVED INSTRUCTION VECTOR
      FADD   R2           ;EXECUTE FLOATING INSTR (0+0)
      MOV    #12,@#10    ;RESTORE VECTOR
      BIS    #300,$CPUOP ;FLAG OPTION PRESENT
      BR     2$
      ;TRAP TO HERE IF NO FIS
1$:    MOV    #12,@#10    ;RESTORE VECTOR
      ADD    #4,SP       ;POP TRAP PC/PSW OFF STACK
      BIC    #300,$CPUOP ;FLAG OPTION NOT PRESENT
      JSR    R5,TYPE      ;TYPE 'NOT ''
      NOT
2$:    JSR    R5,TYPE      ;TYPE 'PRESENT<CR>''
      PRESENT
      JSR    R5,TYPE      ;TYPE 'DIBOL INSTRUCTION SET ''
      DISOPT
      CLR    R2          ;SET UP A 0 DEST LENGTH
      CLR    R4          ;HIGH BYTE MUST BE CLEAR
      MOV    #3$,@#10   ;SET UP TRAP VECTOR
      MOVC   ;MOVE CHARACTER INSTRUCTION
      MOV    #12,@#10   ;RESTORE VECTOR
      BIS    #40,$CPUOP ;FLAG OPTION PRESENT
      BR     4$
      ;TRAP HERE IF NO DIS
3$:    MOV    #12,@#10    ;RESTORE VECTOR
      BIC    #40,$CPUOP  ;FLAG NO OPTION
      ADD    #4,SP       ;POP TRAP PC/PSW OFF STACK
      JSR    R5,TYPE      ;TYPE 'NOT ''
      NOT
4$:    JSR    R5,TYPE      ;'PRESENT<CR>''
      PRESENT
      MOV    (SP)+,R4    ;RESTORE REGISTERS USED
      MOV    (SP)+,R2
      RTS    PC
      ;FLOATING POINT STACK FOR FIS
      .BLKW 4
FSTACK:
    
```

```

2701 ;*****
2702 ;TYPE ROUTINE
2703 ;*****
2704
2705 013652 010046 TYPE: MOV R0,-(SP) ;SAVE REGISTER
2706 013654 012500 MOV (R5)+,R0 ;GET TEXT POINTER
2707 013656 132767 000040 164535 BITB #40,$ENVM ;SUPPRESS OUTPUT??
2708 013664 001011 BNE 3$ ;BRANCH IF YES--RETURN
2709 013666 105737 177564 1$: TSTB @#TPS ;WAIT FOR TTY READY
2710 013672 100375 BPL 1$
2711 013674 112037 177566 MOVB (R0)+,@#TPB ;TYPE CHARACTER
2712 013700 001372 BNE 1$ ;BRANCH IF IT WAS NOT TERMINATOR
2713 013702 105737 177564 2$: TSTB @#TPS ;WAIT FOR READY
2714 013706 100375 BPL 2$
2715 013710 012600 3$: MOV (SP)+,R0 ;RESTORE REGISTER
2716 013712 000205 RTS R5
2717
2718 ;*****
2719 ;POWER FAIL ROUTINE
2720 ;*****
2721
2722 013714 012767 013724 164102 PWRDWN: MOV #PWRUP,24
2723 013722 000000 HALT
2724
2725 013724 012767 013714 164072 PWRUP: MOV #PWRDWN,24
2726 013732 012706 000500 MOV #BUFF,SP
2727 013736 005000 CLR R0 ;SET UP A DELAY
2728 013740 012701 177750 MOV #-30,R1
2729 013744 005200 1$: INC R0
2730 013746 001376 BNE 1$
2731 013750 005201 INC R1
2732 013752 001374 BNE 1$
2733 013754 004567 177672 JSR R5,TYPE ;TYPE POWER FAIL MESSAGE
2734 013760 014004 MSGPWF
2735 013762 000167 164514 JMP START
2736
2737 013766 005015 047105 020104 EOPMSG: .ASCIZ <15><12>.END OF PASS.
2738 013774 043117 050040 051501
2739 014002 000123
2740 014004 005015 047520 042527 MSGPWF: .ASCIZ <15><12>.POWER FAILED!.
2741 014012 020122 040506 046111
2742 014020 042105 000041
2743 014024 005015 053103 040513 TITLE: .ASCIZ <15><12>.CVKADC0 LSI-11 TRAPS TEST.<15><12>
2744 014032 041504 020060 051514
2745 014040 026511 030461 052040
2746 014046 040522 051520 052040
2747 014054 051505 006524 000012
2748 014062 005015 044505 027523 FISOPT: .ASCIZ <15><12>.EIS/FIS OPTION .
2749 014070 044506 020123 050117
2750 014076 044524 047117 000040
2751 014104 047516 020124 000
2752 014111 120 042522 042523 NOT: .ASCIZ .NOT .
2753 014116 052116 005015 000 PRESENT: .ASCIZ .PRESENT.<15><12>
2754 014123 104 041111 046117 DISOPT: .ASCIZ .DIBOL INSTRUCTION SET .
2755 014130 044440 051516 051124
2756 014136 041525 044524 047117

```

.MAIN. MACY11 30A(1052) 18-SEP-78 12:00 PAGE 69  
CVKADC.MAC 18-SEP-78 11:54 T73 THAT ALL RESERVED INSTRUCTIONS TRAP

SEQ 0069

2757 014144 051440 052105 000040  
2758 000001

.END





|         |        |       |       |       |      |
|---------|--------|-------|-------|-------|------|
| PRESEN  | 014111 | 2674  | 2692  | 2752# |      |
| PWRDWIN | 013714 | 352   | 2722# | 2725  |      |
| PWRUP   | 013724 | 2722  | 2725# |       |      |
| RA      | 005412 | 1397  | 1406# |       |      |
| RA1     | 003636 | 1033  | 1042# |       |      |
| RB      | 005376 | 1396* | 1399# | 1406* | 1407 |
| RBBB    | 005400 | 1395  | 1400# |       |      |
| RB1     | 003622 | 1032* | 1035# | 1042* | 1043 |
| RB1AA   | 003624 | 1031  | 1036# |       |      |
| RC      | 005372 | 1398# | 1408  |       |      |
| RC1     | 003616 | 1034# | 1044  |       |      |
| REES    | 02602  | 2456  | 2485  | 2492# |      |
| REES1   | 012570 | 2452  | 2486# |       |      |
| RESET2  | 011542 | 2272  | 2284# |       |      |
| RESTR1  | 000554 | 366#  | 2645  |       |      |
| RET     | 013112 | 2555  | 2562# |       |      |
| RETA    | 002154 | 691   | 698#  |       |      |
| RETAT   | 010014 | 1941  | 1951# |       |      |
| RETA1   | 003010 | 864   | 871#  |       |      |
| RETA2   | 003726 | 1054  | 1061# |       |      |
| RETA3   | 004562 | 1227  | 1234# |       |      |
| RETA4   | 005502 | 1418  | 1425# |       |      |
| RETA5   | 006336 | 1599  | 1606# |       |      |
| RETB    | 002204 | 706   | 708#  |       |      |
| RETB1   | 010070 | 1959  | 1969# |       |      |
| RETB2   | 003040 | 879   | 881#  |       |      |
| RETB3   | 003756 | 1069  | 1071# |       |      |
| RETB4   | 004612 | 1242  | 1244# |       |      |
| RETB5   | 005532 | 1434  | 1436# |       |      |
| RETB6   | 006366 | 1614  | 1616# |       |      |
| RETC    | 002254 | 723   | 725#  |       |      |
| RETC1   | 010150 | 1984  | 1989# |       |      |
| RETC2   | 003110 | 895   | 897#  |       |      |
| RETC3   | 004025 | 1086  | 1088# |       |      |
| RETC4   | 004662 | 1258  | 1260# |       |      |
| RETC5   | 005602 | 1450  | 1452# |       |      |
| RETC6   | 006436 | 1631  | 1633# |       |      |
| RETD    | 002340 | 739   | 744#  |       |      |
| RETD1   | 003174 | 912   | 917#  |       |      |
| RETD2   | 004112 | 1102  | 1107# |       |      |
| RETD3   | 004746 | 1275  | 1280# |       |      |
| RETD4   | 005666 | 1467  | 1472# |       |      |
| RETD5   | 006522 | 1647  | 1652# |       |      |
| RETE    | 002412 | 752   | 757#  |       |      |
| RETE1   | 003244 | 925   | 929#  |       |      |
| RETE2   | 004164 | 1115  | 1120# |       |      |
| RETE3   | 005020 | 1288  | 1293# |       |      |
| RETE4   | 005740 | 1480  | 1485# |       |      |
| RETE5   | 006574 | 1660  | 1665# |       |      |
| RETF    | 002470 | 772   | 775#  |       |      |
| RETF1   | 003322 | 944   | 947#  |       |      |
| RETF2   | 004242 | 1135  | 1138# |       |      |
| RETF3   | 005076 | 1308  | 1311# |       |      |
| RETF4   | 006016 | 1500  | 1503# |       |      |
| RETF5   | 006652 | 1680  | 1683# |       |      |
| RETG    | 002620 | 812   | 815#  |       |      |







|         |        |       |       |       |       |       |       |       |       |       |       |       |       |       |
|---------|--------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| TST42   | 006406 | 1612  | 1617  | 1627# |       |       |       |       |       |       |       |       |       |       |
| TST43   | 006460 | 1629  | 1634  | 1643# |       |       |       |       |       |       |       |       |       |       |
| TST44   | 006616 | 1645  | 1666  | 1676# |       |       |       |       |       |       |       |       |       |       |
| TST45   | 007112 | 1678  | 1754  | 1764# |       |       |       |       |       |       |       |       |       |       |
| TST46   | 007154 | 1766  | 1779# |       |       |       |       |       |       |       |       |       |       |       |
| TST47   | 007224 | 1781  | 1786  | 1796# |       |       |       |       |       |       |       |       |       |       |
| TST5    | 002112 | 619   | 677   | 687#  |       |       |       |       |       |       |       |       |       |       |
| TST50   | 007276 | 1798  | 1803  | 1813# |       |       |       |       |       |       |       |       |       |       |
| TST51   | 007434 | 1815  | 1836  | 1846# |       |       |       |       |       |       |       |       |       |       |
| TST52   | 007740 | 1937# |       |       |       |       |       |       |       |       |       |       |       |       |
| TST53   | 010014 | 1939  | 1955# |       |       |       |       |       |       |       |       |       |       |       |
| TST54   | 010110 | 1957  | 1970  | 1980# |       |       |       |       |       |       |       |       |       |       |
| TST55   | 010172 | 1982  | 1990  | 2001# |       |       |       |       |       |       |       |       |       |       |
| TST56   | 010252 | 2003  | 2012  | 2024# |       |       |       |       |       |       |       |       |       |       |
| TST57   | 010362 | 2026  | 2056# |       |       |       |       |       |       |       |       |       |       |       |
| TST6    | 002154 | 689   | 702#  |       |       |       |       |       |       |       |       |       |       |       |
| TST60   | 010456 | 2058  | 2073  | 2083# |       |       |       |       |       |       |       |       |       |       |
| TST61   | 010606 | 2117# |       |       |       |       |       |       |       |       |       |       |       |       |
| TST62   | 010710 | 2119  | 2133  | 2142# |       |       |       |       |       |       |       |       |       |       |
| TST63   | 010776 | 2144  | 2164# |       |       |       |       |       |       |       |       |       |       |       |
| TST64   | 011162 | 2170  | 2204# |       |       |       |       |       |       |       |       |       |       |       |
| TST65   | 011354 | 2210  | 2241# |       |       |       |       |       |       |       |       |       |       |       |
| TST66   | 011464 | 2243  | 2258  | 2268# |       |       |       |       |       |       |       |       |       |       |
| TST67   | 011564 | 2292# |       |       |       |       |       |       |       |       |       |       |       |       |
| TST7    | 002224 | 704   | 709   | 719#  |       |       |       |       |       |       |       |       |       |       |
| TST70   | 012032 | 2298  | 2358# |       |       |       |       |       |       |       |       |       |       |       |
| TST71   | 012374 | 2450# |       |       |       |       |       |       |       |       |       |       |       |       |
| TST72   | 012616 | 2498# |       |       |       |       |       |       |       |       |       |       |       |       |
| TST73   | 012652 | 2513# |       |       |       |       |       |       |       |       |       |       |       |       |
| TTCSR = | 177564 | 251#  | 2178* | 2212* | 2216* | 2245* | 2250  | 2307* | 2330* | 2342* |       |       |       |       |
| TTY3    | 011700 | 2304  | 2315# |       |       |       |       |       |       |       |       |       |       |       |
| TTY4    | 011774 | 2327  | 2333# |       |       |       |       |       |       |       |       |       |       |       |
| TYPE    | 013652 | 358   | 2632  | 2655  | 2671  | 2673  | 2676  | 2689  | 2691  | 2705# | 2733  |       |       |       |
| WATE    | 012534 | 2459  | 2476# |       |       |       |       |       |       |       |       |       |       |       |
| WATE1   | 012452 | 2461# | 2462  |       |       |       |       |       |       |       |       |       |       |       |
| WATE2   | 012466 | 2464# | 2465  |       |       |       |       |       |       |       |       |       |       |       |
| WATE3   | 012520 | 2470# | 2484  |       |       |       |       |       |       |       |       |       |       |       |
| \$APTHD | 000450 | 330   | 336#  |       |       |       |       |       |       |       |       |       |       |       |
| \$CPUOP | 000426 | 295#  | 2527  | 2542  | 2665* | 2670* | 2683* | 2687* |       |       |       |       |       |       |
| \$DEVCT | 000410 | 286#  |       |       |       |       |       |       |       |       |       |       |       |       |
| \$ENDAD | 013376 | 273   | 2639# |       |       |       |       |       |       |       |       |       |       |       |
| \$ENV   | 000420 | 291#  | 2167  | 2207  | 2295  | 2453  |       |       |       |       |       |       |       |       |
| \$ENVM  | 000421 | 292#  | 360   | 2707  |       |       |       |       |       |       |       |       |       |       |
| \$ERN = | 000255 | 234#  | 393   | 394#  | 403   | 404#  | 413   | 414#  | 424   | 425#  | 435   | 436#  | 446   | 447#  |
|         |        | 457   | 458#  | 467   | 468#  | 486   | 487#  | 499   | 500#  | 512   | 513#  | 525   | 526#  | 538   |
|         |        | 539#  | 552   | 553#  | 560   | 561#  | 569   | 570#  | 577   | 578#  | 584   | 585#  | 592   | 593#  |
|         |        | 600   | 601#  | 608   | 609#  | 624   | 625#  | 631   | 632#  | 638   | 639#  | 645   | 646#  | 657   |
|         |        | 658#  | 664   | 665#  | 671   | 672#  | 678   | 679#  | 693   | 694#  | 710   | 711#  | 727   | 728#  |
|         |        | 746   | 747#  | 759   | 760#  | 777   | 778#  | 784   | 785#  | 791   | 792#  | 798   | 799#  | 806   |
|         |        | 807#  | 817   | 818#  | 824   | 825#  | 831   | 832#  | 838   | 839#  | 849   | 850#  | 866   | 867#  |
|         |        | 883   | 884#  | 899   | 900#  | 919   | 920#  | 931   | 932#  | 949   | 950#  | 956   | 957#  | 963   |
|         |        | 964#  | 970   | 971#  | 978   | 979#  | 989   | 990#  | 996   | 997#  | 1003  | 1004# | 1010  | 1011# |
|         |        | 1020  | 1021# | 1037  | 1038# | 1056  | 1057# | 1073  | 1074# | 1090  | 1091# | 1109  | 1110# | 1122  |
|         |        | 1123# | 1140  | 1141# | 1147  | 1148# | 1154  | 1155# | 1161  | 1162# | 1169  | 1170# | 1180  | 1181# |
|         |        | 1187  | 1188# | 1194  | 1195# | 1201  | 1202# | 1212  | 1213# | 1229  | 1230# | 1246  | 1247# | 1262  |
|         |        | 1263# | 1282  | 1283# | 1295  | 1296# | 1313  | 1314# | 1320  | 1321# | 1327  | 1328# | 1334  | 1335# |

CROSS REFERENCE TABLE -- USER SYMBOLS

|       |       |       |       |       |       |       |       |       |       |       |       |       |
|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| 1342  | 1343# | 1353  | 1354# | 1360  | 1361# | 1367  | 1368# | 1374  | 1375# | 1384  | 1385# | 1401  |
| 1402# | 1420  | 1421# | 1438  | 1439# | 1454  | 1455# | 1474  | 1475# | 1487  | 1488# | 1505  | 1506# |
| 1512  | 1513# | 1519  | 1520# | 1526  | 1527# | 1534  | 1535# | 1545  | 1546# | 1552  | 1553# | 1559  |
| 1560# | 1566  | 1567# | 1577  | 1578# | 1601  | 1602# | 1618  | 1619# | 1635  | 1636# | 1654  | 1655# |
| 1667  | 1668# | 1685  | 1686# | 1692  | 1693# | 1699  | 1700# | 1706  | 1707# | 1714  | 1715# | 1725  |
| 1726# | 1732  | 1733# | 1739  | 1740# | 1746  | 1747# | 1755  | 1756# | 1770  | 1771# | 1787  | 1788# |
| 1804  | 1805# | 1824  | 1825# | 1837  | 1838# | 1855  | 1856# | 1862  | 1863# | 1869  | 1870# | 1876  |
| 1877# | 1884  | 1885# | 1895  | 1896# | 1902  | 1903# | 1909  | 1910# | 1916  | 1917# | 1926  | 1927# |
| 1946  | 1947# | 1964  | 1965# | 1971  | 1972# | 1991  | 1992# | 2013  | 2014# | 2037  | 2038# | 2046  |
| 2047# | 2066  | 2067# | 2074  | 2075# | 2098  | 2099# | 2106  | 2107# | 213   | 2128# | 2128# | 2135# |
| 2152  | 2153# | 2188  | 2189# | 2194  | 2195# | 2226  | 2227# | 2252  | 2253# | 2259  | 2260# | 2279  |
| 2280# | 2310  | 2311# | 2318  | 2319# | 2337  | 2338# | 2379  | 2380# | 2389  | 2390# | 2396  | 2397# |
| 241R  | 2419# | 2429  | 2430# | 2439  | 2440# | 2471  | 2472# | 2479  | 2480# | 2487  | 2488# | 2503  |
| 2504# | 2566  | 2567# | 2573  | 2574# | 2581  | 2582# |       |       |       |       |       |       |
| 262#  | 369*  |       |       |       |       |       |       |       |       |       |       |       |
| 290#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 319#  | 342   |       |       |       |       |       |       |       |       |       |       |       |
| 262   | 283#  | 393*  | 403*  | 413*  | 424*  | 435*  | 446*  | 457*  | 467*  | 486*  | 499*  | 512*  |
| 525*  | 538*  | 552*  | 560*  | 569*  | 577*  | 584*  | 592*  | 600*  | 608*  | 624*  | 631*  | 638*  |
| 645*  | 657*  | 664*  | 671*  | 678*  | 693*  | 710*  | 727*  | 746*  | 759*  | 777*  | 784*  | 791*  |
| 798*  | 806*  | 817*  | 824*  | 831*  | 838*  | 849*  | 866*  | 883*  | 899*  | 919*  | 931*  | 949*  |
| 956*  | 963*  | 970*  | 978*  | 989*  | 996*  | 1003* | 1010* | 1020* | 1037* | 1056* | 1073* | 1090* |
| 1109* | 1122* | 1140* | 1147* | 1154* | 1161* | 1169* | 1180* | 1187* | 1194* | 1201* | 1212* | 1229* |
| 1246* | 1262* | 1282* | 1295* | 1313* | 1320* | 1327* | 1334* | 1342* | 1353* | 1360* | 1367* | 1374* |
| 1384* | 1401* | 1420* | 1438* | 1454* | 1474* | 1487* | 1505* | 1512* | 1519* | 1526* | 1534* | 1545* |
| 1552* | 1559* | 1566* | 1577* | 1601* | 1618* | 1635* | 1654* | 1667* | 1685* | 1692* | 1699* | 1706* |
| 1714* | 1725* | 1732* | 1739* | 1746* | 1755* | 1770* | 1787* | 1804* | 1824* | 1837* | 1855* | 1862* |
| 1869* | 1876* | 1884* | 1895* | 1902* | 1909* | 1916* | 1926* | 1946* | 1964* | 1971* | 1991* | 2013* |
| 2037* | 2046* | 2066* | 2074* | 2098* | 2106* | 2127* | 2134* | 2152* | 2188* | 2194* | 2226* | 2252* |
| 2259* | 2279* | 2310* | 2318* | 2337* | 2379* | 2389* | 2396* | 2418* | 2429* | 2439* | 2471* | 2479* |
| 2487* | 2503* | 2566* | 2573* | 2581* | 2622* |       |       |       |       |       |       |       |
| 337#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 308#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 312#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 315#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 318#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 281#  | 338   | 342   |       |       |       |       |       |       |       |       |       |       |
| 302#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 310#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 313#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 316#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 338#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 288#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 289#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 282#  | 366   | 367*  |       |       |       |       |       |       |       |       |       |       |
| 303#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 311#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 314#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 317#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 285#  | 348*  | 2169  | 2209  | 2297  | 2455  | 2627* |       |       |       |       |       |       |
| 340#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 271#  | 276   |       |       |       |       |       |       |       |       |       |       |       |
| 234#  |       |       |       |       |       |       |       |       |       |       |       |       |
| 293#  | 2364  | 2525  | 2531  | 2536  | 2544  | 2548  | 2630  |       |       |       |       |       |
| 261   | 284#  | 386*  | 387   | 476*  | 477   | 547*  | 548   | 617*  | 618   | 687*  | 688   | 702*  |
| 703   | 719*  | 720   | 735*  | 736   | 768*  | 769   | 860*  | 861   | 875*  | 876   | 891*  | 892   |

\$ERROR= 000402  
\$ETABL 000420  
\$ETEND 000450  
\$FATAL 000402

\$HIBTS 000450  
\$MADR1 000432  
\$MADR2 000436  
\$MADR3 000442  
\$MADR4 000446  
\$MAIL 000400  
\$MAMS1 000430  
\$MAMS2 000434  
\$MAMS3 000440  
\$MAMS4 000444  
\$MBADR 000452  
\$MSGAD 000414  
\$MSGLG 000416  
\$MSGTY 000400  
\$MTYP1 000431  
\$MTYP2 000435  
\$MTYP3 000441  
\$MTYP4 000445  
\$PASS 000406  
\$PASTM 000456  
\$SVPC = 000400  
\$SWR = 000000  
\$SWREG 000422  
\$TESTN 000404







|          |      |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
|----------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
|          | 1566 | 1577 | 1601 | 1618 | 1635 | 1654 | 1667 | 1685 | 1692 | 1699 | 1706 | 1714 | 1725 | 1732 | 1739 |
|          | 1746 | 1755 | 1770 | 1787 | 1804 | 1824 | 1837 | 1855 | 1862 | 1869 | 1876 | 1884 | 1895 | 1902 | 1909 |
|          | 1916 | 1926 | 1946 | 1964 | 1971 | 1991 | 2013 | 2037 | 2046 | 2066 | 2074 | 2098 | 2106 | 2127 | 2134 |
|          | 2152 | 2188 | 2194 | 2226 | 2252 | 2259 | 2279 | 2310 | 2318 | 2337 | 2379 | 2389 | 2396 | 2418 | 2429 |
| \$\$ERRO | 234# | 466  | 537  | 568  | 607  | 677  | 709  | 726  | 758  | 882  | 898  | 930  | 1019 | 1072 | 1089 |
|          | 1121 | 1245 | 1261 | 1294 | 1383 | 1437 | 1453 | 1486 | 1617 | 1634 | 1666 | 1754 | 1786 | 1803 | 1836 |
| \$\$LOOP | 1970 | 1990 | 2012 | 2073 | 2133 | 2258 |      |      |      |      |      |      |      |      |      |
|          | 234# | 396  | 406  | 416  | 427  | 438  | 449  | 460  | 470  | 489  | 502  | 515  | 528  | 541  | 555  |
|          | 563  | 572  | 580  | 587  | 595  | 603  | 611  | 627  | 634  | 641  | 648  | 660  | 667  | 674  | 681  |
|          | 696  | 713  | 730  | 749  | 762  | 780  | 787  | 794  | 801  | 809  | 820  | 827  | 834  | 841  | 852  |
|          | 869  | 886  | 902  | 922  | 934  | 952  | 959  | 966  | 973  | 981  | 992  | 999  | 1006 | 1013 | 1023 |
|          | 1040 | 1059 | 1076 | 1093 | 1112 | 1125 | 1143 | 1150 | 1157 | 1164 | 1172 | 1183 | 1190 | 1197 | 1204 |
|          | 1215 | 1232 | 1249 | 1265 | 1285 | 1298 | 1316 | 1323 | 1330 | 1337 | 1345 | 1356 | 1363 | 1370 | 1377 |
|          | 1387 | 1404 | 1423 | 1441 | 1457 | 1477 | 1490 | 1508 | 1515 | 1522 | 1529 | 1537 | 1548 | 1555 | 1562 |
|          | 1569 | 1580 | 1604 | 1621 | 1638 | 1657 | 1670 | 1688 | 1695 | 1702 | 1709 | 1717 | 1728 | 1735 | 1742 |
|          | 1749 | 1758 | 1773 | 1790 | 1807 | 1827 | 1840 | 1858 | 1865 | 1872 | 1879 | 1887 | 1898 | 1905 | 1912 |
|          | 1919 | 1929 | 1949 | 1967 | 1974 | 1994 | 2016 | 2040 | 2049 | 2069 | 2077 | 2101 | 2109 | 2130 | 2137 |
|          | 2155 | 2191 | 2197 | 2229 | 2255 | 2262 | 2282 | 2313 | 2321 | 2340 | 2382 | 2392 | 2399 | 2421 | 2432 |
| \$\$N    | 2442 | 2474 | 2482 | 2490 | 2506 | 2569 | 2576 | 2584 |      |      |      |      |      |      |      |
|          | 234# | 383  | 473  | 544  | 614  | 684  | 699  | 716  | 732  | 765  | 857  | 872  | 888  | 905  | 937  |
|          | 1026 | 1047 | 1062 | 1079 | 1095 | 1128 | 1220 | 1235 | 1251 | 1268 | 1301 | 1390 | 1411 | 1427 | 1443 |
|          | 1450 | 1493 | 1592 | 1607 | 1624 | 1640 | 1673 | 1761 | 1776 | 1793 | 1810 | 1843 | 1934 | 1952 | 1977 |
|          | 1998 | 2021 | 2053 | 2080 | 2114 | 2139 | 2161 | 2201 | 2238 | 2265 | 2289 | 2355 | 2447 | 2495 | 2510 |
| .\$ACT1  | 264# | 267  |      |      |      |      |      |      |      |      |      |      |      |      |      |
| .\$APT8  | 264# | 277  |      |      |      |      |      |      |      |      |      |      |      |      |      |
| .\$APTH  | 264# | 321  |      |      |      |      |      |      |      |      |      |      |      |      |      |

. ABS. 014152 000

ERRORS DETECTED: 0

CVKADC.CVKADC.SEQ/CRF/SOL/NL:TOC=CVKADC.MAC  
 RUN-TIME: 17 11 1 SECONDS  
 RUN-TIME RATIO: 126/30=4.1  
 CORE USED: 10K ( 9 PAGES)