

PDP-11 Family Projection

4/3/69

MODEL	CP	LOGICAL POWER	ARITH POWER	SPEED	PRICE	CONFIGURATION	SOFTWARE PAPER TAPE	DISK
PDP-11/10		.7	.7	2-3u	4K	Technilogically cost reduced 11/20 with MOS		
PDP-11/20	KA11	1	1	2.2u	5.2K	CP, 1KBROM, 128 by R/W, Turnkey Console		
PDP-11/30	KA11	1	1	2.2u	9.3K	CP, 8KB Core, Console, TTY	Assembler, Editor, Math Utility FOCAL, BASIC, (ASA Basic Fortran) ³	8-like monitor (syst.builder w/ODT, DOT, PIP) ²
PDP-11/40	KB11	2 ¹	10-20	1.2u	13K	adds *, $\sqrt{\quad}$, normal-ize, etc. Possible micro-programmed processor, no EAE saves \$1000	Possible ^{16KB} 8K Fortran IV Improved Assembler	Fortran IV
PDP-11/45	KB11	2 ¹	10-20	1.2u	15K + Disk	11/45 with memory protect/relocate max core 262KB, Max phys memory (using disk) 2 ²² Bytes		Super Monitor ⁴ 65KB virtual mem/user for either small or large Disk
PDP-11/50	KC11	2 ¹	50-100	1.2u	25K	adds hardware floating point 32 bit processor, 16 bit memory (16KB)		
PDP-11/55	KC11	2 ¹	50-100	1.2u	27K + Disk	with memory protect/relocate		
PDP-11/65	KD11	4	100-200	1.2u/ 32 bit	45K + Disk	32 bit separate memory bus 32 bit processor.		

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NOTES:

1. If microprogrammed, then logical power could be tailored to user and go to 20-50, 40-100 for 11/65.
2. Business language system under consideration.
3. Possible by-product of FOCAL.
4. Super monitor for 11-45, 55, 65 is priority multi-user real-time system.