



# INTEROFFICE MEMORANDUM

NO: RC:69:15

DATE: February 11, 1969

SUBJECT: PDP-11 Instruction Set Errata Sheet

TO:

FROM: R. Cady

Page No. 1

## II Storage Organization

### Paragraph No. 2

Reads: "Words 0 through 7F are called Page 0"  
Change to: Bytes 0 through 7F are called Page 0.

Reads: "Words 0 through FF may be used for interrupt status"  
Change to: Bytes 0 through FF may be used for interrupt status.

Page No. 6

## E. Add Byte (ADB)

### Paragraph No. 1

Reads: "with sign extended,"  
Change to: "with sign extended,\*"

Reads: "L is complemented if the operation causes a carry out of Bit Zero."  
Change to: "L is complemented if the operation causes a carry out of Bit 15."

## F. Add Word (ADW)

### Paragraph No. 1

Reads: "L is complemented if the operation causes a carry out of Bit Zero."  
Change to: "L is complemented if the operation causes a carry out of Bit 15."

Page No. 6 Continued.

G. Compare Byte (CPB)

Paragraph No. 1

Reads: "The effective byte is treated as a 2's complement 8-bit quantity."

Change to: "The effective byte is treated as a 2's complement 8-bit quantity with sign extended.\*"

Reads: "L is complemented if the operation causes a carry out at bit zero."

Change to: "L is complemented if the operation causes a carry out at bit 15."

H. Compare Word (CPW)

Paragraph No. 1

Reads: "L is complemented if the operation causes a carry out at bit zero."

Change to: "L is complemented if the operation causes a carry out at bit 15."

Add as footnote to Page 6:

\*"With sign extended" means the sign bit (Bit 7) of the byte is propagated into bits 8-15 to form a 16 bit quantity.

On the following pages, (Nos. 7 and 10) record the change as follows:

Reads: "L is complemented if the operation causes a carry out of Bit Zero."

Change to: "L is complemented if the operation causes a carry out of Bit 15."

Page No. 7

K. Increment (INC)

IV. Operate Instruction Group

B. Increment Accumulator (IAC)

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Page No. 10

VIII. Add to Register Group

- A. Add to M (ATM)
- B. Add to N (ATN)
- C. Add to X-Register (ATX)
- D. Add to Y-Register (ATY)