

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION			REFERENCE DESIGNATOR
					BC	BF	BH	
1	1	B-DD-M8743-BA		DWG DIRECTORY	REF	REF	REF	
2	2	D-UA-M8743-BA-0		UNIT ASSY	REF	REF	REF	
3	3	D-MD-5015117-0-0		CIRCUIT DRILL & ETCH	REF	REF	REF	
4	4	D-EC-5015117-BA-0		ETCH CUT DWG	REF	REF	REF	
5	5	D-CS-M8743-BA-MS01		CIRCUIT SCHEMATIC	REF	REF	REF	
6	6		1105275-00	D 672 TR= 15NS PIV= 60V SI	1	1	1	D1
7	7		5015117-00	DRILL AND ETCH BRD.	1	1	1	
8	8		1005306-00	6.8MFD 35V 10% S.TANT	1	1	1	C1
9	9		1012084-03	150 MFD 15V +75-10% AL EL	2	2	2	C5,C6
10	10		1018200-00	220.0 MMF 50V 1% CER	2	2	2	C2,C3
11	11		1014265-02	.33 MFD 50V +80-20% CER	142	142	142	C4,C7-C147
12	12		1117373-00	LED ASSY GREEN	1	1	1	D3
13	13		1117373-01	LED ASSY RED	1	1	1	D2
14	14		1211164-00	SW,DIP 4POS/1PST 5VDC100MA F	1	1	1	E28
15	15		1211164-04	SW,DIP 08POS/1PST 5VDC100MA F	1	1	1	E38
16	16		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1	1	1	
17	17		1300229-00	100.0 .25 W 5.0 % CF	2	2	2	R5,R22
18	18		1300316-00	470.0 .25 W 5.0 % CF	2	2	2	R3,R4
19	19		1300447-00	4.70 K .25 W 5.0 % CF	3	3	3	R11,R12,R18
20	20		1301322-00	180.0 .25 W 5.0 % CF	1	1	1	R19
21	21		1301808-00	22.0 K .25 W 5.0 % CF	1	1	1	R1
22	22		1302124-00	18.0 .25 W 5.0 % CF	1	1	1	R2
23	23		1302128-00	17.80 .50 W 1.0 % RN60E-F 2	1	1	1	R16
24	24		1302858-00	100.0 .25 W 1.0 % RN55D-F10	1	1	1	R7
25	25		1303064-00	75.0 .25 W 1.0 % RN55D-F10	1	1	1	R6
26	26		1304858-00	348.0 .25 W 1.0 % RN55D-F10	1	1	1	R8
27	27		1305123-00	215.0 .25 W 1.0 % RN55D-F10	1	1	1	R9
28	28		1311522-00	200.0 .25 W 5.0 % CF	1	1	1	R13
29	29		1316395-00	R NETWORK 9-4.7K 2.0 % 10PIN	5	5	5	R14,R15,R23,R25,R26
30	30		1316395-02	R NETWORK 9-1.0K 2.0 % 10PIN	2	2	2	R17,R24

REVISION HISTORY			BASIC PART NO: M8743			DRN: J. RICHARD			DATE: 10-FEB-82			D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A			CHK'D: P. BOSSMAN			DATE: 17-FEB-82			TITLE PARTS LIST			
JJ	INITIAL	A	SECTION VARIATION INDEX			DES. ENG: J. JANETOS <td colspan="3">DATE: 17-FEB-82 <td colspan="3">MS11-P MEMORY</td> </td>			DATE: 17-FEB-82 <td colspan="3">MS11-P MEMORY</td>			MS11-P MEMORY			
			[A]	BC, BF, BH		RESP. ENG.: J. JANETOS <td colspan="3">DATE: 17-FEB-82 <td colspan="3">DOCUMENT NUMBER</td> </td>			DATE: 17-FEB-82 <td colspan="3">DOCUMENT NUMBER</td>			DOCUMENT NUMBER			
			[B]			MFG. ENG.: R. HILL <td colspan="3">DATE: 17-FEB-82 <td>SIZE</td> <td>CODE</td> <td>NUMBER</td> <td>REV</td> </td>			DATE: 17-FEB-82 <td>SIZE</td> <td>CODE</td> <td>NUMBER</td> <td>REV</td>			SIZE	CODE	NUMBER	REV
			[C]			ASSEMBLY NUMBER: <td colspan="3">TOP DOCUMENT NUMBER: <td colspan="3">FILE NAME:</td> </td>			TOP DOCUMENT NUMBER: <td colspan="3">FILE NAME:</td>			FILE NAME:			
			[D]			D-UA-M8743-BA-0 <td colspan="3">#B-DD-M8743-BA <td colspan="3">Z2712A.PLS</td> </td>			#B-DD-M8743-BA <td colspan="3">Z2712A.PLS</td>			Z2712A.PLS			
			[E]									EDIT #			
			[F]									21			
			[G]												
			[H]												
			[I]												
			[J]												
			[K]												
			[L]												
			[M]												
			[N]												

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION			REFERENCE DESIGNATOR
					BC	BF	BH	
31	31		1317494-00	22.10 K .25 W 1.0 % RN55D-F10	2	2	2	R20,R21
32	32		1505321-00	DEC4258 PNF 200MW SI 12 30 M	2	2	2	Q1,Q2
33	33		1610033-00	DELAY= 10-100NS,10TAPS	1	1	1	E10
34	34		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1	1	1	E27
35	35		1910534-00	74S04 INVERTER GATE-HEX 1I	1	1	1	E12
36	36		1910536-00	74S10 NAND GATE-TRIPLE 3IN	1	1	1	E5
37	37		1910540-00	74S22 NAND GATE-DUAL 4INPU	1	1	1	E3
38	38		1910542-00	74S64 A-0-I GATE 4-2-3-2	2	2	2	E9,E17
39	39		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1	1	1	E6
40	40		1910546-00	74S140 NAND GATE-DUAL 4INPU	1	1	1	E4
41	41		1910547-00	74S153 MUX 1 OF 4 (DUAL)	4	4	4	E49,E50,E55,E56
42	42		1910549-00	74S158 MUX 1 OF 2 (QUAD)	1	1	1	E45
43	43		1910550-00	74S174 FF-D HEX	1	1	1	E40
44	44		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	2	2	2	E1,E54
45	45		1911579-00	8641 TRANSCEIVER,BUS,QUA	4	4	4	E7,E13,E18,E22
46	46		1911944-00	555CN TIMER,FUNCT.BLOCK	1	1	1	E47
47	47		1911983-00	74S133 NAND GATE-POSITIVE 1	1	1	1	E37
48	48		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	2	2	2	E2,E23
49	49		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	1	1	1	E35
50	50		1912803-00	LS04 INVERTER GATE,HEX	1	1	1	E11
51	51		1912820-00	LS51 A-0-I GATE 2-WIDE 2I	1	1	1	E32
52	52		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	1	1	1	E30
53	53		1912828-00	LS85 COMPARATOR,4BIT MAGN	1	1	1	E31
54	54		1912834-00	LS112 FF-JK,DUAL,EDGE TRIG	1	1	1	E8
55	55		1913340-00	74S32 OR GATE-QUAD 2IN	1	1	1	E26
56	56		1913670-00	74S373 LATCH 8BIT TRASP TR	3	3	3	E14,E16,E19
57	57		1913777-00	LS240 DRIVER,LINE,OCTAL,T	1	1	1	E43
58	58		1913887-00	74S258 MUX 1 OF 2(QUAD)TRI	1	1	1	E21
59	59		1914451-00	74LS393 COUNTER,BINARY,4BIT	2	2	2	E51,E57
60	60		1915219-00	LS373 FF-D OCTAL-TRANSPARE	2	2	2	E44,E46
61	61		1918822-00	2960 ERROR DETECTOR/CORRE	1	1	1	E15
62	62		1919004-00	DC 750 REGISTER,ADDRESS	1	1	1	E24
63	63		1919015-00	DC 021 BUS TRANSCEIVER,20PI	3	3	3	E36,E42,E48
64	64		1919095-00	2966PC DYNAMIC MEMORY DRIV	2	2	2	E61,E64
65	65		2118467-01	B264-20 RAM 64K X1,200NS 1	176	-	-	E100-E107,E110-E117,E120-E127, CONT E130-E137,E140-E147,E150-E157, CONT E160-E167,E170-E177,E180-E187, CONT E190-E197,E200-E207,E210-E217, CONT E220-E227,E230-E237,E240-E247, CONT E250-E257,E260-E267,E270-E277, CONT E280-E287,E290-E297,E300-E307, CONT E310-E317
66	66		1919635-01	2965 DRIVER,DYNAMIC RAM	5	5	5	E59,E60,E62,E63,E65
67	67		9000024-01	EYELET,ROLLED 0.1210DX0.192	12	12	12	
68	68		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	4	4	4	W1-W4
69	69		1302388-00	2.0 K .25 W 5.0 % CF	1	1	1	R10
70	70		1910544-01	74S74-60GG-D DUAL,EDGE TRIG	1	1	1	E41
71	71	2 EACH IC	1215006-06	SKT,IC 24PIN DIP TIN SOLD	4	4	4	XE24,XE15

! D ! I ! G ! I ! T ! A ! L !	! TITLE	! SECTION A OF A	! SIZE ! CODE !	! DOCUMENT NUMBER	! REV !
! ! ! ! ! ! ! !	MS11-P MEMORY	! ! ! ! ! ! ! !	! K ! PL !	M8743-BA-DBP	! A !

AUTOMATED BY PRTLST.3P(4)

P A R T S L I S T

SHEET A4 OF A4

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

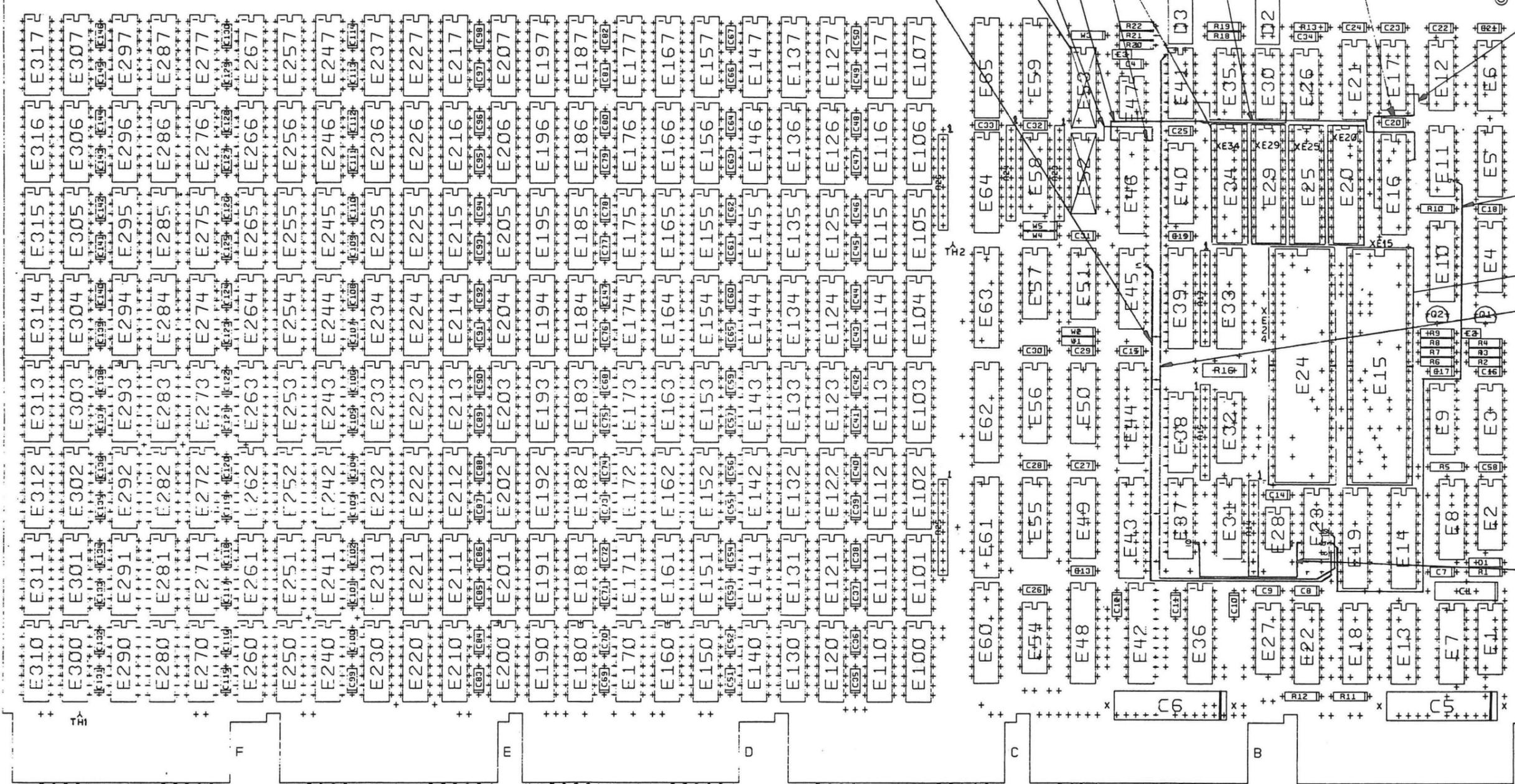
QTY PER VARIATION
BC BF BH

REFERENCE DESIGNATOR

101 NOTE: MAINTAIN ANTI-STATIC PROTECTION PROCEDURES PER DEC STD 067.

DIGITAL							TITLE	SECTION A OF A		SIZE	CODE	DOCUMENT NUMBER	REV
							MS11-P MEMORY			K	PL	M8743-BA-DBP	A

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NOTES: 1. THIS BOARD MEETS REQUIREMENTS FOR UL RATING 94V0.
2. DO NOT INSTALL E52 & E53.

CHK	CHANGE NO	REV	DATE	BY

STEP E → Y AXIS → STEP → TIMES
REPEAT → X AXIS → STEP → TIMES

ETCH REV. C

SIGNATURES	DATE	TITLE
DRN <i>Andy Peterson</i>	9/25/72	MS11-P MEMORY
CHK'D. <i>Tommy...</i>	10/10/72	
DESIGN ENG. <i>James...</i>	10/10/72	
PROD. ENG. <i>...</i>	6/17/72	

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REV. C
DUA M8743-BA-0
SHEET 2 OF 2

FIRST RELEASE

WIRE ADDS:

SIDE 1:

- Ø-1: ADD WIRE FROM E37-1Ø TO E23-6.
- Ø-2: ADD WIRE FROM E12-7 TO E17-9.
- Ø-3: ADD WIRE FROM E17-1Ø TO E16-8.
- Ø-4: ADD WIRE FROM E41-9 TO E16-18.
- Ø-5: ADD WIRE FROM E3Ø-9 TO E46-1.
- Ø-6: ADD WIRE FROM E46-2Ø TO LOCATION E52-16.

ECO # M8743-BA-SHØØ1

REWORK INSTRUCTIONS:

PROCEDURE:

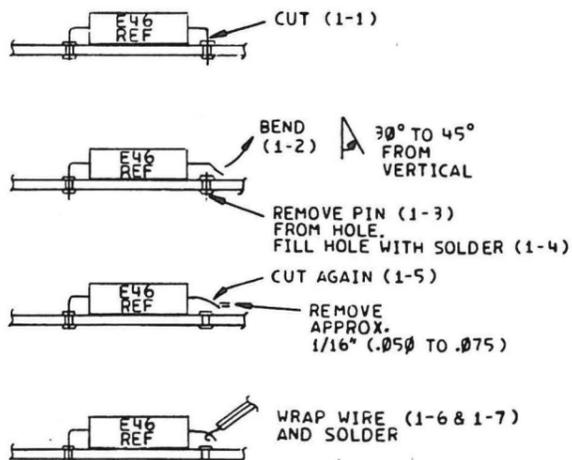
- 1 - 1. CUT PIN 2Ø ON E46 CLOSE TO BOARD.
- 1 - 2. BEND PIN 2Ø UP TO 45° ANGLE FROM VERTICAL.
- 1 - 3. REMOVE CUT PIN FROM HOLE.
- 1 - 4. FILL HOLE WITH SOLDER.
- 1 - 5. MAKE SECOND CUT, REMOVE ANOTHER 1/16".
- 1 - 6. PREPARE ECO WIRE.
- 1 - 7. WRAP ECO WIRE ON STUB OF PIN 2Ø & SOLDER TO PIN 2Ø.
- 1 - 8. PLACE OTHER END OF WIRE IN HOLE (PIN POSITION 16 OF E52 - THIS IS AN EMPTY HOLE), SOLDER WIRE, DRESS AND GLUE WIRE.
- 1 - 9. TEST: 2224 DC TEST, 25°.

ECO #2 REWORK:

WIRE ADDS:

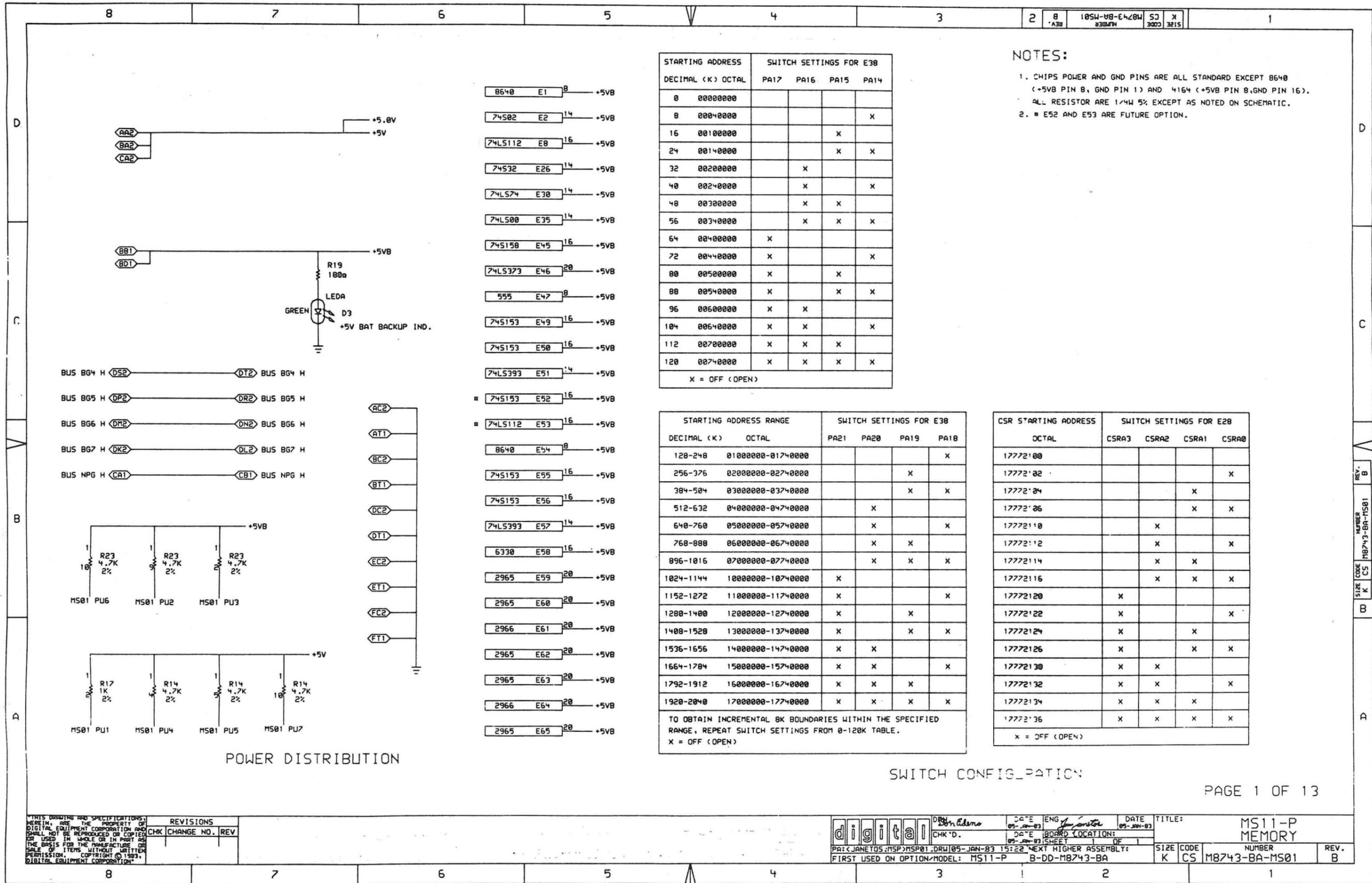
- 2-1. ADD WIRE FROM E11-9 TO E23-9.
- 2-2. ADD WIRE FROM E45-15 TO E23-8.
- 2-3. ADD WIRE FROM E23-1Ø TO E41-1.

M8743-BA REWORK INSTRUCTIONS:



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	MS11-P MEMORY	SIZE CODE	DUA	NUMBER	M8743-BA-0	REV.	C
SCALE	1/4"	SHEET	2	OF	2	DIST.	



POWER DISTRIBUTION

STARTING ADDRESS DECIMAL (K) OCTAL	SWITCH SETTINGS FOR E38			
	PA17	PA16	PA15	PA14
0 00000000				
8 00040000				X
16 00100000			X	
24 00140000			X	X
32 00200000		X		
40 00240000		X		X
48 00300000		X	X	
56 00340000		X	X	X
64 00400000	X			
72 00440000	X			X
80 00500000	X		X	
88 00540000	X		X	X
96 00600000	X	X		
104 00640000	X	X		X
112 00700000	X	X	X	
120 00740000	X	X	X	X

X = OFF (OPEN)

NOTES:

1. CHIPS POWER AND GND PINS ARE ALL STANDARD EXCEPT 8640 (+5VB PIN 8, GND PIN 1) AND 4164 (+5VB PIN 8, GND PIN 16). ALL RESISTOR ARE 1/4W 5% EXCEPT AS NOTED ON SCHEMATIC.
2. E52 AND E53 ARE FUTURE OPTION.

STARTING ADDRESS RANGE DECIMAL (K) OCTAL	SWITCH SETTINGS FOR E38			
	PA21	PA20	PA19	PA18
128-248 01000000-01740000				X
256-376 02000000-02740000			X	
384-504 03000000-03740000			X	X
512-632 04000000-04740000		X		
640-760 05000000-05740000		X		X
768-888 06000000-06740000		X	X	
896-1016 07000000-07740000		X	X	X
1024-1144 10000000-10740000	X			
1152-1272 11000000-11740000	X			X
1280-1400 12000000-12740000	X		X	
1408-1528 13000000-13740000	X		X	X
1536-1656 14000000-14740000	X	X		
1664-1784 15000000-15740000	X	X		X
1792-1912 16000000-16740000	X	X	X	
1920-2040 17000000-17740000	X	X	X	X

TO OBTAIN INCREMENTAL 8K BOUNDARIES WITHIN THE SPECIFIED RANGE, REPEAT SWITCH SETTINGS FROM 0-120K TABLE.
X = OFF (OPEN)

CSR STARTING ADDRESS OCTAL	SWITCH SETTINGS FOR E28			
	CSRA3	CSRA2	CSRA1	CSRA0
17772'00				
17772'02				X
17772'04			X	
17772'06			X	X
17772'10		X		
17772'12		X		X
17772'14		X	X	
17772'16		X	X	X
17772'20	X			
17772'22	X			X
17772'24	X		X	
17772'26	X		X	X
17772'30	X	X		
17772'32	X	X		X
17772'34	X	X	X	
17772'36	X	X	X	X

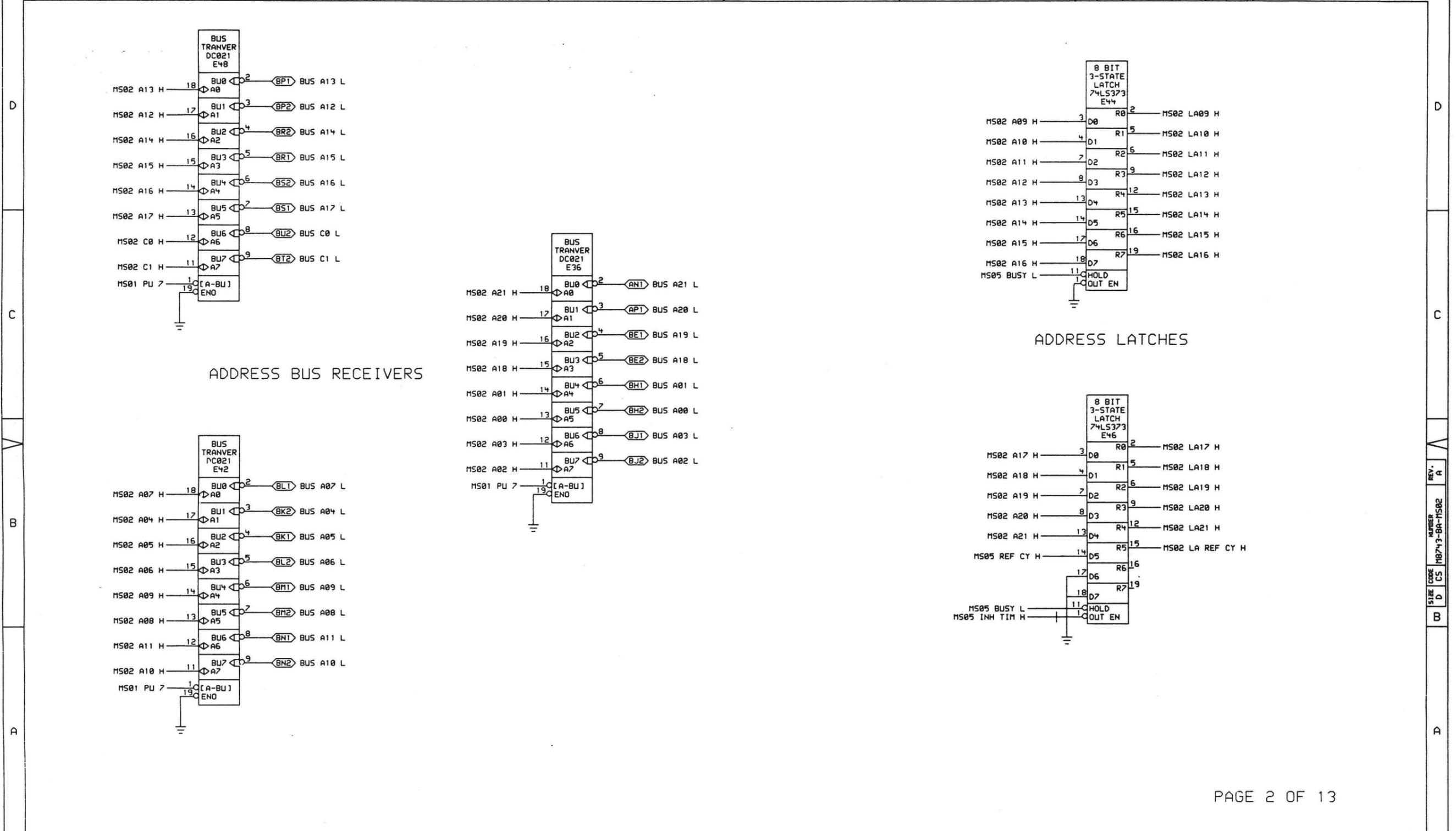
X = OFF (OPEN)

SWITCH CONFIGURATION

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REVISIONS	
CHK	CHANGE NO. REV

digital	DESIGNED BY: <i>Don Edens</i>	DATE: 05-20-83	ENG: <i>J. Smith</i>	DATE: 05-20-83	TITLE: MS11-P MEMORY
	CHK'D:	DATE: 05-20-83	BOARD LOCATION: 1	SHEET: 1	OF: 1
PA: <JANET05.MSP>MSP01.DRW 05-JAN-83 15:22 NEXT HIGHER ASSEMBLY:					SIZE CODE: K CS
FIRST USED ON OPTION/MODEL: MS11-P					NUMBER: M8743-BA-MS01
					REV. B



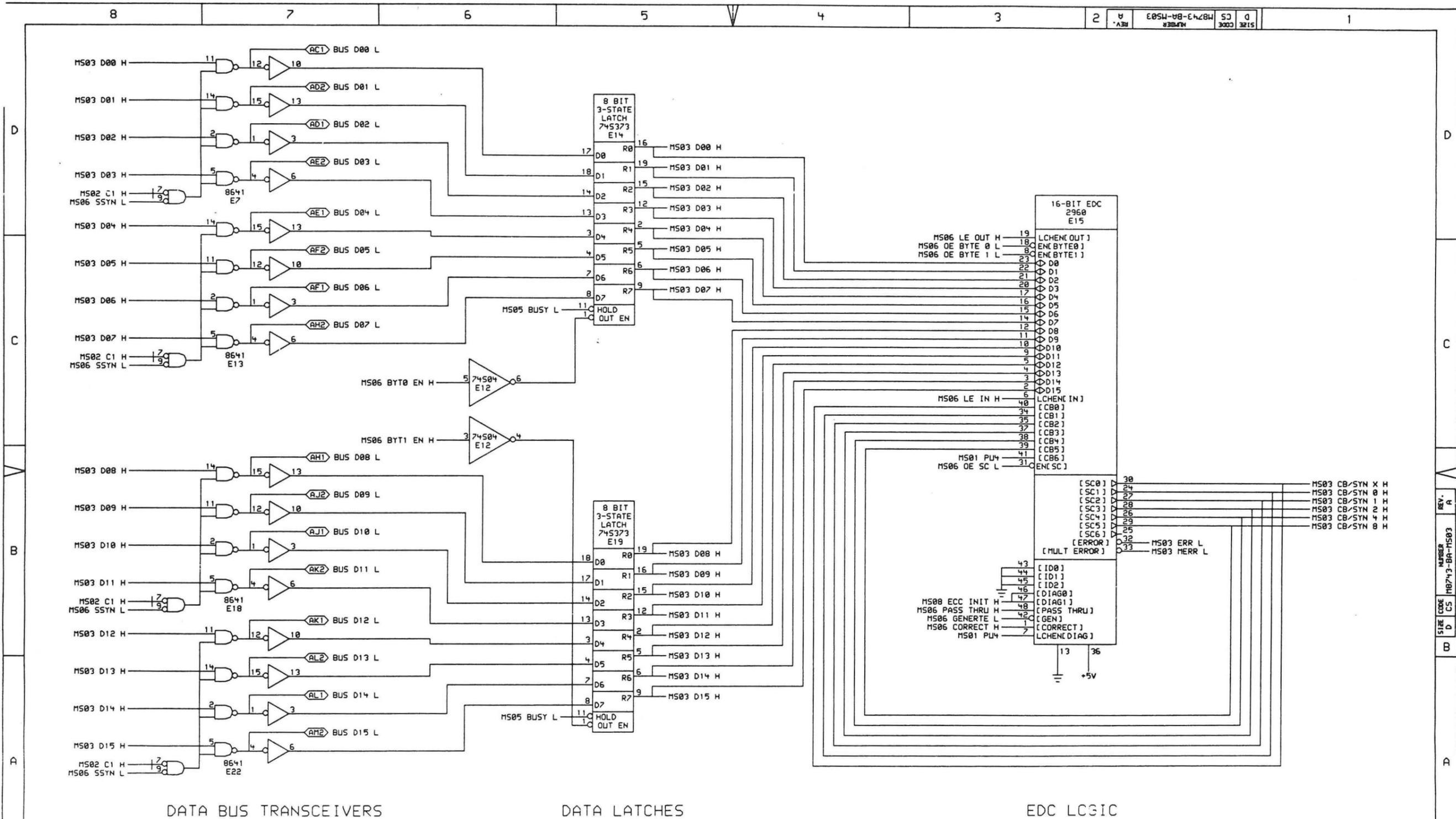
ADDRESS BUS RECEIVERS

ADDRESS LATCHES

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN	DATE	ENG	DATE	TITLE:
	CHK'D	29-JUN-82	P. BOSSMAN	29-JUN-82	MS11-P MEMORY
FIRST USED ON OPTION/MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		SIZE CODE	NUMBER
				D CS	M8743-BA-MS02
				REV.	A



DATA BUS TRANSCEIVERS

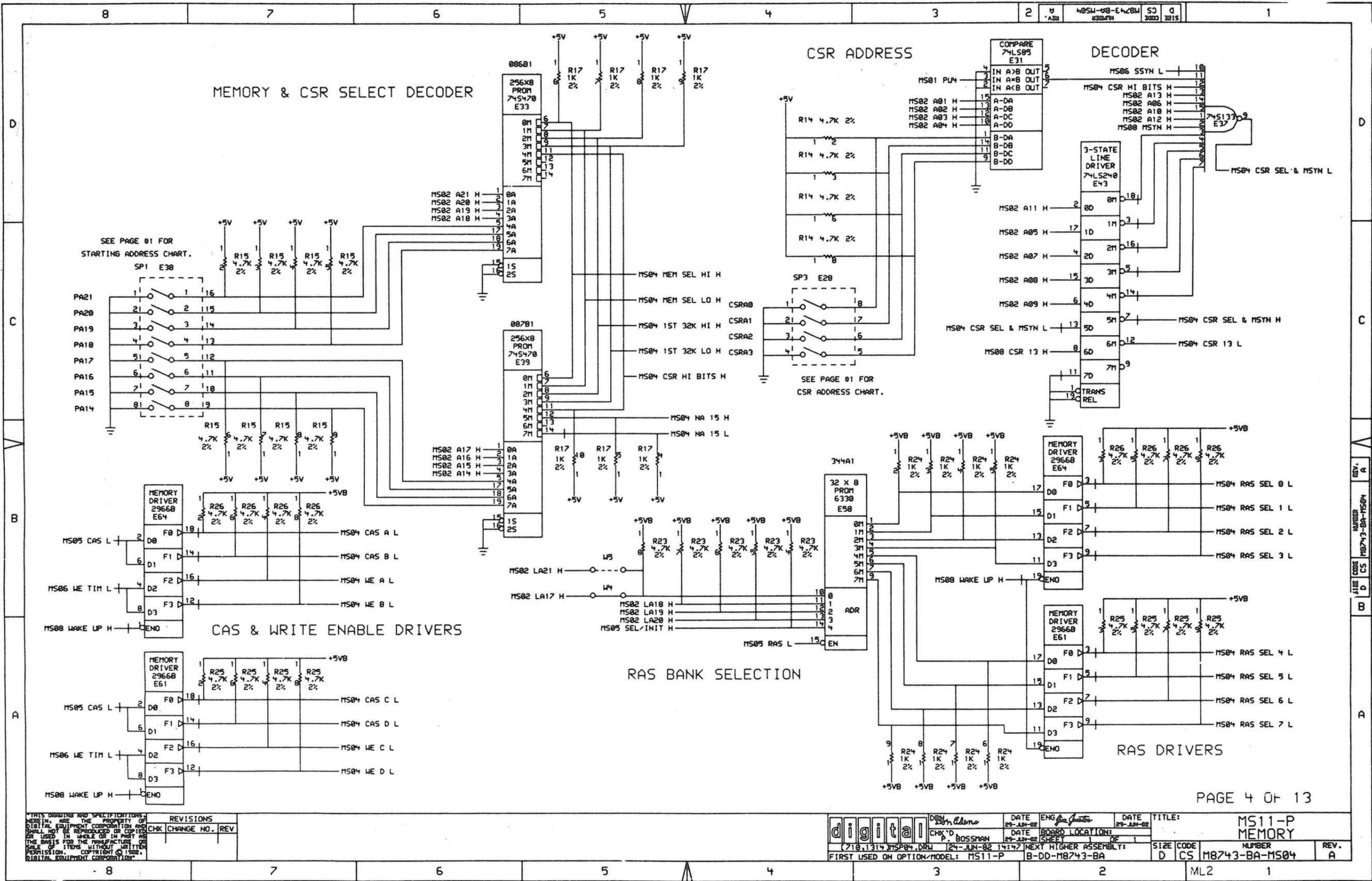
DATA LATCHES

EDC LOGIC

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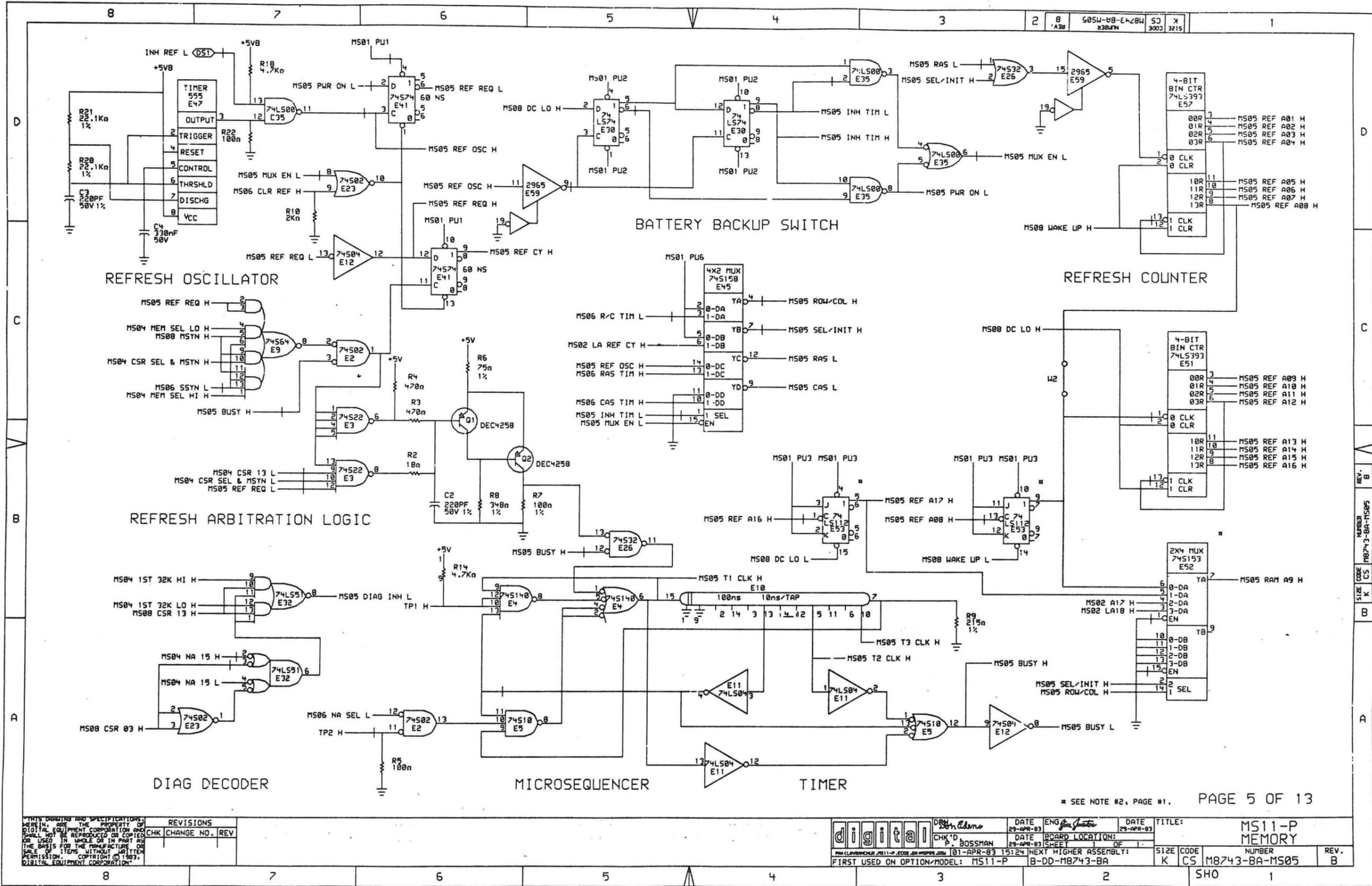
REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	29-JUN-82	ENG	DATE	29-JUN-82	TITLE:	MS11-P
	CHK'D	BOSSMAN	DATE	29-JUN-82	BOARD LOCATION:	MEMORY	REV.
(710,1314) MSP03.DRW	124-JUN-82	14:33	NEXT HIGHER ASSEMBLY:	SHEET	OF	1	
FIRST USED ON OPTION/MODEL:	MS11-P	B-DD-M8743-BA	SIZE	CODE	NUMBER	REV.	
			D	CS	M8743-BA-MS03	A	



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	SIZE CODE: D CS			SHEET: 1 OF 1	NUMBER: M8743-BA-MS04
	FIRST USED ON OPTION/MODEL: MS11-P			BOARD LOCATION:	REV. A

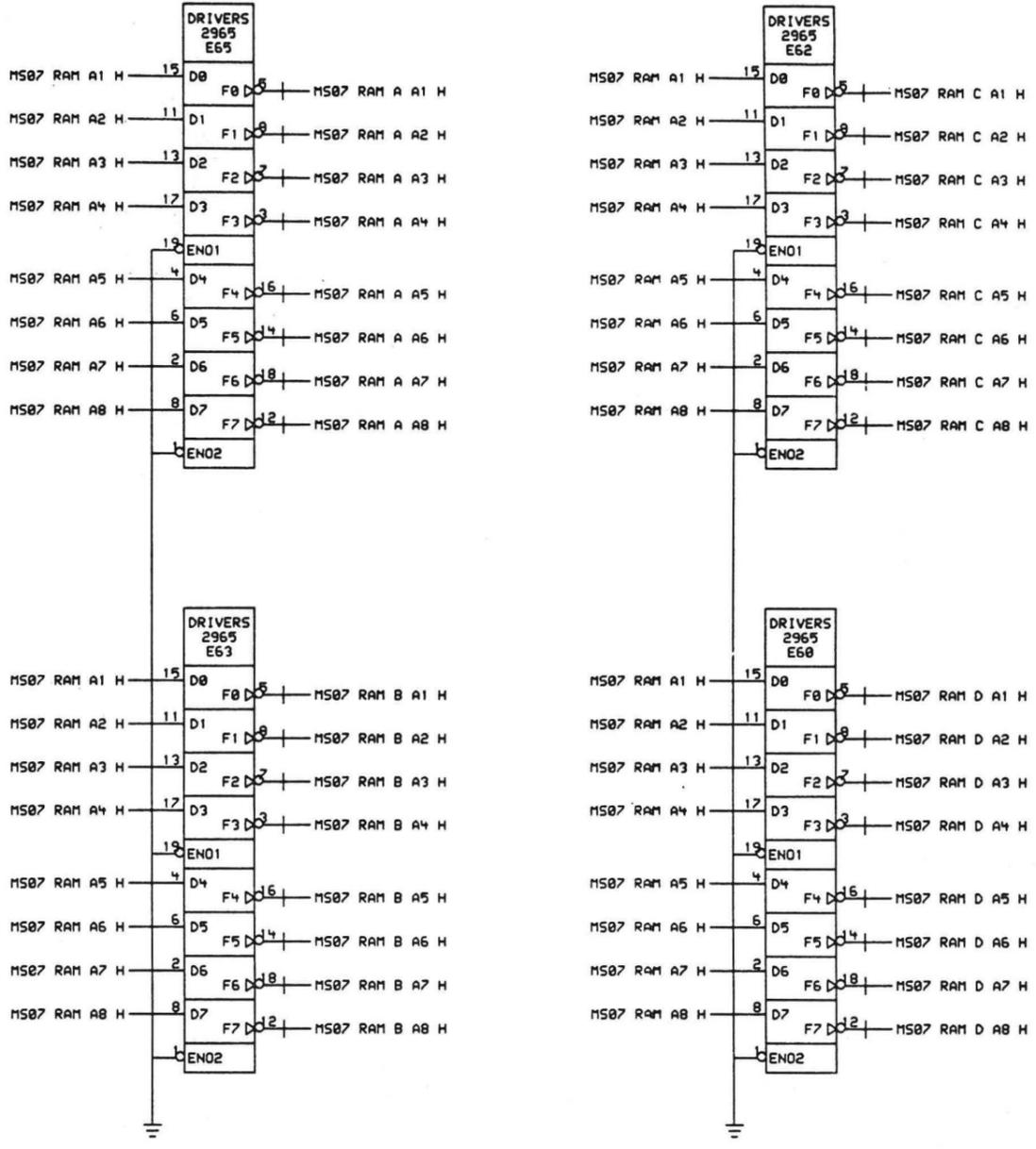
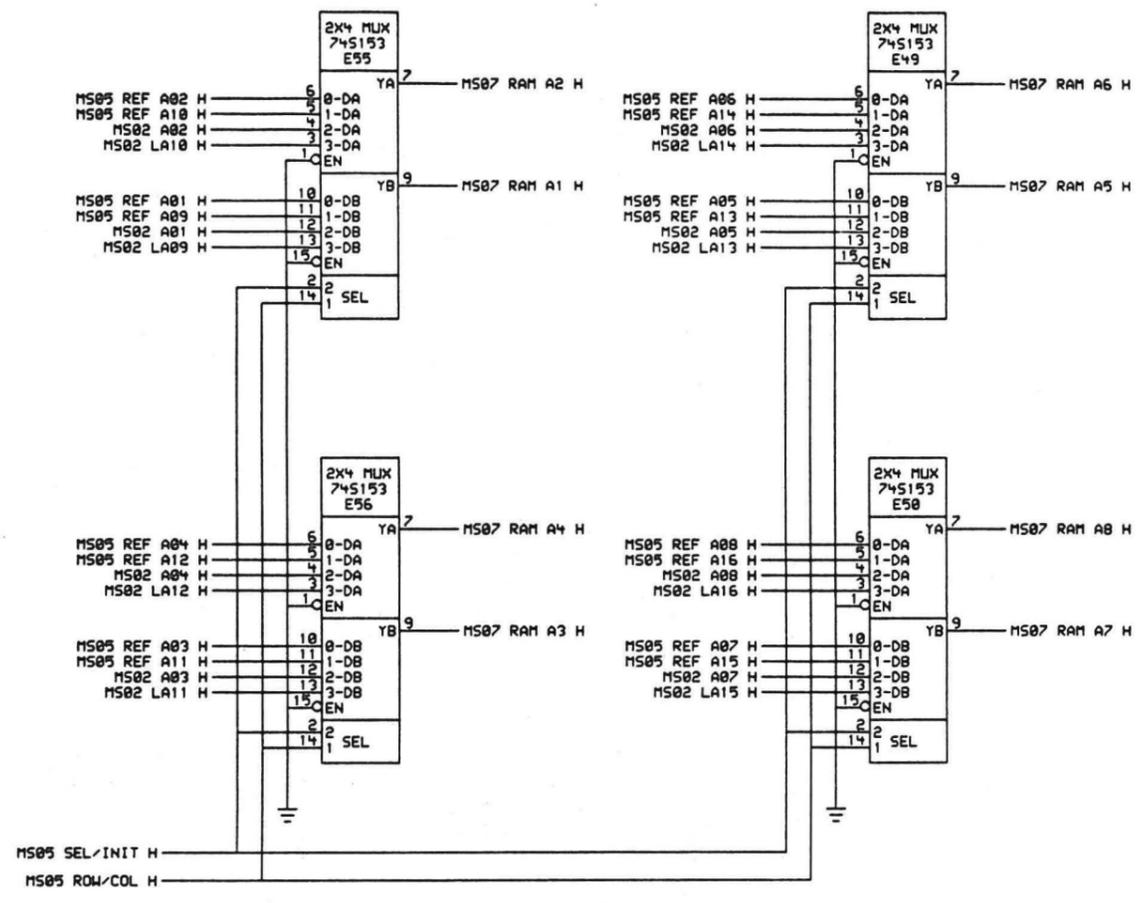
REV. A
 M8743-BA-MS04
 CS
 D
 B
 A



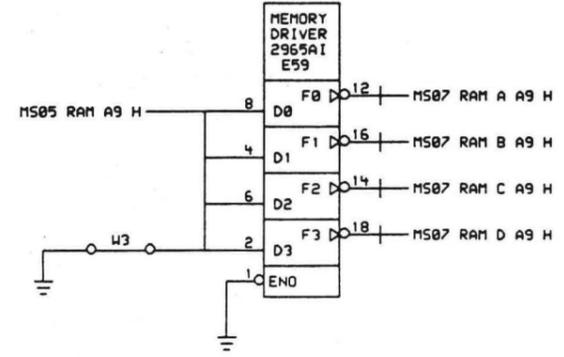
REVISIONS	CHK	CHANGE NO.	REV

digital	DRN	DATE	ENG	DATE	TITLE:
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FIRST USED ON OPTION/MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		SIZE CODE	NUMBER
				K CS	M8743-BA-MS05
				REV.	B

ARRAY ADDRESSING MUXES

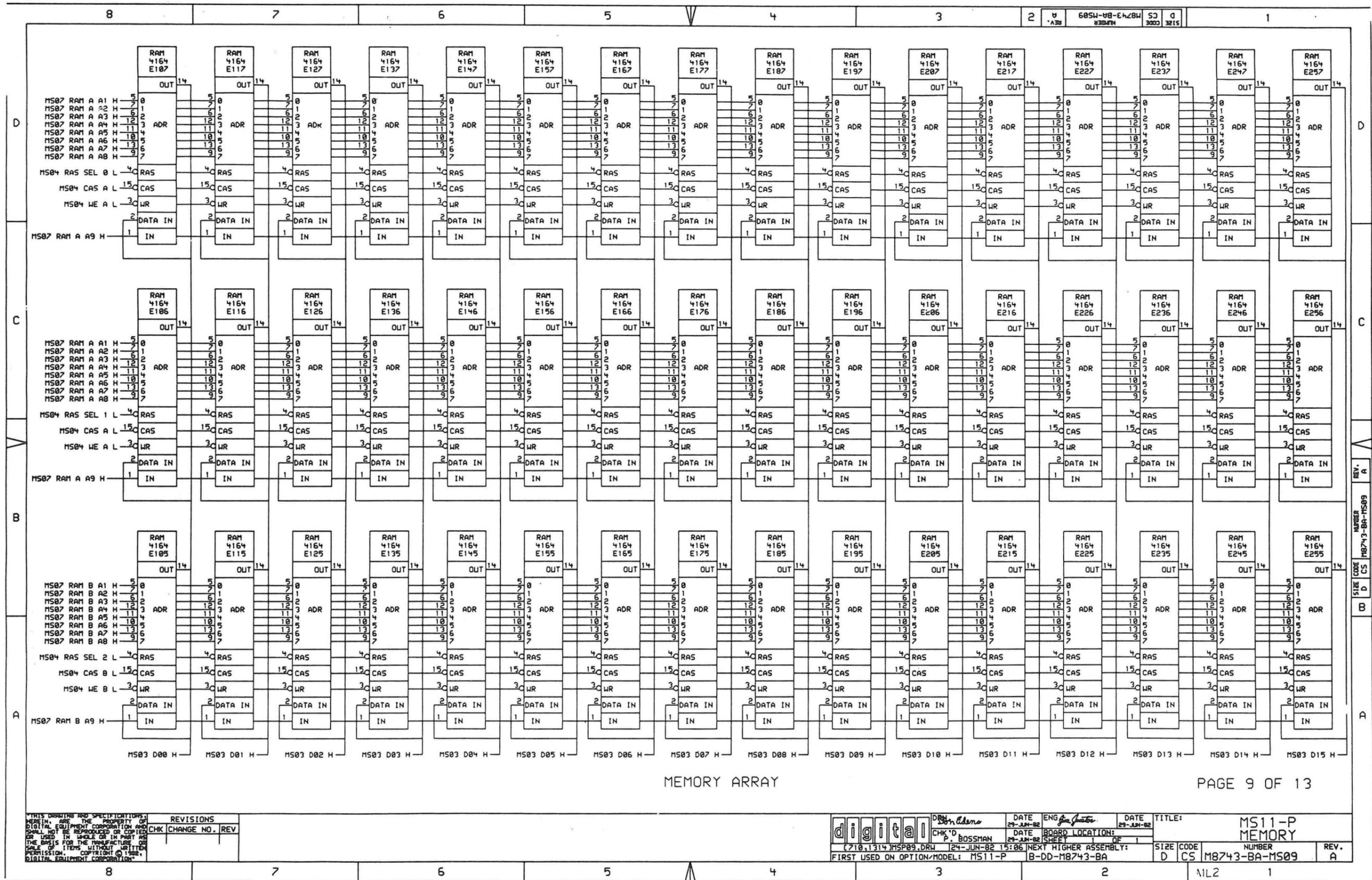


ARRAY ADDRESS DRIVERS



REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	29-JUN-02	ENG	for	DATE	29-JUN-02	TITLE:	MS11-P						
	CHK'D	P. BOSSMAN	DATE	29-JUN-02	BOARD LOCATION:	SHEET	1	MEMORY						
FIRST USED ON OPTION MODEL: MS11-P							SIZE	D	CODE	CS	NUMBER	M8743-BA-MS07	REV.	A



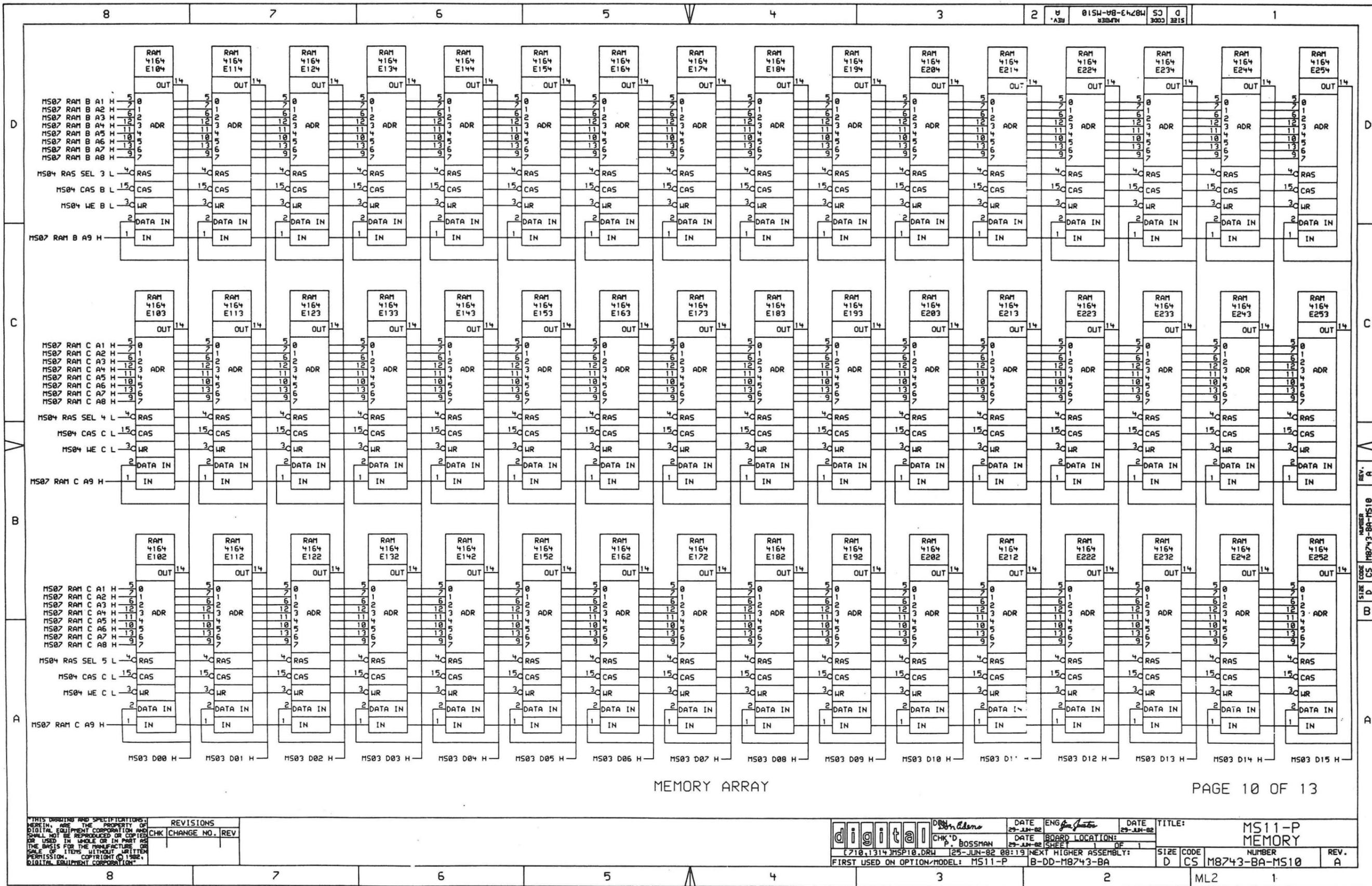
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REVISIONS	
CHK	CHANGE NO. REV.

DATE	ENG.	DATE	TITLE:
29-JUN-82	Don Adams	29-JUN-82	MS11-P
24-JUN-82	P. BOSSMAN	29-JUN-82	MEMORY

710,1314	MSP09.DRW	24-JUN-82 15:06	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: MS11-P				D	CS	M8743-BA-MS09	A

REV. A
M8743-BA-MS09
CS D B

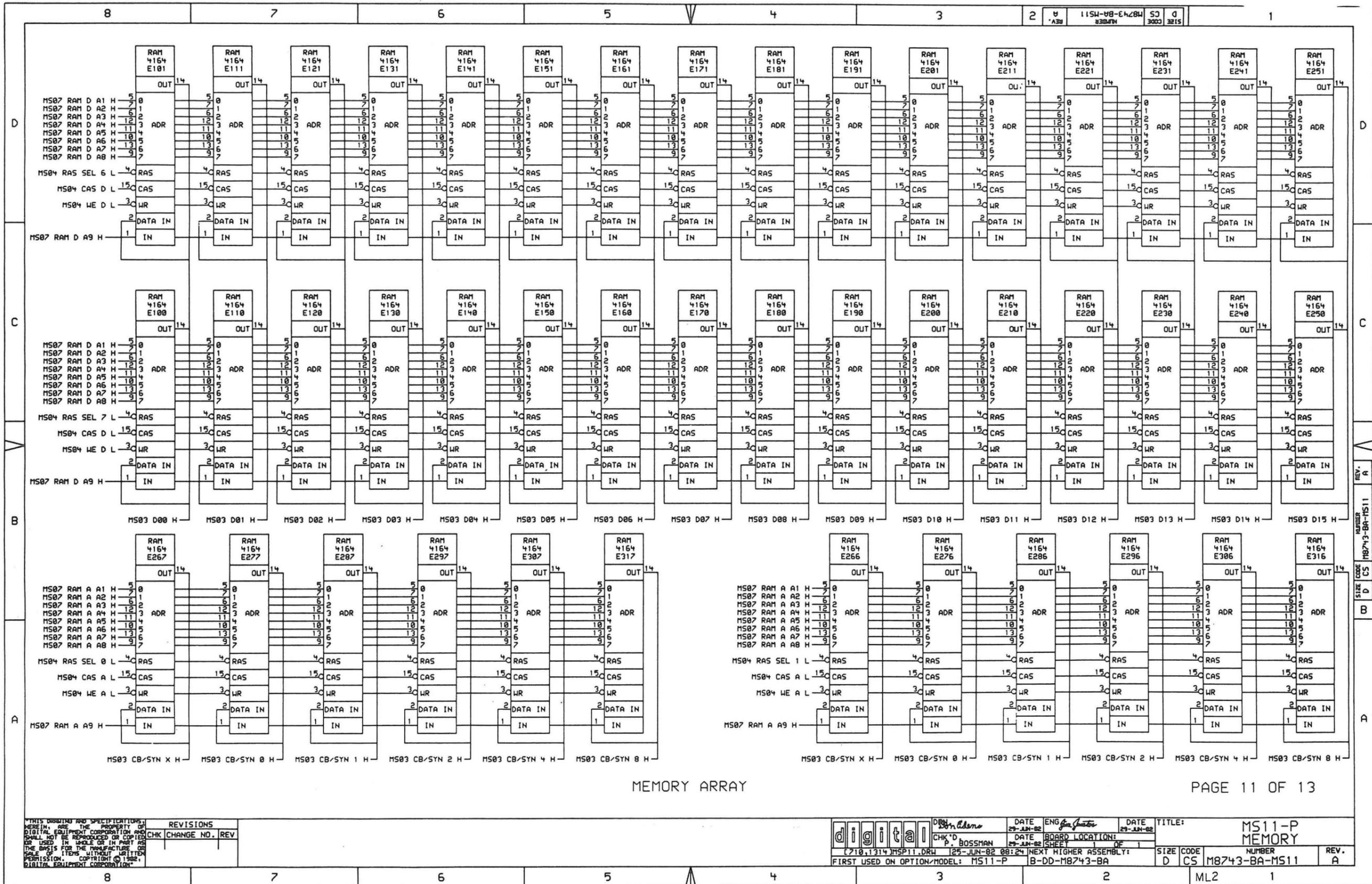


MEMORY ARRAY

REVISIONS	
CHK	CHANGE NO. REV

digital	DATE: 29-JUN-82	DATE: 29-JUN-82	TITLE: MS11-P MEMORY
	CHK'D: P. BOSSMAN	DATE: 29-JUN-82	BOARD LOCATION: SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA	SIZE CODE: D CS
NUMBER: M8743-BA-MS10			REV. A

REV. A
NUMBER M8743-BA-MS10
SIZE CODE CS



MEMORY ARRAY

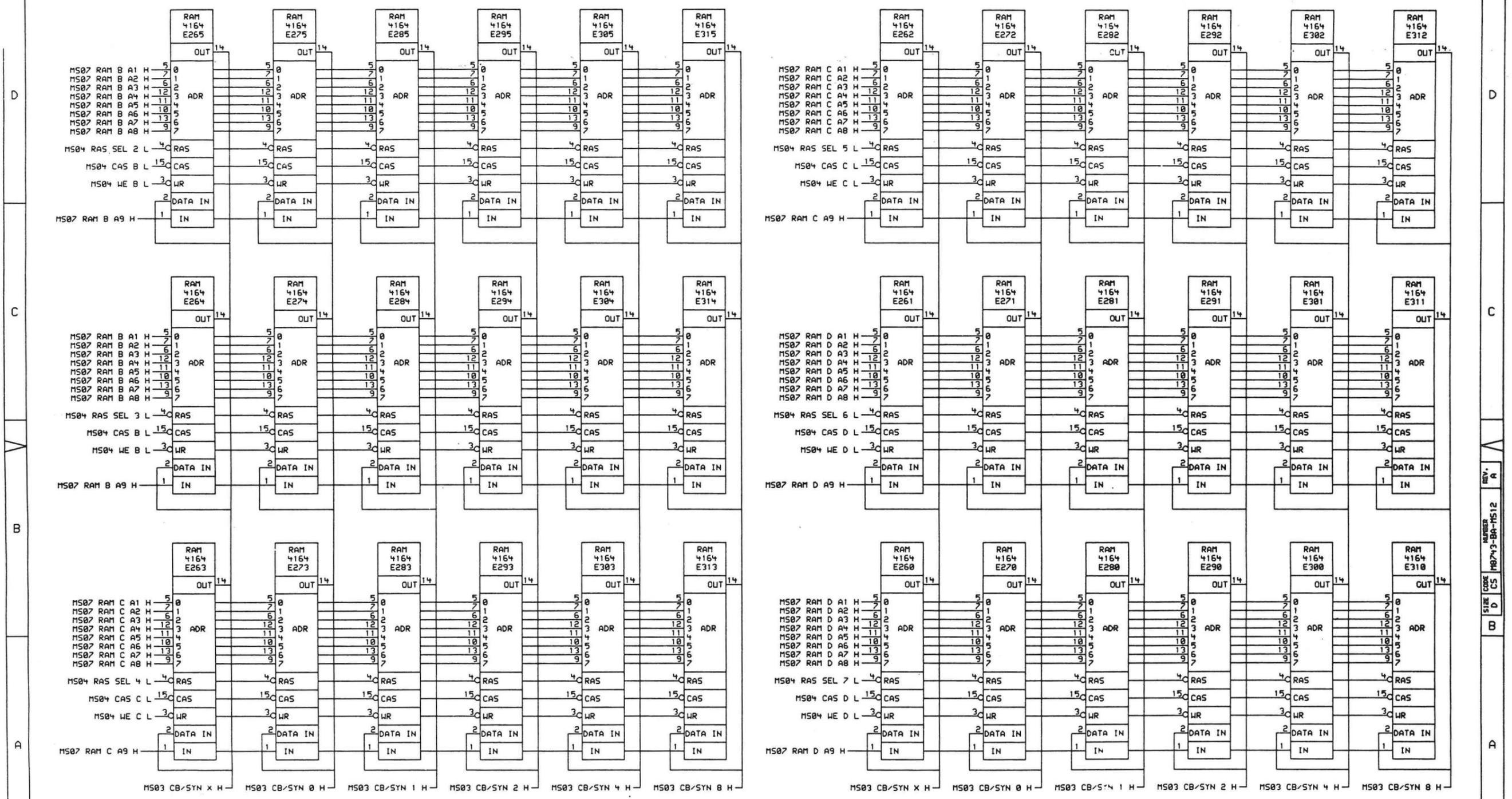
PAGE 11 OF 13

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 29-JUN-82	ENG Jia Jia	DATE 29-JUN-82	TITLE: MS11-P MEMORY
	CHK'D P. BOSSMAN	DATE 29-JUN-82	SHEET 1 OF 1	NUMBER M8743-BA-MS11
FIRST USED ON OPTION MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		REV. A

ML2 1

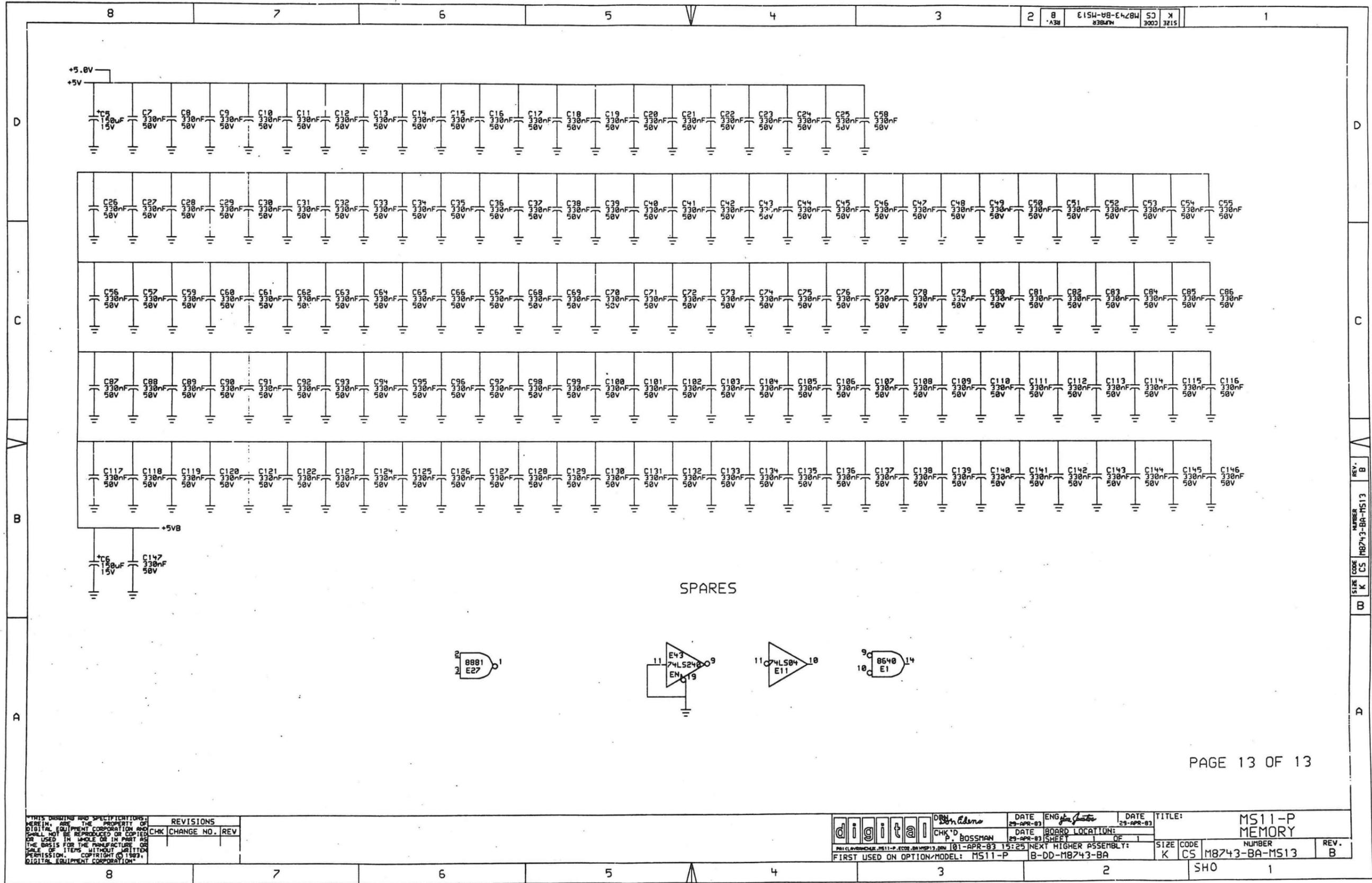


MEMORY ARRAY

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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 29-JUN-82	DATE: 29-JUN-82	TITLE: MS11-P MEMORY
	CHK'D: P. BOSSMAN	DATE: 29-JUN-82	BOARD LOCATION: SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: MS11-P		SIZE CODE: D CS	NUMBER: M8743-BA-MS12
NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		REV.:	A



REVISIONS	
CHK	CHANGE NO. REV

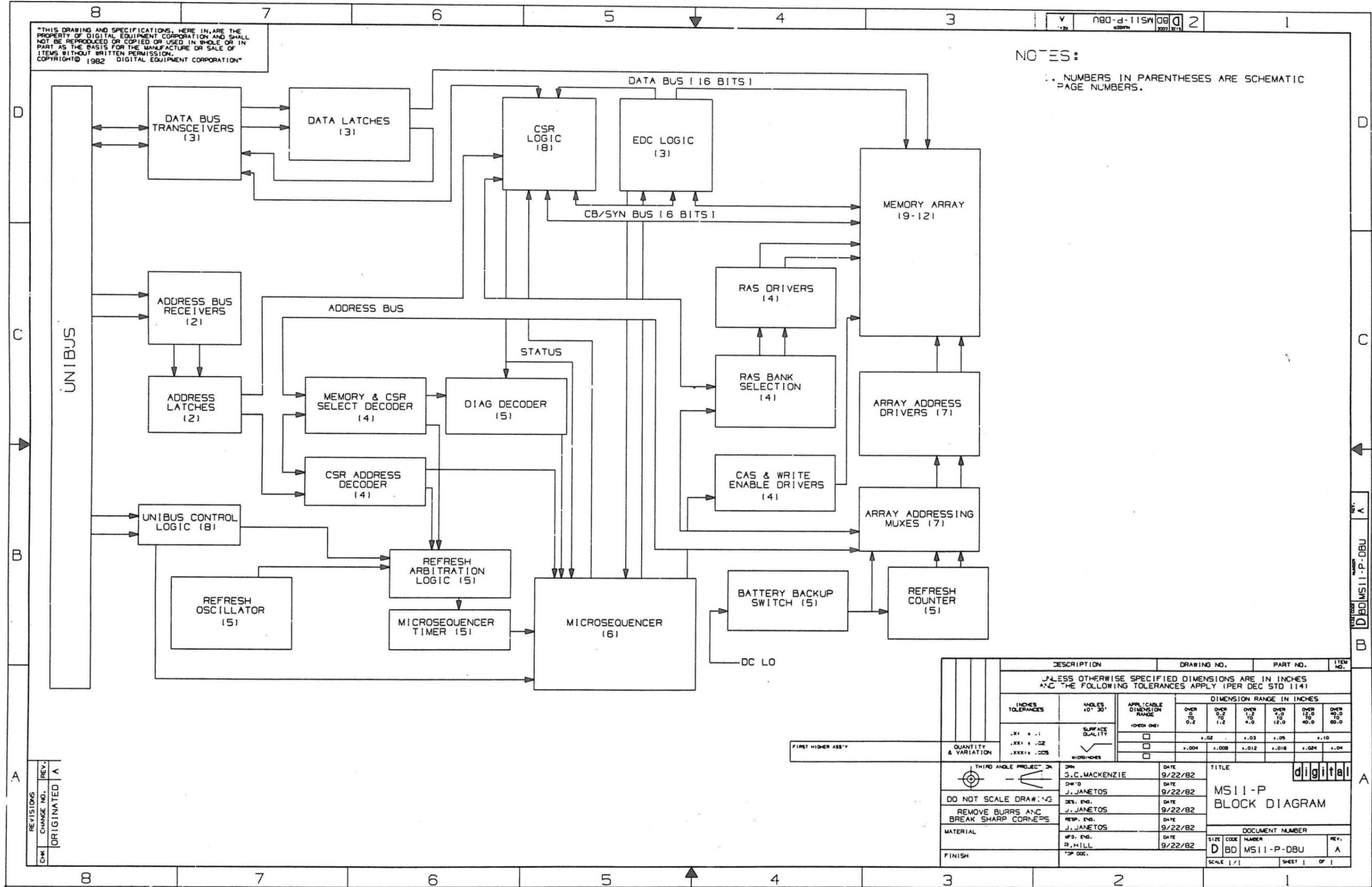
digital	DATE: 29-APR-83	DATE: 29-APR-83	TITLE: MS11-P MEMORY
CHK'D: P. BOSSMAN	DATE: 29-APR-83	BOARD LOCATION: B-DD-MB743-BA	NUMBER: M8743-BA-MS13
FIRST USED ON OPTION/MODEL: MS11-P	101-APR-83 15:25	NEXT HIGHER ASSEMBLY: B-DD-MB743-BA	REV. B

SIZE: K	CODE: CS	NUMBER: M8743-BA-MS13	REV. B

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NOTES:

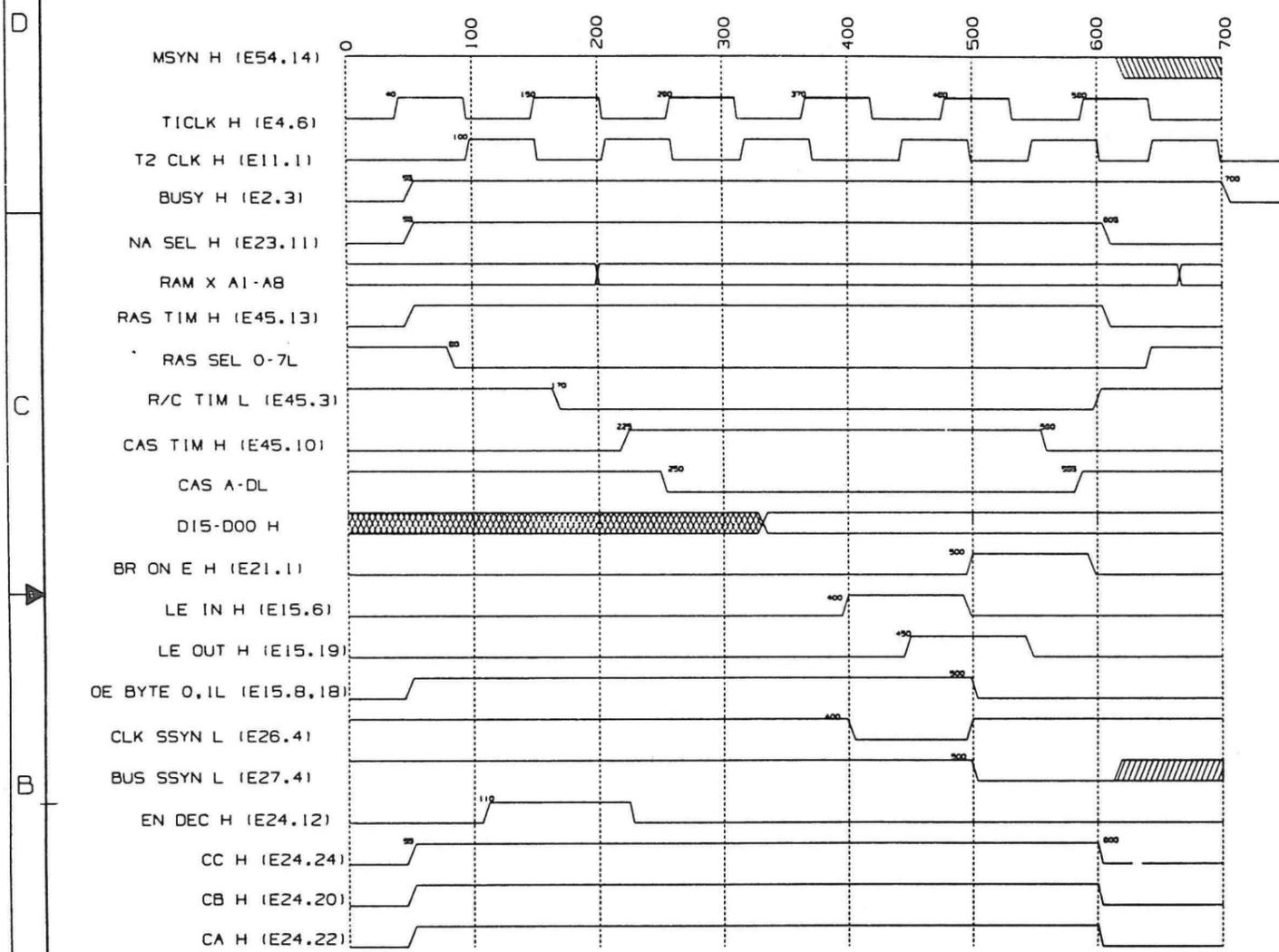
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DESCRIPTION		DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)				
DIMENSION RANGE IN INCHES				
OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0
±.02	±.03	±.05	±.10	±.15
APPLICABLE DIMENSION RANGE				
INCHES ONE		±.004	±.008	±.012
SURFACE QUALITY				
✓		MICROINCHES		
QUANTITY & VARIATION				
□		FIRST HIGHER ASSY		
THIRD ANGLE PROJECT				
DESIGN	S.C. MACKENZIE	DATE	9/22/82	
CHK'D	J. JANETOS	DATE	9/22/82	
DES. ENG.	J. JANETOS	DATE	9/22/82	
RESP. ENG.	J. JANETOS	DATE	9/22/82	
MATERIAL	W.P. ENG.	DATE	9/22/82	
FINISH	R. HILL	DATE	9/22/82	
TITLE		MS11-P BLOCK DIAGRAM		
DOCUMENT NUMBER		D BD MS11-P-DBU		
SCALE	1/1	SHEET	1 OF 1	

REVISIONS	REV.	ORIGINATED
CHK	CHANGE NO.	A

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(DATA, TIMING)

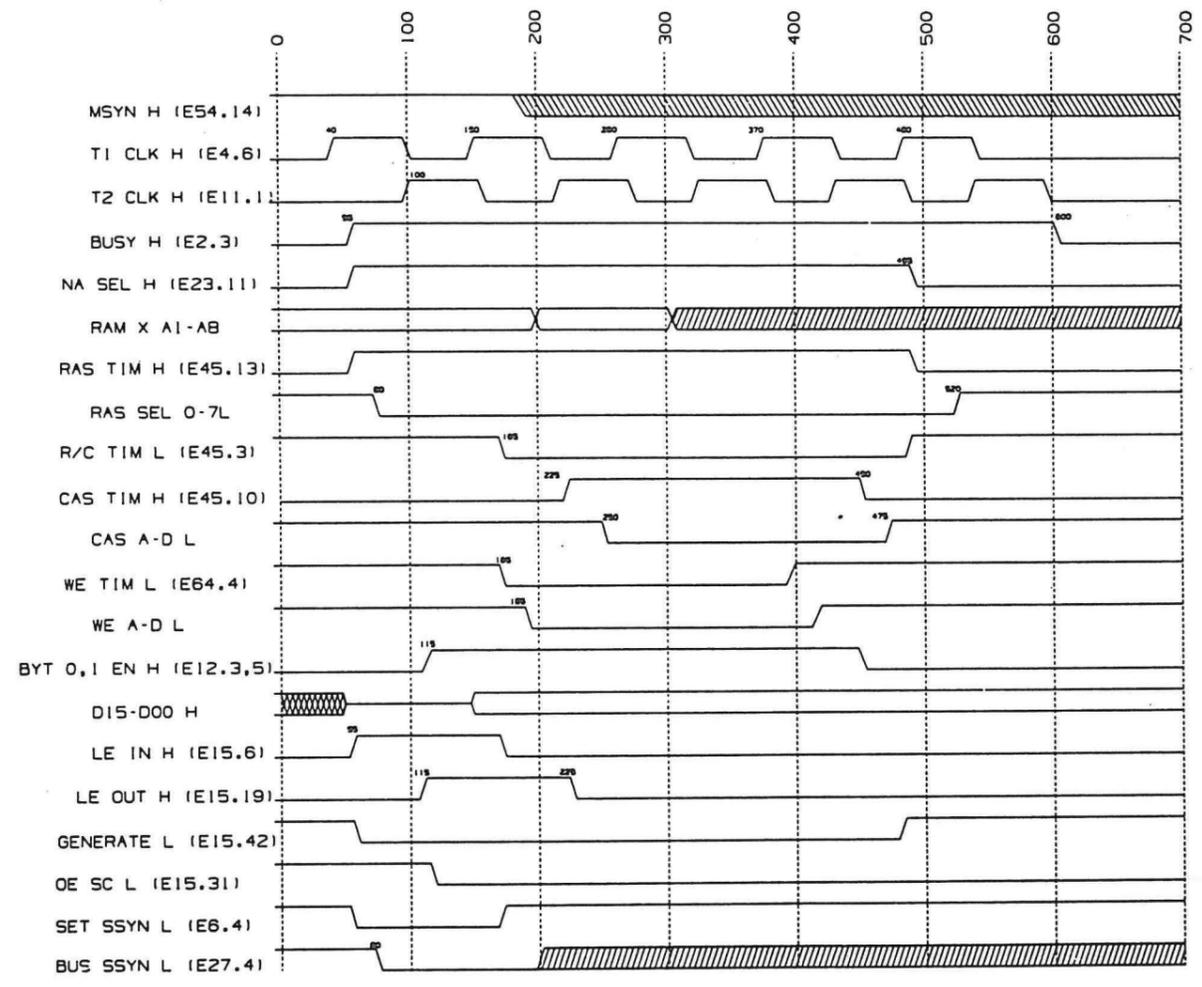
REV. NO.	REV.
1	A

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)			
INCHES TOLERANCES	ANGLES ±0°-30°	APPLICABLE DIMENSION RANGES	
.XX ± .1	SURFACE QUALITY	OVER 0 TO 0.2	OVER 0.2 TO 1.2
.XX ± .02		OVER 1.2 TO 12.0	OVER 12.0 TO 40.0
.XXX ± .005	✓	OVER 40.0 TO 80.0	OVER 80.0 TO 100.0
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DATE	TITLE
		C.W. FIELD 9/8/82	MEMORY SYSTEM TIMING
	DO NOT SCALE DRAWING	DATE	digital
	REMOVE BURRS AND BREAK SHARP CORNERS	J. JANETOS 9/8/82	
MATERIAL	RESP. ENG.	J. JANETOS 9/8/82	DOCUMENT NUMBER
	MFG. ENG.	R. HILL 9/8/82	D TD MS11-P-DBU
FINISH	TOP DOC.		SCALE NONE
			SHEET 1 OF 6

D TD MS11-P-DBU

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D T D MS11-P-DBU



(DATO TIMING)

REVISIONS		
CHK	CHANGE NO	REV

TITLE MEMORY SYSTEM TIMING		SIZE CODE DTD	NUMBER MS11-P-DBU	REV A
SCALE NONE	SHEET 2 OF 6	DIST		

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D TD MS11-P-DBU

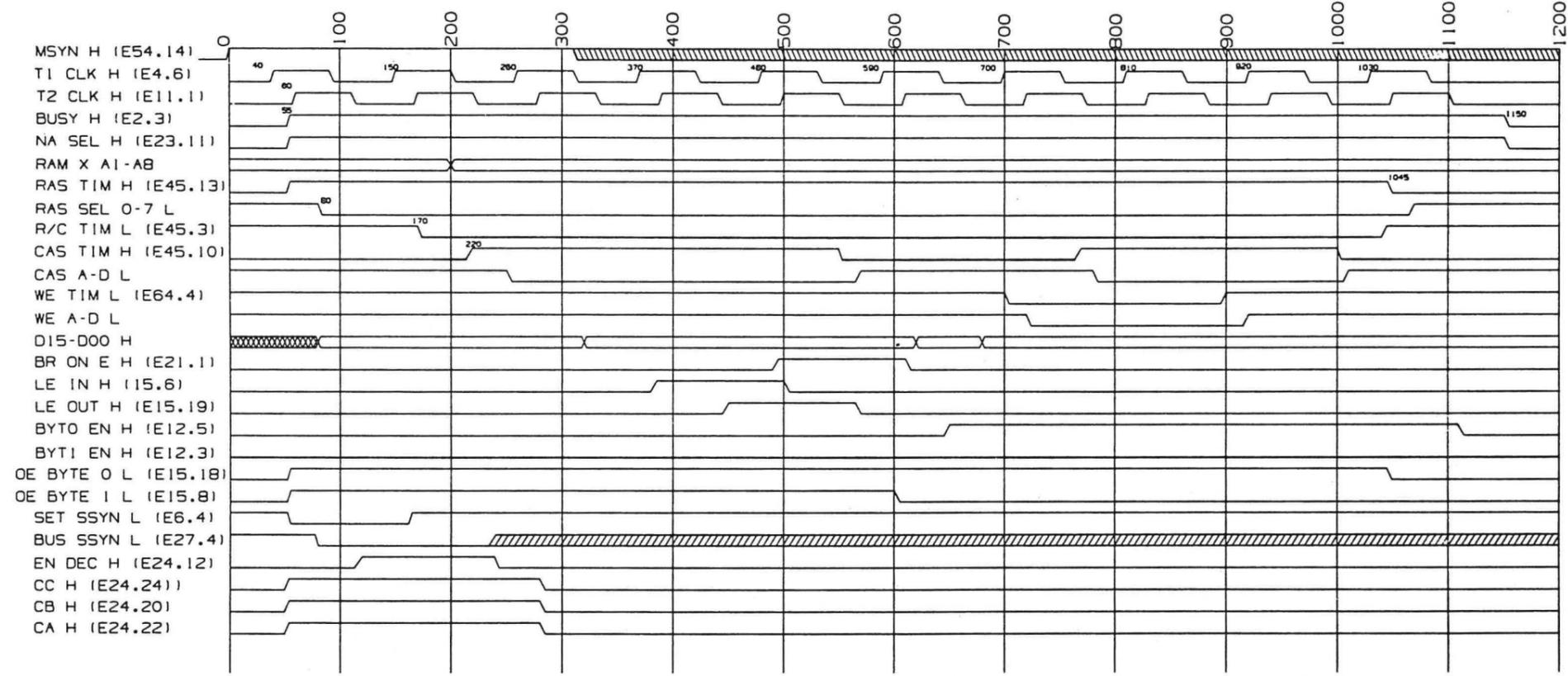
V

C

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REVISIONS		
CHK	CHANGE NO	REV



D TD MS11-P-DBU

D TD MS11-P-DBU

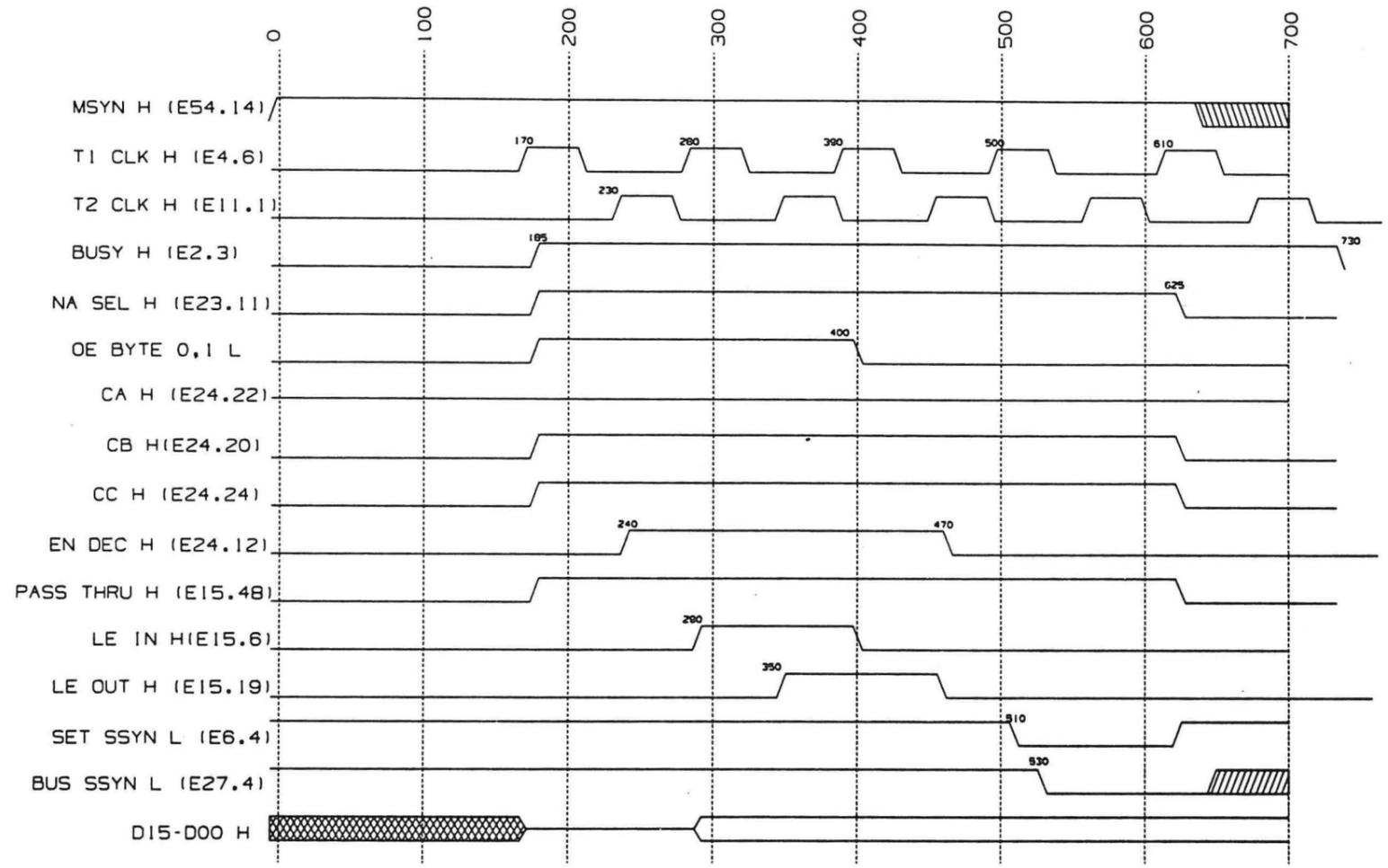
(DATOB TIMING)

TITLE	MEMORY SYSTEM	SIZE CODE	D TD	NUMBER	MS11-P-DBU	REV	A
SCALE	NCNE	SHEET	3	OF	6	DIST	

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MS11-P-DBU 2 1

MS11-P-DBU



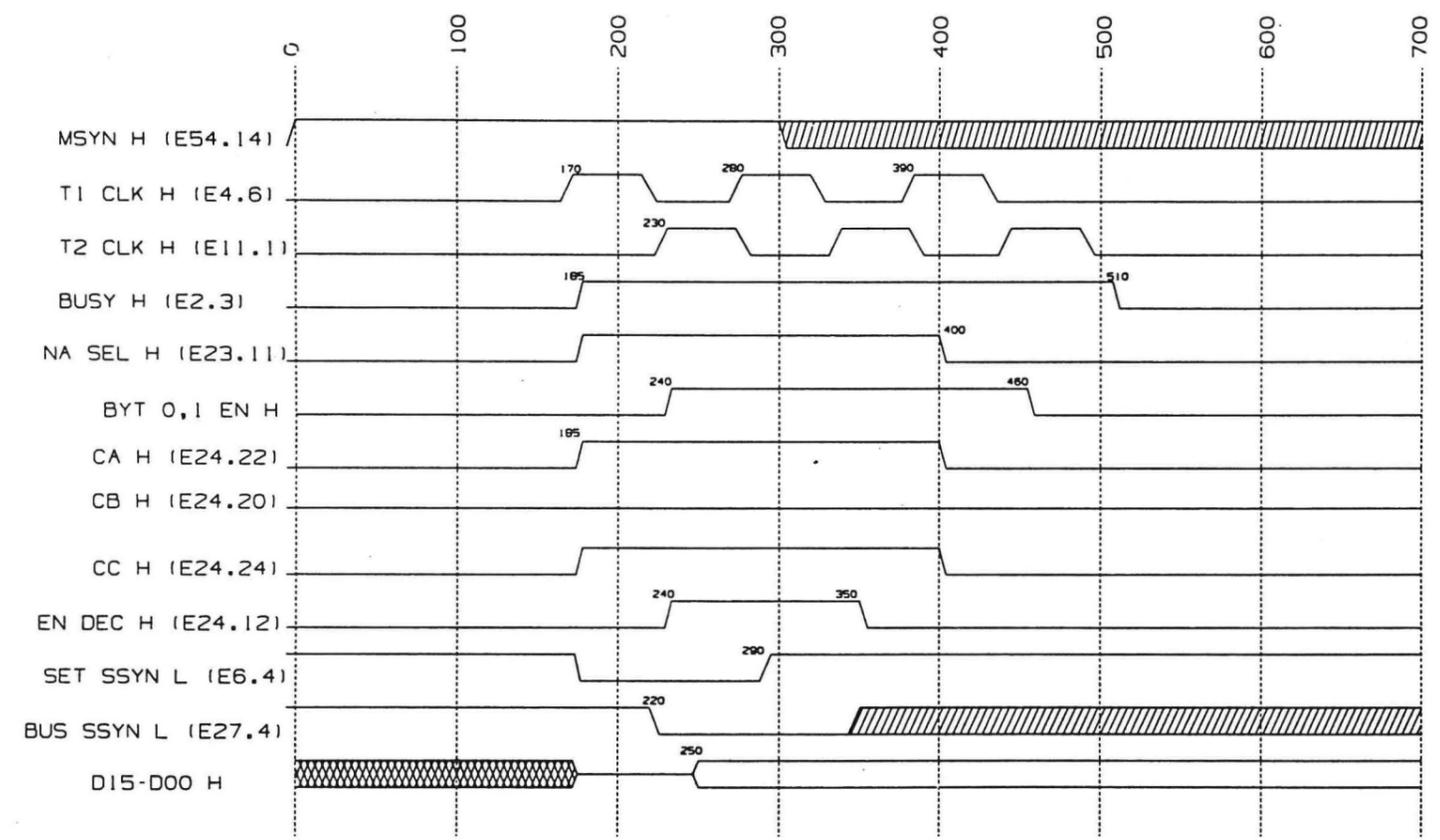
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REVISIONS		
CHK	CHANGE NO	REV

TITLE	MEMCRY SYSTEM	SIZE	CODE	NUMBER	REV
	TIMING	D	TD	MS11-P-DBU	A
SCALE	NONE	SHEET	4	OF	6
		DIST			

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MS11-P-DBU



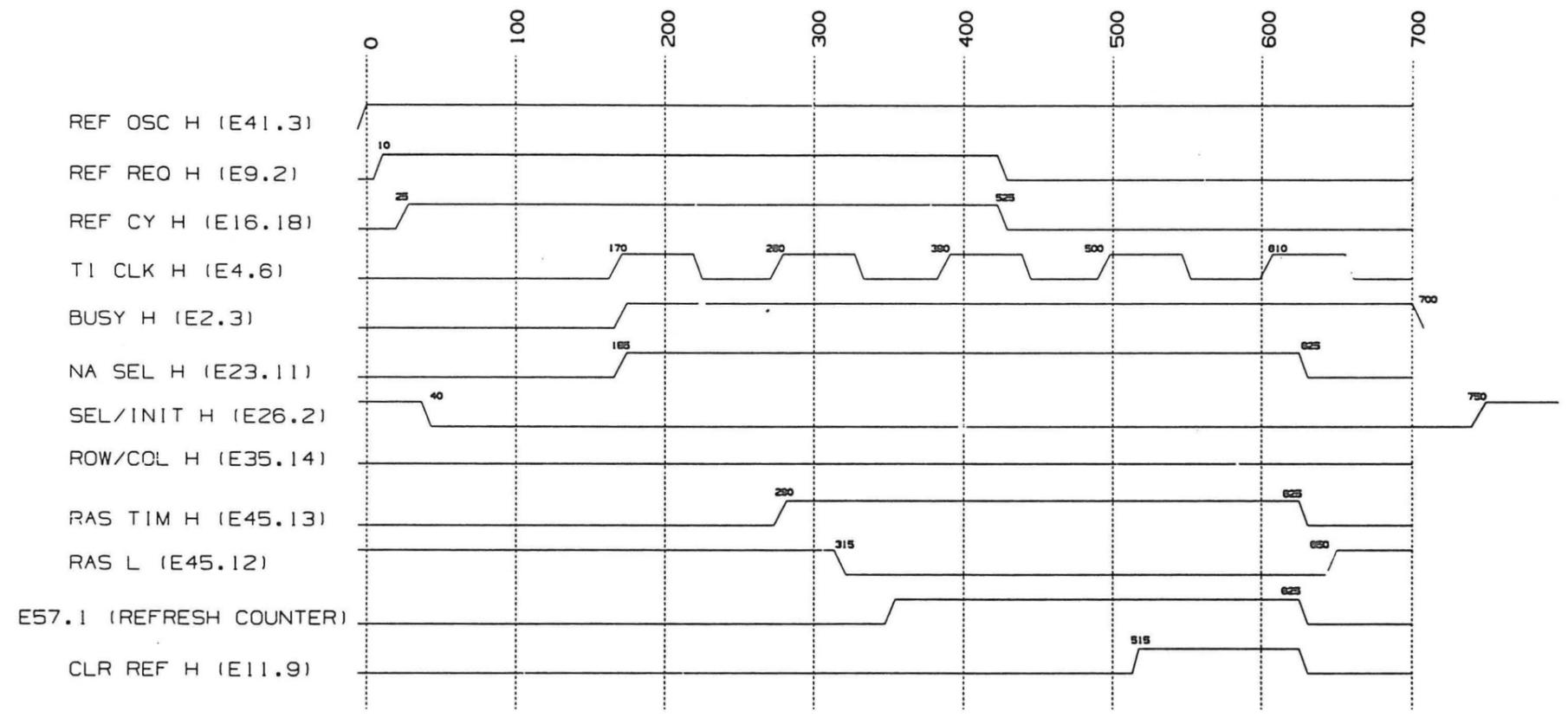
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REVISIONS		
CHK	CHANGE NO	REV

TITLE	MEMCRY SYSTEM	SIZE CODE	D TD	NUMBER	MS11-P-DBU	REV	A
SCALE	NC/E	SHEET	5	OF	6	DIST	

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D 10 MS11-P-DBU



(REFRESH TIMING)

REVISIONS		
CHK	CHANGE NO	REV

