















LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	D-MD-5014421-0-0	5014421-00	ETCH BRD. MAP MODULE	1	
2		1001776-00	1 MFD 35V 10% S.TANT	6	C8-C13
3		1009725-00	1.5MFD 35V 10% NPTANT	1	C54
4		1000075-00	25 MFD 25V +75-10% AL EL	6	C1-C6
5		1012784-00	.047 MFD 50V +80-20% CER	32	C14-C42,C52,C53,C55
6		1103441-00	1N 756A VZ= 8.2 5% .40W P	1	D3
7		1105275-00	D 672 TR= 15NS PIV= 60V SI	1	D1
8		1105796-00	1N 4004 PIV=400 I= 1A D041 SP	2	D4,D5
9		1105871-00	1/4M5.1AZ1 = 5.1 1% .25W N	1	D2
10		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1	
11		1211164-00	SW,DIP 1P 1A 4POS	1	E6
12		1211164-06	SW,DIP 1P 1A 10POS	1	E58
13		1215006-02	SOCKET 16PIN IC LOW PROFILE	4	XE75,XE82,XE83,XE89
14		1215006-03	SOCKET 18PIN IC LOW PROFILE	1	XE74
15		1215006-07	SOCKET 20PIN IC LOW PROFILE	2	XE78,XE92
16		1300005-01	R NETWORK 13-1K 5.0 % 14PIN	1	E68
17		1300005-03	R NETWORK 13-10K 5.0 % 14PIN	1	E77
18		1300005-04	R NETWORK 15-470 5.0 % 16PIN	2	E17,E26
19		1300005-10	R NETWORK 15-560 5.0 % 16PIN	2	E30,E37
20		1300197-00	33.0 .25 W 5.0 % CC	2	R21,R35
21		1300247-00	120.0 .25 W 5.0 % CC	1	R14
22		1300316-00	470.0 .25 W 5.0 % CC	3	R5,R15,R42
23		1300365-00	1.0 K .25 W 5.0 % CC	3	R1,R11,R13
24		1300447-00	4.70 K .25 W 5.0 % CC	4	R9,R17,R40,R41
25		1302411-00	511.0 .25 W 1.0 % RN55D-F10	2	R3,P8
26		1302466-00	100.0 K .25 W 5.0 % CC	1	R16
27		1302645-00	1.10 K .25 W 1.0 % RN55D-F10	1	R24
28		1302931-00	49.90 .25 W 1.0 % RN55D-F10	1	R25
29		1302956-00	196.0 .25 W 1.0 % RN55D-F10	1	R28
30		1303037-00	90.90 .25 W 1.0 % RN55D-F10	2	R26,R30

REVISION HISTORY		BASIC PART NO:	M7134	DRN:	J.CASEY	DATE:	05-FEB-81	D I G I T A L			
ENG	ECO NUMBER	RFV	SECTION A OF A	CHK'D:	J.CASEY	DATE:	05-FEB-81	TITLE	PARTS LIST		
---	INITIAL	A	SECTION VARIATION INDEX					MAP MODULE			
			[A] 00								
			[B]								
			[C]	DES.ENG:	A.CALAMARI	DATE:	05-FEB-81				
			[D]								
			[E]								
			[F]	RESP.ENG.:	A.CALAMARI	DATE:	05-FEB-81		DOCUMENT NUMBER		
			[H]								
			[J]					SIZE CODE  NUMBER	REV		
			[K]	MPG.ENG.:	G.ABREU	DATE:	05-FEB-81	K   PL   M7134-0-DBP	A		
			[L]								
			[M]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #		
			[N]	ID-UA-M7134-0-0				21281A,PLS	16		

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*TLU*

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
31	31		1303047-00	464.0 .25 W 1.0 % RN55D-F10	1		R24
32	32		1303114-00	1.0 K .25 W 1.0 % RN55D-F10	1		R27
33	33		1304728-00	4.0 K .25 W .10% RN55E-B 2	2		R31,R38
34	34		1304863-00	316.0 .25 W 1.0 % RN55D-F10	2		R2,R7
35	35		1305127-00	4.0 K .25 W 1.0 % RN55D-F10	1		R36
36	36		1305336-00	200.0 K .25 W 1.0 % RN55D-F10	1		R37
37	37		1305356-00	8.0 K .25 W .10% RN55C-B 5	2		R32,R39
38	38		1305431-00	10.0 K .25 W .10% RN55E-B 2	2		R22,R34
39	39		1305596-00	300.0 .25 W .10% RN55C-B 5	1		R29
40	40		1310248-00	30.10 K .25 W .10% RN55E-B 2	2		R23,R33
41	41		1310633-00	2.55 K .25 W 1.0 % RN55D-F10	1		R19
42	42		1312384-04	10.0 K .25 W 5.0 % FLAME	1		R12
43	43		1312545-00	2.32 K .25 W 1.0 % RN55D-F10	1		R18
44	44		1509338-00	DEC6531B NPN 310MA SI 40 90 P	1		Q1
45	45		1601562-00	1.0 UH 10% 475MA #DD1.00	1		L1
46	46		1612463-00	DELAY= 150NS,10TAPS	1		E31
47	47		1613120-00	DELAY= 200NS,10TAPS	1		E94
48	48		1910532-00	74S00 NAND GATE-QUAD 2IN	2		F70,E80
49	49		1910534-00	74S04 INVERTER GATE-HEX 1I	4		E53,E56,E54,E71
50	50		1910536-00	74S10 NAND GATE-TRIPLE 3IN	1		E72
51	51		1910537-00	74S11 AND GATE-TPIPLE 3INP	1		E73
52	52		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1		E66
53	53		1910546-00	74S140 NAND GATE-DUAL 4INPU	2		E67,F91
54	54		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1		E63
55	55		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	5		E69,E76,E64,E85,E51
56	56		1911579-00	8641 TRANSCEIVER,BUS,QUA	5		E1,F2,F9,E10,E60
57	57		1911676-00	74S139 DECODEP-DUAL TWO-IMP	1		E86
58	58		1911712-00	74S51 AND-OR GATE-INVERT D	1		E62
59	59		1911944-00	555CN TIMER,FUNCT,BLOCK	1		E24
60	60		1912096-00	DEC 74S86 XOR GATE,QUAD 2IN	1		E38
61	61		1912097-00	SN 74S182 LOOK AHD CARRY GEN	2		E43,E45
62	62		1912107-01	324ANOP AMP,QUAD	1		E5
63	63		1912108-00	339 VOLT CNPRIP,QUAD	3		E7,F15,F23
64	64		1912388-00	74S02 NOP GATE-QUAD 2IN,PO	3		E61,E87,E93
65	65		1912389-00	74S08 AND GATE-QUAD 2IN,PO	3		E46,E57,E79
66	66		1912647-00	LS257 MUX 1 OF 2 (QUAD)	2		E21,E39
67	67		1912805-00	LS08 AND GATE-QUAD 2IN,PO	2		E52,F65
68	68		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	2		E47,E48
69	69		1912829-00	LS86 X-OR GATE-QUAD 2IN	1		E49
70	70		1912854-00	LS193 COUNTER,SYNCHP,4BIT,	1		E81
71	71		1913340-00	74S32 OR GATE-QUAD 2IN	2		E55,F86
72	72		1913462-00	74S240 OCTAL BUFFER,INVERTI	3		E16,E25,E33
73	73		1913493-00	74S241 OCTAL BUFFER,TRI-STA	1		E32
74	74		1913663-00	93419DC RAM 64X9 28PIN TT	3		E27-E29
75	75		1913700-00	74S381 ALU-4BIT	5		F34-E36,E42,E44
76	76		1913777-00	LS240 DRIVER,LINE,OCTAL,T	5		E3,F13,E18,E19,E59
77	77		1914086-00	74S30 NAND GATE-POS 8IN	1		E64
78	78		1914214-00	LS374 FF-D OCTAL EDGE TRIG	5		E4,E11,E12,E41,E50

D	I	G	I	T	A	L	TITLE	MAP MODULE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M7134-0-DRP	A

TW

AUTOMATED BY PRTLST,3P(44)

P A R T S L I S T

SHEET A3 OF A3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
79	79	1915193-00	LS244 DRIVER,LINE,OCTAL,T	4	E14,E20,E22,E40
80	80	23000A9-01	*** THIS ITEM IS NOT USED ***	-	
81	81	23054C6-00	C6-01	1	E78
82	82	23774F1-00	F1-01	1	E74
83	83	9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12	
84	84	9009149-00	PIN, STAKING, P.C. BOARD, .025 X	12	TP1-TP12
85	85	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	2	W13,W22
86	86	23063C6-00	C6-01	1	E92

D	I	G	I	T	A	L	TITLE	MAP MODULE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M7134-0-DBP	A

TW

# M7134 JUMPER AND SWITCH SETTINGS

## BOOT ROMS

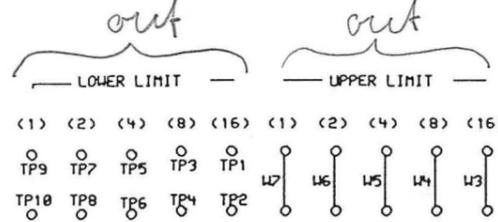
THE CPU SPECIFIC DIAGNOSTIC ROM GOES INTO SOCKET E74. ROMS FOR OTHER BOOTABLE DEVICES GO INTO THE FOLLOWING SOCKETS IN ORDER:

- 1. BOOT ROM 1 E75
- 2. BOOT ROM 2 E89
- 3. BOOT ROM 3 E82
- 4. BOOT ROM 4 E83

JUMPER W13 ENABLES THE DIAGNOSTIC ROM WHEN IN, AND IS USUALLY PRESENT.

*E6 S1 on  
S2 on  
S3 off  
S4 on*

## UNIBUS MEMORY SELECT

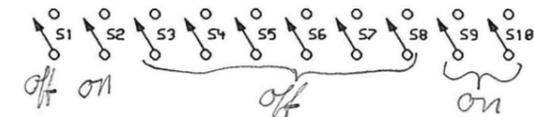


W3-W7 AND TP1-TP10 ARE THE BINARY-CODED PAGE NUMBER LIMIT (UPPER OR LOWER). A JUMPER IN CORRESPONDS TO A LOGIC "0" A JUMPER OUT TO A LOGIC "1". TO SET THE JUMPERS, DETERMINE WHICH UNIBUS PAGE THE MEMORY RESIDES IN (0-31) BY CHECKING WHICH OF THE 5 ADDRESS BITS BA17-BA13 ARE ASSERTED. NOTE THAT THE JUMPERS ARE IN REVERSE BINARY ORDER. ALL ZEROES IS PAGE 0, BA13 ASSERTED ALONE IS PAGE 1, ETC UP TO 31 (11111). FOR UNIBUS MEMORY TO BE DETECTED, IT MUST LIE AT OR ABOVE THE LOWER LIMIT JUMPER SETTING AND BELOW THE UPPER LIMIT JUMPER SETTING. THUS TO HAVE UNIBUS MEMORY IN PAGES 5-9, ONE WOULD SET THE LOWER LIMIT TO PAGE 5 (01010), OR W12, W10 OUT, W11, W9, W8 IN, AND THE UPPER LIMIT TO PAGE 10 (01010) OR W6, W4 OUT AND W7, W5, W3 IN. UNIBUS MEMORY MUST BE CONTIGUOUS WHEN USED—NO GAPS ALLOWED. BEWARE THAT DMA TRAFFIC TO MAIN MEMORY WILL BE INTERCEPTED BY UNIBUS MEMORY PRESENT ON THE UNIBUS.

*TP11 - TP12 = out  
W13 = in*

## BOOT SETTINGS

BOOT SWITCHES—E58, K4-9  
FOR ALL SWITCHES ON=CLOSED=LOGIC "1"



- S1—DETERMINES START ADDR (UPPER 3 DIGITS) WHEN OPEN 773000; WHEN CLOSED 765000
- S2—WHEN ON ENABLES INTERNAL BOOT (SEE NOTE)
- S3-S10 ARE BITS 0-1 OF THE STARTING ADDRESS A CLOSED SWITCH EQUALS A "1" IN THE ADDRESS

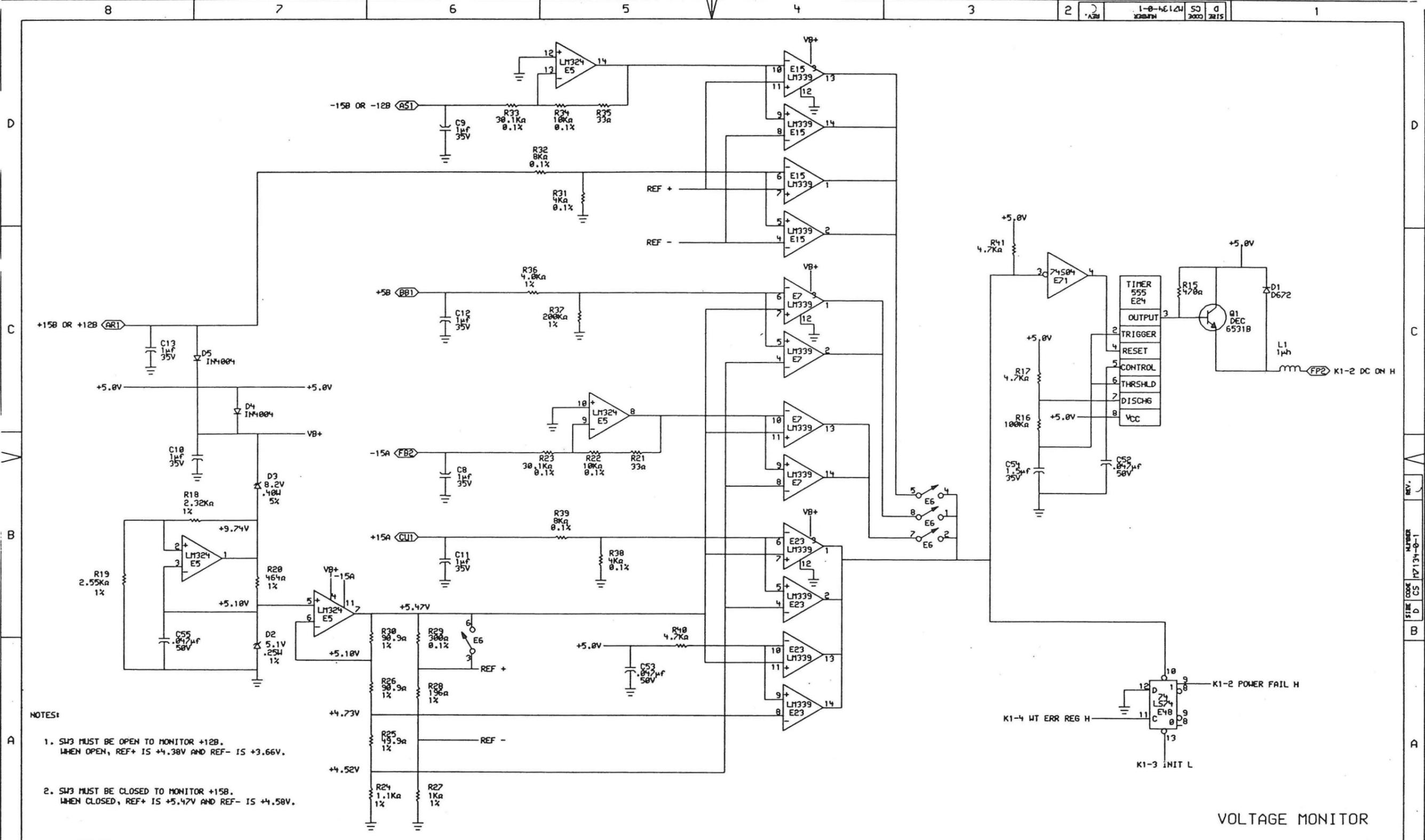
THE SWITCHES ARE VIEWED WITH THE BOARD COMPONENT SIDE UP, GOLD FINGERS AT THE BOTTOM.

NOTE: A M9302 TERMINATOR MUST BE INSTALLED IN PLACE OF THE M9312 BOOT MODULE WHEN THE INTERNAL BOOT IS ENABLED.

*E75 = Bootrom installed  
Adr. 773004 zum starte  
765000 Diagnostik/Boot  
Diagn. Prom = S6 8321 774 F1*

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	CHK	CHANGE NO.	REV		160,240 K101A.DRW	DATE 25	BOARD LOCATION: SHEET 1 OF 10	SIZE CODE D CS	NUMBER M2134-0-1	REV. C
					R. GHUDA	FIRST USED ON OPTION/MODEL: 11/24	103-FEB-81 11129	NEXT HIGHER ASSEMBLY: B-DD-M7134-0		

REV. 2  
M7134-0-1  
CS  
D  
B



- NOTES:
- SW3 MUST BE OPEN TO MONITOR +12B. WHEN OPEN, REF+ IS +4.38V AND REF- IS +3.66V.
  - SW3 MUST BE CLOSED TO MONITOR +15B. WHEN CLOSED, REF+ IS +5.47V AND REF- IS +4.58V.

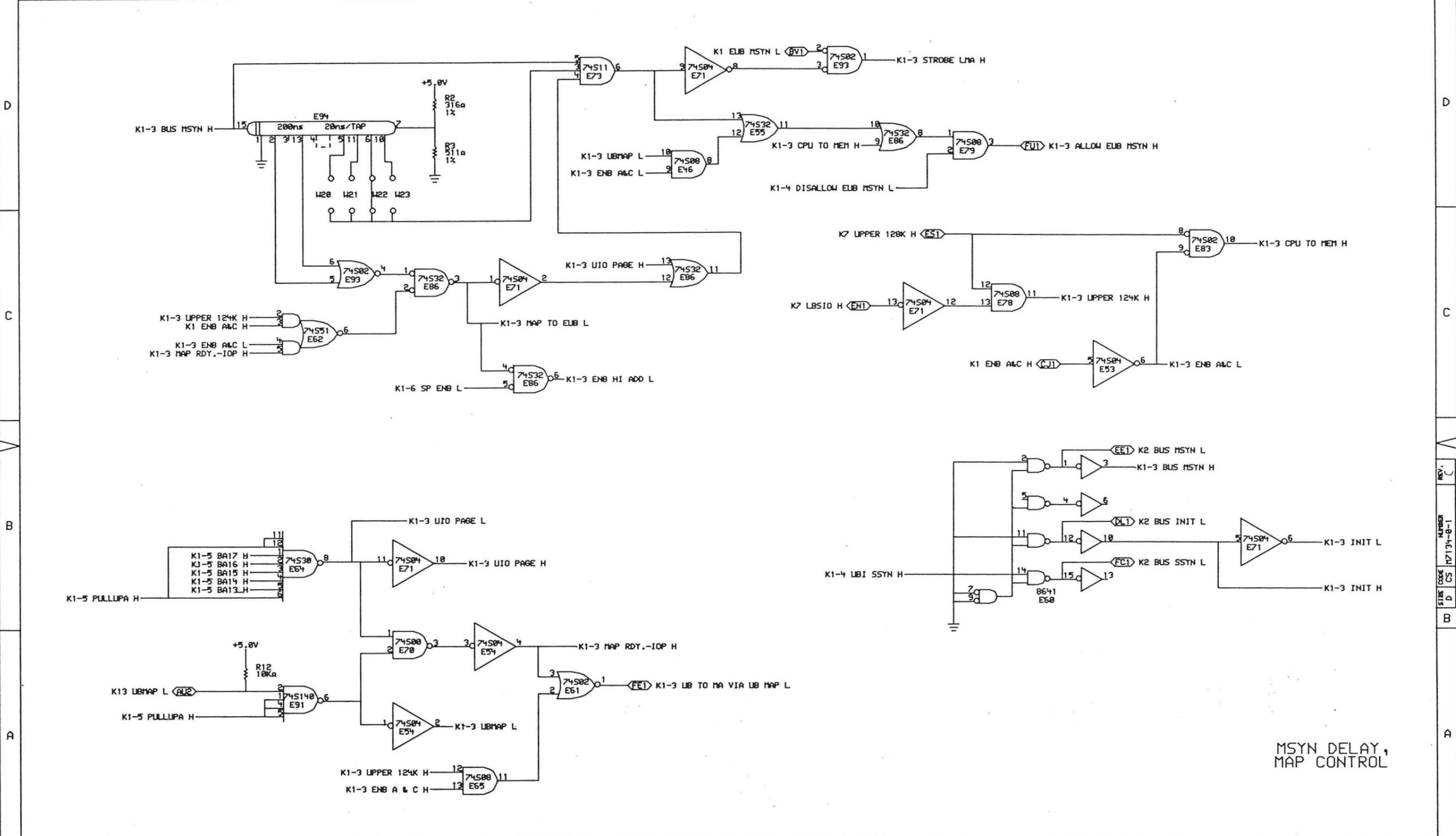
VOLTAGE MONITOR

REV.	CHG.	NO.	REV.

REVISIONS	DATE	ENG.	DATE	TITLE:

digital	DATE: 03-FEB-81	ENG: [Signature]	DATE: [Signature]	TITLE: (K1-2) UNIBUS MAP MODULE
1160.240 X102A.DRW	23-JAN-81 10:41	NEXT HIGHER ASSEMBLY:	SIZE CODE: D CS	NUMBER: M7134-0-1
FIRST USED ON OPTION/MODEL: PDP 1124	B-DD-M7134-0			REV. C

TW 1

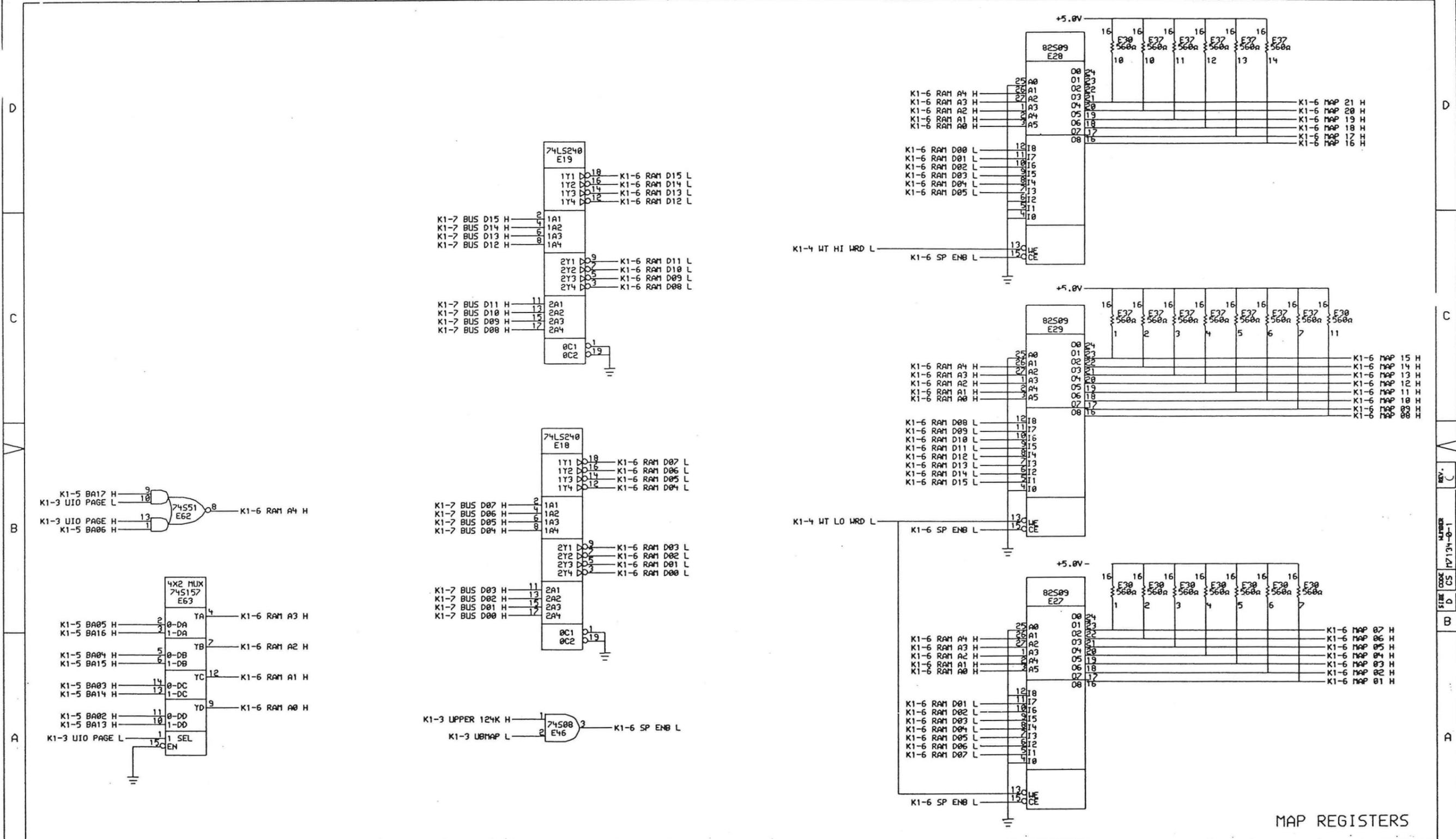


MSYN DELAY, MAP CONTROL

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FIRST USED ON OPTION/MODEL: PDP 1124		NEXT HIGHER ASSEMBLY: B-DD-M7134-0	SIZE CODE D CS	NUMBER M7134-0-1





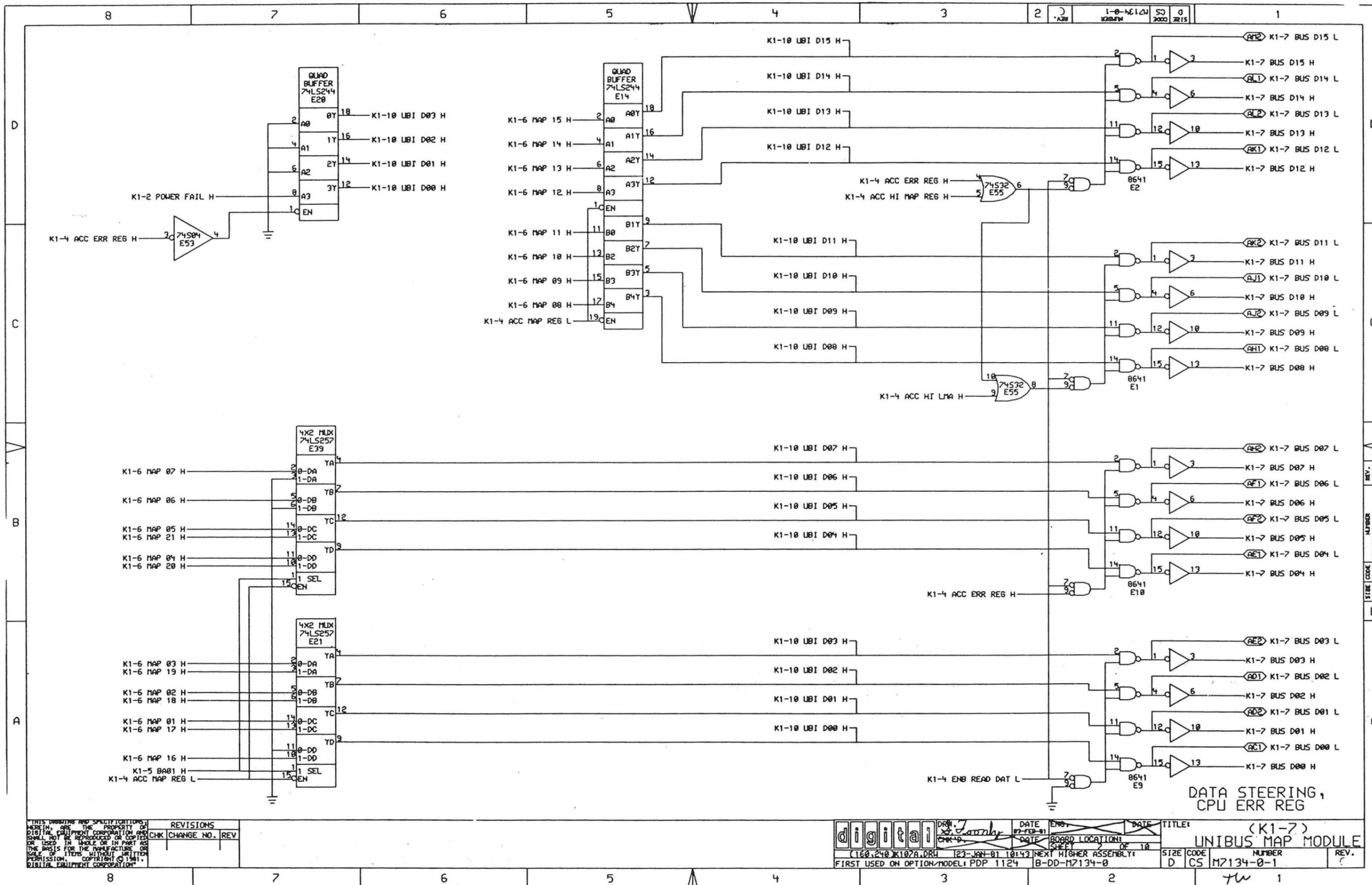


MAP REGISTERS

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REVISIONS	CHK	CHANGE NO.	REV

digital	DRN: J. J. J.	DATE: 03-FEB-81	ENG:	DATE:	TITLE:
					(K1-6) UNIBUS MAP MODULE
FIRST USED ON OPTION/MODEL: PDP 1124					SIZE CODE: D CS
NEXT HIGHER ASSEMBLY: B-DD-M7134-0					NUMBER: M7134-0-1

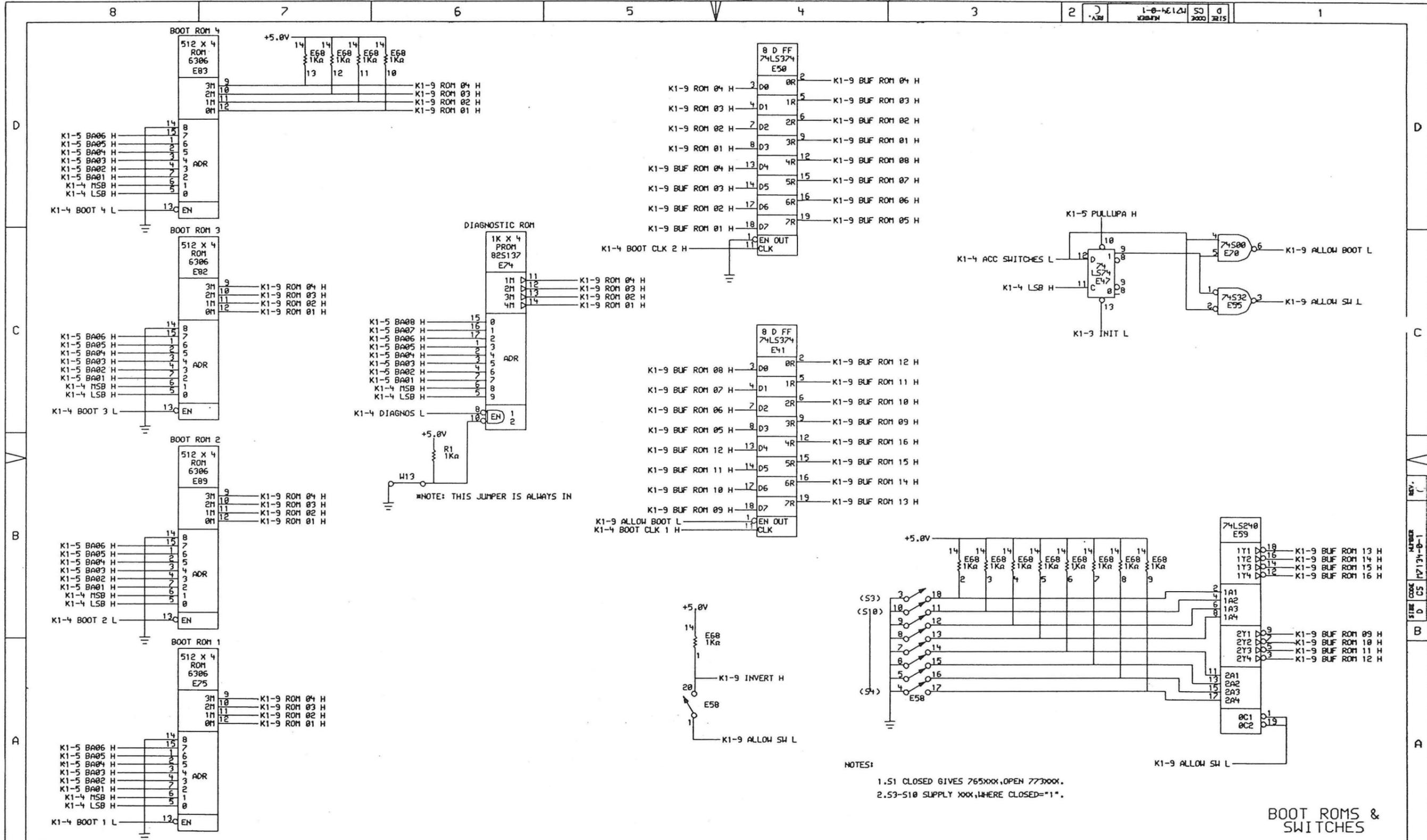
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE
	23-JAN-81	J. Family	10:43	(K1-7) UNIBUS MAP MODULE
FIRST USED ON OPTION/MODEL: PDP 1124		BOARD LOCATION: B-DD-M7134-0		REV. 1
SIZE	CODE	NUMBER	REV.	
D	CS	M7134-0-1		

JW





NOTES:  
 1.S1 CLOSED GIVES 765XXX, OPEN 773XXX.  
 2.S3-S10 SUPPLY XXX, WHERE CLOSED="1".

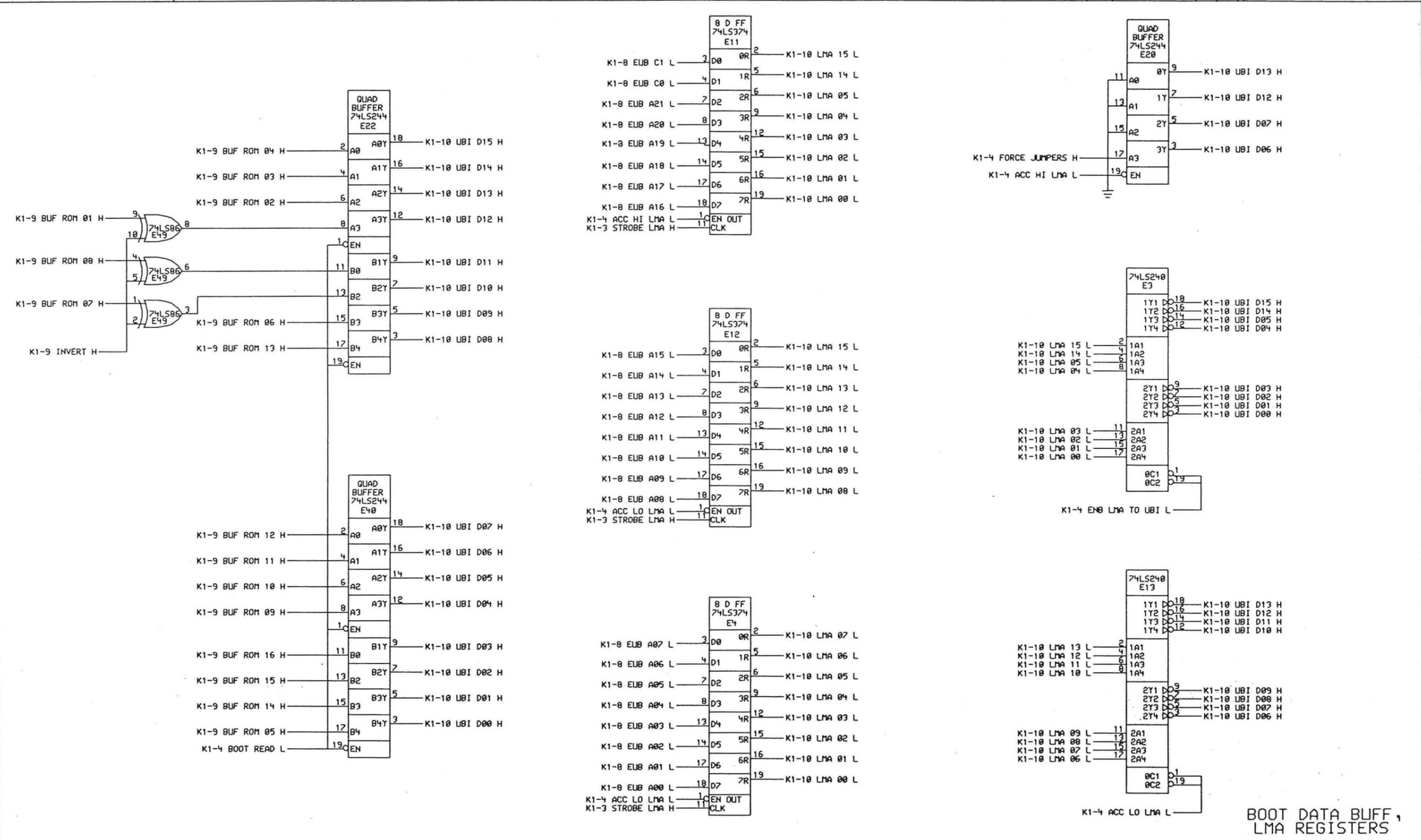
BOOT ROMS & SWITCHES

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REV.	CHG.	CHANGE NO.	REV.

digital	DRN. <i>J. Looney</i>	DATE 07-FEB-81	ENG. <i>[Signature]</i>	DATE	TITLE: (K1-9) UNIBUS MAP MODULE
	CHK. <i>[Signature]</i>	DATE	BOARD LOCATION: SHEET 9 OF 18	SIZE CODE D CS	NUMBER M7134-0-1
C160.240 X109A.DRW			123-JAN-81 10:43	NEXT HIGHER ASSEMBLY: B-DD-M7134-0	REV. C
FIRST USED ON OPTION/MODEL: PDP 1124					

tw 1



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	23-JAN-81	10144	DATE		TITLE:	(K1-10)
	DRN			ENG		BOARD LOCATION:	UNIBUS MAP MODULE
						SHEET	10 OF 10
						SIZE	D CS
						NUMBER	M7134-0-1
						REV.	C