

# SN5400, SN54LS00, SN54S00, SN7400, SN74LS00, SN74S00 QUADRUPLE 2-INPUT POSITIVE-NAND GATES

DECEMBER 1983—REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

## description

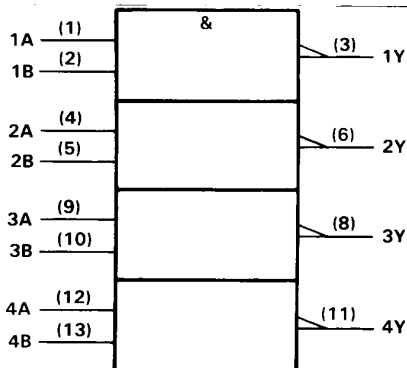
These devices contain four independent 2-input-NAND gates.

The SN5400, SN54LS00, and SN54S00 are characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN7400, SN74LS00, and SN74S00 are characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

FUNCTION TABLE (each gate)

| INPUTS |   | OUTPUT |
|--------|---|--------|
| A      | B | Y      |
| H      | H | L      |
| L      | X | H      |
| X      | L | H      |

## logic symbol†

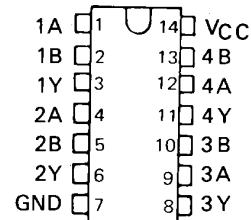


†This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

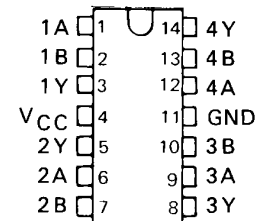
Pin numbers shown are for D, J, and N packages.

SN5400 . . . J PACKAGE  
SN54LS00, SN54S00 . . . J OR W PACKAGE  
SN7400 . . . N PACKAGE  
SN74LS00, SN74S00 . . . D OR N PACKAGE

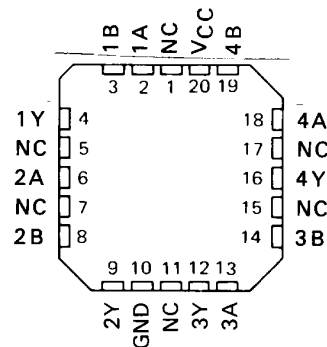
(TOP VIEW)



SN5400 . . . W PACKAGE  
(TOP VIEW)

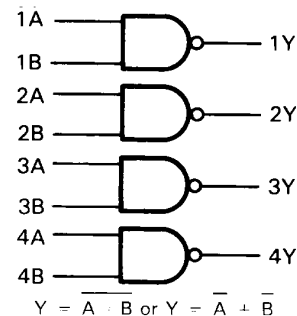


SN54LS00, SN54S00 . . . FK PACKAGE  
(TOP VIEW)



NC - No internal connection

## logic diagram (positive logic)



PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS  
INSTRUMENTS

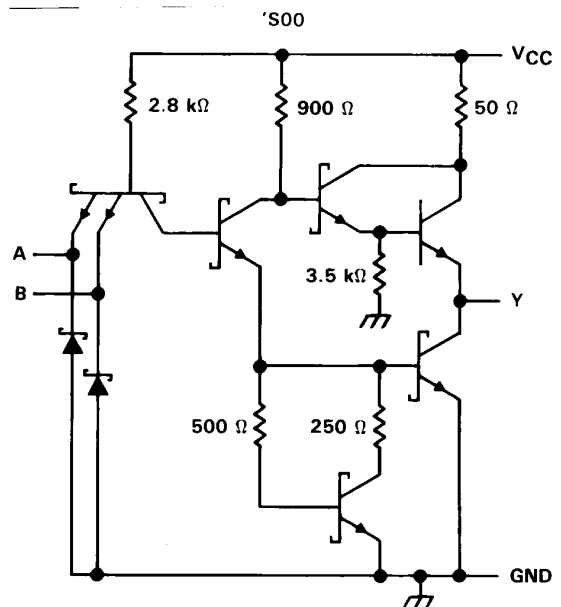
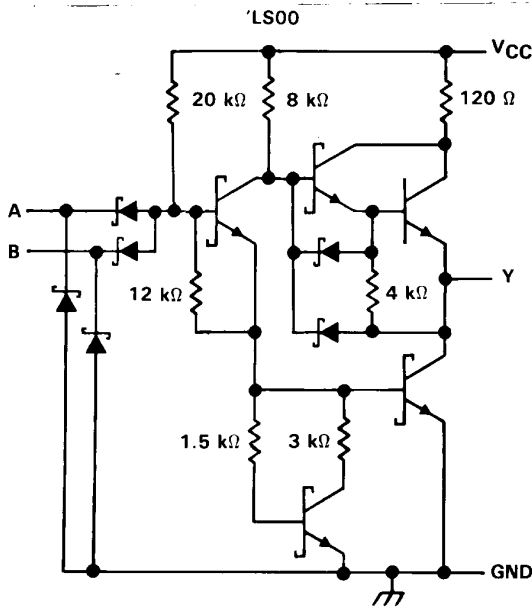
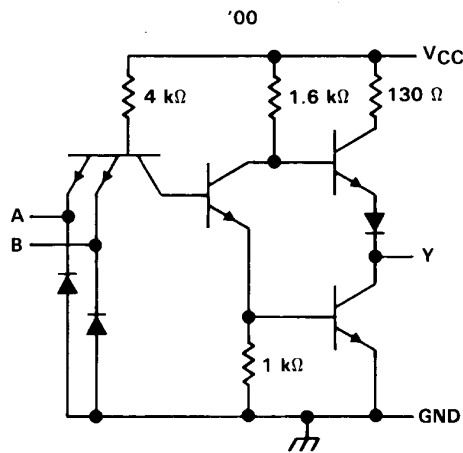
POST OFFICE BOX 655012 • DALLAS, TEXAS 75265

2

TTL Devices

**SN5400, SN54LS00, SN54S00,  
SN7400, SN74LS00, SN74S00  
QUADRUPLE 2-INPUT POSITIVE-NAND GATES**

schematics (each gate)



Resistor values shown are nominal.

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

|   |                |
|---|----------------|
| Supply voltage, $V_{CC}$ (see Note 1)       | 7 V            |
| Input voltage: '00, 'S00                    | 5.5 V          |
| 'LS00                                       | 7 V            |
| Operating free-air temperature range: SN54' | -55°C to 125°C |
| SN74'                                       | 0°C to 70°C    |
| Storage temperature range                   | -65°C to 150°C |

NOTE 1: Voltage values are with respect to network ground terminal.

**2**

TTL Devices

# SN5400, SN7400 QUADRUPLE 2-INPUT POSITIVE-NAND GATES

## recommended operating conditions

|   | SN5400 |     |     | SN7400 |     |      | UNIT |
|---|--------|-----|-----|--------|-----|------|------|
|   | MIN    | NOM | MAX | MIN    | NOM | MAX  |      |
| V <sub>CC</sub> Supply voltage                | 4.5    | 5   | 5.5 | 4.75   | 5   | 5.25 | V    |
| V <sub>IH</sub> High-level input voltage      | 2      |     |     | 2      |     |      | V    |
| V <sub>IL</sub> Low-level input voltage       | 0.8    |     |     | 0.8    |     |      | V    |
| I <sub>OH</sub> High-level output current     | -0.4   |     |     | -0.4   |     |      | mA   |
| I <sub>OL</sub> Low-level output current      | 16     |     |     | 16     |     |      | mA   |
| T <sub>A</sub> Operating free-air temperature | -55    |     | 125 | 0      |     | 70   | °C   |

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER         | TEST CONDITIONS †   | SN5400 |       |     | SN7400 |       |     | UNIT |
|-------------------|---|--------|-------|-----|--------|-------|-----|------|
|                   |   | MIN    | TYP ‡ | MAX | MIN    | TYP ‡ | MAX |      |
| V <sub>IK</sub>   | V <sub>CC</sub> = MIN, I <sub>I</sub> = -12 mA                            | -1.5   |       |     | -1.5   |       |     | V    |
| V <sub>OH</sub>   | V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V, I <sub>OH</sub> = -0.4 mA | 2.4    | 3.4   |     | 2.4    | 3.4   |     | V    |
| V <sub>OL</sub>   | V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 16 mA     | 0.2    |       | 0.4 | 0.2    |       | 0.4 | V    |
| I <sub>I</sub>    | V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V                             | 1      |       |     | 1      |       |     | mA   |
| I <sub>IH</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.4 V                             | 40     |       |     | 40     |       |     | μA   |
| I <sub>IL</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V                             | -1.6   |       |     | -1.6   |       |     | mA   |
| I <sub>OS</sub> § | V <sub>CC</sub> = MAX   | -20    |       | -55 | -18    |       | -55 | mA   |
| I <sub>CCH</sub>  | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0 V                               | 4      |       | 8   | 4      |       | 8   | mA   |
| I <sub>CCL</sub>  | V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V                             | 12     |       | 22  | 12     |       | 22  | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

§ Not more than one output should be shorted at a time.

## switching characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C (see note 2)

| PARAMETER        | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS                                | MIN | TYP | MAX | UNIT |
|------------------|--------------|-------------|--|-----|-----|-----|------|
| t <sub>PLH</sub> | A or B       | Y           | R <sub>L</sub> = 400 Ω, C <sub>L</sub> = 15 pF |     | 11  | 22  | ns   |
| t <sub>PHL</sub> |              |             |  |     | 7   | 15  | ns   |

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

2

TTL Devices

**SN54LS00, SN74LS00**  
**QUADRUPLE 2-INPUT POSITIVE-NAND GATES**

TABLE 1  
 STYLL 100-100 001 100101111111

recommended operating conditions

|   | SN54LS00 |     |      | SN74LS00 |     |      | UNIT |
|---|----------|-----|------|----------|-----|------|------|
|   | MIN      | NOM | MAX  | MIN      | NOM | MAX  |      |
| V <sub>CC</sub> Supply voltage                | 4.5      | 5   | 5.5  | 4.75     | 5   | 5.25 | V    |
| V <sub>IH</sub> High-level input voltage      | 2        |     |      | 2        |     |      | V    |
| V <sub>IL</sub> Low-level input voltage       |          |     | 0.7  |          |     | 0.8  | V    |
| I <sub>OH</sub> High-level output current     |          |     | -0.4 |          |     | -0.4 | mA   |
| I <sub>OL</sub> Low-level output current      |          |     | 4    |          |     | 8    | mA   |
| T <sub>A</sub> Operating free-air temperature | -55      |     | 125  | 0        |     | 70   | °C   |

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER         | TEST CONDITIONS †   | SN54LS00 |      |      | SN74LS00 |      |      | UNIT |
|-------------------|---|----------|------|------|----------|------|------|------|
|                   |   | MIN      | TYP‡ | MAX  | MIN      | TYP‡ | MAX  |      |
| V <sub>IK</sub>   | V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA                          |          | -1.5 |      |          | -1.5 | V    |      |
| V <sub>OH</sub>   | V <sub>CC</sub> = MIN, V <sub>IL</sub> = MAX, I <sub>OH</sub> = -0.4 mA | 2.5      | 3.4  |      | 2.7      | 3.4  | V    |      |
| V <sub>OL</sub>   | V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 4 mA    |          | 0.25 | 0.4  |          | 0.25 | 0.4  | V    |
|                   | V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 8 mA    |          |      |      |          | 0.35 | 0.5  |      |
| I <sub>I</sub>    | V <sub>CC</sub> = MAX, V <sub>I</sub> = 7 V                             |          | 0.1  |      |          | 0.1  | mA   |      |
| I <sub>IH</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V                           |          | 20   |      |          | 20   | µA   |      |
| I <sub>IL</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V                           |          | -0.4 |      |          | -0.4 | mA   |      |
| I <sub>OS</sub> § | V <sub>CC</sub> = MAX   | -20      |      | -100 | -20      |      | -100 | mA   |
| I <sub>CCH</sub>  | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0 V                             |          | 0.8  | 1.6  |          | 0.8  | 1.6  | mA   |
| I <sub>CCL</sub>  | V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V                           |          | 2.4  | 4.4  |          | 2.4  | 4.4  | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C (see note 2)

| PARAMETER        | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS        |                        | MIN | TYP | MAX | UNIT |
|------------------|--------------|-------------|------------------------|------------------------|-----|-----|-----|------|
| t <sub>PLH</sub> | A or B       | Y           | R <sub>L</sub> = 2 kΩ, | C <sub>L</sub> = 15 pF |     | 9   | 15  | ns   |
| t <sub>PHL</sub> |              |             |                        |                        |     | 10  | 15  | ns   |

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

2

TTL Devices

# SN54S00, SN74S00 QUADRUPLE 2-INPUT POSITIVE-NAND GATES

## recommended operating conditions

|   | SN54S00 |     |     | SN74S00 |     |      | UNIT |
|---|---------|-----|-----|---------|-----|------|------|
|   | MIN     | NOM | MAX | MIN     | NOM | MAX  |      |
| V <sub>CC</sub> Supply voltage                | 4.5     | 5   | 5.5 | 4.75    | 5   | 5.25 | V    |
| V <sub>IH</sub> High-level input voltage      | 2       |     |     | 2       |     |      | V    |
| V <sub>IL</sub> Low-level input voltage       |         |     | 0.8 |         |     | 0.8  | V    |
| I <sub>OH</sub> High-level output current     |         |     | -1  |         |     | -1   | mA   |
| I <sub>OL</sub> Low-level output current      |         |     | 20  |         |     | 20   | mA   |
| T <sub>A</sub> Operating free-air temperature | -55     |     | 125 | 0       |     | 70   | °C   |

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER         | TEST CONDITIONS †   | SN54S00 |      |      | SN74S00 |      |      | UNIT |
|-------------------|---|---------|------|------|---------|------|------|------|
|                   |   | MIN     | TYP‡ | MAX  | MIN     | TYP‡ | MAX  |      |
| V <sub>IK</sub>   | V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA                          |         |      | -1.2 |         |      | -1.2 | V    |
| V <sub>OH</sub>   | V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V, I <sub>OH</sub> = -1 mA | 2.5     | 3.4  |      | 2.7     | 3.4  |      | V    |
| V <sub>OL</sub>   | V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 20 mA   |         |      | 0.5  |         |      | 0.5  | V    |
| I <sub>I</sub>    | V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V                           |         |      | 1    |         |      | 1    | mA   |
| I <sub>IH</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V                           |         |      | 50   |         |      | 50   | μA   |
| I <sub>IL</sub>   | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.5 V                           |         |      | -2   |         |      | -2   | mA   |
| I <sub>OS</sub> § | V <sub>CC</sub> = MAX   | -40     |      | -100 | -40     |      | -100 | mA   |
| I <sub>CC</sub> H | V <sub>CC</sub> = MAX, V <sub>I</sub> = 0 V                             |         | 10   | 16   |         | 10   | 16   | mA   |
| I <sub>CC</sub> L | V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V                           |         | 20   | 36   |         | 20   | 36   | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

## switching characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C (see note 2)

| PARAMETER        | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS                                | MIN | TYP | MAX | UNIT |
|------------------|--------------|-------------|--|-----|-----|-----|------|
| t <sub>PLH</sub> | A or B       | Y           | R <sub>L</sub> = 280 Ω, C <sub>L</sub> = 15 pF |     | 3   | 4.5 | ns   |
| t <sub>PHL</sub> |              |             |  |     | 3   | 5   | ns   |
| t <sub>PLH</sub> |              |             | R <sub>L</sub> = 280 Ω, C <sub>L</sub> = 50 pF |     | 4.5 |     | ns   |
| t <sub>PHL</sub> |              |             |  |     | 5   |     | ns   |

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

2

TTL Devices

## **IMPORTANT NOTICE**

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

**TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.**

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.